Reachability Analysis for Quantum Model Checking using TDD

毕业设计开题报告

高丁超导师: 应明生

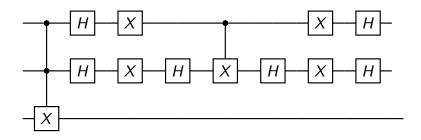
2023年6月29日



Background

Related work

Work plan



B: Quantum circuit of Grover algorithm

$$H = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}, X = \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$$
$$\begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 \end{bmatrix}$$

$$CX = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}, CCX = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \end{bmatrix}$$

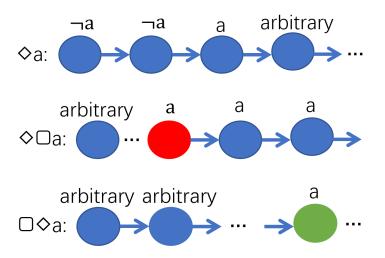


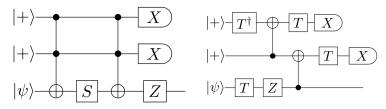
(1)

transition system: (S, I, Act, T)

Quantum transition system: $(\mathcal{H}, \mathcal{H}_0, Act, \{U_\alpha, \alpha \in Act\})$

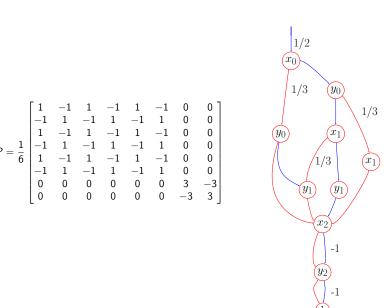
Reachability problem: \diamondsuit , $\diamondsuit\Box$, $\Box\diamondsuit$





8: Circuit Equivalence Checking

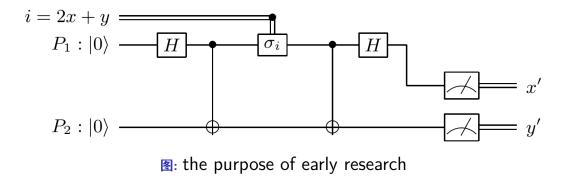
8: a TDD example



Background

Related work

Work plan



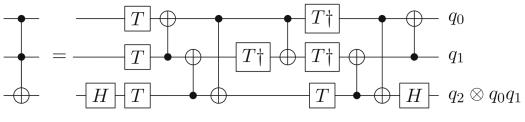
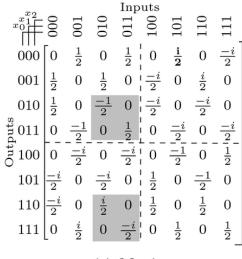
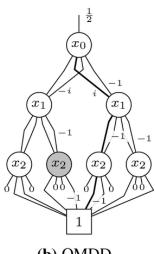


图: Decomposition of a Toffoli gate



(a) Matrix



(b) QMDD

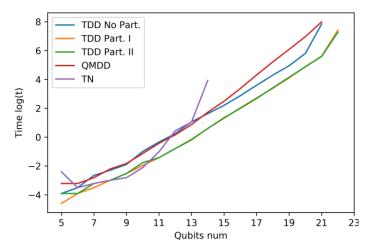


图: time consumption for constructing the functionality of qft circuits

Background

Related work

Work plan

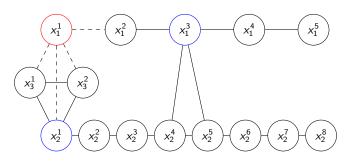


图: addition partition

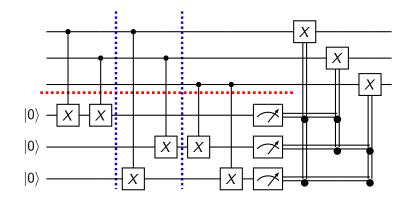


图: contraction partition

benchmark	basic	$\operatorname{addition}$	contraction
Grover 20	\sim 5min	\sim 4min	\sim 4sec
Quantum Fourier Transform 20	\sim 20min	$\sim\!\!11 min$	<1sec
Quantum Random walk 20	\sim 6min	\sim 4min	$\sim\!$ 15sec
Bernstein-Vazirani 50	\sim 4min	\sim 4min	${\sim}16{\sf sec}$
GHZ 500	\sim 3sec	\sim 1.5sec	\sim 1.7sec

表: quantum image computation

Reachability space

Reachability problem

Actual problems

Hardware supports

Supports from professor Ying, Phd Hong et al.