







ARITHMETIC-LOGIC				CONTROL				DISPLACEMENT				MEMORY			
Instruction	Syntax			Instruction	Syntax			Instruction	Syntax			Instruction	Syntax		
<a href="#">add</a>	add	reg_rs1	reg_or_imm	reg_rd	<a href="#">ba</a>	ba	label	<a href="#">sll</a>	sll	reg_rs1	reg_or_imm	reg_rd	<a href="#">st</a>	st	reg_rd
<a href="#">addcc</a>	addcc	reg_rs1	reg_or_imm	reg_rd	<a href="#">bn</a>	bn	label	<a href="#">srl</a>	srl	reg_rs1	reg_or_imm	reg_rd	<a href="#">stb</a>	stb	reg_rd
<a href="#">sub</a>	sub	reg_rs1	reg_or_imm	reg_rd	<a href="#">bne</a>	bne	label	<a href="#">sra</a>	sra	reg_rs1	reg_or_imm	reg_rd	<a href="#">sth</a>	sth	reg_rd
<a href="#">subcc</a>	subcc	reg_rs1	reg_or_imm	reg_rd	<a href="#">be</a>	be	label					<a href="#">ld</a>	ld	[address]	reg_rd
<a href="#">and</a>	and	reg_rs1	reg_or_imm	reg_rd	<a href="#">bg</a>	bg	label					<a href="#">ldsb</a>	ldsb	[address]	reg_rd
<a href="#">andcc</a>	andcc	reg_rs1	reg_or_imm	reg_rd	<a href="#">ble</a>	ble	label					<a href="#">ldsh</a>	ldsh	[address]	reg_rd
<a href="#">andn</a>	andn	reg_rs1	reg_or_imm	reg_rd	<a href="#">bge</a>	bge	label					<a href="#">ldub</a>	ldub	[address]	reg_rd
<a href="#">andncc</a>	andncc	reg_rs1	reg_or_imm	reg_rd	<a href="#">bl</a>	bl	label					<a href="#">lduh</a>	lduh	[address]	reg_rd
<a href="#">or</a>	or	reg_rs1	reg_or_imm	reg_rd	<a href="#">bgu</a>	bgu	label								
<a href="#">orcc</a>	orcc	reg_rs1	reg_or_imm	reg_rd	<a href="#">bleu</a>	bleu	label								
<a href="#">orn</a>	orn	reg_rs1	reg_or_imm	reg_rd	<a href="#">bcc</a>	bcc	label								
<a href="#">orncc</a>	orncc	reg_rs1	reg_or_imm	reg_rd	<a href="#">bcs</a>	bcs	label								
<a href="#">xor</a>	xor	reg_rs1	reg_or_imm	reg_rd	<a href="#">bpos</a>	bpos	label								
<a href="#">xorcc</a>	xorcc	reg_rs1	reg_or_imm	reg_rd	<a href="#">bneg</a>	bneg	label								
<a href="#">xnor</a>	xnor	reg_rs1	reg_or_imm	reg_rd	<a href="#">bvc</a>	bvc	label								
<a href="#">xnorcc</a>	xnorcc	reg_rs1	reg_or_imm	reg_rd	<a href="#">bvs</a>	bvs	label								
<a href="#">sethi</a>	sethi	const22		reg_rd	<a href="#">call</a>	call	label								
					<a href="#">jmpl</a>	jmpl	address	reg_rd							

sll																		
1	0	rd	1	0	0	1	0	1	rs1	0	0	0	0	0	0	0	0	rs2
1	0	rd	1	0	0	1	0	1	rs1	1	0	0	0	0	0	0	0	shcnt

srl																		
1	0	rd	1	0	0	1	1	0	rs1	0	0	0	0	0	0	0	0	rs2
1	0	rd	1	0	0	1	1	0	rs1	1	0	0	0	0	0	0	0	shcnt

sra																		
1	0	rd	1	0	0	1	1	1	rs1	0	0	0	0	0	0	0	0	rs2
1	0	rd	1	0	0	1	1	1	rs1	1	0	0	0	0	0	0	0	shcnt

st																		
1	1	rd	0	0	0	1	0	0	rs1	0	0	0	0	0	0	0	0	rs2
1	1	rd	0	0	0	1	0	0	rs1	1	simmm13							

stb																		
1	1	rd	0	0	0	1	0	1	rs1	0	0	0	0	0	0	0	0	rs2
1	1	rd	0	0	0	1	0	1	rs1	1	simmm13							

sth																		
1	1	rd	0	0	0	1	1	0	rs1	0	0	0	0	0	0	0	0	rs2
1	1	rd	0	0	0	1	1	0	rs1	1	simmm13							

ld																		
1	1	rd	0	0	0	0	0	0	rs1	0	0	0	0	0	0	0	0	rs2
1	1	rd	0	0	0	0	0	0	rs1	1	simmm13							

ldsb																		
1	1	rd	0	0	1	0	0	1	rs1	0	0	0	0	0	0	0	0	rs2
1	1	rd	0	0	1	0	0	1	rs1	1	simmm13							

ldsh																		
1	1	rd	0	0	1	0	1	0	rs1	0	0	0	0	0	0	0	0	rs2
1	1	rd	0	0	1	0	1	0	rs1	1	simmm13							

ldub																		
1	1	rd	0	0	0	0	0	1	rs1	0	0	0	0	0	0	0	0	rs2
1	1	rd	0	0	0	0	0	1	rs1	1	simmm13							

lduh																		
1	1	rd	0	0	0	0	1	0	rs1	0	0	0	0	0	0	0	0	rs2
1	1	rd	0	0	0	0	1	0	rs1	1	simmm13							