

Task		Year 1				Year 2			Year 3			
Q1,2,3	R1. Simulator augmentation.											
Q1,4	R2. Workload development.											
Q5	R3. Workload-driven transitions.											
Q3,4,5,6	R4. Fluctuating availability or demand.											
Q6,8	R5. Power-aware memory layout.											
Q2,7	R6. Build prototype system.											

B1. Component power database.												
B2. Graduate curriculum integration.												
B3. Undergraduate curriculum integration.												

Co-PI Challen	R2	R3	R2	R4	R3	R5	R6
UB Graduate Student	R2			R4	R3	R6	
UB Undergraduate				R4		R6	
Co-PI Hempstead	R1			R4		R5/R6	
Drexel Graduate Student	R1			R4		R5/R6	
Drexel Undergraduate		R2		R4		R6	