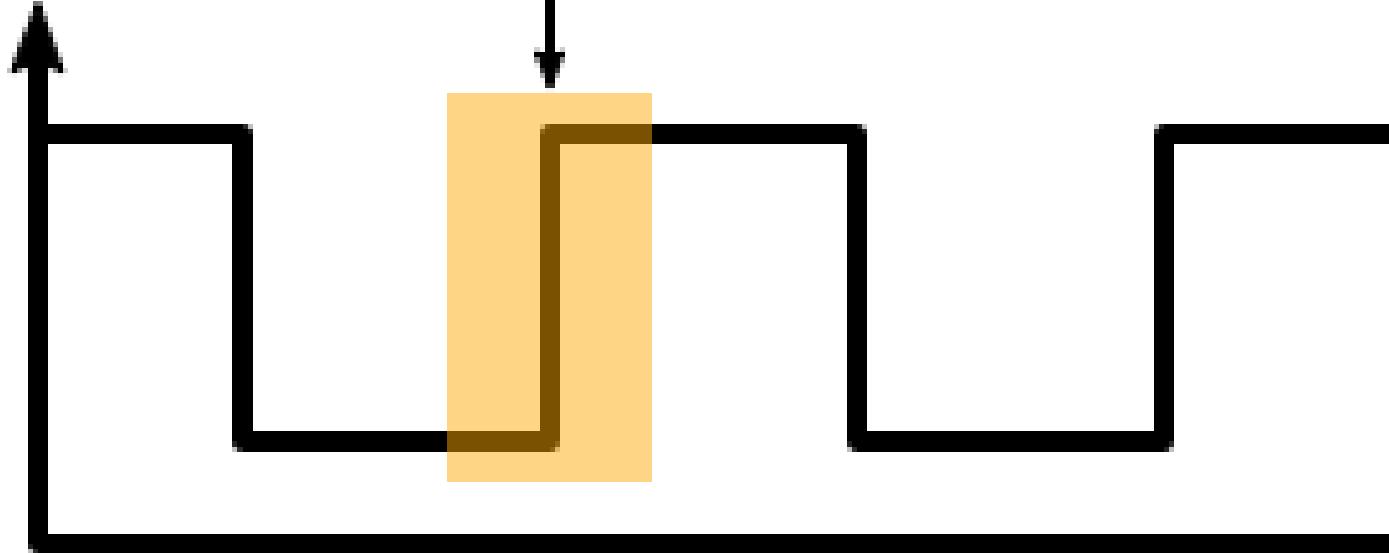


IC CLOCK

$t_{sample}$



$t_{sample} - t_{setup}$

$t_{sample} + t_{hold}$