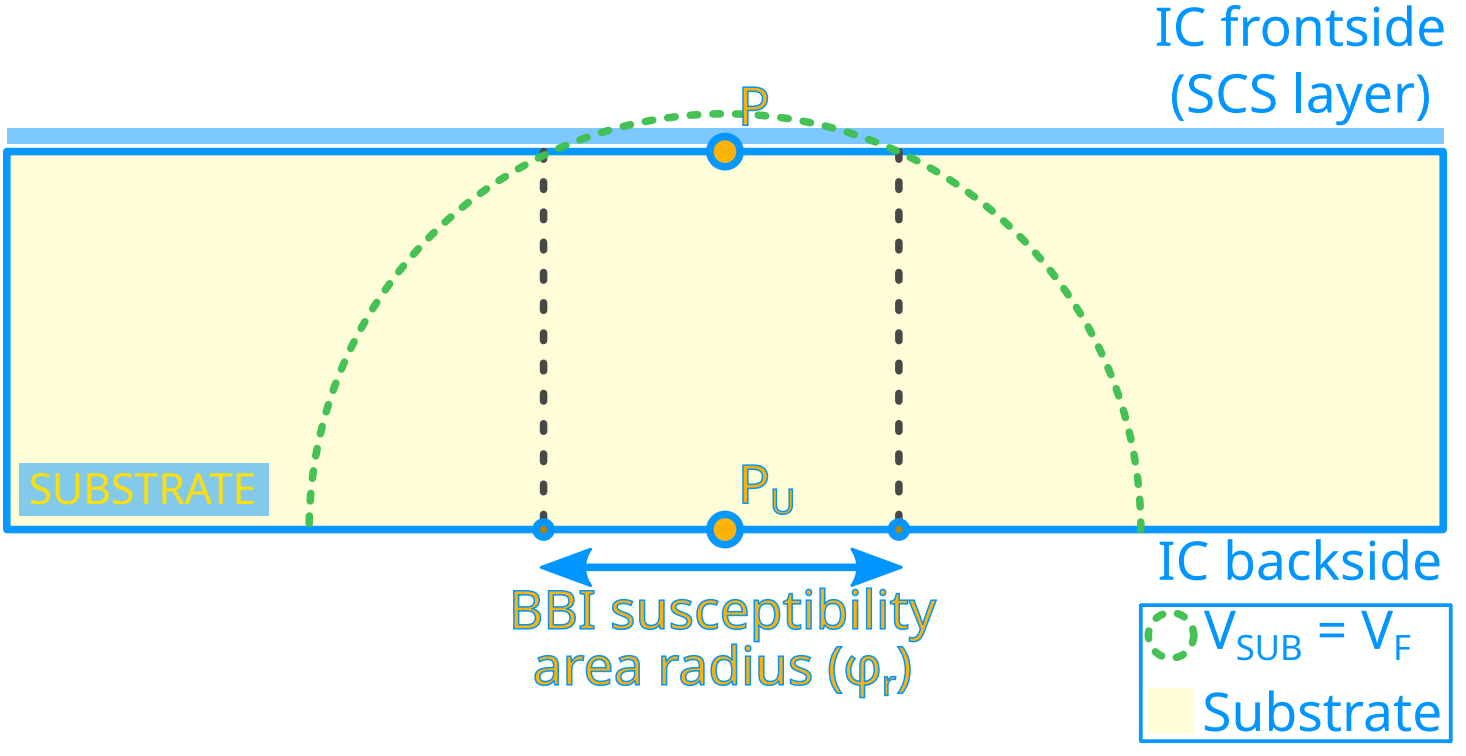
IC frontside
(SCS layer)
P
SUBSTRATE
Pu
IC backside
BBI susceptibility
area radius (φr)
VSUB = VF
Substrate