

SPE0

SPE1

SPE2

SPE3

EIB

The diagram illustrates a system architecture with four SPEs (SPE0, SPE1, SPE2, SPE3) connected to a common EIB (Event Interface Bus). The EIB is represented by a thick black bar at the bottom. SPE0 and SPE1 are connected via a green line, and SPE2 and SPE3 are connected via a blue line. Red lines indicate signal paths from the EIB to the SPEs.