A15	A14	A13	RAM_CS	RAM_OE	ROM_CS	ROM_OE	IO_CS	START	END	DESCRIPTION
1	1	1	1	1	0	0	1	E000	FFFF	ROM 8K FOR OS
1	1	0	1	1	0	1	0	C000	DFFF	UART
1	0	1	1	1	0	1	0	A000	BFFF	UART
1	0	0	1	1	0	1	0	8000	9FFF	UART
0	1	1	0	0	1	0	1	6000	7FFF	RAM 8K
0	1	0	0	0	1	1	1	4000	5FFF	RAM 8K
0	0	1	0	0	1	1	1	2000	3FFF	RAM 8K
0	0	0	0	0	1	1	1	0000	1FFF	RAM 8K