CPE 315: Computer Architecture
Fall 2020
Lab 8: ARM Thumb Simulator Part III
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Questions:
1. Assume the branch is taken is the better option for shang
    a. If you could specify on forward and backward branches
        i. Forward: Not taken
        ii. Backward: Taken
2. 32 byte block size would be the most efficient
3. -O2 optimization is a bit faster than no Optimization , it also appears that it reduced code redundancy and makes the instructions more efficient.

Github Link:

https://github.com/gdesrosi00/CPE315/tree/main/Lab5PartB