

PIO Muxing & KEY & SW setting

PIOA	USAGE	PIOA	USAGE	PIOB	USAGE	PIOB	USAGE	PIOC	USAGE
PA0	SDHC0_CK	PA16	SPI0_MISO	PB0	LED	PB16	ETH_GRXDV	PC0	DO3
PA1	SDHC0_CMD	PA17	SPI0_CS0	PB1	CLASSD_R0	PB17	ETH_GRXER	PC1	CANTX0
PA2	SDHC0_DAT0	PA18	SDHC1_DAT0	PB2	CLASSD_R1	PB18	ETH_GRX0	PC2	CANRX0_PC2
PA3	SDHC0_DAT1	PA19	SDHC1_DAT1	PB3	CLASSD_R2	PB19	ETH_GRX1	PC3	DO2
PA4	SDHC0_DAT2	PA20	SDHC1_DAT2	PB4	CLASSD_R3	PB20	ETH_GTX0/EXP	PC4	DO1
PA5	SDHC0_DAT3	PA21	SDHC1_DAT3	PB5	NO	PB21	ETH_GTX1/EXP	PC5	DO0
PA6	SDHC0_DAT4	PA22	SDHC1_CK/QSPI0_SCK/EXP	PB6	NO	PB22	ETH_GMDC/EXP	PC6	DI3
PA7	SDHC0_DAT5	PA23	SPI1_MOSI	PB7	LCD_IRQ1	PB23	ETH_GMDIO/EXP	PC7	DI2
PA8	SDHC0_DAT6	PA24	SPI1_MISO	PB8	LCD_IRQ2	PB24	NO	PC8	PMIC_LBO
PA9	SDHC0_DAT7	PA25	SPI1_NPCS0	PB9	SW	PB25	NO	PC9	ETH_INT
PA10	SDHC0_RSTN	PA26	NC	PB10	NO	PB26	URXD0	PC10	LCD_DAT2
PA11	SDHC0_VDDSEL	PA27	NC	PB11	URXD3	PB27	UTXD0	PC11	LCD_DAT3
PA12	PMIC_CHGLEV	PA28	SDHC1_CMD	PB12	UTXD3	PB28	NO	PC12	LCD_DAT4
PA13	SDHC0_CD	PA29	DI0	PB13	PMIC_IRQ	PB29	NO	PC13	LCD_DAT5
PA14	SPI0_SPCK	PA30	SDHC1_CD	PB14	ETH_GTXCK	PB30	NO	PC14	LCD_DAT6
PA15	SPI0_MOSI	PA31	USBA_VBUS Detection	PB15	ETH_GTXEN	PB31	NO	PC15	LCD_DAT7

PIOC	USAGE	PIOD	USAGE	PIOD	USAGE
PC16	LCD_DAT10	PD0	LCD_PCK	PD16	JTAG_TDO
PC17	LCD_DAT11	PD1	LCD_DEN	PD17	JTAG_TMS
PC18	LCD_DAT12	PD2	DBGU_URXD1	PD18	BUZZER
PC19	LCD_DAT13	PD3	DBGU_UTXD1	PD19	LCD_XP
PC20	LCD_DAT14	PD4	LCD_TWD1	PD20	LCD_XM
PC21	LCD_DAT15	PD5	LCD_TWCK1	PD21	LCD_YP/PMIC_TWD0
PC22	LCD_DAT18	PD6	NO	PD22	LCD_YM/PMIC_TWCK0
PC23	LCD_DAT19	PD7	NO	PD23	URXD2
PC24	LCD_DAT20	PD8	NO	PD24	UTXD2
PC25	LCD_DAT21	PD9	NO	PD25	SPI1_SPCK
PC26	LCD_DAT22	PD10	NO	PD26	NO
PC27	LCD_DAT23	PD11	NO	PD27	DI5
PC28	LCD_PWM	PD12	DO6	PD28	4G_RESET
PC29	LCD_DISP	PD13	NO	PD29	NO
PC30	LCD_VSYNC	PD14	JTAG_TCK	PD30	DO4
PC31	LCD_HSYNC	PD15	JTAG_TDI	PD31	DI4

KEY/SW DESCRIPTION		
PART	DEFAULT	FUNCTION
BP1	OPEN	RESET
BP2	OPEN	WAKE UP
SW1	OPEN	BOOT_DIS
SW2	OPEN	RESERVED

DRAWN: Wujikun	DATED: 20181218
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY:		TOMORR	
TITLE:		<Title>	
CODE:	SIZE:	DRAWING NO:	REV:
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SCALE: <Scale>		SHEET: 3F 12	

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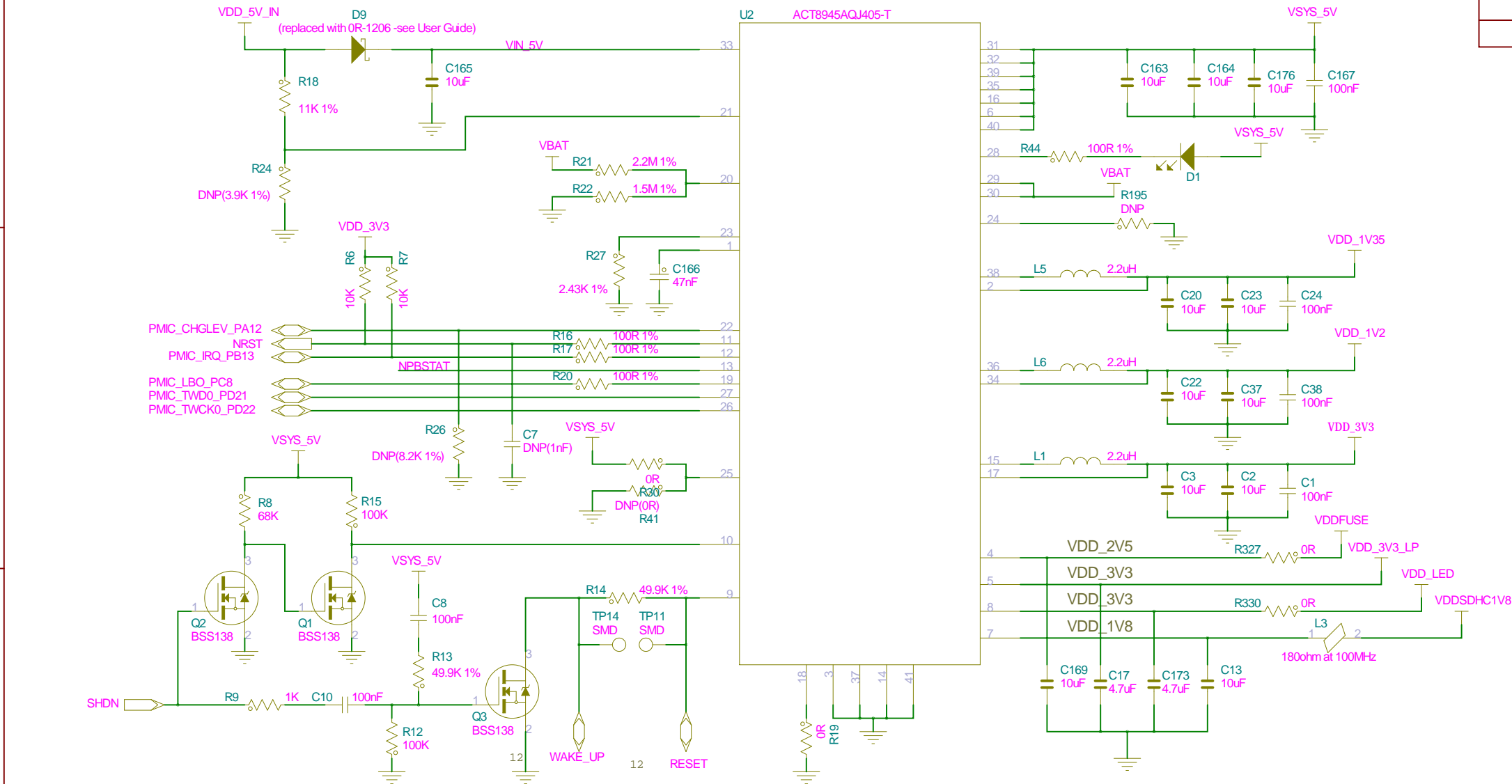
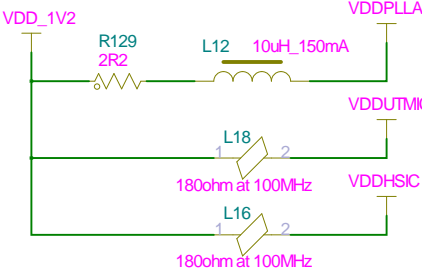
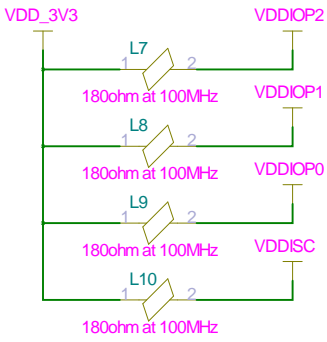
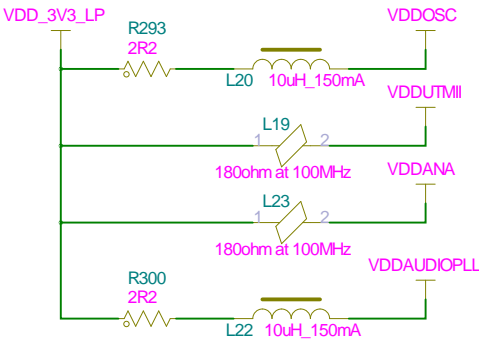
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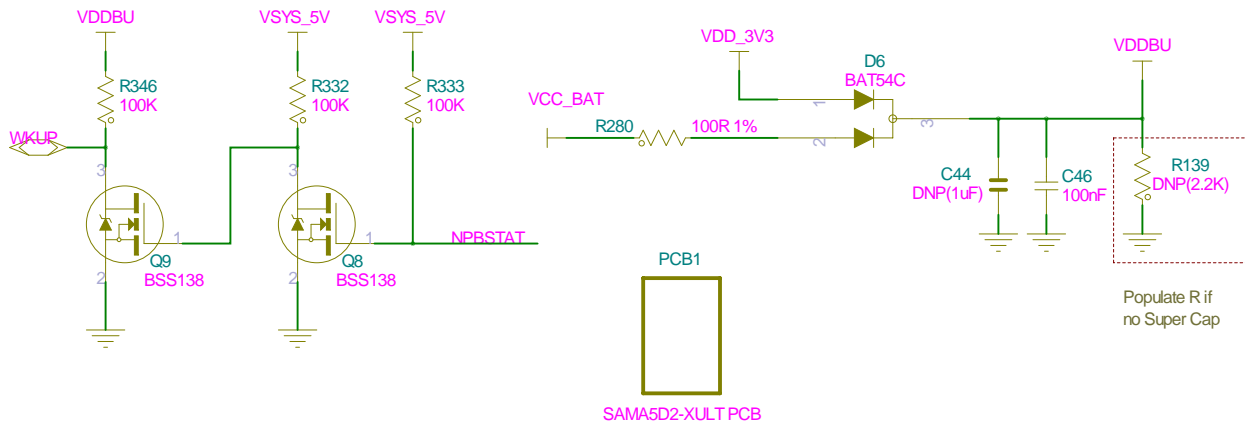
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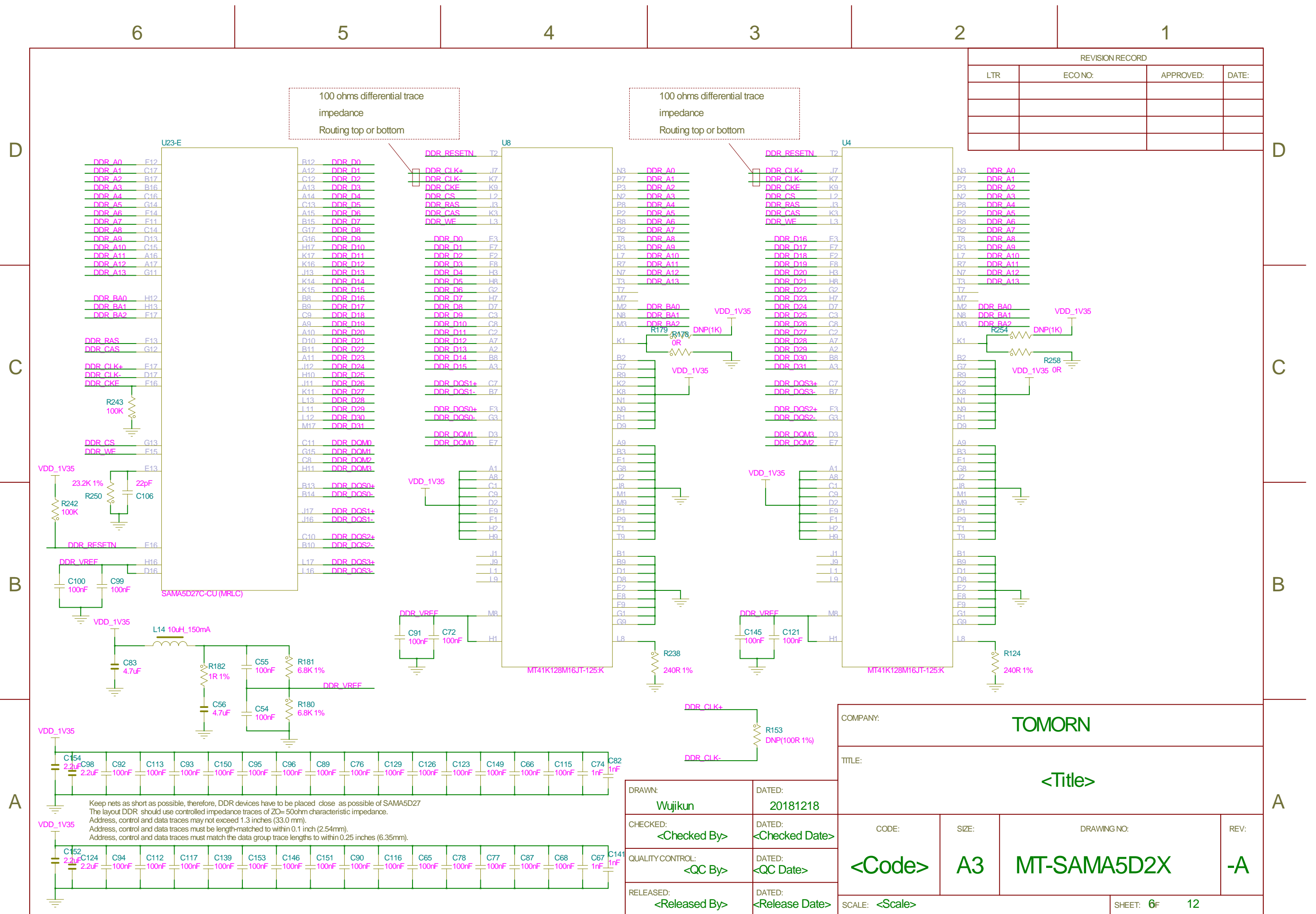


WAKE UP RESET
Place TP11 and TP14 to Bottom.



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COMPANY: TOMORN			
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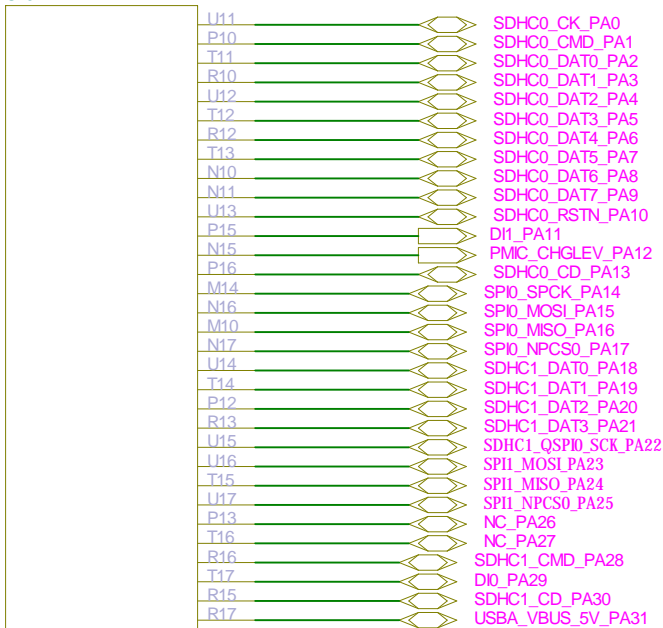
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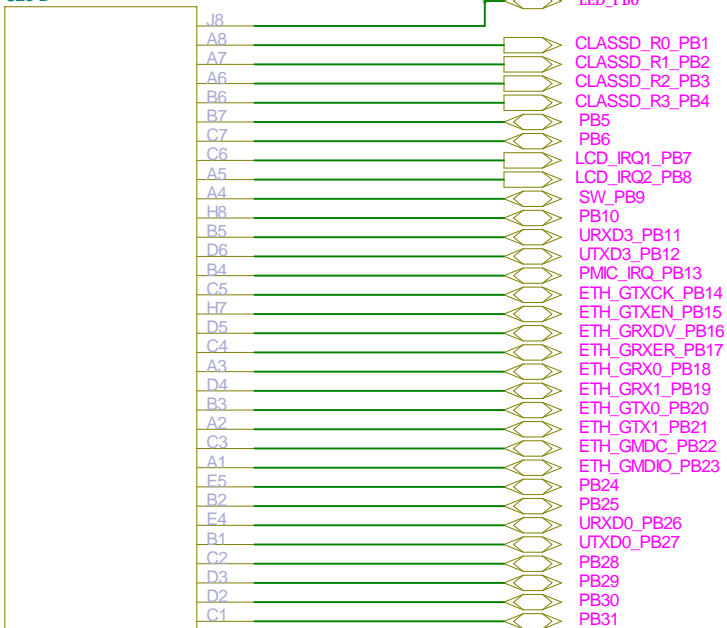
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U23-A



SAMA5D27C-CU (MRLC)

U23-B



SAMA5D27C-CU (MRLC)



COMPANY:

TOMORN

TITLE:

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DRAWN: Wujikun	DATED: 20181218
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QUALITY CONTROL: <QC By>	DATED: <QC Date>
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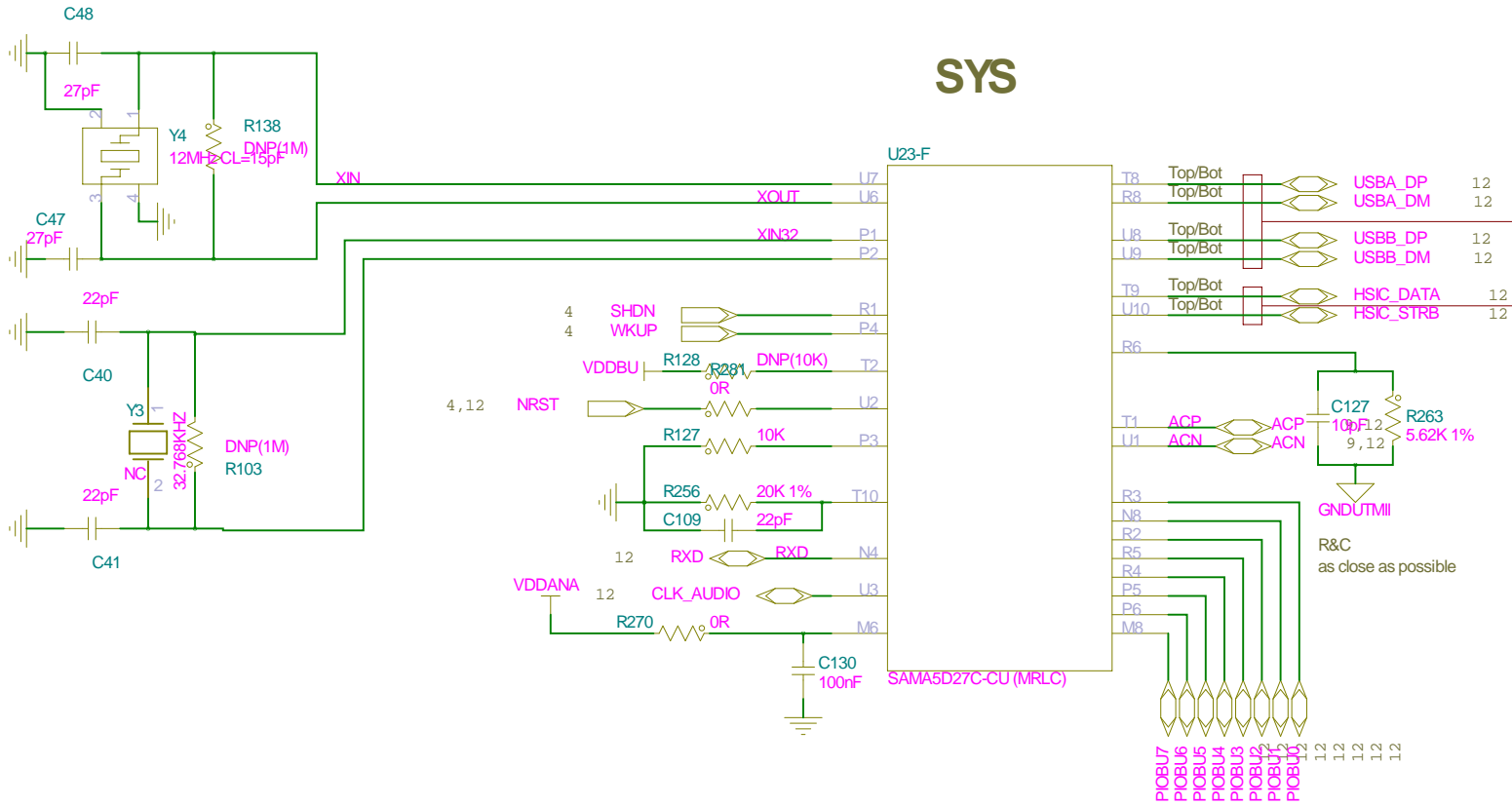
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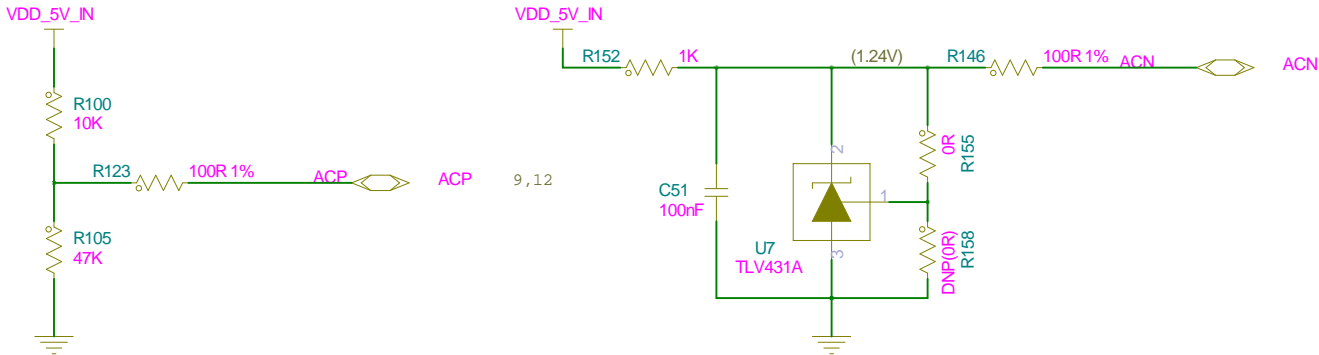


Routing USB

Max trace-length mismatch between USB signals pairs should be no greater than 3.8mm 90 ohms differential trace impedance.

Routing HSIC

Routing HSIC Refers to Specif



COMPANY: TOMORN			
TITLE: <Title>			
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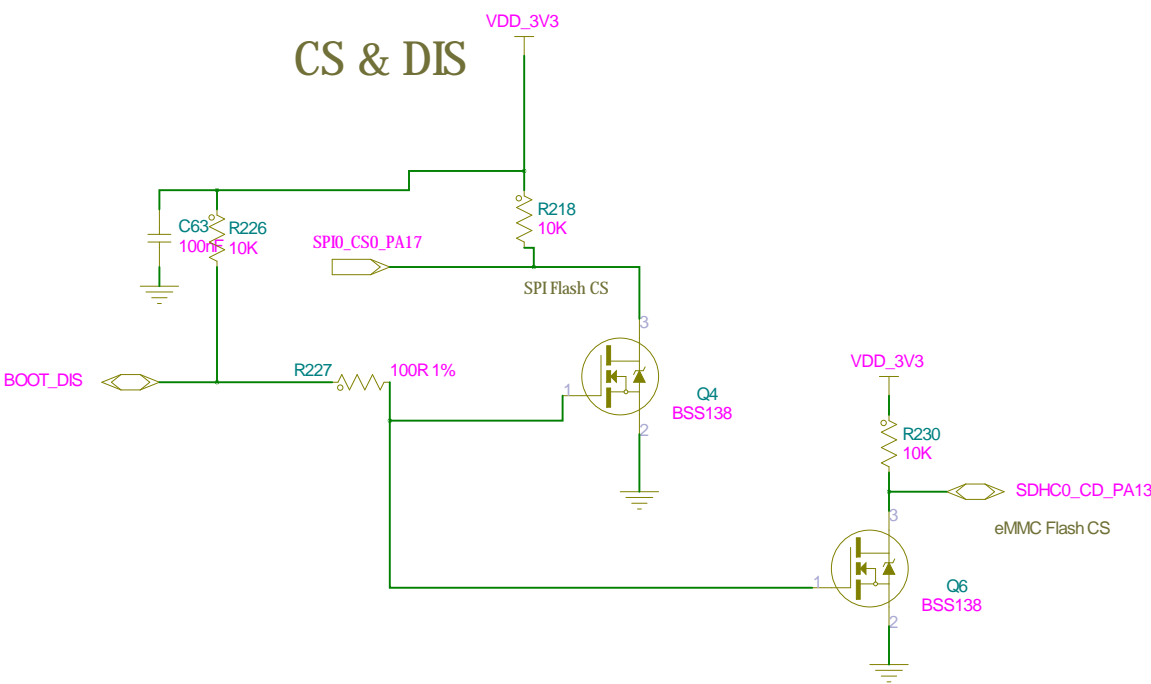
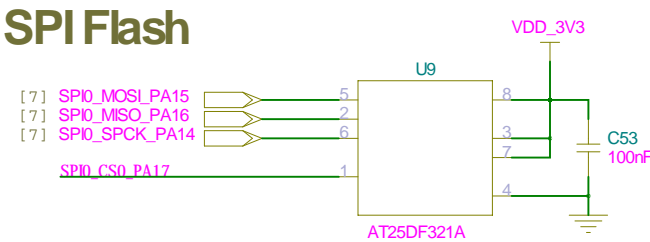
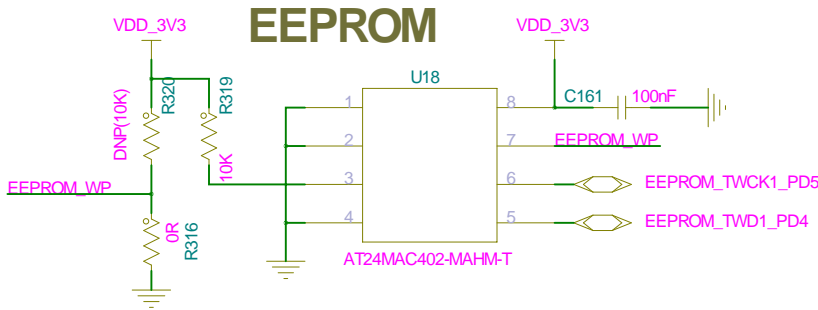
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REVISION RECORD

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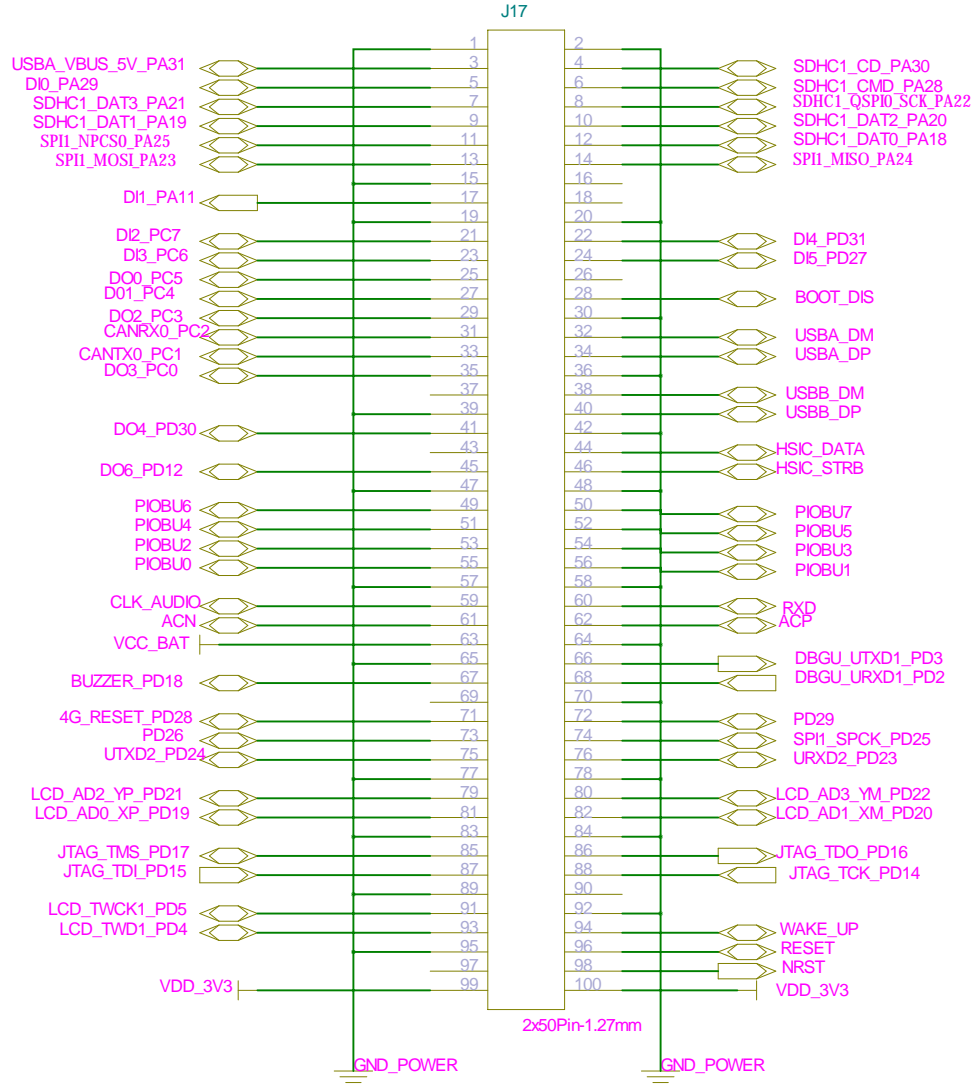
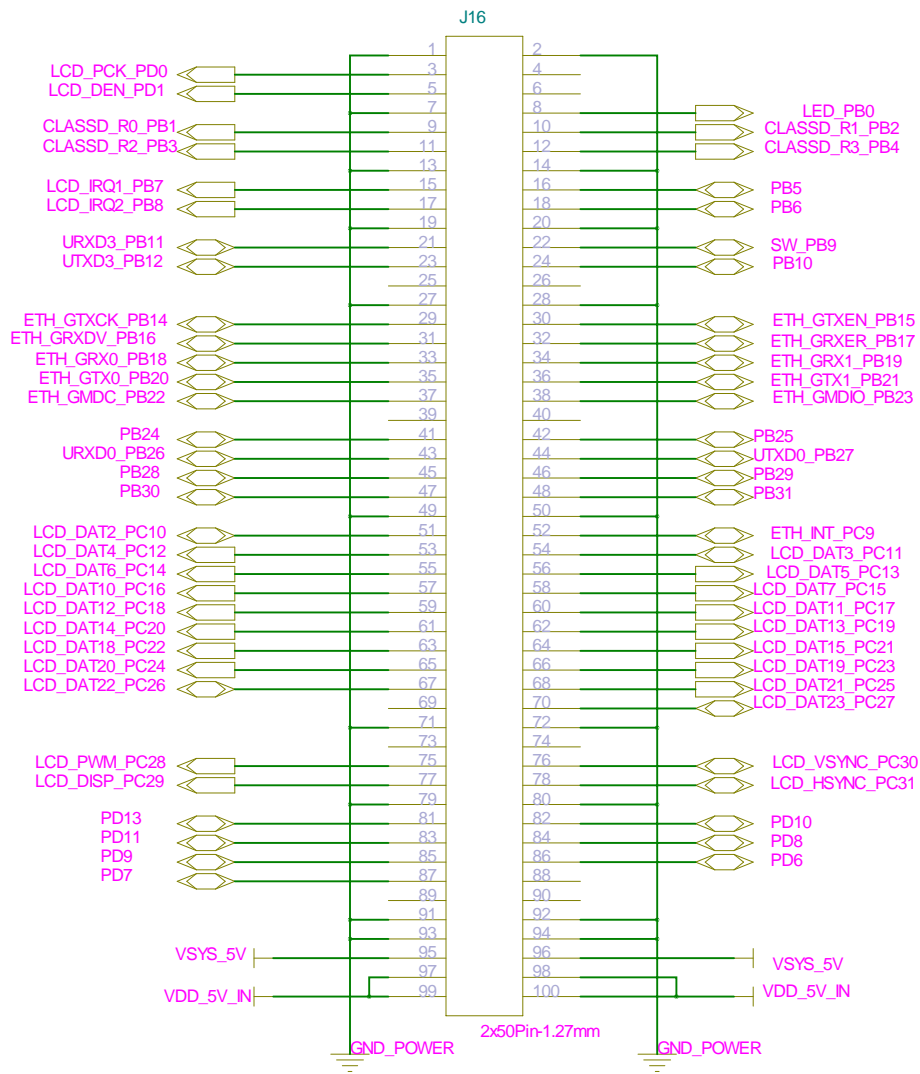
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Boards Connectors



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QUALITY CONTROL: <QC By>	DATED: <QC Date>	DRAWING NO: MT-SAMA5D2X	
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