COVER

Revision	Description	Date	Drawn	Checked
V10A	Initial	2017_07_01	xxxlzjxxx	

Schematics Index:

00 COVER
01 GPIO ASSIGNMENT
02 MCU
03 SWITCH
04 POWER

BUG

Schematics Block:

NO.2, XinYe Road, GaoXinXiQu, Chengdu, Sichuan,China.

SICHUAN HUIYUAN OPTICAL COMMUNICATIONS CO.,LTD

Title: MK60_88E6071_TEST_V10A		
Size: A3	Document: 01 COVER	Rev: V10A
Date: Wednesday, July 05, 2017	Sheet: 1 of 5	
Designed by: xxxlzjxxx	Checked by: ?	Approved by: ?

GPIO ASSIGNMENT

PIN1~PIN25

NUMBER	PIN	Define	Function
1	PTE0		
2	PTE1		
3	PTE2		
4	PTE3		
5	PTE4	**UART3-TX**	UART3
6	PTE5	**UART3-RX**	
7	PTE6		
8	VDD		
9	VSS		
10	USB0_DP		
11	USB0_DM		
12	VOUT33		
13	VREGIN		
14	ADC0_DP1		
15	ADC0_DM1		
16	ADC1_DP1	WORK_VOLT	电压检测
17	ADC1_DM1		
18	PGA0_DP		
19	PGA0_DM		
20	PGA1_DP		
21	PGA1_DM		
22	VDDA		
23	VREFH	**VREFH**	参考源
24	VREFL	**VREFL**	
25	VSSA		

PIN26~PIN50

NUMBER	PIN	Define	Function
26	VREF_OUT		
27	DAC0_OUT		
28	XTAL32		
29	EXTAL32		
30	VBAT		
31	PTE24		
32	PTE25		
33	PTE26	WDI_INPUT_A	看门狗
34	PTA0	**JTAG_TCLK**	JTAG
35	PTA1	**JTAG_TDI**	
36	PTA2	**JTAG_TDO**	
37	PTA3	**JTAG_TMS**	
38	PTA4		
39	PTA5		
40	VDD		
41	VSS		
42	PTA12	RMII0_RXD1	RMII
43	PTA13	RMII0_RXD0	
44	PTA14	RMII0_CRS	
45	PTA15	RMII0_TXEN	
46	PTA16	RMII0_TXD0	
47	PTA17	RMII0_TXD1	
48	VDD		
49	VSS		
50	PTA18	**CLK_25M**	时钟

PIN51~PIN75

NUMBER	PIN	Define	Function
51	PTA19		
52	RESET_b	SYS_RESET#	系统复位
53	PTB0	RMII0_MDIO	RMII
54	PTB1	RMII0_MDC	
55	PTB2	**IIC0_SCL**	IIC0
56	PTB3	**IIC0_SDA**	
57	PTB9	**IIC0_PORT**	
58	PTB10	**W25B_CS#**	FLASH
59	PTB11	**W25B_CLK**	
60	VSS		
61	VDD		
62	PTB16	**W25B_DO**	FLASH
63	PTB17	**W25B_DI**	
64	PTB18	**W25B_HOLD#**	
65	PTB19	**W25B_WP#**	
66	PTB20		
67	PTB21	DAC_HORI_SYNC	DAC
68	PTB22	DAC_HORI_SCLK	
69	PTB23	DAC_HORI_DIN	
70	PTC0	DAC_VERT_SYNC	
71	PTC1	DAC_VERT_SCLK	
72	PTC2	DAC_VERT_DIN	
73	PTC3	VOLT_TANS_DAC_OE	
74	VDD		
75	VSSA		

PIN76~PIN100

NUMBER	PIN	Define	Function
76	PTC4	**PhotoSW1**	光电开关
77	PTC5	**PhotoSW2**	
78	PTC6	**PhotoSW3**	
79	PTC7		
80	PTC8	MOTOR_VERT_DIAG_A	MOTOR
81	PTC9	MOTOR_VERT_EN_A	
82	PTC10	MOTOR_VERT_TACHO_A	
83	PTC11	MOTOR_VERT_FWD/REV_A	
84	PTC12	MOTOR_VERT_BRAKE_A	
85	PTC13	MOTOR_VERT_H1_A	
86	PTC14	MOTOR_VERT_H3_A	
87	PTC15	MOTOR_VERT_H2_A	
88	VSS		
89	VDD		
90	PTC16	VOLT_TRAN_VERT_OE	MOTOR
91	PTC17		
92	PTC18	MOTOR_HORI_DIAG_A	MOTOR
93	PTD0	MOTOR_HORI_EN_A	
94	PTD1	MOTOR_HORI_TACHO_A	
95	PTD2	MOTOR_HORI_FWD/REV_A	
96	PTD3	MOTOR_HORI_BRAKE_A	
97	PTD4	MOTOR_HORI_H1_A	
98	PTD5	MOTOR_HORI_H3_A	
99	PTD6	MOTOR_HORI_H2_A	
100	PTD7	VOLT_TRAN_HORI_OE	

NO.2, XinYe Road, GaoXinXiQu, Chengdu, Sichuan,China.

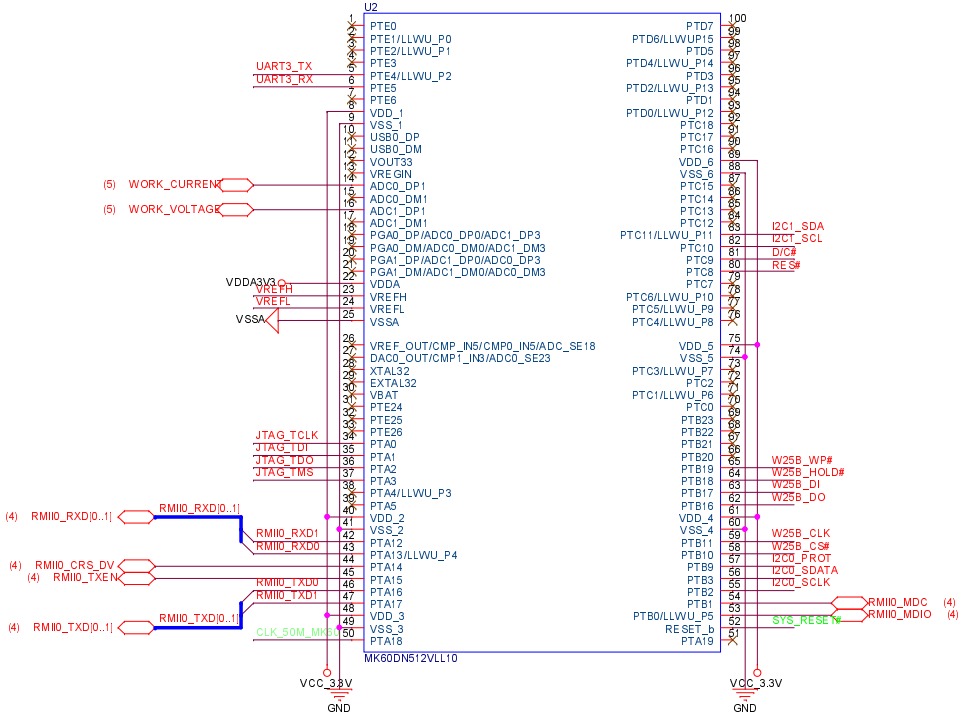
SICHUAN HUIYUAN OPTICAL COMMUNICATIONS CO.,LTD

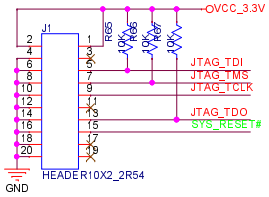
File	MK60_88E6071_TEST_V10A	
Size: A3	Document: 02 GPIO ASSIGNMENT	Rev: V10A
Date: Wednesday, July 05, 2017	Sheet 2 of 5	
Designed by: xxxlzjxxx	Checked by: ?	Approved by: ?

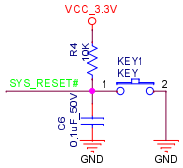
MK60DN512VLL10

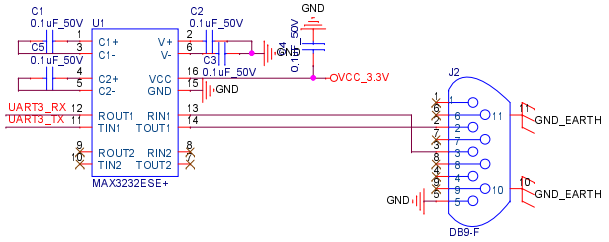
JTAG、串口调试口、主机通信口

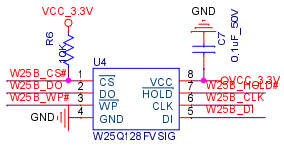
SPI FLASH、温度传感器、EEPROM

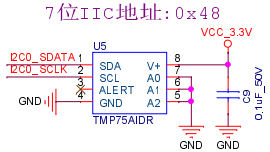


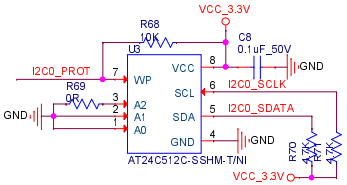


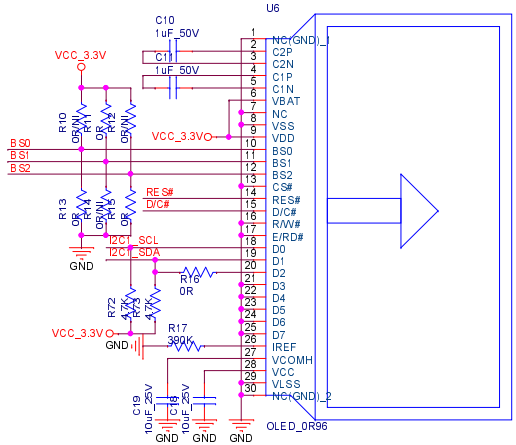




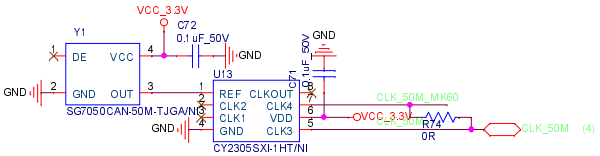


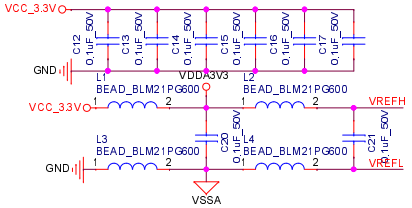






1、IIC模式，D/C#作为SA0，slave地址选择

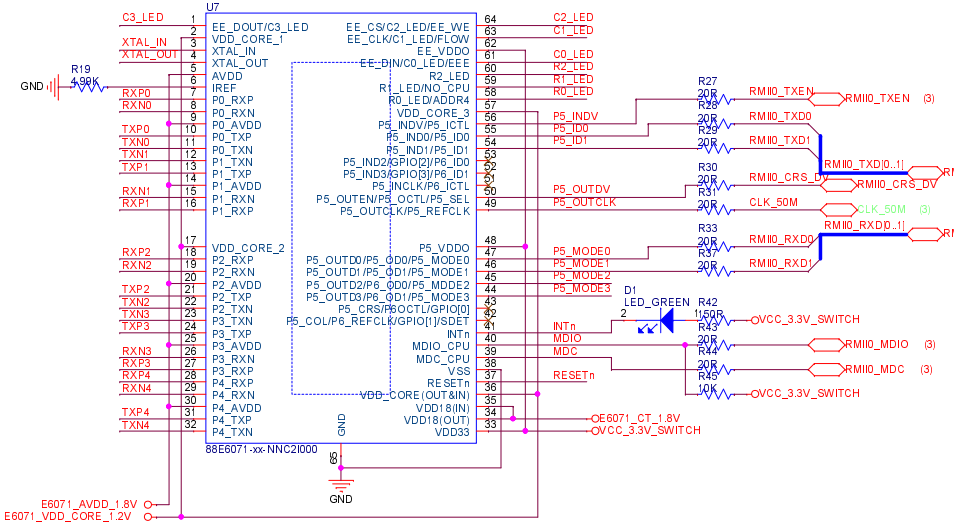




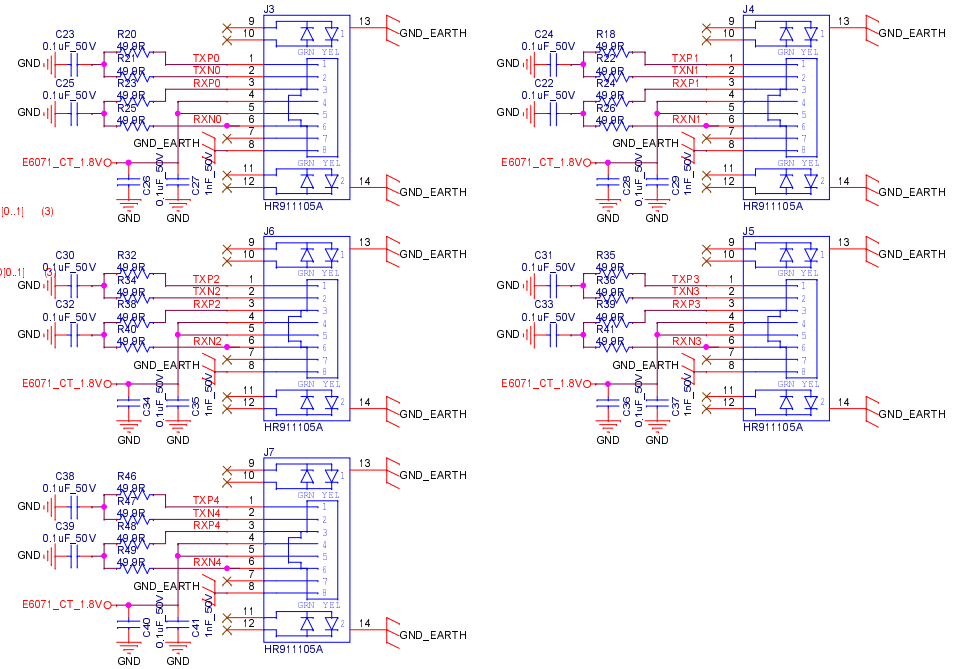
SICHUAN HUIYUAN OPTICAL COMMUNICATIONS CO.,LTD			
Title: MK60_88E6071_TEST_V10A			
Size: A3	Document: 03 MCU		Rev: V10A
Date: Thursday, January 25, 2018		Sheet: 3 of 5	
Designed by: xxxlzjxxx	Checked by: ?	Approved by: ?	

NO.2, XinYe Road, GaoXinXiQu, Chengdu, Sichuan, China.

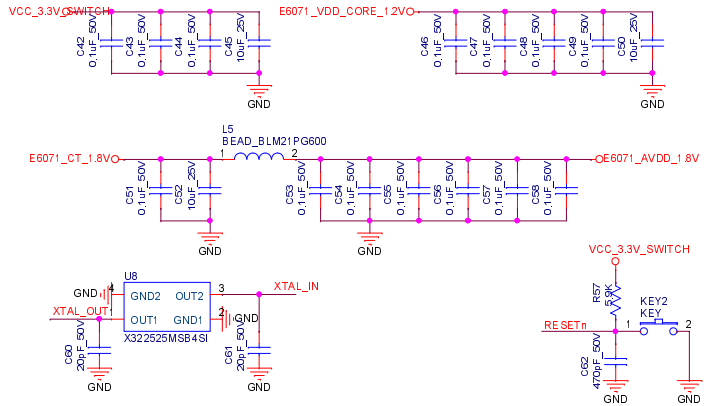
交换机



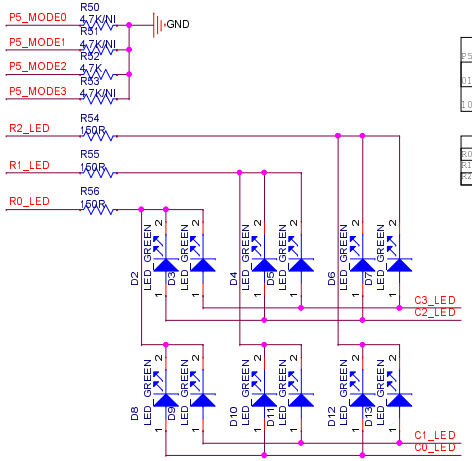
网口电路



电源电路



交换机芯片配置电路



P5_MODE[3:0]	
0111	Single RMII MAC Mode|100Mbps FD with 50MHz clock input|
1011	Single RMII PHY Mode|100Mbps with 50MHz output clock|

	C0_LED	C1_LED	C2_LED	C3_LED
R0_LED	Port 0 LED 0	Port 1 LED 0	Port 0 LED 1	Port 1 LED 1
R1_LED	Port 2 LED 0	Port 3 LED 0	Port 2 LED 1	Port 3 LED 1
R2_LED	Port 4 LED 0	Port 5 LED 0	Port 4 LED 1	Port 5 LED 1

SICHUAN HUIYUAN OPTICAL COMMUNICATIONS CO.,LTD

NO.2, XinYe Road, GaoXinXiQu, Chengdu, Sichuan,China.

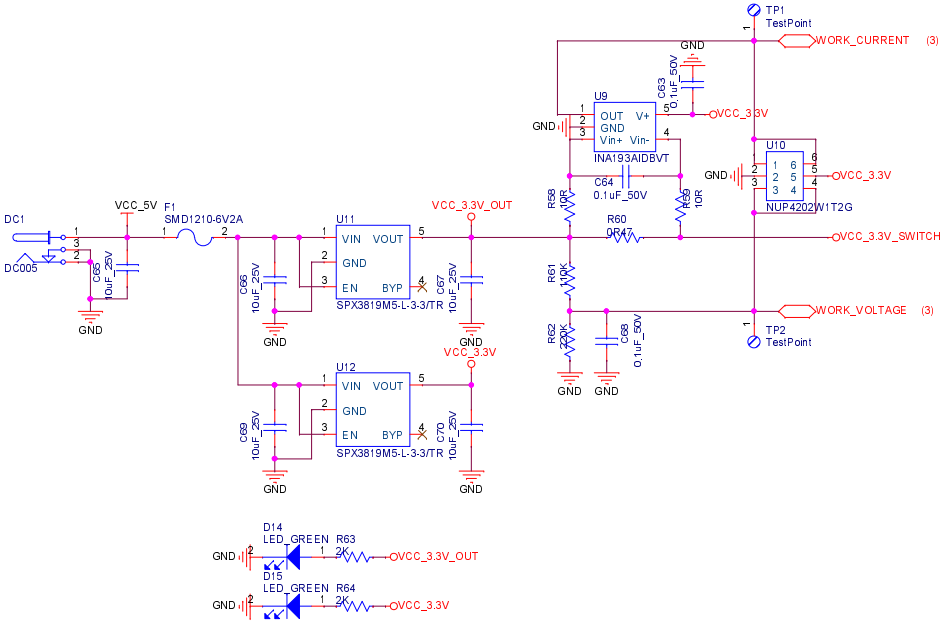
Title: MK60_88E6071_TEST_V10A

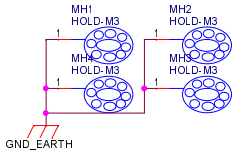
Size: A3 | Document: 04 SWITCH | Rev: V10A

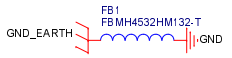
Date: Wednesday, July 05, 2017 | Sheet 4 of 5

Designed by: xxxlzjxxx | Checked by: ? | Approved by: ?

电源输入







NO.2, XinYe Road, GaoXinXiQu, Chengdu, Sichuan,China.	SICHUAN HUIYUAN OPTICAL COMMUNICATIONS CO.,LTD		
	Title: MK60_88E6071_TEST_V10A		
	Size: A3	Document: 05 POWER	Rev: V10A
	Date: Wednesday, July 05, 2017	Sheet: 5 of 5	
	Designed by: xxxlzjxxx	Checked by: ?	Approved by: ?