

COVER

Revision	Description	Date	Drawn	Checked
V10A	Initial	2018-09-26	xxx1zjxxx	

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02 GPIO

03 CPU-6UL

04 CPU-F091

05 4G

06 ETH

07 AUDIO
- 08 IO-DI

09 IO-DO

10 IO-485

11 IO-AI

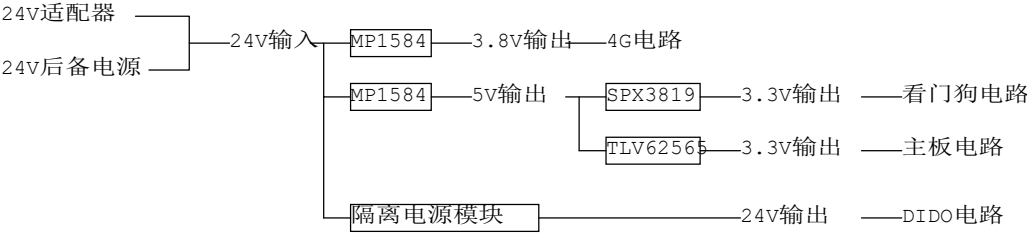
12 IO-HEADER

13 POWER

Schematics Block:

MYD-Y6ULX底板

Power Block:



BUG

- 增加485通道的LED，调整另外4个LED位置
- 调整DB9，3.5mm耳机孔位置
- 调整32与6ul通信的串口为32的串口1
- 修改继电器的电路

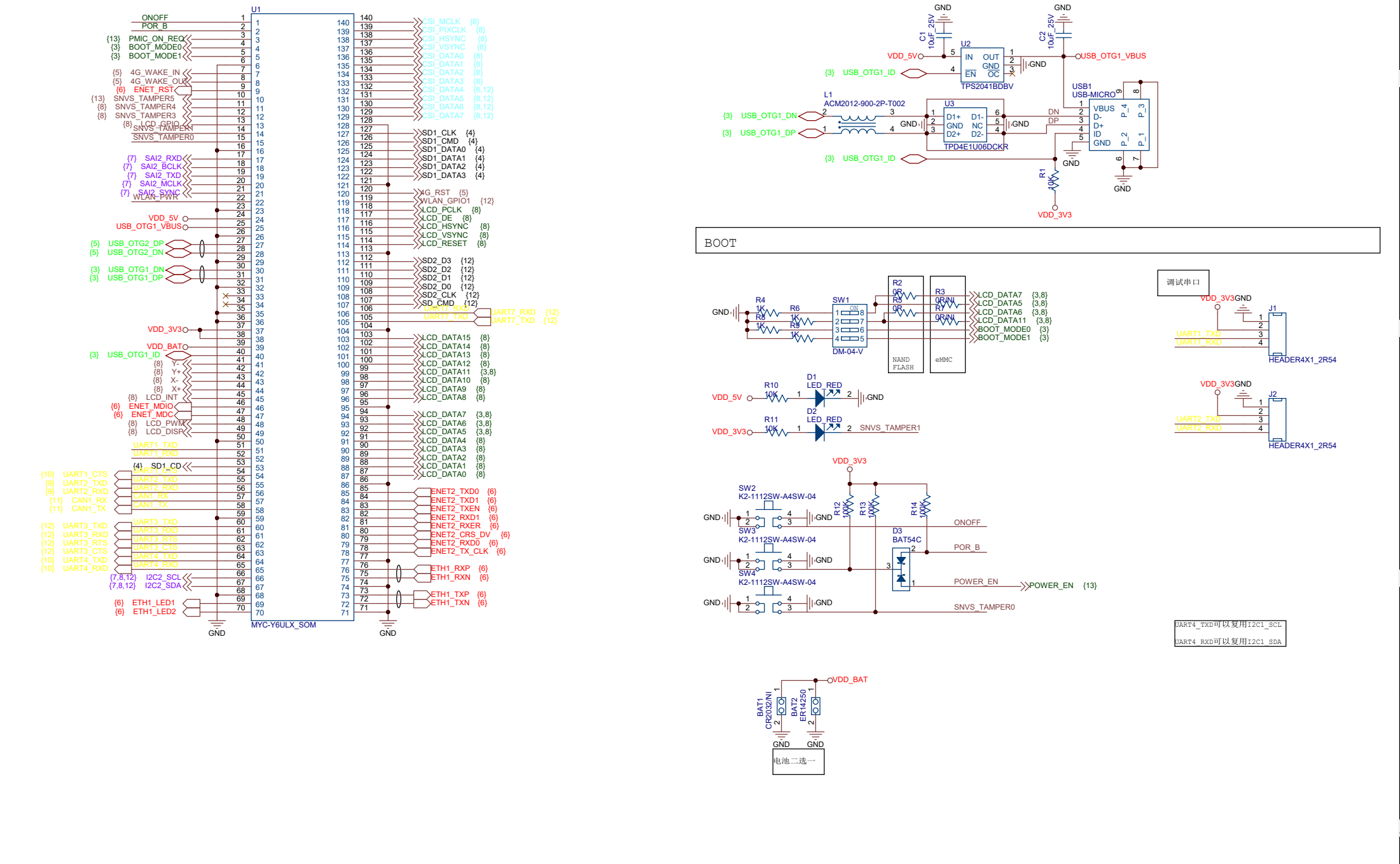
# GPIO ASSIGNMENT

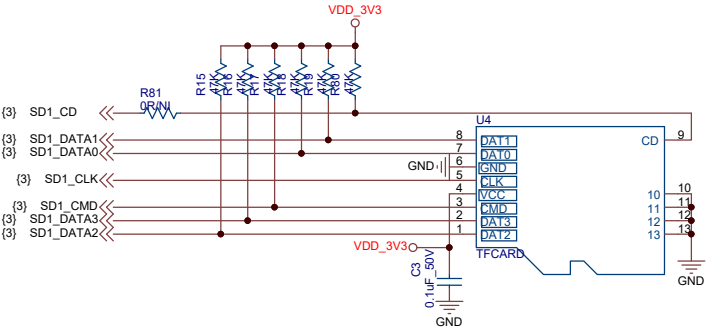
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										D
										C
										B
										A
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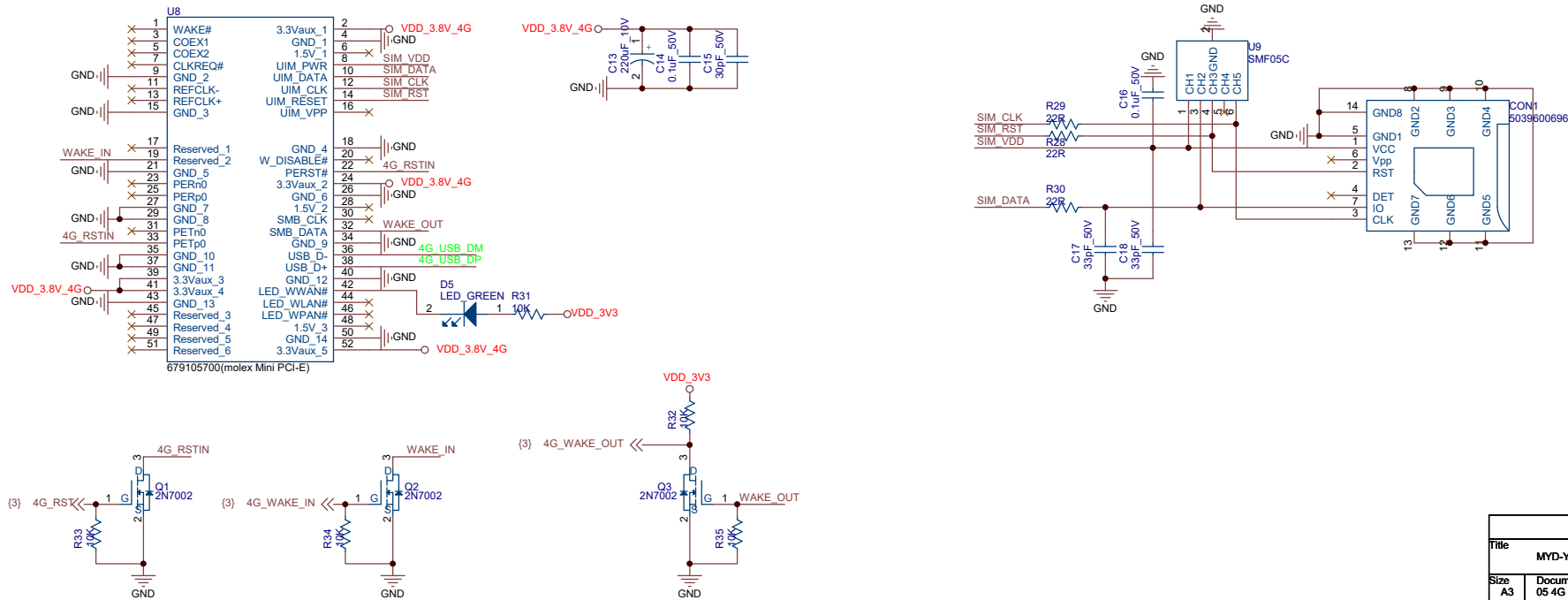
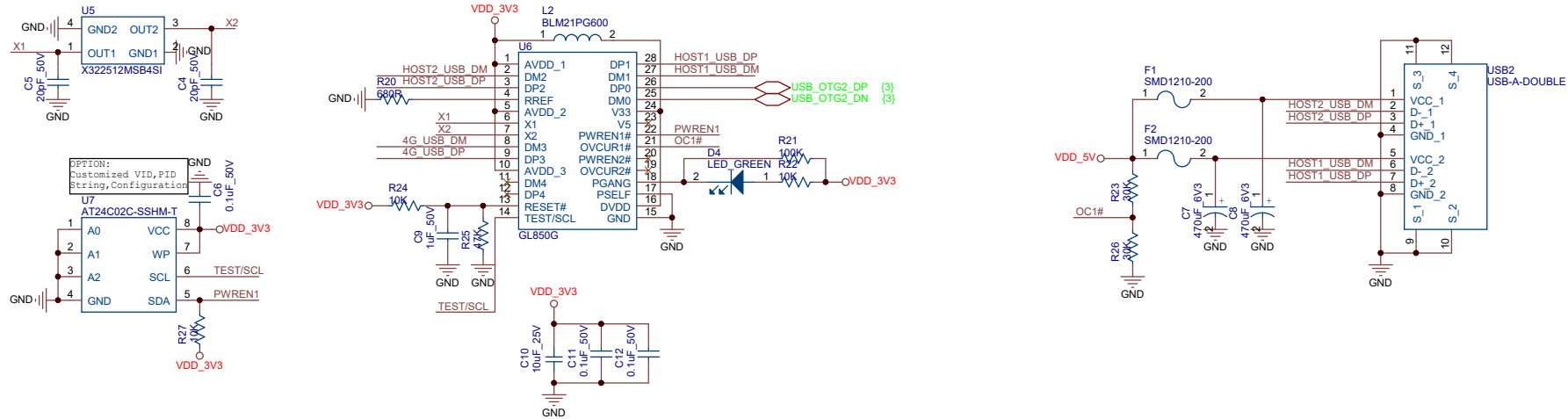
# GPIO ASSIGNMENT

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A3	02 GPIO ASSIGNMENT	V10A			
Date:	Saturday, December 29, 2018	Sheet	2	of	13

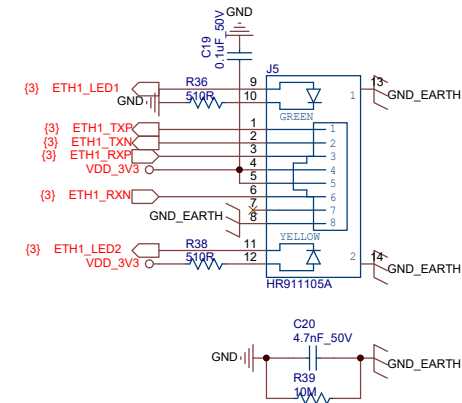
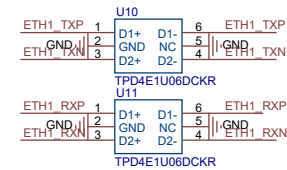
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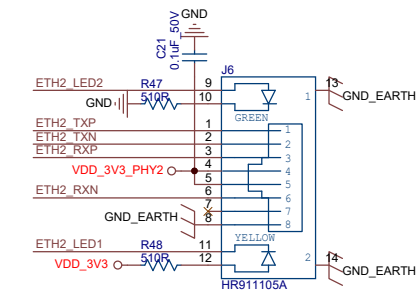
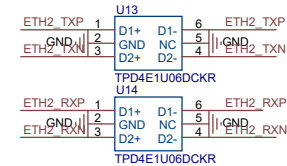
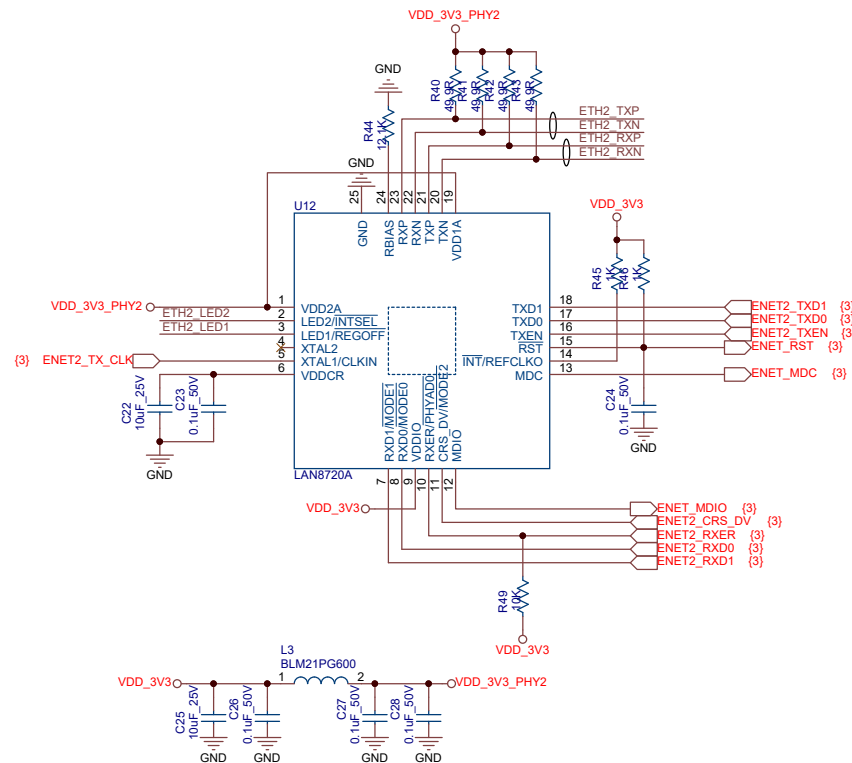




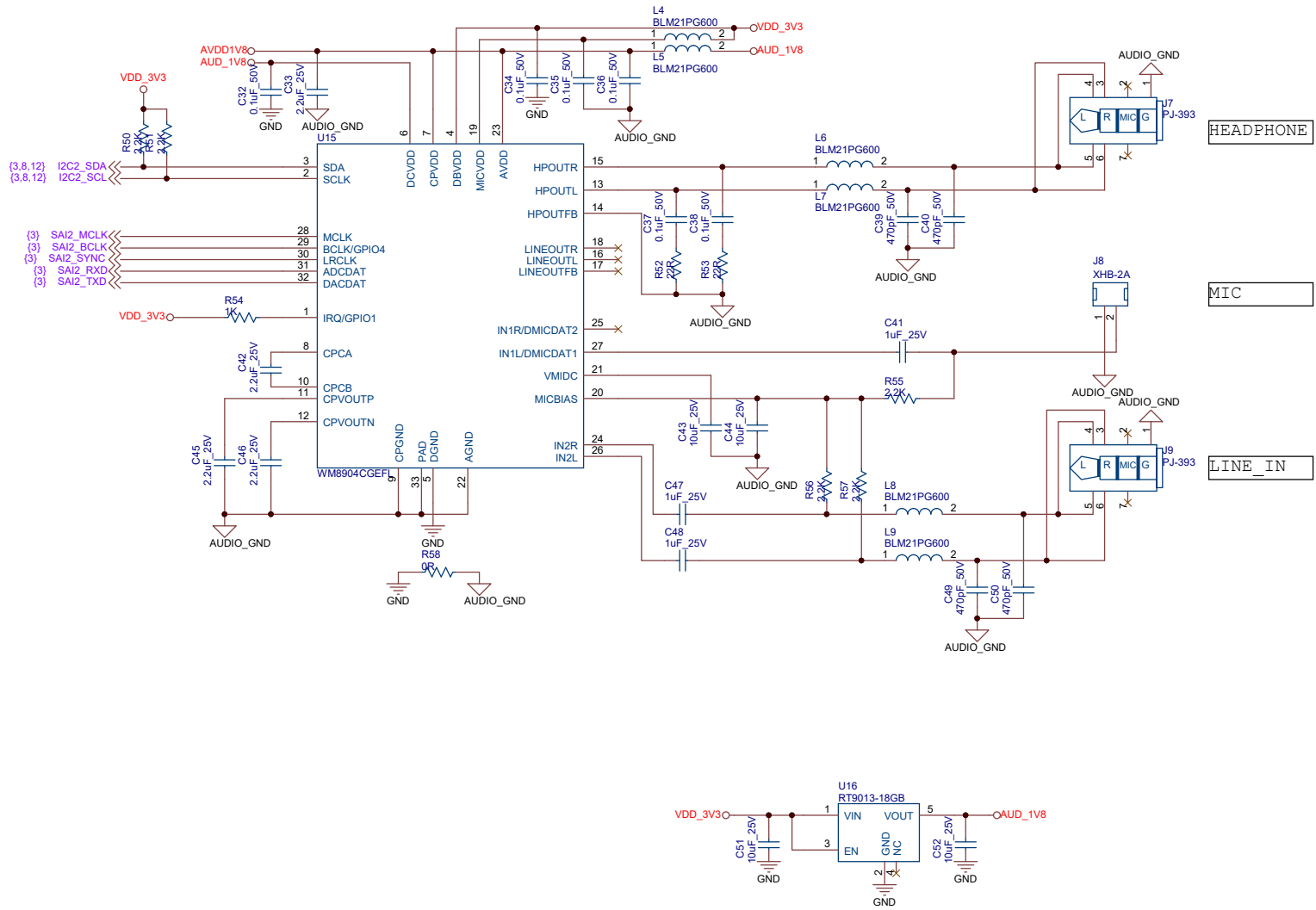
ETH1

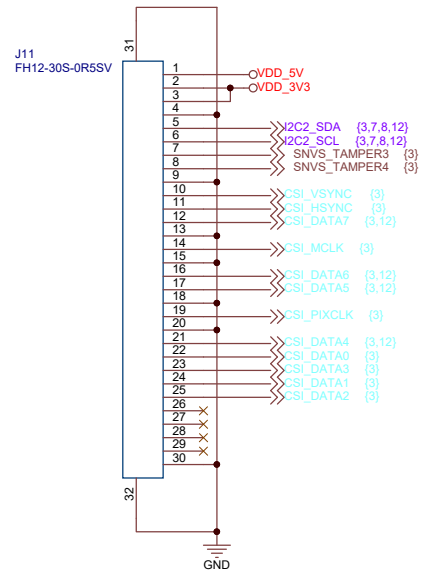
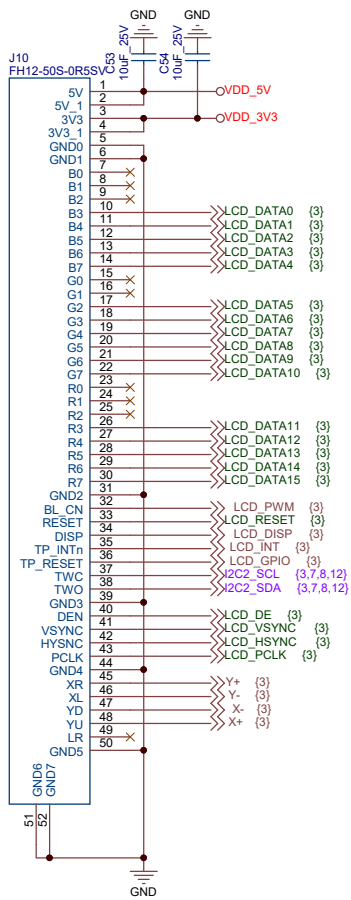


ETH2



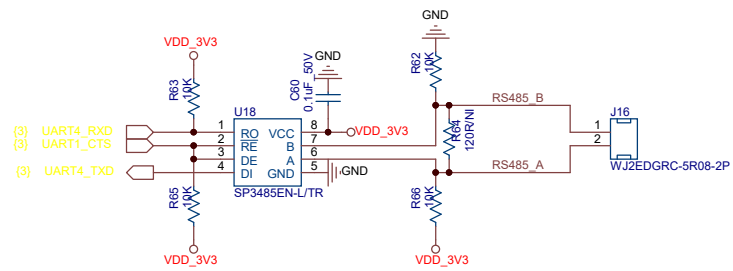
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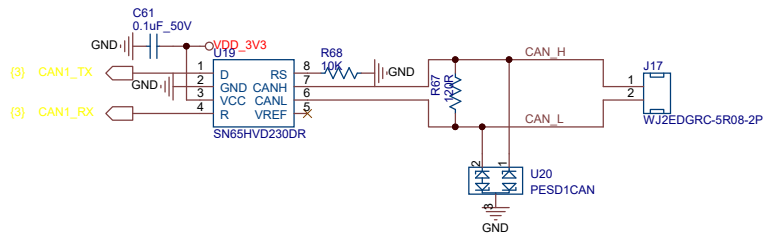


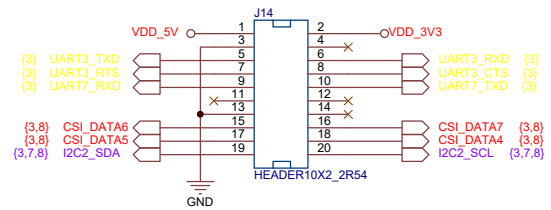
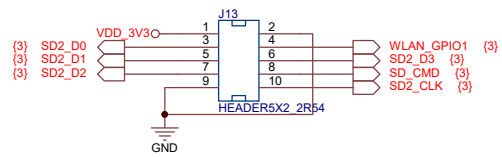




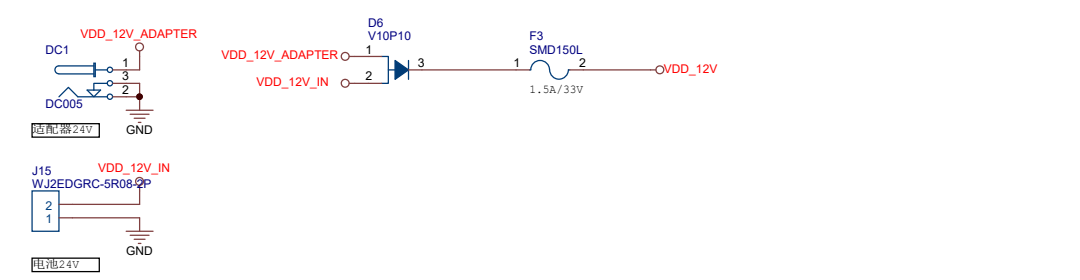




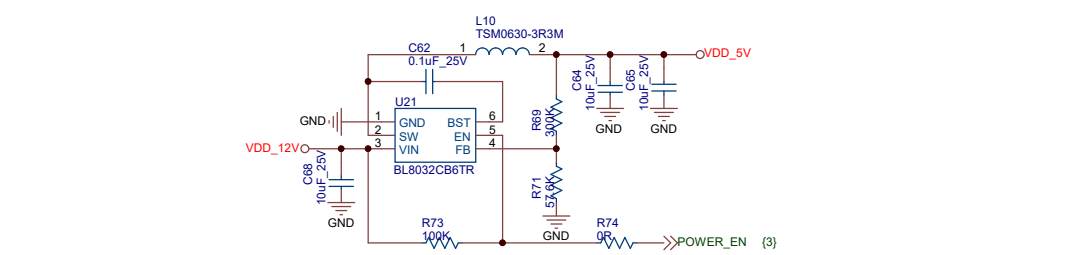




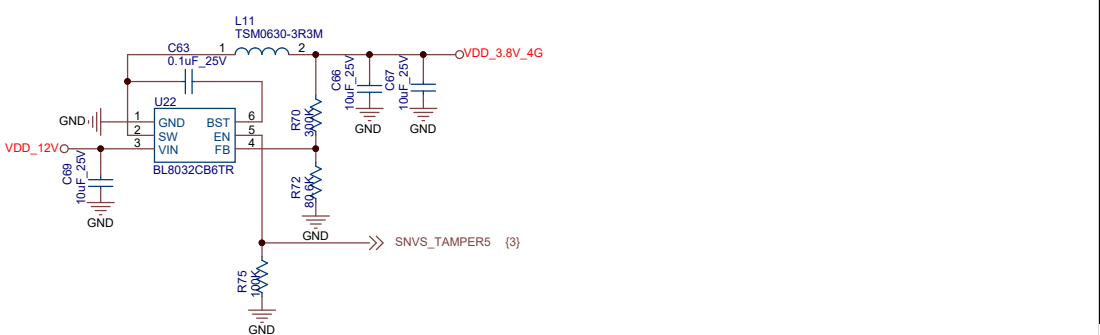
电源输入保护



主电源



4G电源



3.3V电源

