SIM7600\_M\_V2.1.1接口说明

SIM7600\_M\_V2.1.1的对外接口是双排插针J4和J6，其接口定义见表1 —J4接口说明和表2—J6接口说明

表1 —J4接口说明

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| 序号 | 名称 | 说明 | 序号 | 名称 | 说明 |
| 1 | +3.3V | +3.3V输出 | 2 | +3.3V | +3.3V输出 |
| 3 | GND | 地 | 4 | GND | 地 |
| 5 | 485­­\_B | 485输入负端 | 6 | 485­­\_A | 485输入正端 |
| 7 | VBAT | 电池输入端 | 8 | PB7 | IO |
| 9 | PB6 | IO | 10 | PB5 | IO |
| 11 | PB4 | IO | 12 | PB3 | IO |
| 13 | PA15 | IO | 14 | PF7 | IO |
| 15 | PF6 | IO | 16 | PA12 | IO |
| 17 | PA11 | IO | 18 | NC | 空脚 |
| 19 | GND | 地 | 20 | GND | 地 |

表2 —J6接口说明

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| 序号 | 名称 | 说明 | 序号 | 名称 | 说明 |
| 1 | 5V | 模块+5V供电端 | 2 | 5V | 模块+5V供电端 |
| 3 | GND | 地 | 4 | GND | 地 |
| 5 | PA4 | IO | 6 | PA1 | IO |
| 7 | PA6 | IO | 8 | PA5 | IO |
| 9 | PB0 | IO | 10 | PA7 | IO |
| 11 | TXD3 | MCU串口3 | 12 | PB1 | IO |
| 13 | RXD3 | MCU串口3 | 14 | PB12 | IO |
| 15 | PB13 | IO | 16 | PB14 | IO |
| 17 | PB15 | IO | 18 | PA8 | IO |