

Title		
Size	Number	Revision
A4		
Date:	2018/2/25	Sheet of
File:	E:\github\...\DK_STM32L151.SchDoc	Drawn By:

A

B

C

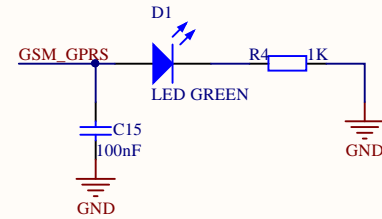
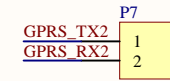
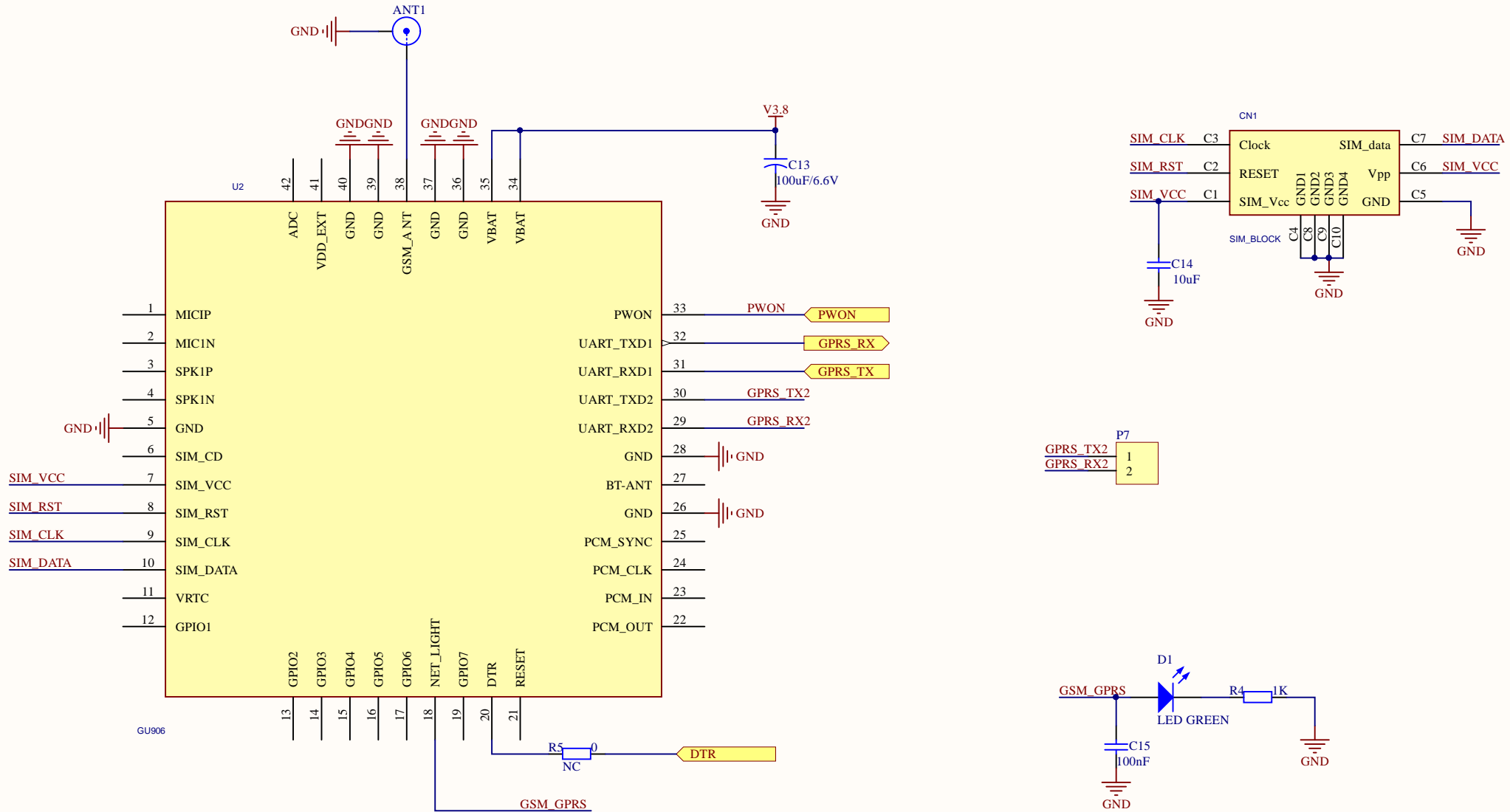
D

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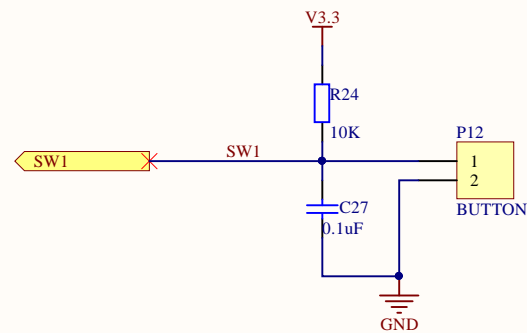
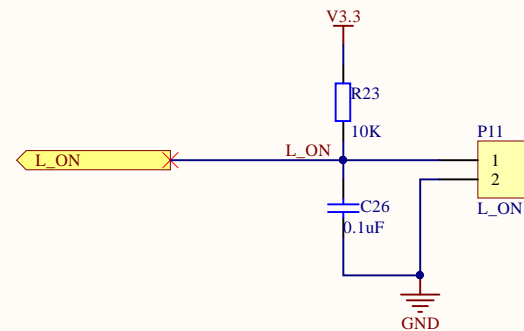
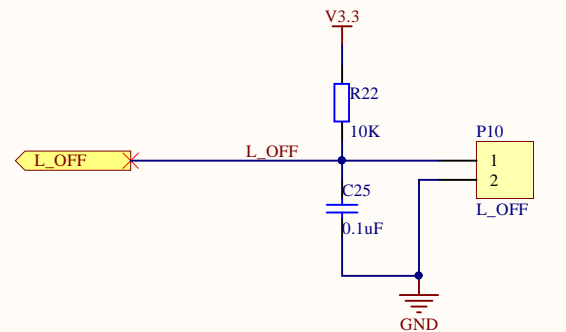
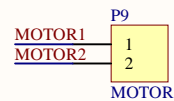
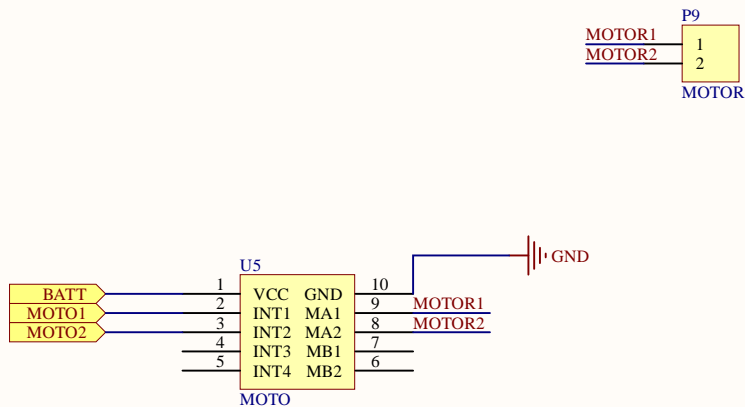
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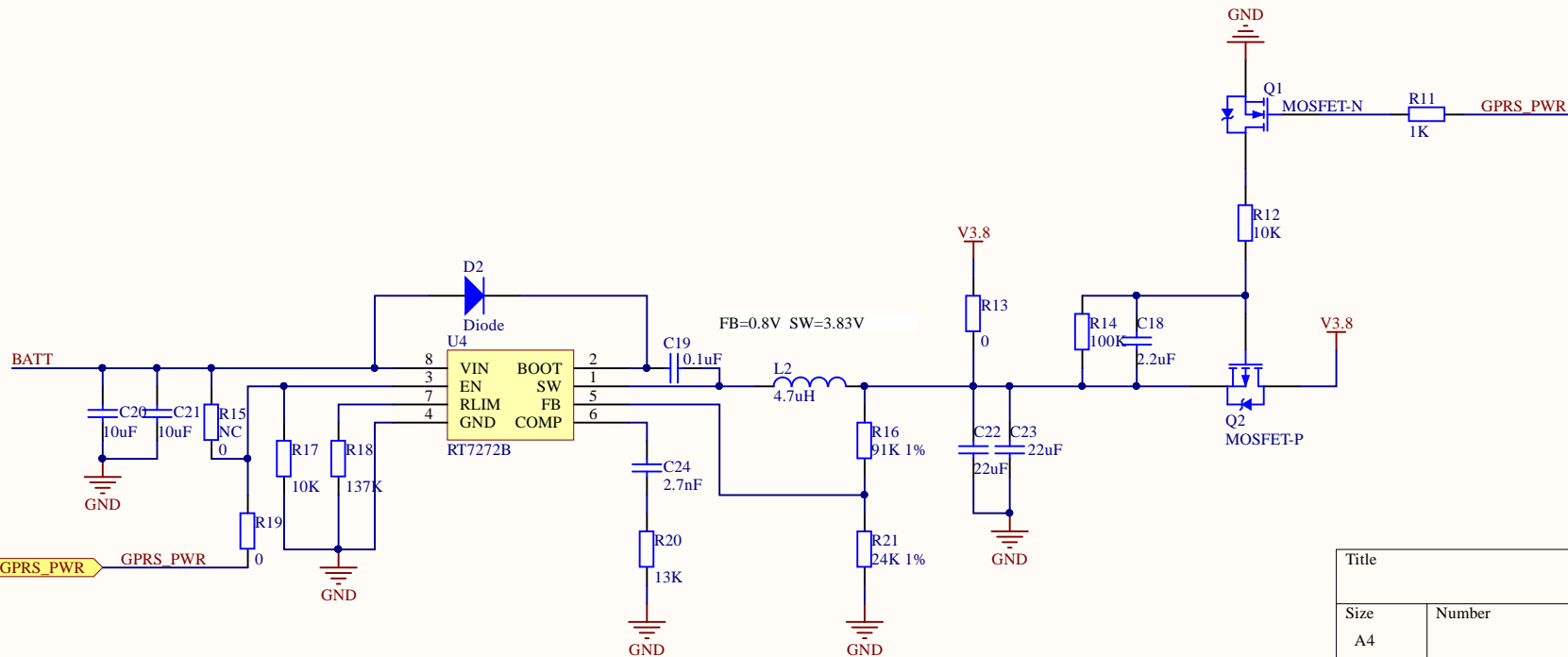
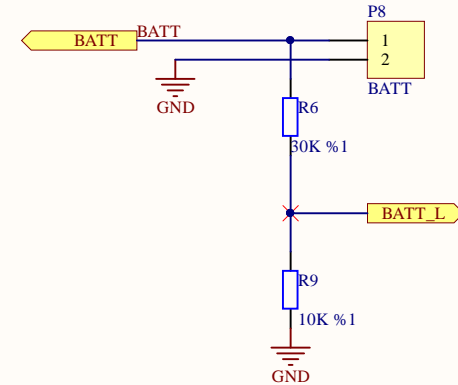
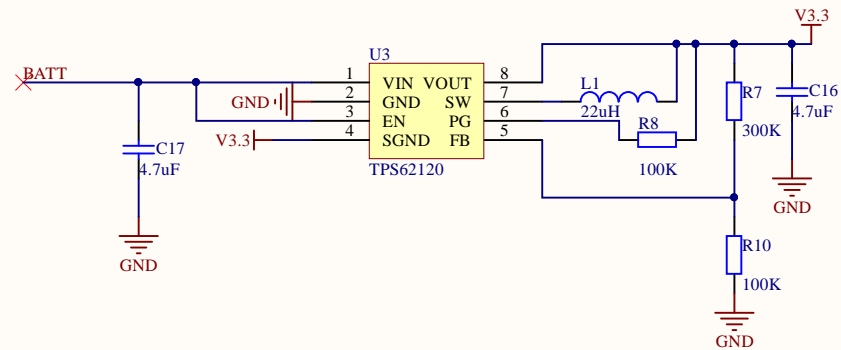
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Title		
Size	Number	Revision
A4		
Date:	2018/2/25	Sheet of
File:	E:\github\...\DK_GU906_HW_V1.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2018/2/25	Sheet of
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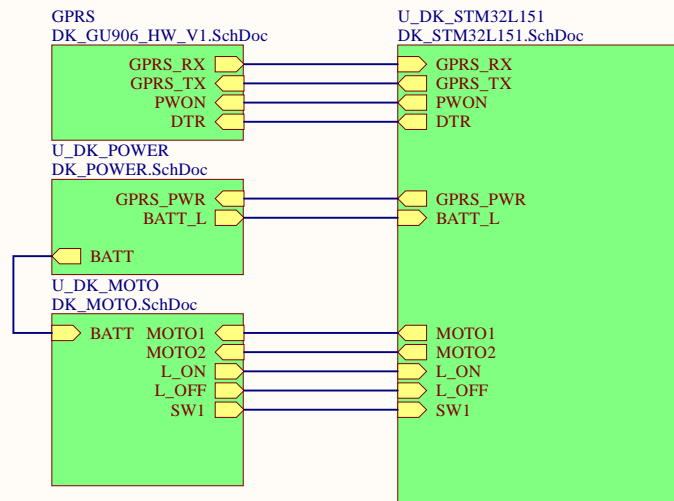


Title		
Size	Number	Revision
A4		
Date:	2018/2/25	Sheet of
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A							
B							
C							
D							
1		2		3		4	

Title		
Size	Number	Revision
A4		
Date:	2018/2/25	Sheet of
File:	E:\github\...\DK_RS485.SchDoc	Drawn By:

Title		
Size A4	Number	Revision
Date:	2018/2/25	Sheet of
File:	E:\github\...\DK_RS485.SchDoc	Drawn By:



Designator
DK_RS485.SchDoc

Comment	Description	Designator	Footprint	LibRef	Quantity
SMB	SMB Straight Connector	ANT1	SMB_V-RJ45	SMB	1
Cap	Capacitor	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C14, C15, C16, C17, C18, C19, C20, C21, C22, C23, C24, C25, C26, C27	RAD-0.3	Cap	25
NV	Capacitor	C12	RAD-0.3	Cap	1
Cap2	Capacitor	C13	CAPR5-4X5	Cap2	1
SIM_BLOCK		CN1	SIM_BLOCK	SIM_BLOCK_2	1
LED GREEN	Typical RED, GREEN, YELLOW, AMBER GaAs LED	D1	3.2X1.6X1.1	LED2	1
Diode	Default Diode	D2	SMC	Diode	1
Inductor	Inductor	L1, L2	0402-A	Inductor	2
BOOT0	Header, 3-Pin	P1	HDR1X3	Header 3	1
BOOT1	Header, 3-Pin	P2	HDR1X3	Header 3	1
SWD	Header, 4-Pin	P3	HDR1X4	Header 4	1
UART1	Header, 4-Pin	P4	HDR1X4	Header 4	1
UART3	Header, 4-Pin	P5	HDR1X4	Header 4	1
Header 5	Header, 5-Pin	P6	HDR1X5	Header 5	1
Header 2	Header, 2-Pin	P7	HDR1X2	Header 2	1
BATT	Header, 2-Pin	P8	HDR1X2	Header 2	1
MOTOR	Header, 2-Pin	P9	HDR1X2	Header 2	1
L_OFF	Header, 2-Pin	P10	HDR1X2	Header 2	1
L_ON	Header, 2-Pin	P11	HDR1X2	Header 2	1
BUTTON	Header, 2-Pin	P12	HDR1X2	Header 2	1
MOSFET-N	N-Channel Power MOSFET	Q1	TO-220AB	NMOS-2	1
MOSFET-P	P-Channel Power MOSFET	Q2	TO-220AB	PMOS-2	1
Res2	Resistor	R1, R2, R3, R4, R6, R7, R8, R9, R10, R11, R12, R14, R16, R17, R18, R19, R20, R21, R22, R23, R24	AXIAL-0.4	Res2	21
NC	Resistor	R5, R13, R15	AXIAL-0.4	Res2	3
SW-PB	Switch	S1	SPST-2	SW-PB	1
STM32L151RCT6	ARM Cortex-M3 32-bit MCU, 256 KB Flash, 32 KB Internal RAM, 51 I/Os, 64-pin LQFP, -40 to 85 degC, Tray	U1	STM-LQFP64_N	STM32L151RCT6	1
GU906		U2	HFY_QFN_42_DTU	GU600_LIB_2	1
TPS62120		U3		TPS62120	1
RT7272B		U4		RT7272B	1
MOTO		U5		MOTO	1
4M	Crystal Oscillator	Y1	R38	XTAL	1
32.768M	Crystal Oscillator	Y2	R38	XTAL	1