

GDSFactory: An Open-Source Python Library for Chip Design and Simulation

Joaquin Matres,^{1,*} Simon Bilodeau,³ Niko Savola,^{2,4} Marc de Cea,² Wai Kwan Yeung,⁵ Erman Timurdogan,⁷ Helge Gehring,⁶ Floris Laporte,¹ Sebastian Goeldi,¹ and Troy Tamas¹

¹GDSFactory, 650 Castro St Ste 120 PMB 98035, Mountain View, CA 94041, USA

²Taara Co., Mountain View, CA, USA

³Department of Electrical and Computer Engineering, Princeton University, Princeton, NJ 08544, USA

⁴Department of Applied Physics, Aalto University, PO Box 13500, FIN-00076 Aalto, Finland

⁵Department of Physics, University of Oxford, Oxford, UK

⁶Google LLC, 1600 Amphitheatre Parkway, Mountain View, CA 94043, USA

⁷Lumentum, San Jose, CA, USA

*jmatres@gdsfactory.com

Abstract: We present GDSFactory, an open-source Python library for integrated circuit design automation supporting photonics, analog, quantum, and MEMS applications. The platform provides unified workflows spanning layout design, device simulation through FDTD (MEEP, Tidy3D, Lumerical), FEM (Femwell), and TCAD (DEVSIM) solvers, and circuit simulation via SAX S-parameter analysis.

1 Introduction

Hardware iterations typically require months and millions of dollars, while software iterations cost hundreds of dollars and take hours. GDSFactory bridges this gap by providing a comprehensive Python API for chip development, including layout design, device simulation, circuit simulation, and verification [1].

Unlike constrained logic-driven electronic design flows [3], integrated photonics requires freeform parametric geometries and tight integration between layout and electromagnetic simulation. GDSFactory addresses this with scriptable parametric cells (PCells), hierarchical assembly, routing, and seamless interfaces to industry-standard simulators—all in Python’s extensive scientific ecosystem [14] (Fig. 1).

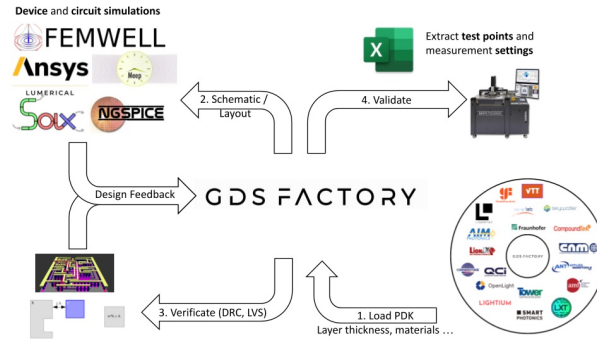


Figure 1. GDSFactory workflow: Design phase (simulation, schematic capture, layout), Verification (DRC, LVS), and Validation to ensure the fabricated product meets specifications.

2 Layout Design

GDSFactory implements cells as Python functions returning a Component class with polygons, port metadata, and convenience methods. Using KLayout’s C++ geometry engine [4], users define parametric cells through a functional programming approach where the `@gf.cell` decorator handles caching to eliminate redundant regeneration. Port metadata enables automatic routing via `route_bundle` that defines an automated route between groups of ports.

3 Device Simulation

GDSFactory’s gplugins repository [2] provides unified interfaces to device simulators by reusing layout abstractions. Components can be meshed via GMSH for cross-sectional or 3D analysis (Fig. 2).

Full-wave electromagnetic simulation is supported through open-source FDTD backends including MEEP [5] and Luminescent AI, as well as proprietary solvers Tidy3D [6] (cloud-based GPU acceleration) and Lumerical FDTD. FEM and multiphysics solvers include Femwell [7] for waveguide mode analysis and thermal simulation, Palace [9] for RF and microwave simulations, MPB for photonic crystals, and MEOW for eigenmode expansion. TCAD simulation uses DEVSIM [8] for semiconductor device physics and Sentaurus for advanced process simulation.

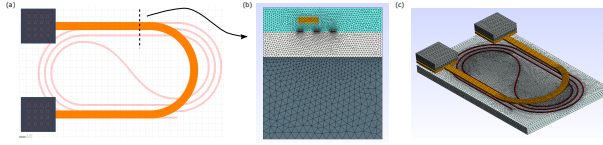


Figure 2. GDSFactory meshing: (a) heater layout, (b) cross-sectional mesh, (c) 3D mesh.

4 Circuit Simulation

Circuit-level simulation enables system-scale photonic design through netlist extraction and S-parameter composition. SAX [10] provides differentiable S-parameter circuit simulation using JAX, supporting automatic differentiation for gradient-based optimization, Monte Carlo analysis for yield estimation, and wavelength-dependent S-parameter interpolation from FDTD results. VLSIR provides SPICE netlist export for mixed photonic-electronic simulation.

5 Process Design Kits

Open-source PDKs include GlobalFoundries 180nm, SkyWater 130nm [11], VTT 3 μm SOI, SiEPIC, Cornerstone, IHP, Luxtelligence, and Quantum-RF-PDK. Commercial PDKs available through GDSFactory+ subscription [13] include AIM Photonics, AMF, CompoundTek, Fraunhofer HHI, Smart Photonics, Tower PH18, OpenLight, III-V Labs, LioniX, Ligentec, Lightium, and QCI. The generic PDK follows standard layer conventions [12] for cross-foundry compatibility.

6 Conclusion

GDSFactory provides a unified Python-driven workflow spanning layout design, device simulation, and circuit simulation. The tight integration between device solvers (Palace, Femwell, DEVSIM), and circuit simulators (SAX) enables rapid design iteration from component to system level. The library is freely available at <https://github.com/GDSFactory/GDSFactory>.

References

- [1] J. Matres *et al.*, “GDSFactory,” GitHub (2024), <https://github.com/GDSFactory/GDSFactory>.
- [2] J. Matres *et al.*, “gplugins,” GitHub (2024), <https://github.com/gdsfactory/gplugins>.
- [3] W. Bogaerts *et al.*, “Silicon photonics circuit design: methods, tools and challenges,” *Laser Photon. Rev.* **12**, 1700237 (2018).
- [4] M. Köfferlein, “KLayout,” <https://www.klayout.de/>.
- [5] A. F. Oskooi *et al.*, “MEEP: A flexible free-software package for electromagnetic simulations by the FDTD method,” *Comput. Phys. Commun.* **181**, 687–702 (2010).
- [6] Flexcompute Inc., “Tidy3D,” <https://www.flexcompute.com/tidy3d/>.
- [7] H. Gehring *et al.*, “Femwell,” GitHub (2023), <https://github.com/HelgeGehring/femwell>.
- [8] J. E. Sanchez, “DEVSIM,” <https://devsim.org/>.
- [9] AWS, “Palace: 3D Finite Element Solver for Computational Electromagnetics,” GitHub (2024), <https://github.com/aws-labs/palace>.
- [10] F. Laporte, “SAX,” GitHub (2023), <https://github.com/flaport/sax>.
- [11] SkyWater Technology Foundry and Google, “SkyWater Open Source PDK,” GitHub (2023), <https://github.com/google/skywater-pdk>.
- [12] L. Chrostowski and M. Hochberg, *Silicon Photonics Design* (Cambridge, 2015).
- [13] GDSFactory, “GDSFactory+,” <https://gdsfactory.com/>.
- [14] J. Matres, “Awesome Photonics,” GitHub (2024), https://github.com/joamatab/awesome_photonics.