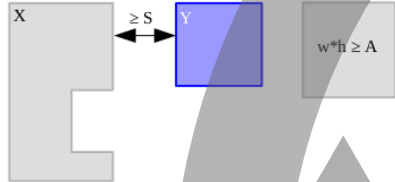
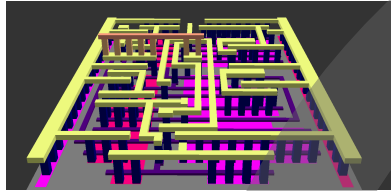


# Verification (DRC, LVS)



## GDS FACTORY

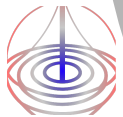
Design Intend



**pandas**  
DO DATA

Measurement

Simulation



### FEMWELL



### Ansys

LUMERICAL



Design Cycle

**Tower**  
Semiconductor  
Where Analog and Value Meet



Fraunhofer  
HHI



GlobalFoundries



SMART  
PHOTONICS



CompoundTek



ANT APPLIED  
NANOTOOLS INC.



skywater



## Foundary PDK