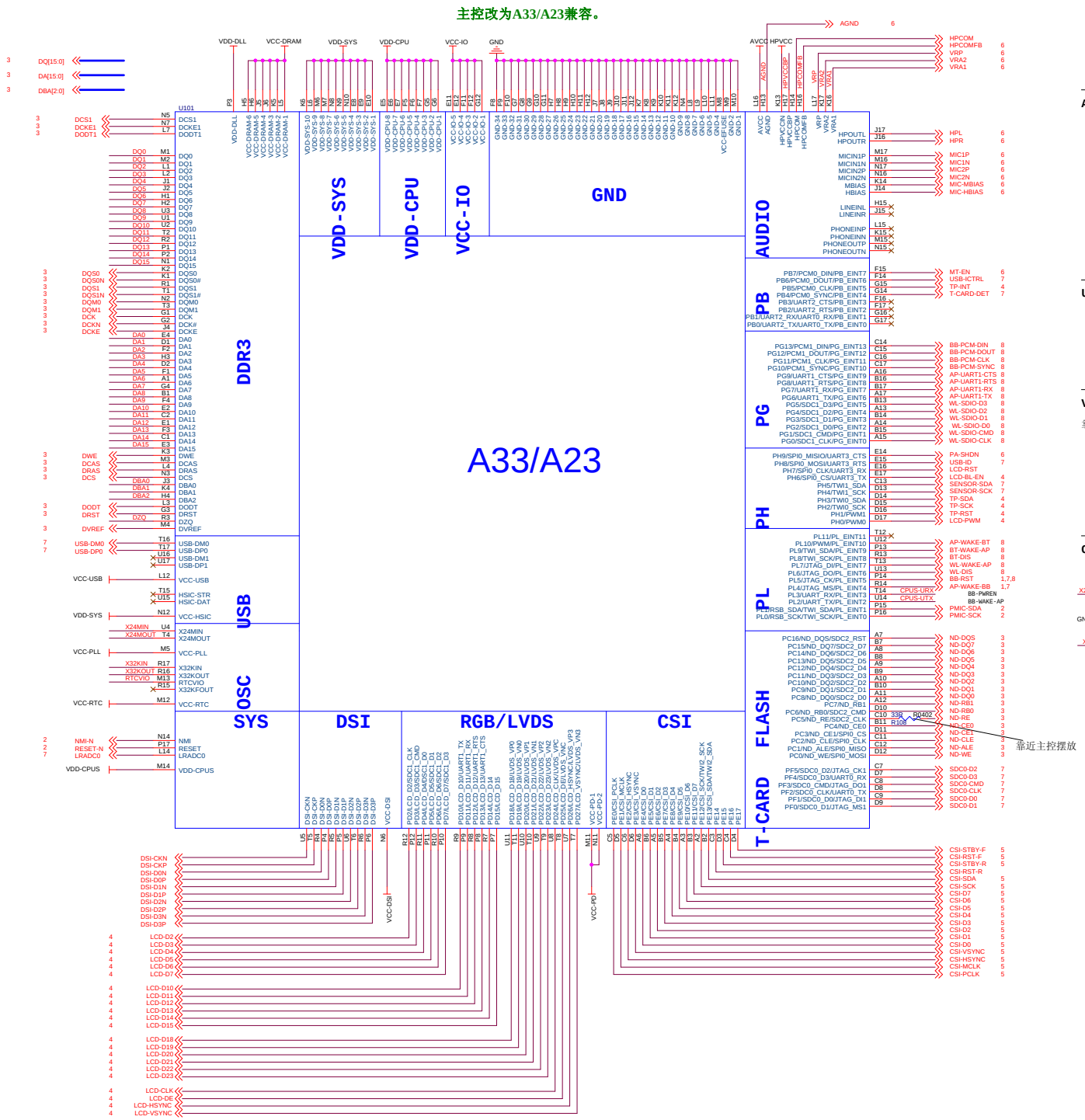
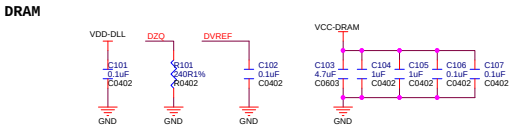
CPU

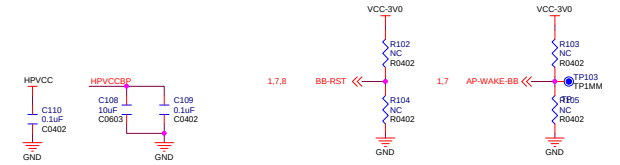
主控改为A33/A23兼容。



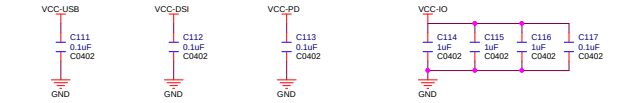
DRAM



AUDIO



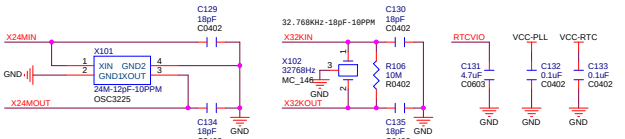
USB DSI PD VCC-IO



VDD-CPU VDD-SYS

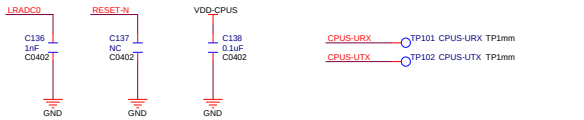
靠近主控摆放

OSC



晶振下方不可以走线

SYS



Please Close to CPU.

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POWER

NOTE:
Q1 vth <1.5V & R <30mohm@vgs=4.5V

Routing rules:
100mil
30mil
10mil

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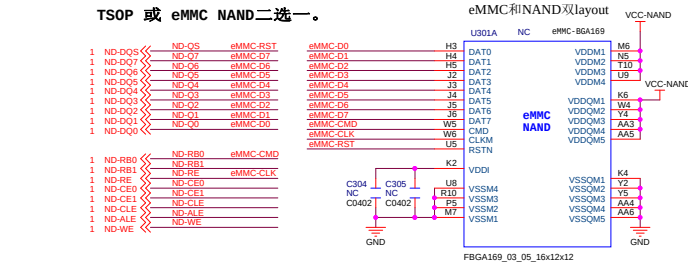
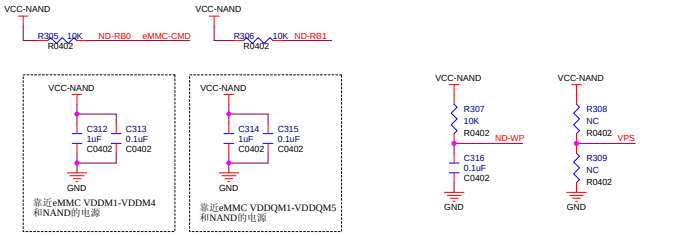
NAND/EMMC

TSOP 或 eMMC NAND二选一。

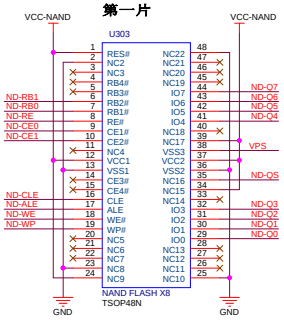
eMMC和NAND双layout



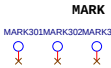


只贴一片NAND，可以是1 CE或2 CE；
贴两片NAND，两片都为1 CE

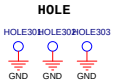
第一片

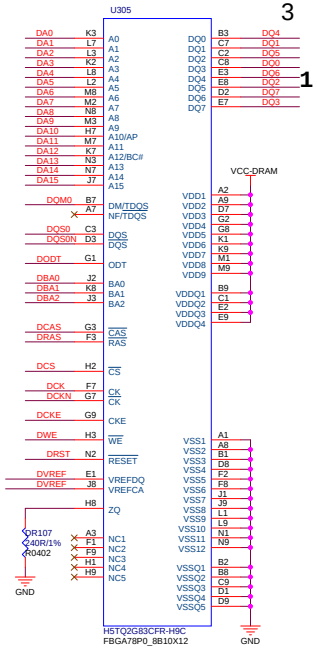


MARK POINT

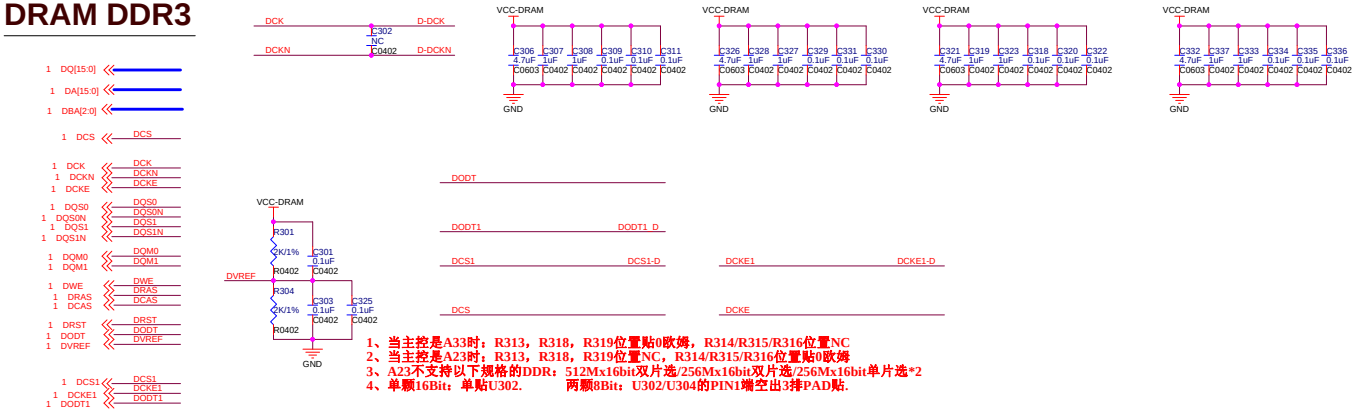


HOLE





DRAM DDR3



1、当主控是A33时：R313，R318，R319位置贴0欧姆，R314/R315/R316位置NC
2、当主控是A23时：R313，R318，R319位置NC，R314/R315/R316位置贴0欧姆
3、A23不支持以下规格的DDR：512Mx16bit双片选/256Mx16bit双片选/256Mx16bit单片选*2
4、单颗16Bit：单贴U302.　　两颗8Bit：U302/U304的PIN1端空出3排PAD贴.

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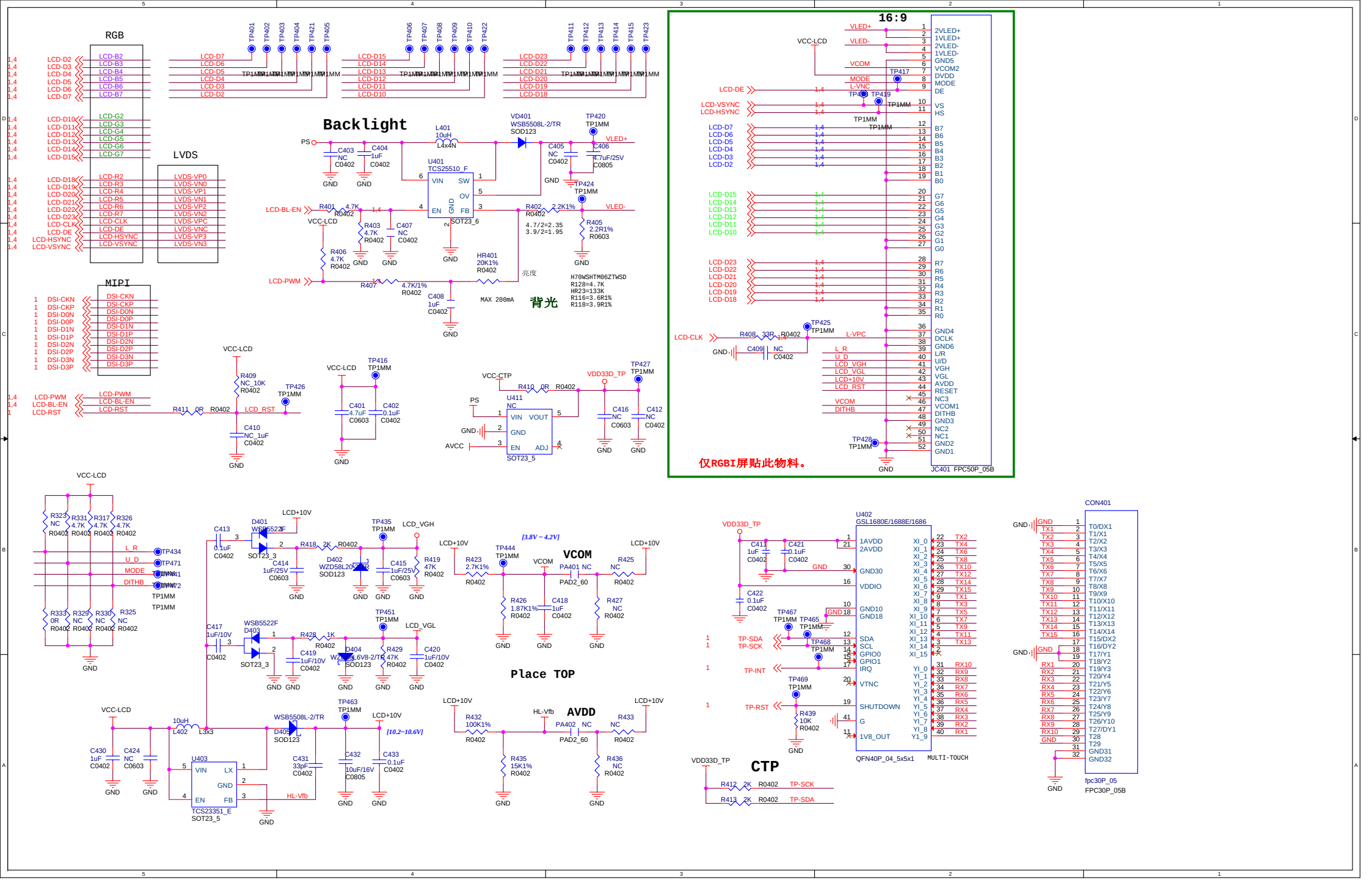
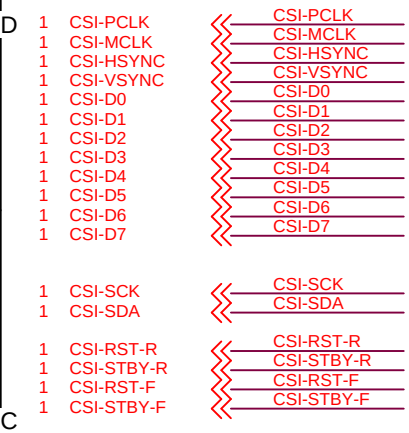
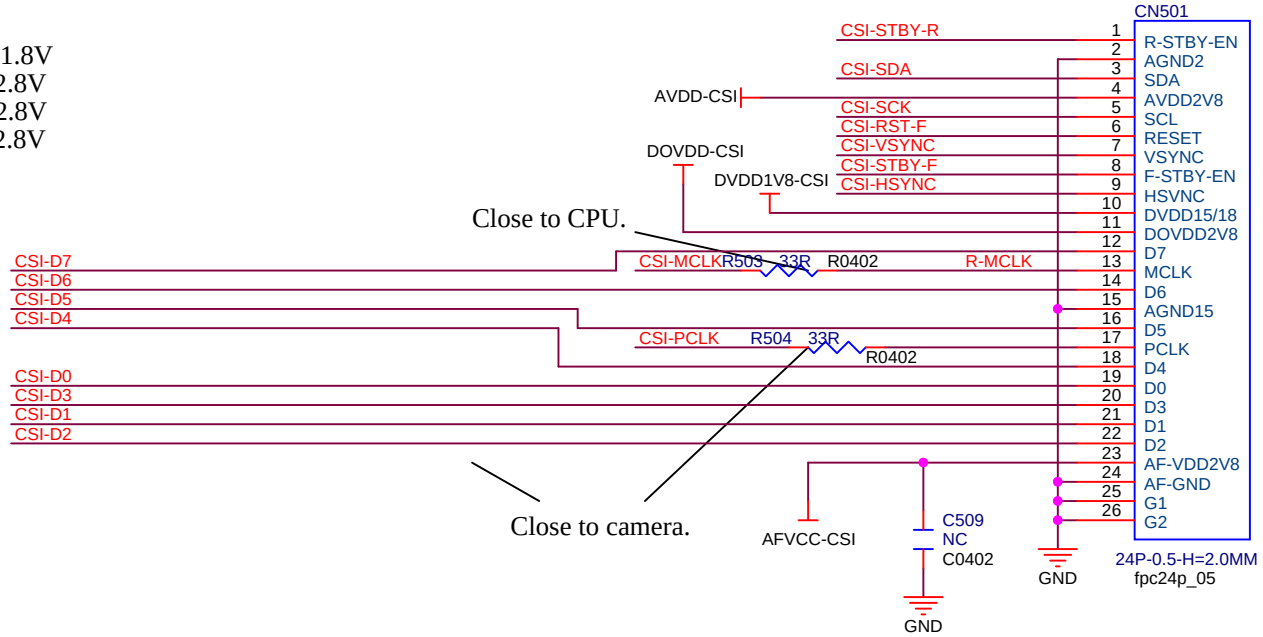

RGB
LVDS
MIPI
Backlight
16:9
仅RGBI屏贴此物料。
Place TOP
VCOM
AVDD
CTP
背光
亮度
H70WSHTM06ZTWSD
R128=4.7K
HR23=133K
R116=3.6R1%
R118=3.9R1%
MAX 200mA
JC401 FPC50P_05B
QFN40P_04_5x5x1
MULTI-TOUCH
fpc30P_05
FPC30P_05B

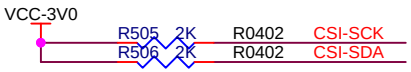
CAMERA

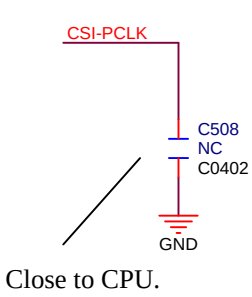
Front & Rear Camera

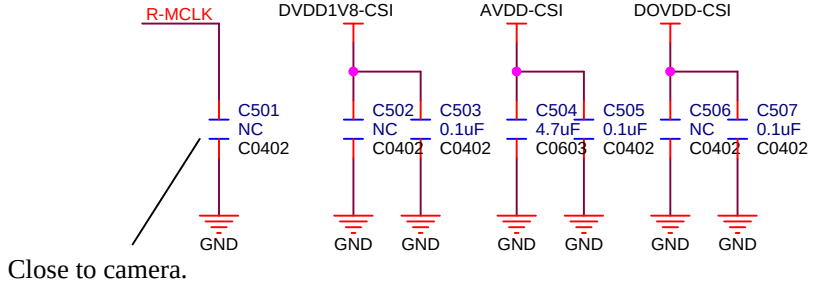
DVDD1V8-CSI =1.8V
DOVDD-CSI =2.8V
AVDD-CSI =2.8V
AFVCC-CSI =2.8V

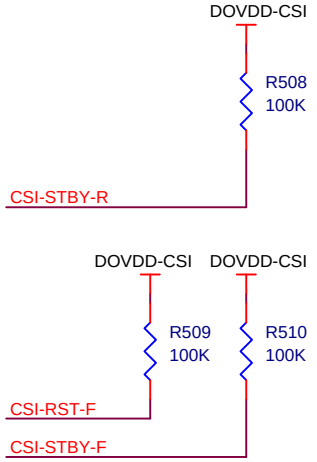












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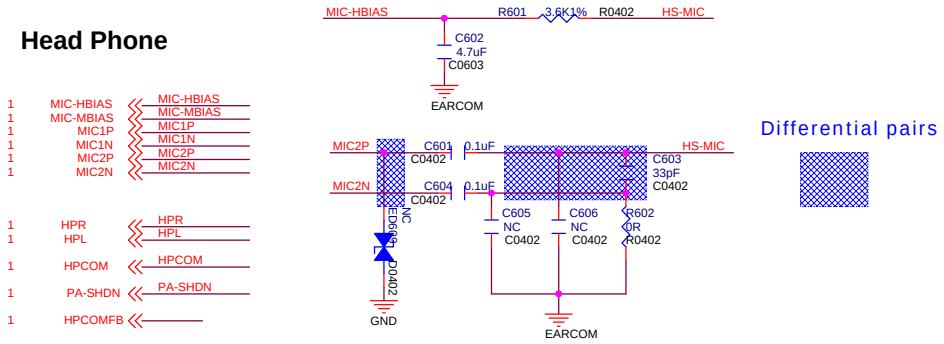
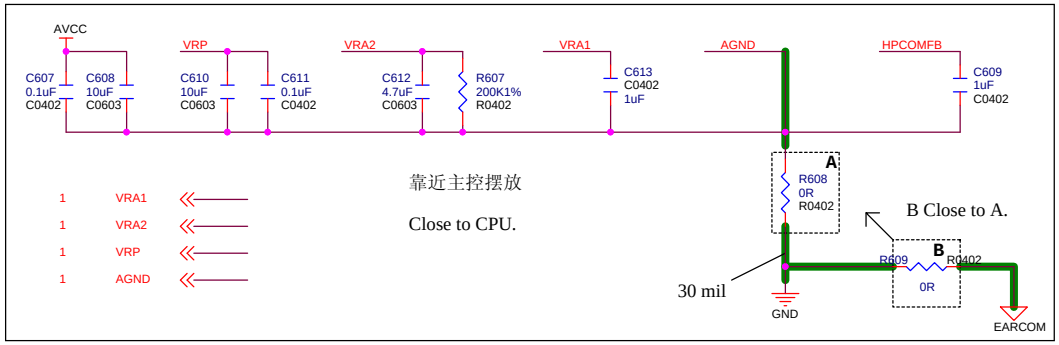
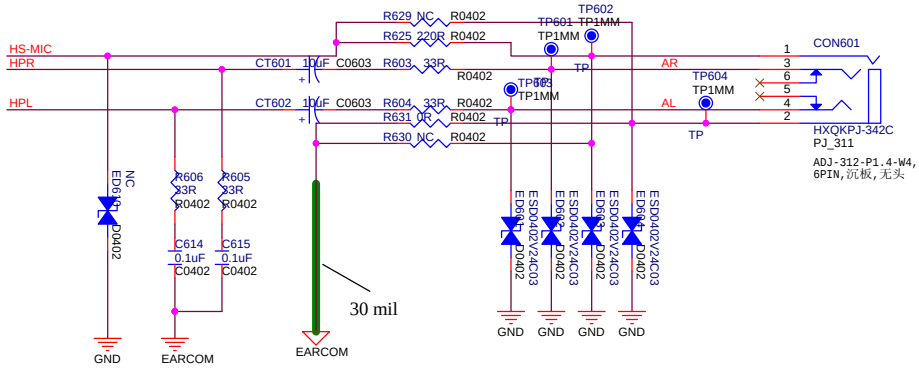
AUDIO(AC couple)

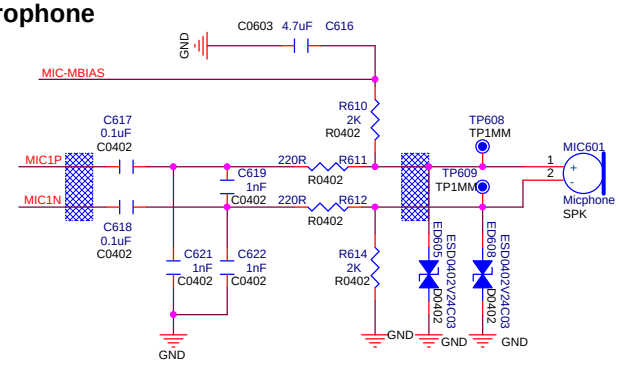
Head Phone



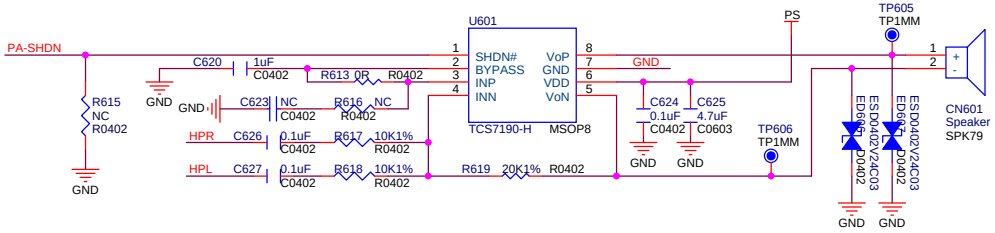




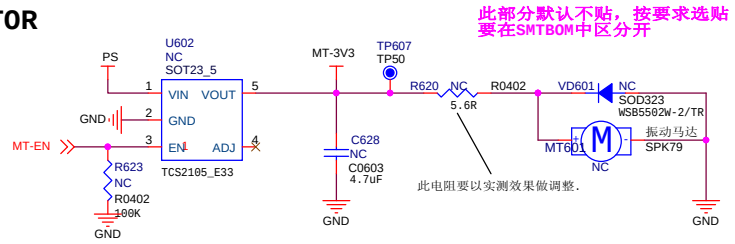
Analog Microphone



Speaker



MOTOR



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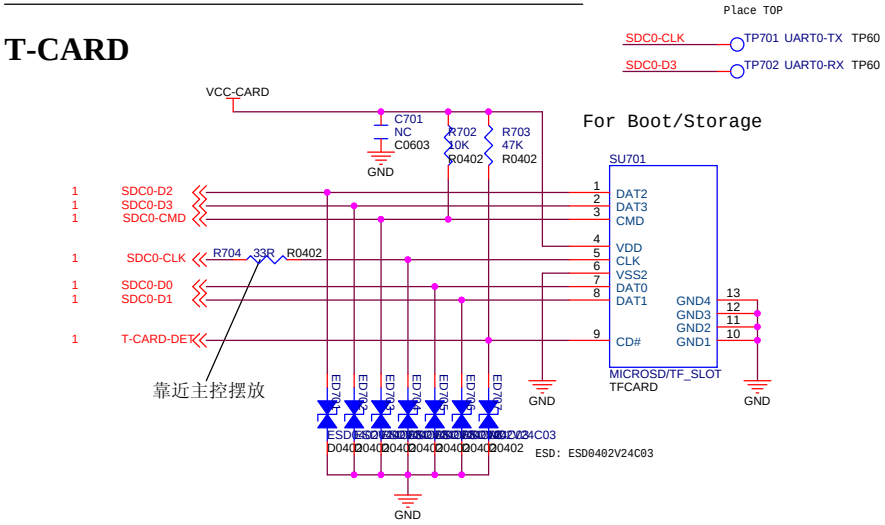
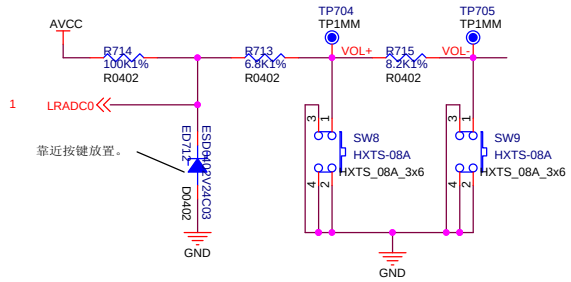
T-CARD/USB/KEY/SENSOR

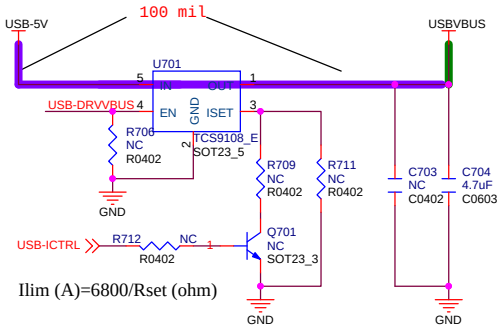
T-CARD



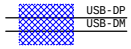
KEY

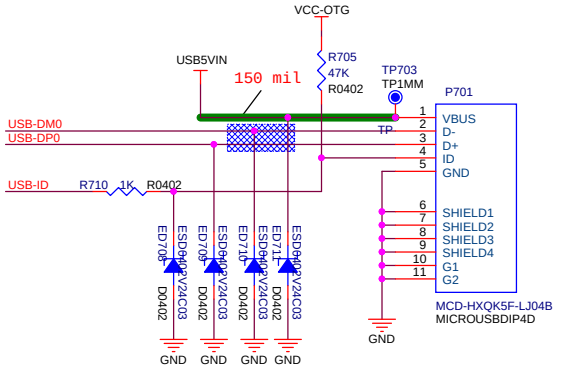


USB

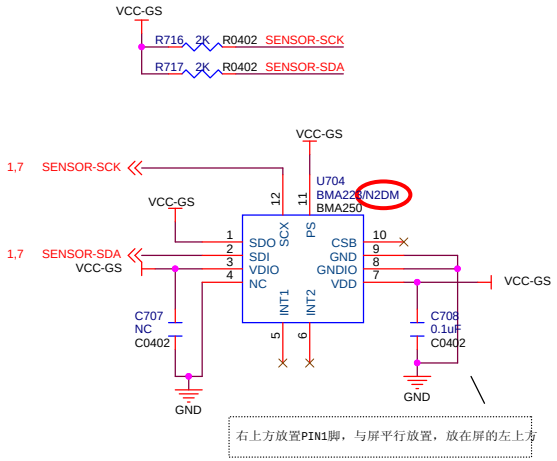


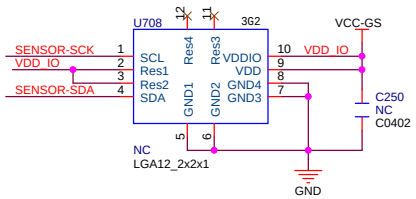
Differential pairs
Z0= 90 ohm

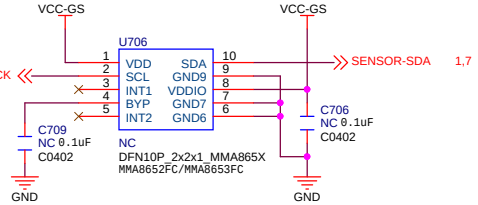


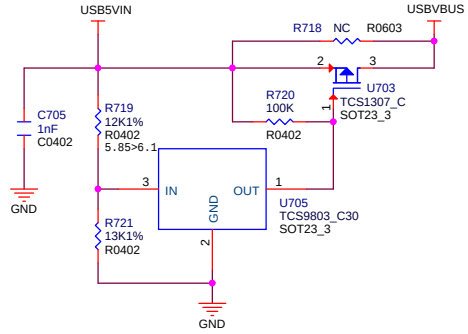


SENSOR









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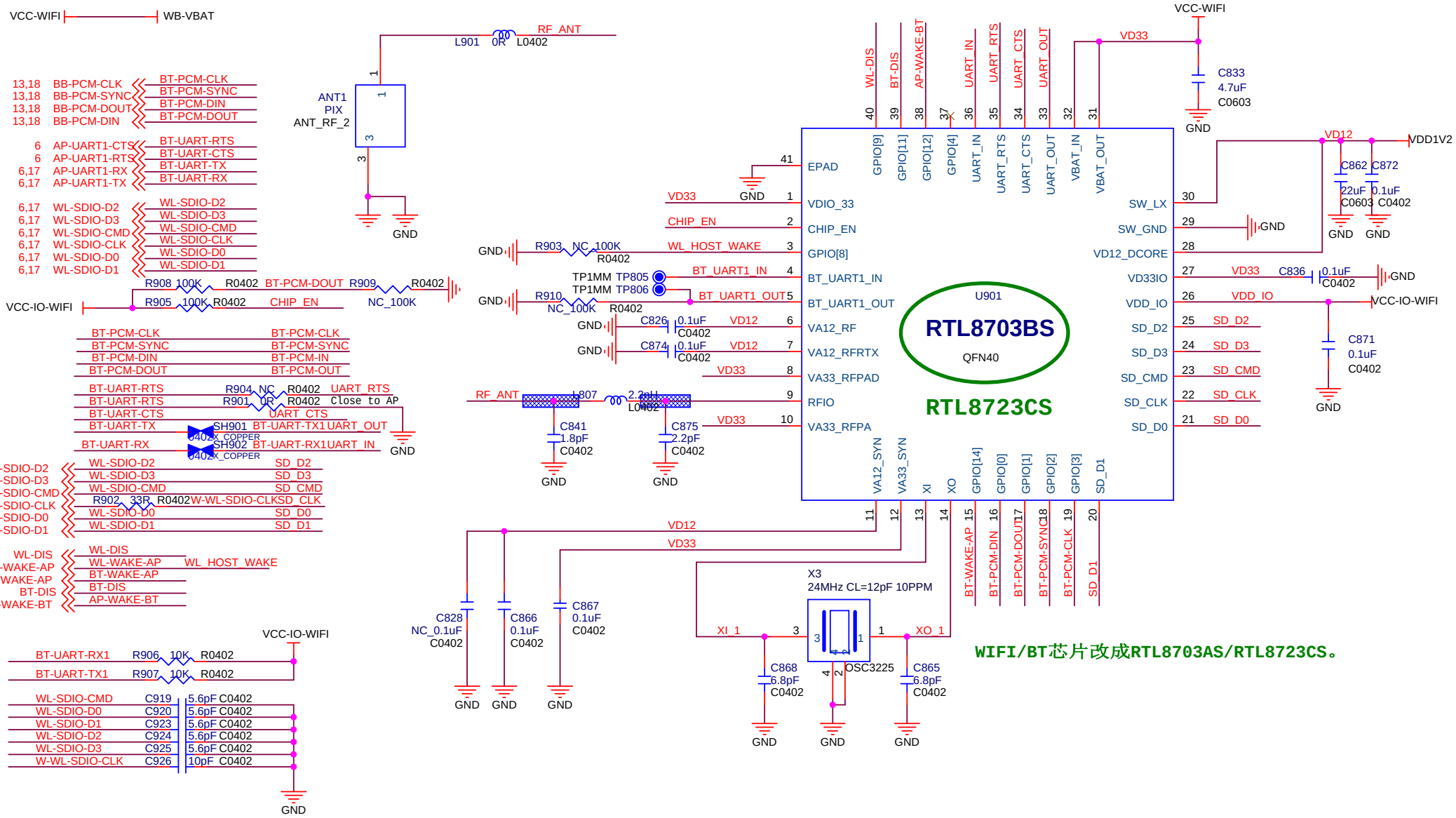
SDIO WIFI



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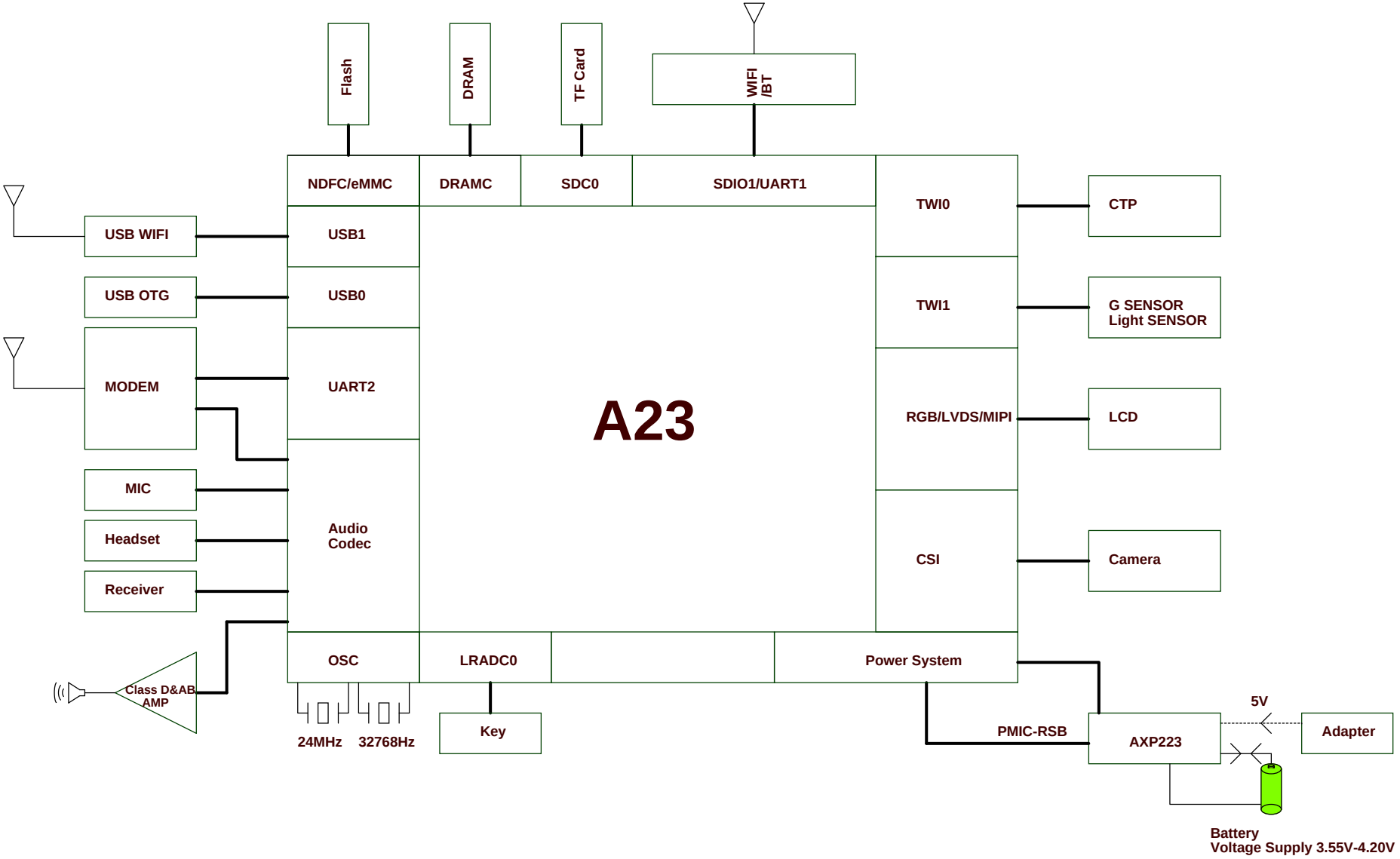
BLOCK DIAGRAM



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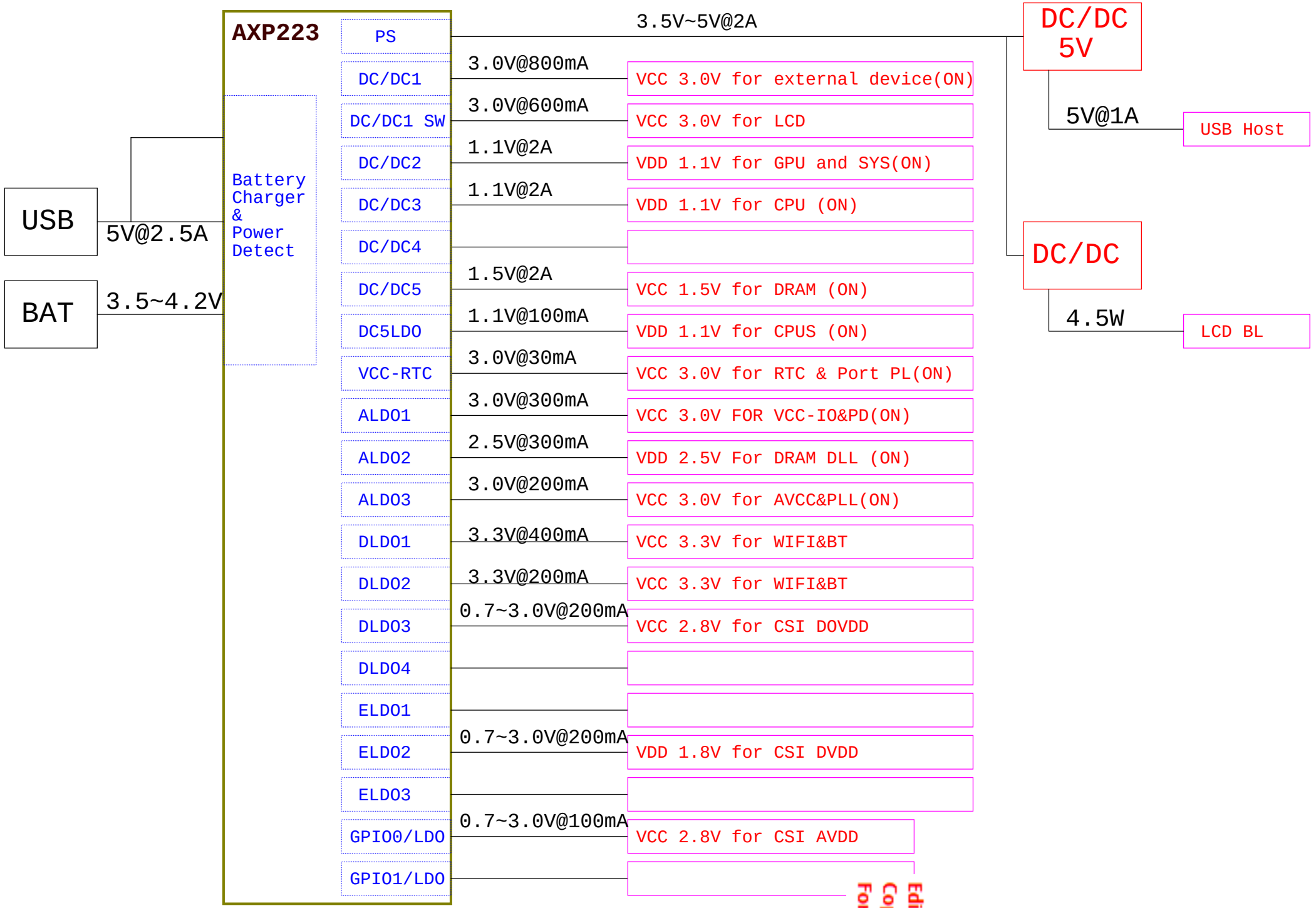
POWER TREE



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GPIO ASSIGNMENT

PIN	Name	Function
PB0	AP-UART2-TX	MODEM
PB1	AP-UART2-RX	
PB2	AP-UART2-RTS	
PB3	AP-UART2-CTS	
PB4	T-CARD-DET	T CARD
PB5	TP-INT	TP
PB6	GS-INT	G SENSOR
PB7	LS-INT	ALS

PIN	Name	Function
PG0	WL-SDIO-CLK	WLAN
PG1	WL-SDIO-CMD	
PG2	WL-SDIO-D0	
PG3	WL-SDIO-D1	
PG4	WL-SDIO-D2	
PG5	WL-SDIO-D3	
PG6	AP-UART1-TX	BT
PG7	AP-UART1-RX	
PG8	AP-UART1-RTS	
PG9	AP-UART1-CTS	
PG10		
PG11		
PG12		
PG13		

PIN	Name	Function
PH0	LCD-PWM	LCM
PH1	TP-RST	TP
PH2	TP-SCK	
PH3	TP-SDA	
PH4	SENSOR-SCK	SENSOR
PH5	SENSOR-SDA	
PH6	LCD-BL-EN	LCM
PH7	LCD-RST	
PH8	USB-ID	USB
PH9	PA-SHDN	AUDIO PA

PIN	Name	Function
PL0	PMIC-SCK	PMIC
PL1	PMIC-SDA	
PL2	BB-WAKE-AP	MODEM
PL3	BB-PWREN	
PL4	AP-WAKE-BB	
PL5	BB-RST	
PL6	WL-DIS	WLAN/BT
PL7	WL-WAKE-AP	
PL8	BT-DIS	
PL9	BT-WAKE-AP	
PL10	AP-WAKE-BT	
PL11	WL-BT-ENABLE	

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