Recursive Procedure

- A recursive procedure is one which calls itself
- We can use the stack to implement recursive procedures

Recursive Example: Factorial

```
static int factorial( int n )
{
  if ( n <= 0 ) // base case
    return 1;
  else // general case
    return ( n * factorial ( n - 1 ) );
}</pre>
```

Recursive Factorial in MIPS

```
\# int fact(int n): return n <= 0 ? 1 : n * fact(n-1);
fact:
 addi $sp, $sp, -8 # space for two words
 sw $ra, 4($sp) # save return address - push
 sw $a0, 0($sp) # temporary variable to hold n - push
 li $v0, 1
 ble $a0, $zero, fact return
 addi $a0, $a0, -1
 jal fact
 lw $a0, 0($sp)  # retrieve original n
 mul $v0, $v0, $a0 # n * fact(n - 1)
fact return:
 lw $ra 4($sp) # restore $ra - pop
 addi $sp, $sp, 8  # restore $sp - pop
                     # back to caller
 jr $ra
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact_return addi \$a0, \$a0, -1 jal fact tmpl: lw \$a0, 0(\$sp) mul \$v0, \$v0, \$a0 fact_return: lw \$ra 4(\$sp)
_ .
addi \$sp, \$sp, 8 ir \$ra
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Address	Stack
\$sp/\$fp → 7fffeffc	00000000

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp →7fffeffc	00000000
\$sp →7fffeff4	

```
fact: addi $sp, $sp, -8
  sw $ra, 4($sp)
  sw $a0, 0($sp)
  li $v0, 1
  ble $a0, $zero, fact_return
  addi $a0, $a0, -1
  jal fact
tmpl: lw $a0, 0($sp)
  mul $v0, $v0, $a0
fact_return:
  Iw $ra 4($sp)
  addi $sp, $sp, 8
  jr $ra
```

Address	Stack
\$fp ->7fffeffc	00000000
	?
\$sp ->7fffeff4	_

```
fact: addi $sp, $sp, -8
  sw $ra, 4($sp)
  sw $a0, 0($sp)
  li $v0, 1
  ble $a0, $zero, fact_return
  addi $a0, $a0, -1
  jal fact
tmpl: lw $a0, 0($sp)
  mul $v0, $v0, $a0
fact_return:
  Iw $ra 4($sp)
  addi $sp, $sp, 8
  jr $ra
```

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp ->7fffeff4	

Assume an input n=3, fact= 00400024, tmpl=00400044, fact_return= 0040004c

```
fact: addi $sp, $sp, -8
  sw $ra, 4($sp)
  sw $a0, 0($sp)
  li $v0, 1
  ble $a0, $zero, fact_return
  addi $a0, $a0, -1
  jal fact
tmpl: lw $a0, 0($sp)
  mul $v0, $v0, $a0
fact_return:
  lw $ra 4($sp)
  addi $sp, $sp, 8
  jr $ra
```

Address	Stack
\$fp →7fffeffc	00000000
	00400068
\$sp →7fffeff4	?

Assume an input n=3, fact= 00400024, tmpl=00400044, fact_return= 0040004c

```
fact: addi $sp, $sp, -8
  sw $ra, 4($sp)
  sw $a0, 0($sp)
  li $v0, 1
  ble $a0, $zero, fact_return
  addi $a0, $a0, -1
  jal fact
tmpl: lw $a0, 0($sp)
  mul $v0, $v0, $a0
fact_return:
  lw $ra 4($sp)
  addi $sp, $sp, 8
  jr $ra
```

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp ->7fffeff4	0000003

Assume an input n=3, fact= 00400024, tmpl=00400044, fact_return= 0040004c

```
fact: addi $sp, $sp, -8
 sw $ra, 4($sp)
  sw $a0, 0($sp)
  li $v0, 1 #($v0)=1
  ble $a0, $zero, fact return
 addi $a0, $a0, -1
 jal fact
tmpl: lw $a0, 0($sp)
  mul $v0, $v0, $a0
fact_return:
 Iw $ra 4($sp)
 addi $sp, $sp, 8
 jr $ra
```

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp →7fffeff4	00000003

```
fact: addi $sp, $sp, -8
  sw $ra, 4($sp)
  sw $a0, 0($sp)
  li $v0, 1
  ble $a0, $zero, fact_return
  addi $a0, $a0, -1
  jal fact
tmpl: lw $a0, 0($sp)
  mul $v0, $v0, $a0
fact return:
  Iw $ra 4($sp)
  addi $sp, $sp, 8
  jr $ra
```

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp ->7fffeff4	0000003

```
n=2, fact= 00400024,
tmpl=00400044, fact_return= 0040004c
```

```
fact: addi $sp, $sp, -8
  sw $ra, 4($sp)
  sw $a0, 0($sp)
  li $v0, 1
  ble $a0, $zero, fact_return
  addi $a0, $a0, -1 #($a0)=2
  jal fact
tmpl: lw $a0, 0($sp)
  mul $v0, $v0, $a0
fact return:
  Iw $ra 4($sp)
  addi $sp, $sp, 8
  jr $ra
```

Address	Stack
\$fp →7fffeffc	00000000
	00400068
\$sp →7fffeff4	00000003

```
n=2, ($ra) =00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

```
fact: addi $sp, $sp, -8
  sw $ra, 4($sp)
  sw $a0, 0($sp)
  li $v0, 1
  ble $a0, $zero, fact_return
  addi $a0, $a0, -1
  jal fact
tmpl: lw $a0, 0($sp)
  mul $v0, $v0, $a0
fact return:
  lw $ra 4($sp)
  addi $sp, $sp, 8
  jr $ra
```

Address	Stack
\$fp →7fffeffc	00000000
	00400068
\$sp →7fffeff4	0000003

```
n=2, ($ra) =00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	0000000
	00400068
	0000003
\$sp ->7fffefec	

```
n=2, ($ra) =00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	0000003
	?
\$sp ->7fffefec	

```
n=2, ($ra) =00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact_return addi \$a0, \$a0, -1 jal fact tmpl: lw \$a0, 0(\$sp) mul \$v0, \$v0, \$a0 fact_return: lw \$ra 4(\$sp)
. , , ,
,
addi \$sp, \$sp, 8 jr \$ra
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Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
\$sp ->7fffefec	

```
n=2, ($ra) =00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

Address	Stack
\$fp ->7fffeffc	0000000
	00400068
	0000003
	00400044
\$sp ->7fffefec	?

```
n=2, ($ra) =00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact return:
 lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
ir \$ra
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Address	Stack
\$fp ->7fffeffc	0000000
	00400068
	0000003
	00400044
\$sp ->7fffefec	0000002

```
n=2, ($ra) =00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact return:
 lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
ir \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	0000003
	00400044
\$sp ->7fffefec	00000002

```
n=2, ($ra) =00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact return:
 lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
ir \$ra
J. 4

Address	Stack
\$fp →7fffeffc	00000000
	00400068
	0000003
	00400044
\$sp →7fffefec	00000002

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	0000003
	00400044
\$sp ->7fffefec	00000002

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra
-

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	0000003
	00400044
\$sp ->7fffefec	00000002

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
\$sp →7fffefe4	

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra
• -

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	?
\$sp ->7fffefe4	

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	
•	

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	?

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact return:
 lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra
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Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001
•	

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact_return addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001

n=0, (\$ra)=00400044 fact= 00400024, tmpl=00400044, fact_return= 0040004c

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001

```
n=0, ($ra)=00400044
```

fact= 00400024, tmpl=00400044, fact_return= 0040004c

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001
•	

```
n=0, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp →7fffeffc	00000000
	00400068
	0000003
	00400044
	00000002
	00400044
	0000001
\$sp →7fffefdc	

```
n=0, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
	0000001
	?
\$sp ->7fffefdc	

```
n=0, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
	0000001
	00400044
\$sp ->7fffefdc	

```
n=0, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
ir \$ra
•

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	0000003
	00400044
	00000002
	00400044
	0000001
	00400044
\$sp ->7fffefdc	,

```
n=0, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	0000003
	00400044
	00000002
	00400044
	0000001
	00400044
\$sp →7fffefdc	00000000

n=0, (\$ra)=00400044 fact= 00400024, tmpl=00400044, fact_return= 0040004c

Address	Stack
\$fp →7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
	0000001
	00400044
\$sp ->7fffefdc	00000000

n=0, (\$ra)=00400044 fact= 00400024, tmpl=00400044, fact_return= 0040004c

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp →7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
	0000001
	00400044
\$sp ->7fffefdc	00000000

```
n=0, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8	
sw \$ra, 4(\$sp)	
sw \$a0, 0(\$sp)	
li \$v0, 1	
ble \$a0, \$zero, fact_return	
addi \$a0, \$a0, -1	
jal fact	
tmpl: lw \$a0, 0(\$sp)	
mul \$v0, \$v0, \$a0	
fact_return:	
lw \$ra 4(\$sp)	
addi \$sp, \$sp, 8	
jr \$ra	

Address	Stack
\$fp →7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
	0000001
	00400044
\$sp ->7fffefdc	00000000

```
n=0, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact_return addi \$a0, \$a0, -1 jal fact tmpl: lw \$a0, 0(\$sp) mul \$v0, \$v0, \$a0 fact_return: lw \$ra 4(\$sp) addi \$sp, \$sp, 8 jr \$ra
•

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001
	00400044
7fffefdc	00000000

n=0, (\$ra)=00400044 fact= 00400024, tmpl=00400044, fact_return= 0040004c

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0
fact return:
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact_return addi \$a0, \$a0, -1 jal fact tmpl: lw \$a0, 0(\$sp) mul \$v0, \$v0, \$a0 fact_return: lw \$ra 4(\$sp) addi \$sp, \$sp, 8

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact_return addi \$a0, \$a0, -1 jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0 #(\$v0)=1
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
ir \$ra
Ji Yi G

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
	00000002
	00400044
\$sp ->7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
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fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact_return addi \$a0, \$a0, -1 jal fact tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0 #(\$v0)=1
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
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	00400044
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\$sp ->7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp)
mul \$v0, \$v0, \$a0 #(\$v0)=1
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
\$sp →7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=1, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

-8
act_return
o)
0 #(\$v0)=1

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
\$sp ->7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=2, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp) #(\$a0)=2
mul \$v0, \$v0, \$a0 #(\$v0)=1
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
\$sp →7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=2, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp) #(\$a0)=2
mul \$v0, \$v0, \$a0 #(\$v0)=2
fact return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
\$sp ->7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=2, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp) #(\$a0)=2
mul \$v0, \$v0, \$a0 #(\$v0)=2
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
	00000003
	00400044
\$sp →7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=2, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact_return addi \$a0, \$a0, -1 jal fact tmpl: lw \$a0, 0(\$sp) #(\$a0)=2 mul \$v0, \$v0, \$a0 #(\$v0)=2 fact_return: lw \$ra 4(\$sp)	
— .	
jr \$ra	

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp ->7fffeff4	00000003
	00400044
7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=2, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp) sw \$a0, 0(\$sp) li \$v0, 1 ble \$a0, \$zero, fact return
Die Jau, Jzeru, ract_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp) #(\$a0)=2
mul \$v0, \$v0, \$a0 #(\$v0)=2
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp ->7fffeff4	00000003
	00400044
7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=3, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp) #(\$a0)=3
mul \$v0, \$v0, \$a0 #(\$v0)=2
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp ->7fffeff4	00000003
	00400044
7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

```
n=3, ($ra)=00400044
fact= 00400024, tmpl=00400044, fact_return= 0040004c
```

fact: addi \$sp, \$sp, -8
sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp) #(\$a0)=3
mul \$v0, \$v0, \$a0 #(\$v0)=6
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
ir \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp ->7fffeff4	00000003
	00400044
7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

n=3, (\$ra)=00400068 (return address of main)

fact= 00400024, tmpl=00400044, fact_return= 0040004c

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp)
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp) #(\$a0)=3
mul \$v0, \$v0, \$a0 #(\$v0)=6
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
ir \$ra

Address	Stack
\$fp ->7fffeffc	00000000
	00400068
\$sp ->7fffeff4	00000003
	00400044
7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

n=3, (\$ra)=00400068 (return address of main) fact= 00400024, tmpl=00400044, fact_return= 0040004c

Address	Stack
\$sp/\$fp →7fffeffc	00000000
	00400068
7fffeff4	00000003
	00400044
7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000

n=3, (\$ra)=00400068 (return address of main) fact= 00400024, tmpl=00400044, fact_return= 0040004c

fact: addi \$sp, \$sp, -8 sw \$ra, 4(\$sp)
, , , ,
sw \$a0, 0(\$sp)
li \$v0, 1
ble \$a0, \$zero, fact_return
addi \$a0, \$a0, -1
jal fact
tmpl: lw \$a0, 0(\$sp) #(\$a0)=3
mul \$v0, \$v0, \$a0 #(\$v0)=6
fact_return:
lw \$ra 4(\$sp)
addi \$sp, \$sp, 8
jr \$ra #return to main

Address	Stack
\$sp/\$fp ->7fffeffc	00000000
	00400068
7fffeff4	00000003
	00400044
7fffefec	00000002
	00400044
7fffefe4	0000001
	00400044
7fffefdc	00000000