

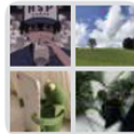
# **Comp1036 (CSF)**

# **Computer Fundamentals**

Dr Tianxiang Cui and Dr Jianfeng Ren

# WeChat group

- Scan the barcode below to join the CSF 2021/22 WeChat group



Computer Fundamentals  
21-22



Valid until Sept 29.

该二维码7天内(9月29日前)有效, 重新进入将更新

# Student Transfer and Others

- Students in the Faculty of Science and Engineering are allowed to transfer to other programmes within the Faculty in the first two teaching weeks of Semester 1 in Year 2, subject to the approval from the Head of relevant school/departments and the capacity at the University of Nottingham UK Campus.
- Therefore, given some students won't start until the third week, first two weeks must be light, non-assessed etc

# Outline

- Who is teaching the module
- Admin
- How will this module be taught and assessed
- What is this module about

# Who is teaching the module

- Dr Tianxiang Cui
  - Research interests: Computational Intelligence, Operation Research, Machine Learning, Reinforcement Learning
  - Previously worked in Huawei (autonomous driving) and PingAn (quantitative trading)
- Dr Jianfeng Ren
  - PhD degree from Nanyang Technological University, Singapore
  - Research interests: Computer Vision and Machine Learning
  - Postgraduate Course Director, School of Computer Science
  - <https://scholar.google.com/citations?user=ZZ928OgAAAAJ&hl=zh-CN>
  - [https://www.researchgate.net/profile/Jianfeng\\_Ren5](https://www.researchgate.net/profile/Jianfeng_Ren5)

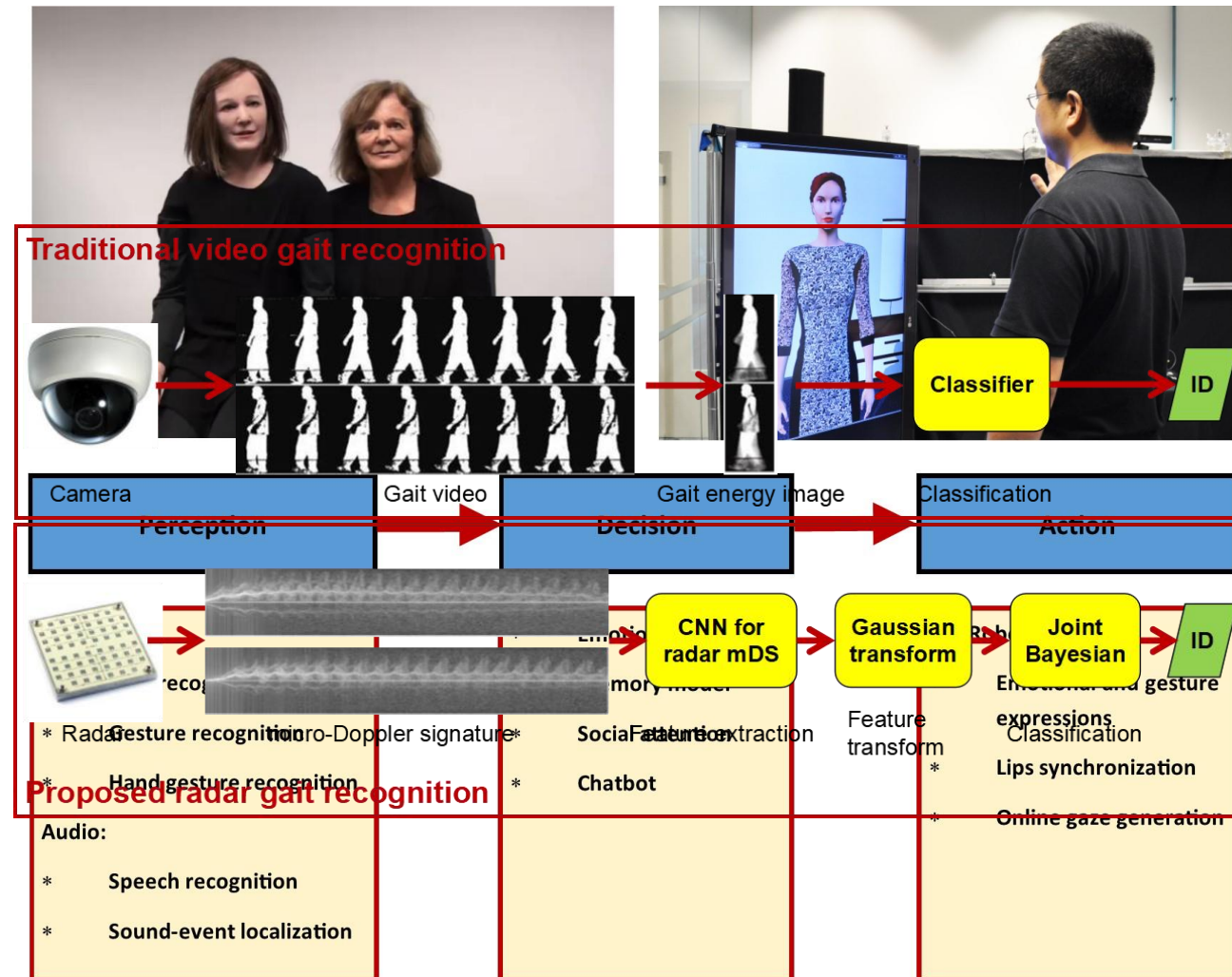
# Research interests - Tianxiang

- Operation Research
  - Deal with the development and application of advanced analytical methods to improve decision-making
- Computational Intelligence
  - Set of nature-inspired computational methodologies and approaches to address complex real-world problems
- Machine Learning
  - An area of AI concerned with development of techniques which allow machines to learn
- Reinforcement Learning
  - Framework for learning to solve sequential decision making problems



# Research interests - Jianfeng

- Image/video processing,
- Computer vision,
- Statistical pattern recognition,
- Machine learning/deep learning,
- Human computer interaction,
- Radar target recognition.



# Teaching - Tianxiang

- Computer Fundamental (Qualify Year)
- Software Engineering (Qualify Year)
- Computer Vision (Part II)



# Teaching - Jianfeng

- Computer Fundamental (Qualify Year)
- Algorithm Correctness & Efficiency (Part I)
- Introduction to Image Processing (Part II)

# Research opportunities

- You may find the profiles of Professors
  - <https://www.nottingham.edu.cn/en/science-engineering/people/listing.aspx>
- Identify your own research interests and match them with professors
- Undergraduate research
- Summer research
- GRP project (Part I, a bit research, more on Software Engineering)
- DT project (Part II, research focused)
- PhD research (Direct PhD programme)

# Contact hours, Tianxiang

- Contact
  - Office: PMB426
  - Email: [tianxiang.cui@nottingham.edu.cn](mailto:tianxiang.cui@nottingham.edu.cn)
- Office hours
  - Thursday, 10:00-12:00
- Outside office hours
  - Drop an email to book appointment.

# Contact hours, Jianfeng

- Contact
  - Office: PMB445
  - Email: [jianfeng.ren@nottingham.edu.cn](mailto:jianfeng.ren@nottingham.edu.cn)
- Office hours
  - Monday, 15:00-17:00
- Outside office hours
  - Drop an email to book appointment.

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# Admin

- Plagiarism
  - Very strict on plagiarism.
  - We do check the code similarity.
  - The offender will be reported to School or University, and subject to disciplinary actions.
- 10 credits - workload 100 hours

# Lectures and labs

- Lectures
  - Tuesday, 9:00-11:00, DB-A05+.
- Lab
  - Wednesday, 9:00-11:00, **PMB432+**. (Group 1)
  - Wednesday, 11:00-13:00, **PMB432+**. (Group 2)
- Team live streaming, join course MS-Team:
  - [https://teams.microsoft.com/l/team/19%3a4JoxvcEnYJOHzuzm7c6UfVnE\\_vU7RyTlh7Pug0objyc1%40thread.tacv2/conversations?groupId=6ffc65a1-cd5a-4ab5-895d-236ca0678ca8&tenantId=04c4c5c8-db8c-41b1-882b-5bb7948405e8](https://teams.microsoft.com/l/team/19%3a4JoxvcEnYJOHzuzm7c6UfVnE_vU7RyTlh7Pug0objyc1%40thread.tacv2/conversations?groupId=6ffc65a1-cd5a-4ab5-895d-236ca0678ca8&tenantId=04c4c5c8-db8c-41b1-882b-5bb7948405e8)
- Recording:
  - The recorded lectures/labs will be uploaded to Panopto and shared on Moodle

# Moodle

- **Formal source of communication**
  - Lecture materials, lecture/lab recordings,
  - Announcements on coursework and updates.
- Do check regularly for Moodle updates, especially on coursework requirements.
- <https://moodle.nottingham.ac.uk/course/view.php?id=124469>



# Lectures

- Start at 9:00 am sharply
- Finish at 10:50 with a 10 minute break in the middle
- No attendance register
- If you arrive **late** or need to **pop out** or leave **early**
  - Sit toward the door side/back.
- I will **attempt** to answer questions after lectures
- Feedback will be arranged
  - Feel free to let us know how we are doing.

# Labs

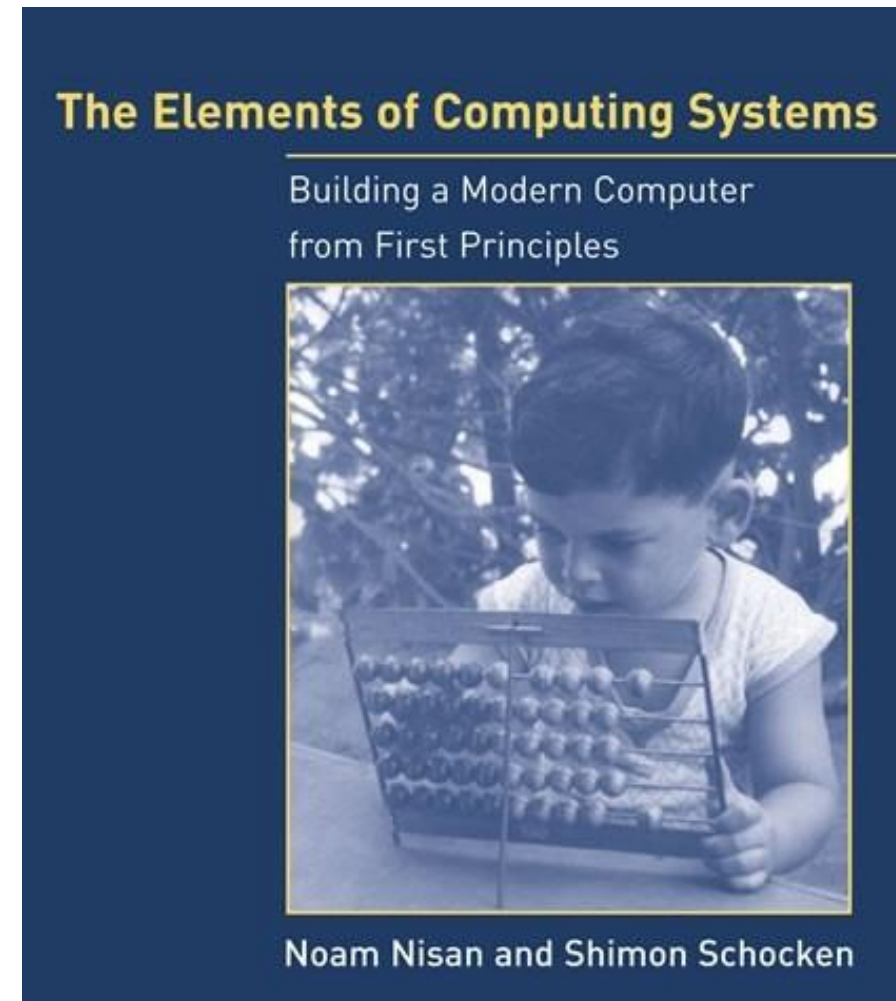
- (PMB 432) – bring your own laptops
- 1<sup>st</sup> lab **will be simple for most of you**, just check you can log in, download software, run software, edit files, do basic checks etc.
- Attendance will be registered
  - No signing in for others!
  - **Warning letter will be issued if miss too many labs.**
  - If you can't make your day, try arranging a swap with someone (let us know and I will amend the register)...

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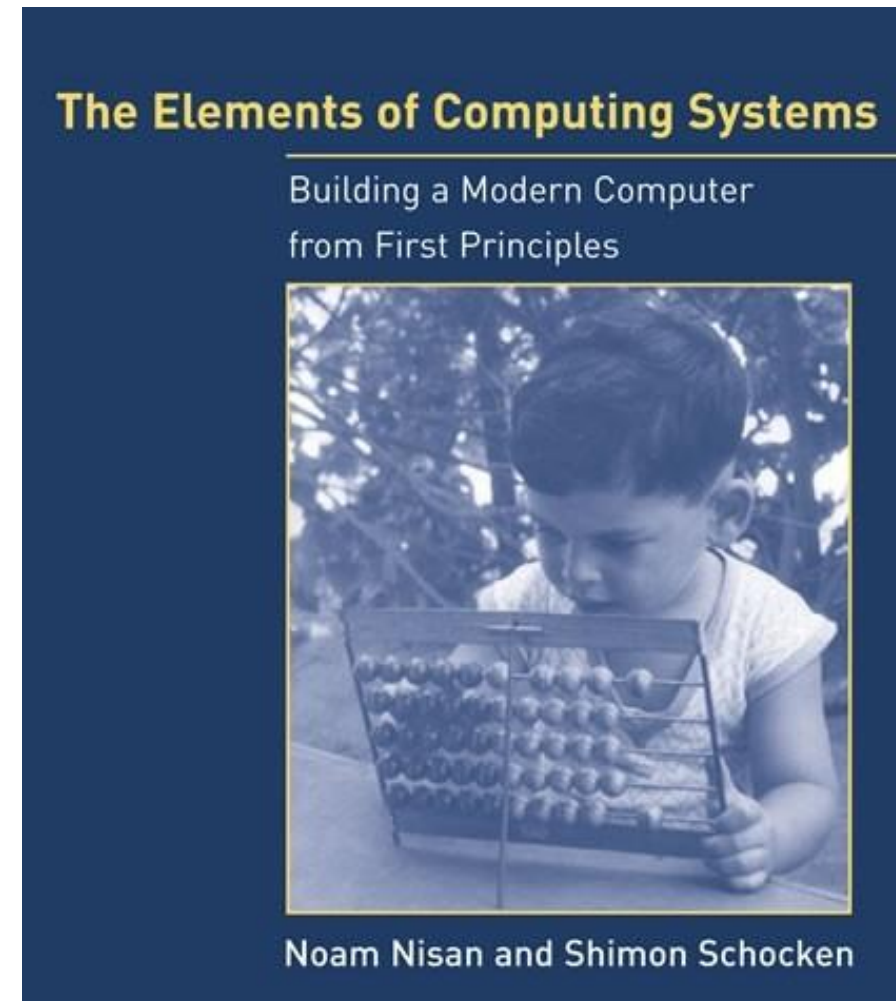
# How will this module be taught

- Recommended Text books
  - Noam Nisan and Shimon Schocken. The elements of computing systems: building a modern computer from first principles. MIT press, 2005.
- Lectures
  - Will loosely follow chapters 1-8 of Nisan and Schocken.
- Labs
  - Also will loosely follow tasks from that book.



# Textbook

- Nisan, Noam, and Shimon Schocken. *The elements of computing systems: building a modern computer from first principles*. MIT press, 2005.
- Patterson, David A., and John L. Hennessy. *Computer Organization and Design MIPS Edition: The Hardware/Software Interface*. Newnes, 2013.
- More online information:
  - From Nand to Tetris, Building a Modern Computer From First Principles.
  - <https://www.nand2tetris.org/>



# How will this module be assessed

- Coursework
  - Project -50% of final mark
  - Released mid November
  - Deadline mid December
  - Online submission
  - You can submit it early!!!!
- Exam
  - 50% of final mark
  - 1 hour
  - More updates on exam during the last lecture.

# Coursework

- This will involve writing software using the nand2tetris simulators.
  - HardwareSimulator
  - CPUEmulator
  - VMEmulator
- This will be tested using test scripts
- Exact test scripts will **NOT** be given to you, but sample test scripts will be provided.
- The software will be described
  - Writing software according to a set of requirements
- Previously students have been asked to write a simple calculator.
  - One student finished the coursework in **one afternoon!**

# More on coursework

- You can ask clarification questions on Moodle but..
  - You can't ask “is this right?” We do not assess your CW until you submit it.
- You can ask for help on debugging your programme, but
  - Not on implementing the coursework.
- Make sure that you attend all the lab sections and are able to do the lab exercises independently.
  - Coursework questions are usually built on the lab exercises.
- A good enough coursework score will mean you can pass the module before you even do the exam. (**50% of the final mark!**)



# Previous assessment results

	2019/20	2018/19	2019/20
<b>1st</b>	<b>54%</b>	<b>47%</b>	<b>47%</b>
II-1	17%	24%	19%
II-2	13%	12%	11%
3rd	6%	8%	11%
Fail	9%	9%	12%
Number of students	164	188	160

# COVID update

- Some student will be (at least) starting the module online,
  - These students will be supported by **online lectures** and **online labs via MS-Team**.
- Talk to us and/or your personal tutor if you have any problems attending lectures or keeping up with the module.

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# What is this module about

- **Summary Of Content**

- *This module gives a basic understanding of the fundamental architecture of computers. This module will introduce how the simple building blocks of digital logic can be put together in different ways to build an entire computer. It will also show how modern computer systems are constructed of hierarchical layers of functionality which build on and abstract the layers below. You will spend four hours per week in lectures and computer labs for this module.*

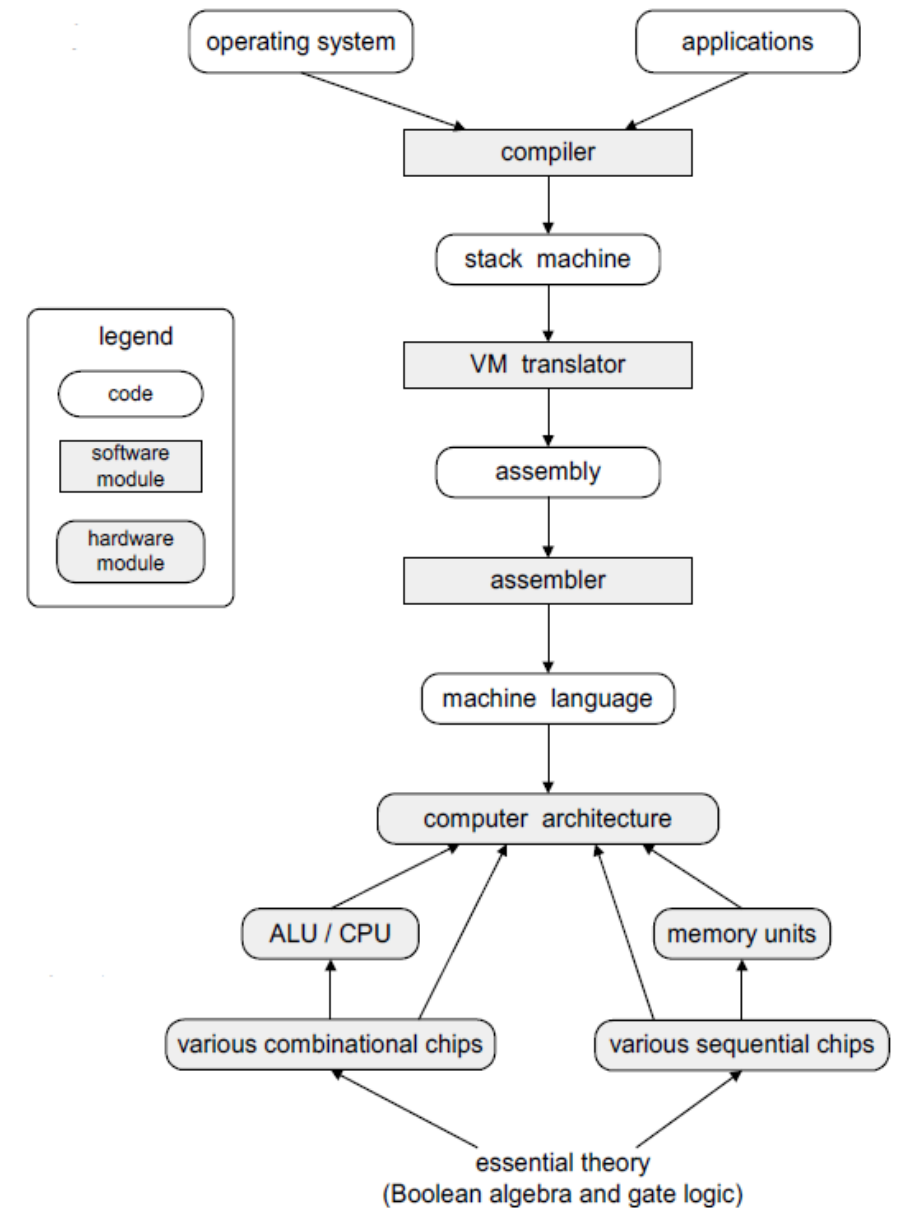
- **Education Aims**

- *To give a broad understanding of the internal operation and structure of computer. To show how a computer is built up from a relatively simple digital circuit by successive elaboration to form a number of logical layers of functionality; to show that hardware and software are often equivalent in this context. To allow the student to appreciate the typical facilities and mechanisms which underlie the operation of various high-level programming operations and facilities. To allow the student to appreciate the key conceptual steps which underlie the evolution or realisation of a conventional stored-program digital computer.*

- [http://modulecatalogue.nottingham.ac.uk/ningbo/asp/main\\_search.asp](http://modulecatalogue.nottingham.ac.uk/ningbo/asp/main_search.asp)

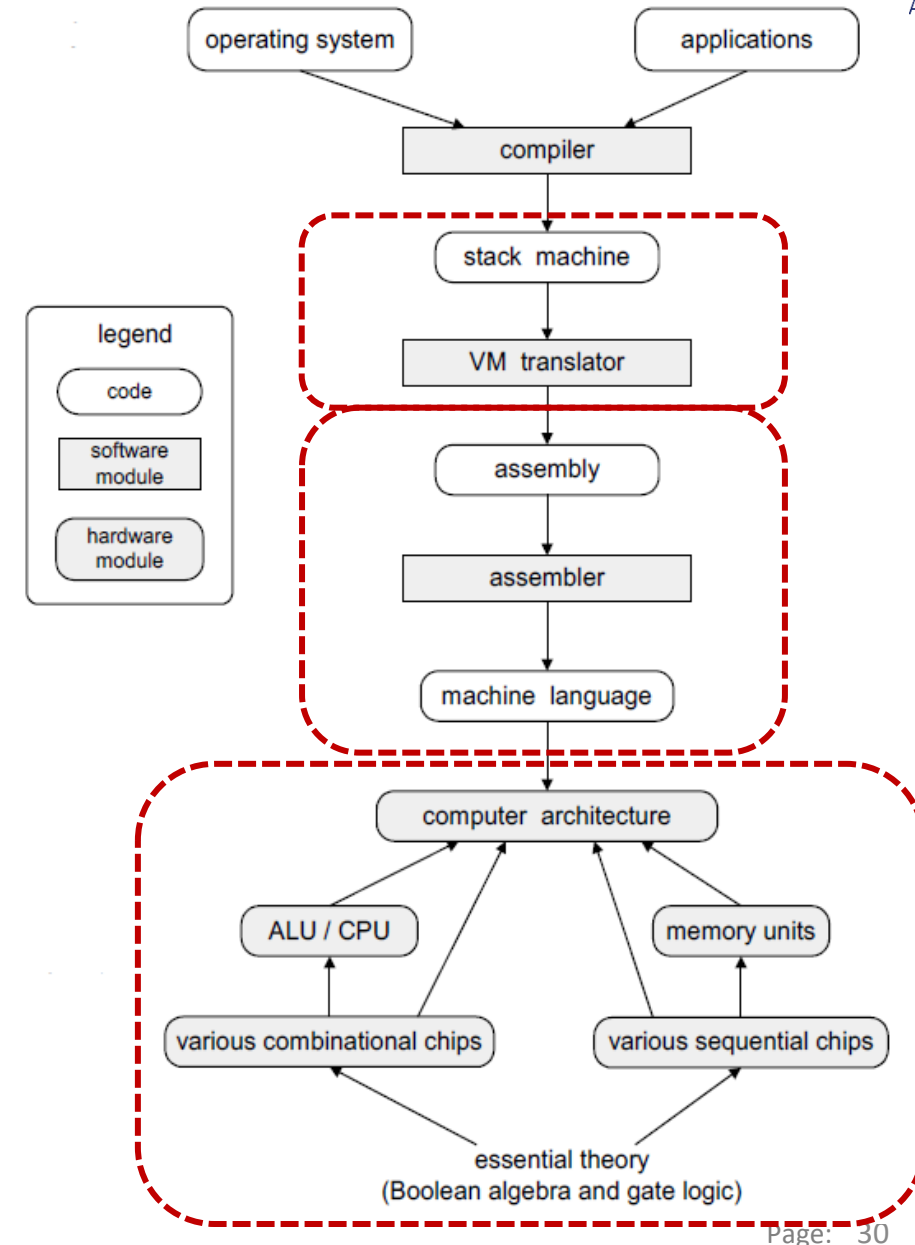
# What is this module about

- A **10-credit module**, bottom up approach to how computers work
- Starts at gate logic 11010101010110
- How to build simple computing devices
- How join these devices together
- Translations.....all the way up to high level systems
- Compilers, OS, Networking, High level languages taught elsewhere, will be only dealt with briefly here.



# Programming Languages

- Hardware Description Language (HDL)
  - Use to construct the hardware, e.g. chips and ALU
  - Hardware Simulator.
- Machine Language
  - Binary machine language
  - Assembly language (Hack assembly)
  - CPU Emulator.
- Virtual Machine Language
  - Stack machine mode
  - VM Emulator.



# Levels of Programming Code

- Compiler – A program that translates high-level language statements into assembly language statements
- Assembly language – A symbolic representation of machine instructions
- Assembler – A program that translates a symbolic version of instructions into the binary versions
- Machine language – A binary representation of machine instructions
- Instruction – A command that computer hardware understands and obeys

High-level  
language  
program  
(in C)

```
swap(int v[], int k)
{int temp;
  temp = v[k];
  v[k] = v[k+1];
  v[k+1] = temp;
}
```

Compiler

Assembly  
language  
program  
(for MIPS)

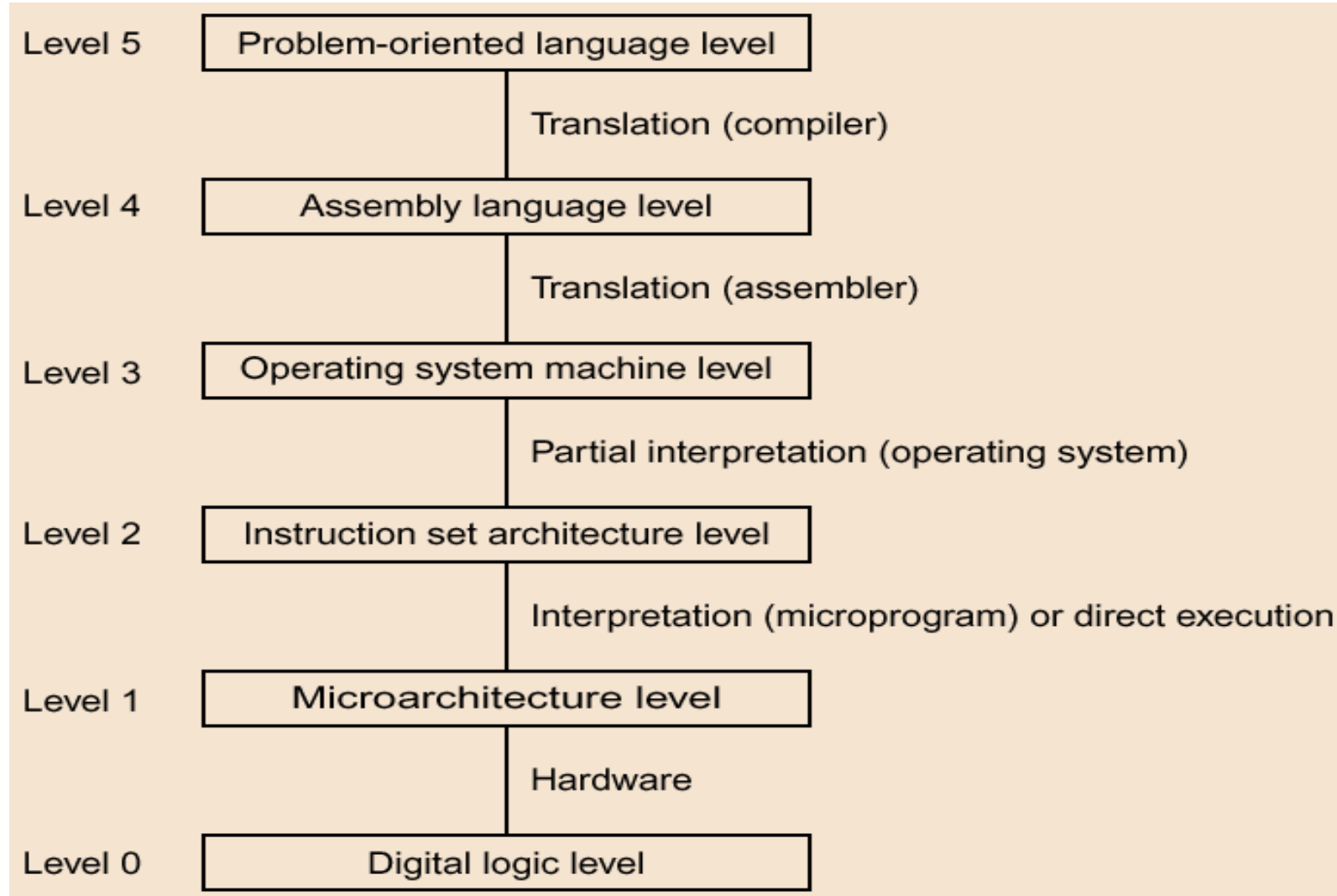
```
swap:
    multi $2, $5, 4
    add   $2, $4, $2
    lw    $15, 0($2)
    lw    $16, 4($2)
    sw    $16, 0($2)
    sw    $15, 4($2)
    jr    $31
```

Assembler

Binary machine  
language  
program  
(for MIPS)

```
000000001010001000000000100011000
00000000100000100001000000100001
10001101111000100000000000000000
100011100001001000000000000000100
101011100001001000000000000000000
101011011110001000000000000000100
000000111110000000000000000001000
```

# Computer Systems Hierarchy





# Learning Outcome

- To be able to understand simple assembly language programs.
- To understand the major components (especially hardware) which make up a computer system.
- To be able to program in assembly language.

# Content Plan (provisional!)

	Week beginning	Lecture	Lab
Week 1	13 <sup>th</sup> Sep	No lecture	No lab
Week 2	20 <sup>th</sup> Sep	Introduction to COMP1036	No lab
Week 3	27 <sup>th</sup> Sep	Boolean Logic	Download and test Nand2tetris tools ( <b>optional</b> )
Week 4	4 <sup>th</sup> Oct	No lecture	No lab
Week 5	11 <sup>th</sup> Oct	Boolean Arithmetic	Overview of the Hardware Description Language (HDL)
Week 6	18 <sup>th</sup> Oct	Sequential Logic and ALU	Combinational logic: using the logic gates
Week 7	25 <sup>th</sup> Oct	Memory and CSF History	Sequential logic/ALU
Week 8	1 <sup>st</sup> Nov	Machine Language	Machine language
Week 9	8 <sup>th</sup> Nov	Assembler	Coursework release
Week 10	15 <sup>th</sup> Nov	Virtual Machine I	Assembler: Basic language translation techniques
Week 11	22 <sup>th</sup> Nov	Virtual Machine II	Virtual machine: abstraction
Week 12	29 <sup>th</sup> Nov	Compilers and High level languages	Virtual machine: implementation
Week 13	6 <sup>th</sup> Dec	Revision	Coursework deadline
Week 14	13 <sup>th</sup> Dec	TBD	No Lab

# Why is this module important

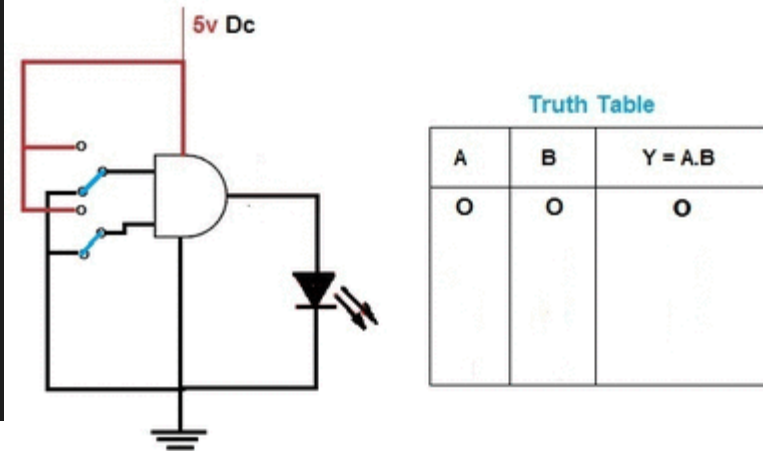
- Car analogy –
  - Most modules of CS degree is about how to ‘drive the car’ (Programming, DB, SE...)
    - how driving each car is different
    - how to drive it faster, safer, more efficiently
  - This module is how the car is **built** and even how to build the components that go into building the car
  - Not essential but most good drivers know how the car actually works



# Why CSF?

```
/* C Hello World program */
#include<stdio.h>

int main()
{
    printf("Hello World!\n");
    return 0;
}
```



- Programming is about giving a computer instructions that we want it to perform
- What happens when we run 'Hello World'? How does it make it appear on the screen?
- What happens when we press a key on the keyboard?

# What does it take to run a program

- **Program is just a set of dead characters in a text file**
- This text file needs to be parsed
- Re-expressed as a lower level language
  - Convert something human friendly into something human unfriendly
  - Produces another text file, containing machine level code
- This machine code realised by a hardware architecture
  - Hardware devices – registers, memory, ALU
- Hardware devices constructed using logic gates
- Logic gates constructed from primitive gates (eg. NAND)
- Primitive gates constructed from switching devices (eg. Transistors)
- ....Physics.....



# Understanding Performance

- The performance of a program depends on a combination of the effectiveness of hardware or software components
- Algorithm
  - Determines the number of I/O operations executed
- Programming language, compiler, architecture
  - Determine number of machine instructions executed per operation
- Processor and memory system
  - Determine how fast instructions can be executed
- I/O system (including OS)
  - Determines how fast I/O operations may be executed

# Why CSF?

- Long ago, every computer specialist had a complete, transparent understanding of the inner workings of computers, this has been somewhat lost – the computer is a black box even to many modern computer science students
- The best way to understand computers is to build one from scratch (transistors -> logic gates -> adders -> ALU and Registers -> CPU)
- Without seeing how the interaction of elementary components work will leave you with an uneasy feeling that you don't fully know what's going on inside computers

# Why CSF?

- Makes it possible to write computer programs that are faster and smaller
- Allows programmers to balance performance and relative cost of operations with appropriate programming choices
- Prepare for other CS courses, such as programming, compilers, operating systems, etc.



# Summary

- From logic gates to CPUs (in terms of hardware)
- From machine language to virtual machine, compiler and all the way to high-level program.
- From hardware all the way to high-level program (C/C++).

