Digital Design & Computer Architecture Edition 2

Week 1 – performance : Sec. 7.2

Week 2 – Arithmetic : Sec. 1.4, 5.2, 5.3

Weeks 3 & 4 – Assembly Programming : Sec. 6.2, 6.4, 6.7.1, 6.7.2, 6.7.4

Week 5 – MIPS ALU : Sec. 5.2.1 – 5.2.4, (5.4 if interested)

Week 5 – Instruction Set : Sec. 6.3

Week 6 – MIPS Single-Cycle CPU : Sec. 7.3.1 – 7.3.3, except j instruction

Week 7 – MIPS Pipelined CPU : Sec. 7.5.1 – 7.5.3, except j instruction

MIPS instructions specification – Appendix B

Doing research with me (incl. FYP) – Chapters 1-5, 7.6 – 7.8, Appendix A, besides all above.