# STM32 Programmer for the Multi-cycle OTTER

Use this guide to set up and use the STM32 programmer.

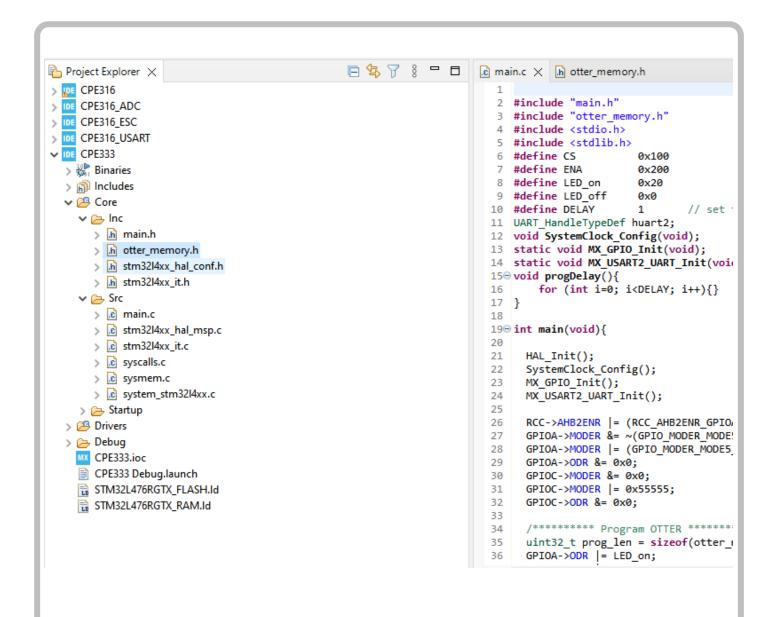
Source files found here:

https://github.com/geoneill12/OTTER\_Multicycle\_STM32\_Programmer

Download and install the STM32CubeIDE from the vendor's website:

https://www.st.com/en/development-tools/stm32cubeide.html

The board used for this project is the NUCLEO-L476RG board.



Create a new STM32 project. Create a C header file under the "Inc" folder. Call this file "otter\_memory.h".

In the "main.c" file, delete everything, and replace it with the contents of the "STM32programmer.c" file from github.

Use the following pinout chart to connect the Nucleoboard to the BASYS3 board using jumper wires. All connections, including Ground connections, must be wired exactly as below.

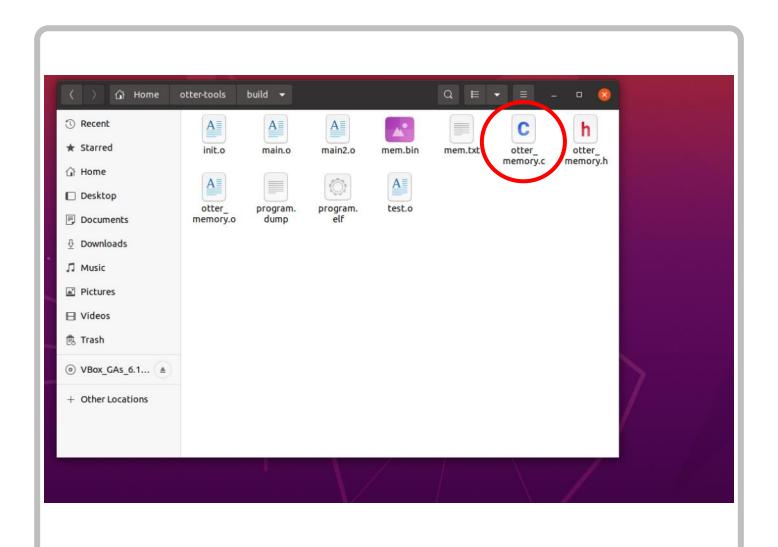
Nucleoboard		BASYS3
Pin#	Signal Name	Pin #
PC0	Data Bit 0	JB.1
PC1	Data Bit 1	JB.2
PC2	Data Bit 2	JB.3
PC3	Data Bit 3	JB.4
PC4	Data Bit 4	JB.7
PC5	Data Bit 5	JB.8
PC6	Data Bit 6	JB.9
PC7	Data Bit 7	JB.10
PC8	CS	JC.1
PC9	ENA	JC.2
GND	Ground	JB.11
GND	Ground	JC.11

Use the "OTTER\_STM32\_Programmer\_BBD.pdf" file to modify the Multi-cycle OTTER. Sample code for both the Wrapper and the OTTERMCU are provided to show how to integrate the STM32\_programmer module into the OTTER. The STM32\_programmer module can be thought of as a wrapper for the memory module. In normal mode, it just passed signals through to the memory as though it weren't there. In programmer mode, it takes control of all signals going to memory.

Refer to the "Basys3\_constraints.xdc" file to see how to connect the pmod ports to the OTTER. Pulldown resistors must be utilized on all ports that act as inputs.

In the "otter\_memory.mem" file in Vivado, fill each line with "00000013" (assembly code for "nop" instructions). Create several thousand lines of this (file provided on github). This step may not be necessary, however the programmer has not been tested without doing this.

Synthesize the design and load the new bitstream onto the BASYS3.



Start up the OTTER developer virtual machine. Copy the "otter\_memory.c" file from github into the "otter-tools/build" directory.

```
student@student-VirtualBox:-S cd otter-tools/build
student@student-VirtualBox:-S cd otter-tools/buildS is
init.o nain.o nen.txt otter_memory.h program.elf
main.o.o nen.bin otter_memory.c program.dump test.o
student@student-VirtualBox:-/otter-tools/buildS ls
init.o nain.o nen.txt otter_memory.c otter_memory.c -o otter_memory.o
student@student-VirtualBox:-/otter-tools/buildS ls
init.o nain.o nen.txt otter_memory.o program.dump test.o
main.o. nen.bin otter_memory.c program.elf
student@student-VirtualBox:-/otter-tools/buildS
```

Open a terminal window and navigate to the "otter-tools/build" directory. Use gcc to compile the "otter\_memory.c" program. Give it the name "otter\_memory.o" (not necessary, but recommended to keep things organized).

Everything is now set up and ready to begin programming the OTTER!

Edit your code.

```
student@student-VirtualBos:-S cd otter-tools/build student@student-VirtualBos:-S cd otter-tools/build student@student-VirtualBos:-/butter-tools/build is student@student-VirtualBos:-/butter-tools/build is init.o nanin.o nen.txt otter.menory.h program.elf nanin.o nen.but otter_menory.c program.elf nanin.o nen.but otter_menory.build is conter_menory.c otter_menory.o student@student-VirtualBos:-/butter-tools/build is conter_menory.build is conter_menory.o student@student-VirtualBos:-/butter-tools/build is conter_menory.build is conten_menory.build i
```

Run "make" to assemble your code.

```
student@student-VirtualBox:-5 cd otter-tools/builds is intit. o main. o men.txt otter.mons/builds is intit. o main. o men.txt otter.monsy. brogram.elf main. o men.txt otter.monsy. brogram.elf main. o men.txt otter.monsy. brogram.elf main. o men.txt otter.monsy. o men.txt ott
```

Navigate to the "build" directory and execute the "otter memory.o" file.

This action will create a new "otter\_memory.h" header file, with the contents of the "mem.txt" file placed into an array. Each line of the "mem.txt" file becomes an element of the array. If an "otter\_memory.h" file was already present, it will delete it first.

```
otter_memory.h
                                                                                                                                                                                                  Save ≡ _ □ 😢
 Open ▼ 📭
  2 #ifndef INC_OTTER_MEMORY_H_
3 #define INC_OTTER_MEMORY_H_
4
  5 uint32_t otter_memory[] = {
  7 0x00010117,
8 0xfe010113,
9 0x00000097,
10 0x010080e7,
11 0x0000006f,
12 0x00000000,
13 0×11000837,
14 0×110c05b7,
15 0x11080637,
16 0xfff00713,
17 0x00000097,
18 0x1c8080e7,
19 0x1f400fef,
20 0x00000097,
21 0x1bc080e7,
22 0x44400fef,
23 0x00000097,
                                                                                                                                                                                                                                                    nedany -0
24 0x1b0080e7,
25 0x66c00fef,
26 0x00000097,
27 0x1a4080e7,
28 0x79400fef,
29 0x00000097,
30 0x198080e7,
31 0x0c100fef,
32 0x00000097,
33 0x18c080e7,
34 0x1e900fef,
35 0x00000097,
36 0x180080e7,
37 0x48500fef,
38 0x00000097,
39 0x174080e7,
40 0x6e900fef,
41 0x00000097
                                                                                                                                              C/ObjC Header ▼ Tab Width: 8 ▼
                                                                                                                                                                                                          Ln 1, Col 1
                                                                                                                                                                                                                                       INS
```

Open the "otter\_memory.h" file in a text editor.

Select and copy the contents of the file using "CTRL+A" and "CTRL+C".

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Project Explorer X
                                                                                                                                             h otter_memory.h X
> [FE CPE316
                                                                                                                                   #ifndef INC_OTTER_MEMORY_H_
> IDE CPE316_ADC
                                                                                                                                   #define INC_OTTER_MEMORY_H_
> IDE CPE316_ESC
> IDE CPE316_USART

• IDE CPE333
                                                                                                                                   uint32_t otter_memory[] = {
    > 🐉 Binaries
                                                                                                                                   0x00010117.
                                                                                                                                   0xfe010113,
    > 👸 Includes
                                                                                                                                   0x00000097
    10 0x010080e7.
        🗸 🗁 Inc
                                                                                                                            11 0x0000006f
           > h main.h
                                                                                                                            12 0x00000000,
            > h otter_memory.h
                                                                                                                            13 0x11000837,
                                                                                                                            14 0x110c05b7,
15 0x11080637,
            > lh stm32l4xx hal conf.h
            > lin stm32l4xx_it.h
        🗸 📂 Src
                                                                                                                            17 0x00000097,
            > 🖟 main.c
                                                                                                                            18 0x1c8080e7,
            > c stm32l4xx_hal_msp.c
                                                                                                                            19 0x1f400fef,
                                                                                                                            20 0x00000097.
           > c stm32l4xx_it.c
                                                                                                                            21 0x1bc080e7,
           > 🖟 syscalls.c
                                                                                                                            22 0x44400fef
           > c sysmem.c
                                                                                                                            23 0x00000097,
            > 🖟 system_stm32l4xx.c
                                                                                                                            24 0x1b0080e7,
        > 🗁 Startup
                                                                                                                            25 0x66c00fef.
                                                                                                                            26 0x00000097,
    > 🕮 Drivers
                                                                                                                            27 0x1a4080e7,
    > 🗁 Debug
                                                                                                                            28 0x79400fef,
        MX CPE333.ioc
        CPE333 Debug.launch
                                                                                                                            30 0x198080e7
       STM32L476RGTX_FLASH.Id
                                                                                                                            31 0x0c100fef,
                                                                                                                                  0x00000097,
       STM32L476RGTX_RAM.Id
                                                                                                                            33 0x18c080e7,
                                                                                                                            35 0x00000097.
                                                                                                                            36 0x180080e7,
                                                                                                                       🦹 Problems 🔊 Tasks 📮 Console 🗶 🔲 Properties
                                                                                                                      <terminated> CPE333 Debug [STM32 Cortex-M C/C++ Application] ST-LINK (ST-LINK GDB server) (Terminated Jun 10
                                                                                                                      Download in Progress:
```

In the STM32CubeIDE, delete all the contents of the "otter\_memory.h" file ("CTRL+A" and "delete"), then paste in the contents of the "otter\_memory.h" file from the virtual machine ("CTRL+V"). Save the header file.

Press the green play button to run the project and upload it to the Nucleoboard. Once uploaded to the Nucleoboard, it will immediately program the OTTER. Pressing the black reset button on the Nucleoboard will cause it to program the OTTER again.

```
c main.c X
            .h otter_memory.h
  1
  2 #include "main.h"
  3 #include "otter memory.h"
  4 #include <stdio.h>
  5 #include <stdlib.h>
  6 #define CS
                         0x100
  7 #define ENA
                         0x200
  8 #define LED on
                         0x20
  9 #define LED off
                         0x6
 10 #define DELAY
                                 // set to 1 for 50MHz OTTER; 7 gives ~1ms delay
 11 UART HandleTypeDef
 12 void SystemClock_Config(void);
 13 static void MX_GPIO_Init(void);
 14 static void MX_USART2_UART_Init(void);
 15⊖ void progDelay(){
 16
         for (int i=0; i<DELAY; i++){}</pre>
 17 }
```

### **Notes**

The code on github is designed to run the Nucleoboard at 80MHz, and the OTTER at 50MHz. If the OTTER's clock rate is reduced below 50MHz, then the programmer on the Nucleoboard must be slowed down as well for the OTTER to "see" the changing signals. To do this, increase the "DELAY" value (circled above). A value of 1 works for a 50MHz OTTER. Ratio this value up in proportion to however much the OTTER clock rate is slowed down (ex: set DELAY to 10 for an OTTER clock rate of 5MHz). This may require some trial and error.

# How the STM32\_programmer.sv module works.

The programming module is an FSM with 6 states:

- **State 0**: Programmer passes signals through to memory. If it sees the CS line go high, it goes to state 1. Else, it stays in State 0.
- State 1: Programmer holds all the memRead/memWrite lines at 0. It sets the size and sign signals to 0 for loading data into memory 1 byte at a time. It passes data from the Nucleoboard directly to the "din2" port of memory (preceded by 24 zeros). It keeps track of the address using a register. This register starts at 0, and increments by 1 each time a write event occurs. The programmer remains in State 1 until ENA goes high, at which point it goes to State 2.
- **State 2**: Programmer writes the data to memory. It stays in State 2 until ENA goes low, at which point it goes to State 3.
- <u>State 3</u>: Programmer increments the address register by 1. It then jumps unconditionally to State 4.
- State 4: Programmer waits to see if more data is coming. If CS stays high and ENA stays low, the programmer stays in State 4. If ENA goes high again, more data is coming, so the programmer jumps back to State 2 to repeat the write process. This continues until CS goes low while in State 4, which only happens when there is no more data. When this happens, the programmer jumps to State 5.
- State 5: The programmer resets the internal address register to 0, so that the OTTER can be programmed again. A reset signal is also sent to the Program Counter, Control Unit FSM, and Control Status Registers to restart to OTTER. The programmer then jumps unconditionally back to State 0. At the start of the next clock cycle, the OTTER re-starts at instruction 0.

# How the STM32\_programmer.c program works.

The C file that runs on the Nucleoboard has 3 main steps:

- 1. Set CS line high to tell the OTTER it's about to receive program data.
- 2. Enters a programming loop that repeats for each array element:
  - a) Send lowest 8 bits to OTTER.
  - b) Set ENA high, then low.
  - c) Shift right 8 bits, send lowest 8 bits to OTTER.
  - d) Set ENA high, then low.
  - e) Shift right 8 bits, send lowest 8 bits to OTTER.
  - f) Set ENA high, then low.
  - g) Shift right 8 bits, send lowest 8 bits to OTTER.
  - h) Set ENA high, then low.
  - i) Go back to start of the loop. Repeat this process for as many array elements (instructions) that are in the "otter memory.h" file.
- 3. Reset CS line to low to tell the OTTER that no more data is coming.

**NOTE:** A delay is placed in between each event to allow the OTTER enough time to "see" each signal change.

# How the otter\_memory.c program works.

The C file that creates the "otter\_memory.h" header file first deletes the "otter\_memory.h" file (if already present), creates a new file, and then reads the contents of the "mem.txt" file that is automatically generated using the "make" command. It reads this file line by line and places each instruction into an array with a "0x" prefix.

### Video Demo:

# https://www.youtube.com/watch?v=vsKfYsN15Cc

The above demo shows the OTTER being programmed with the standard "test-all" program. It shows the complete process, from editing the code in the virtual machine, all the way to building the project and loading it onto the Nucleoboard. The video also shows that the OTTER can be programmed again using the reset button.

**Note:** In this demo, the OTTER is slightly modified so that when SWITCH15 is set high, the OTTER runs at 500Hz, and when it is set low, it runs at 50MHz. This is to allow the OTTER to be programmed quickly at 50MHz, while also giving us the option to slow down the OTTER to see the test-all program running at a slower clock rate.

# Summary:

This programmer achieves extremely fast data transfer rates due to have 8 parallel data lines. Using an oscilloscope, the programmer was timed programming a 50MHz OTTER at a maximum speed of 1.65 MB/s (bytes!) with the delay function commented out of the "main.c" file. With the delay function left in, and given a "DELAY" value of 1, a 50MHz OTTER can be programmed at 573 KB/s.

Happy programming!