

George Bisbas

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Professional Summary

Researcher and developer with deep expertise in high-performance computing (HPC), compiler design, and parallel programming. Proven track record in scalable code generation, performance engineering, and open-source leadership. Experienced in cross-disciplinary collaboration and mentoring. Seeking impactful roles in HPC software engineering or compiler development.

Technical Skills

Programming: Python, C, C++, MPI, OpenMP, CUDA, SYCL, OpenACC, Matlab (Expert); Julia, Java, Cilk, AVR/MIPS Assembly, Cerebras CSL (Proficient)

HPC Tooling: Intel VTune, Advisor, Trace Analyzer & Collector, NVIDIA Nsight (Compute, Systems), Likwid

Job Scheduling: Slurm, PBS

Compiler/IR: MLIR, LLVM (familiar), Devito DSL

Containerization: Docker

Cloud: Microsoft Azure

Education

PhD, High Performance, Embedded and Distributed Systems, Imperial College London 2023

- Thesis [[Online](#)]: *Automated cache optimizations of stencil computations for partial differential equations*
- Supervisor: Prof. P. H.J. Kelly (with F. Luporini, G. J. Gorman)

MSc, Intelligent Systems/Methods of Computational Intelligence and Applications, Aristotle University of Thessaloniki, Greece 2019

- Grade: 9.59/10 (Distinction)
- Thesis [[Online \(GR\)](#)]: *On developing and accelerating point cloud simplification methods*
- Supervisor: Prof. N. P. Pitsianis

Diploma of Engineering, Electrical and Computer Engineering, Aristotle University of Thessaloniki, Greece 2017

- Grade: 8.75/10 (Distinction)
- Thesis [[Online \(GR\)](#)]: *Forecast demand using Extended Discrete Fourier Transform*
- Supervisor: Prof. N. P. Pitsianis

Professional Experience

Post-Doctoral Research Assistant, Imperial College London May 2022 – Jun 2023
Post-Doctoral Research Associate, Imperial College London Jun 2023 – Mar 2025

- XDSL/Devito Project, led research on domain-specific languages, HPC, compiler IRs, and automatic code generation, contributed to and reviewed code for Devito and xDSL, focusing on scalable parallelism and performance.

Researcher/Developer, Imperial College London Oct 2018 – Mar 2025

- Devito Project, developed and optimized high-performance stencil computation kernels, focusing on automating cache-blocking related optimizations, implemented loop nest and cache related optimizations, contributed to and reviewed code for Devito, focusing on scalable parallelism and performance.

Research Assistant, Aristotle University of Thessaloniki Nov 2017 – Oct 2018

- DigiPro Project

- Developed efficient algorithms in MATLAB and C/Cilk for point cloud simplification to create a novel, cost-efficient, portable solution for photo-realistic 3D digitization of rigid objects.

Teaching Experience

Graduate Teaching Assistant , Imperial College London	Nov 2018 – Oct 2022
Coursework preparation, lecture support, marking for:	
• (ACSE-6) Parallel Programming using the Message Passing Interface (MPI)	Jan 2021 - Mar 2021
• (COMP60001) Advanced Computer Architecture	Nov 2018 - Jan 2021
• Second Year Laboratory program (C++ Picture Processing/Pintos)	Nov 2018 - Jun 2019
• (COMP50006) Compilers	Jan 2021 - Jan 2022
• (COMP60017) Performance Engineering	Jan 2021 - Mar 2021
• (COMP40006) Reasoning about Programs	Jan 2021 - Mar 2021
DiRAC Training Course Mentor , University College London, DiRAC HPC Facility, Department of Physics & Astronomy DiRAC Training Course Mentor (20 hours)	Nov 2023
• Foundation HPC-Skills course: FM01 Bash Shell: Using the Command Line (4 hours), FM02 Version Control with Git (4 hours), FM03 Principles of Software Engineering (4 hours), FM04 Testing, Documenting, and Reviewing Code (4 hours), FM05 Principles of Code Scaling (4 hours)	

Publications

Conference Publications

- **George Bisbas**, Rhodri Nelson, Mathias Louboutin, Fabio Luporini, Paul H.J. Kelly, Gerard Gorman (2024). Automated MPI-X code generation for scalable finite-difference solvers.
[\[Accepted to IPDPS'25 - Available on Arxiv\]](#) – Finalist for IPDPS'25 Open Source Contribution Award
- **George Bisbas***, Anton Lydike*, Emilien Bauer*, Nick Brown*, Mathieu Fehr, Lawrence Mitchell, Gabriel Rodriguez-Canal, Maurice Jameson, Paul H.J. Kelly, Michel Steuwer, Tobias Grosser (2023). A shared compilation stack for distributed-memory parallelism in stencil DSLs. (2024). In Proceedings of the 29th ACM International Conference on Architectural Support for Programming Languages and Operating Systems, Volume 3 (ASPLOS '24), Vol. 3. Association for Computing Machinery, New York, NY, USA, 38–56.
[\[Paper available online\]](#)
*equal contribution
- **George Bisbas**, Fabio Luporini, Mathias Louboutin, Rhodri Nelson, George Bisbas, Gerard Gorman, Paul H.J. Kelly. 2020. Temporal blocking of finite-difference stencil operators with sparse "off-the-grid" sources. (2020). In 35th IEEE International Parallel & Distributed Processing Symposium (IPDPS), Portland, OR, USA, 2021, pp. 497-506.
[\[Paper available online\]](#)

Workshop publications

- Joao Speglitch, Navjot Kukreja, **George Bisbas**, Atila Saraiva, Jan Hückelheim, Fabio Luporini, John Washbourne (2024). Optimizing wavefield storage with high-speed media. In ESSA'24, IEEE International Parallel and Distributed Processing Symposium Workshops (IPDPSW)
[\[Paper available online\]](#)

In preparation/Submitted

- Mathias Louboutin, Fabio Luporini, Philipp Witte, Rhodri Nelson, **George Bisbas**, Jan Thorbecke, Felix J. Herrmann, and Gerard Gorman. 2020. Scaling through abstractions – high-performance vectorial wave simulations for seismic inversion with Devito. (2020).
[\[Available on Arxiv\]](#)

- Rhodri Nelson, Fabio Luporini, Mathias Louboutin, **George Bisbas**, Gerard Gorman (2020). TheMatrix: An automated cross-platform benchmarking suite. Submitted to The Journal of Open Source Software
[\[Available on Github\]](#)

Talks and Presentations

- **G. Bisbas**, A. Lydike, E. Bauer, N. Brown, M. Fehr, P. H.J. Kelly, T. Grosser *A shared compilation stack for distributed-memory parallelism in stencil DSLs.* (2024). Presented at HiPEAC 2025, Barcelona, Spain
[\[Slides\]](#)
- **G. Bisbas**, A. Lydike, E. Bauer, N. Brown, M. Fehr, P. H.J. Kelly, T. Grosser *A shared compilation stack for distributed-memory parallelism in stencil DSLs.* (2024). Presented at 29th ACM International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS '24)
[\[Slides\]](#)
- **G. Bisbas**, A. Lydike, E. Bauer, N. Brown, M. Fehr, P. H.J. Kelly, T. Grosser *A shared compilation stack for HPC stencil DSLs*, Presented at PASC24, Minisymposium: Motif-Based Automated Performance Engineering for HPC.
[\[Slides\]](#)
- F. Luporini, M. Louboutin, R. Nelson, **G. Bisbas**, E. Caunt, P.H.J. Kelly, G. Gorman *The Devito DSL and Compiler Framework: From Symbolic PDEs to HPC Code*, Presented at PDE Simulations with High-Productivity Languages at the Dawn of Exascale MiniSymposium, SIAM CSE 2023 conference.
[\[Slides\]](#)
- **G. Bisbas**, F. Luporini, M. Louboutin, R. Nelson, G. Gorman, P.H.J. Kelly *Automated Temporal Blocking in the Devito Compiler*, Presented at Stencil Computation for Scientific Applications MiniSymposium, SIAM CSE 2023 conference.
[\[Slides\]](#)
- **G. Bisbas**, F. Luporini, M. Louboutin, R. Nelson, G. Gorman, P.H.J. Kelly *Temporal blocking for wave propagation with sparse off-the-grid sources* Presented at Rice Oil and Gas HPC (OGHPC 2021) conference.
[\[Available: Youtube\]](#)
- **G. Bisbas**, F. Luporini, M. Louboutin, R. Nelson, G. Gorman, P.H.J. Kelly *Temporal blocking of finite-difference stencil operators with sparse “off-the-grid” sources* Presented at 21st Workshop on Compilers for Parallel Computing (CPC21, Porto) conference.
[\[Slides\]](#)
- F. Luporini, R. Nelson, M. Louboutin, **G. Bisbas**, E. Caunt, G. Gorman *Devito: A DSL and compiler for automated generation of production-grade wave propagators*, Presented at Domain-Specific Languages in High-Performance Computing 2020.
[\[Available: Youtube\]](#)
- **G. Bisbas**, F. Luporini, M. Louboutin, R. Nelson, G. Gorman, P.H.J. Kelly *Temporal blocking of finite-difference stencil operators with sparse non-grid-aligned sources and receivers in Devito*, Presented at Domain-Specific Languages in High-Performance Computing 2020.
[\[Available: Youtube\]](#)
- F. Luporini, R. Nelson, M. Louboutin, N. Kukreja, **G. Bisbas**, P. Witte, Amik St-Cyr, C. Yount, T. Burgess, F. Herrmann, G. Gorman *Automatic Generation of Production-Grade Hybrid MPI-OpenMP Parallel Wave Propagators using Devito*, Presented at Platform for Advanced Scientific Computing (PASC 2019) Conference.
[\[Slides\]](#)

Poster Presentations

- **G. Bisbas**, A. Lydike, E. Bauer, N. Brown, M. Fehr, L. Mitchell, G. Rodriguez-Canal, M. Jameson, P. H.J. Kelly, M. Steuwer, T. Grosser *A shared compilation stack for distributed-memory parallelism in stencil DSLs* Poster presented at 29th ACM International Conference on Architectural Support for Programming Languages and Operating Systems, Volume 3 (ASPLOS '24).
[\[Poster\]](#)

- **G. Bisbas**, R. Nelson, M. Louboutin, F. Luporini, P. H.J. Kelly, G. Gorman *Automated MPI-X code generation for scalable finite-difference solvers* Poster referring to [\[Accepted to IPDPS'25 - Available on Arxiv\]](#).
[Poster]
- **G. Bisbas**, R. Nelson, M. Louboutin, P. H.J. Kelly, F. Luporini, G. Gorman *Automated MPI-X code generation for scalable finite-difference solvers* Poster presented at Rice Energy HPC 2024.
[Poster]
- **G. Bisbas**, F. Luporini, M. Louboutin, G. Gorman, P.H.J. Kelly *Accelerating real-world stencil computations using temporal blocking: handling sparse sources and receivers* Poster presented at the International Conference for High Performance Computing, Networking, Storage, and Analysis (SC 2019).
[Poster]

Selected contributions to open-source projects

- My merged PRs in <https://github.com/devitocodes/devito>
- Lead Maintainer/Developer in <https://github.com/xdslproject/devito>
- My merged PRs in <https://github.com/xdslproject/xdsl>

Peer reviewing

Conferences

- Supercomputing conference, Reproducibility committee (SC 25, SC 24)
- 31st International European Conference on Parallel and Distributed Processing (Euro-Par 2025), Committee Member
- International Conference on Parallel Processing (ICPP 2024), Poster Committee Member
- JuliaCon Conference Proceedings [[Reviews available here](#)]
- International Workshop on Polyhedral Compilation Techniques (IMPACT 2024)
- International Conference on Parallel Processing (ICPP 2021)
- PPoPP 2020 Artifact Evaluation Committee
- JupyterCon 2020 Proposal Community Review

Journals

- The Journal of Supercomputing, Springer
- Future Generation Computer Systems, Elsevier

Student supervision

MEng individual project (Imperial college)

- **N. Duer**, *Temporal Tiling for Distributed Parallel Solution of Partial Differential Equations*, Department of Computing, Imperial College London, 2023, in collaboration with P. H.J. Kelly. [\[Thesis Online\]](#)
Distinguished undergraduate project award for 2022-2023 cohort.

Honours and awards

- Finalist, IPDPS'25 Open Source Contribution Award
- Invited to ACM Student Research Competition and awarded the SRC Travel Award (500\$)
- PhD student position, fully funded by a joint HiPEDS/DoC scholarship
- Scholarship for achieving the best grade in the 1st semester of MSc in Advanced Computer and Communication Systems (650 Euros)
- Eurobank Ergasias monetary prize for excellence in Panhellenic exams (1000 Euros)

Certifications on Online Courses (selected)

- Workshop/Fundamentals of Deep Learning, earned on September 26, 2024 [[Show credential](#)]
- Workshop/Fundamentals of Accelerated Computing with CUDA C/C++, earned on June 26, 2024 [[Show credential](#)]

Memberships

- SIAM Early Career Membership, #020900949
- IEEE Student Member, #93014477 Greece Section
- ACM Professional Member, #1563304
- Technical Chamber of Greece, Member

References

[Paul HJ Kelly](#), Faculty of Engineering, Department of Computing, Imperial College London
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[Gerard Gorman](#), Faculty of Engineering, Department of Earth Science & Engineering, Imperial College London
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[Nikos Pitsianis](#), Electrical and Computer Engineering, Aristotle University of Thessaloniki
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