Po-Wei (George) HUANG

📞 <u>+65 8891 3219</u> | 💌 <u>huangpowei22@u.nus.edu</u> | 🛅 <u>huangpowei</u> | 🕥 georgepwhuang | ♦ georgepwhuang.github.io

EDUCATION

National University of Singapore

Aug 2020 - Jun 2023 (Exp.)

BComp (Hons.) in Computer Science with Second Major in Mathematics (CAP 4.80/5.00)

- Turing Programme (Honours Research Specialization Program: Programme Details)
- Study Focus Area: Algorithm and Theory / Artificial Intelligence
- Coursework: Design and Analysis of Algorithms (A^+) , Information Theory (A^+) , Quantum Computing (A^-) , Quantum Mechanics I (A), Randomized Algorithms (A), CS Research Methodology (A^-) , Artificial Intelligence (A), Machine Learning (A^-) , Optimization Algorithms, Algorithms at Scale, Algorithm Mechanism Design, Quantum Mechanics II

Nanyang Technological University

Aug 2019 - May 2020

BEng/BBus Double Degree in Business and Computing (CAP 4.91/5.00(CS) 4.83/5.00(Business))

• Coursework: Computational Thinking (A^+) , Data Structures (A^+) , Data Science and AI (A^+)

RESEARCH EXPERIENCE

Hybrid Classical-Quantum Neural Networks

Aug 2022 - Present

Supervisor: Asst. Prof. Patrick Rebentrost, Professor Rahul Jain (Centre for Quantum Technologies)

- Proposed and designed "post-variational" regression and multilayer perceptron models for quantum neurons.
- Analyzed and optimized quantum error propagation and amplifications for both online and offline algorithms.
- Aiming to explore the quantum advantages of the "post-variational" model.

Neural Logical Structure Recovery in Scholarly Articles

Apr 2021 - Jul 2022

Supervisor: Assoc. Prof. Min-Yen Kan (NUS WEB IR/NLP GROUP)

- Optimized logical structure recovery model performance by 10% for Marco-F1 against state-of-the-art model.
- Adapted sliding attention framework reducing computation cost from $O(n^2)$ to O(n).
- Applied deep semi-supervised learning techniques to increase model robustness to out-of-domain data.

Synthesis of Bio-Carbon and its Applications on Electric Components

Aug 2018 - May 2019

Supervisor: Professor Hung-Ping Lin (NATIONAL CHENG-KUNG UNIVERSITY)

- Trial tested porous bio-carbon synthesis process to replace graphene-based electric double-layer capacitors.
- Developed Arduino-controlled capacitance measurement system for super-capacitors.

PUBLICATIONS

Po-Wei Huang (2022). <u>Domain Specific Augmentations as Low Cost Teachers for Large Students</u>. *Proceedings of the First Workshop on Information Extraction from Scientific Publications (WIESP@AACL-IJCNLP2022)*.

Po-Wei Huang, Abhinav Ramesh Kashyap, Yanxia Qin, Yajing Yang, and Min-Yen Kan (2022). <u>Lightweight Contextual Logical Structure Recovery</u>. Proceedings of the Third Workshop on Scholarly Document Processing (SDP@COLING2022).

ACHIEVEMENTS AND AWARDS

Top Students for Design and Analysis of Algorithms

Jan 2022

Dean's List, AY 2020/2021 Semester 2

Jun 2021

Dean's List, AY 2020/2021 Semester 1

Feb 2021

TEACHING EXPERIENCE

NUS School of Computing

Jan 2021 - Present

Teaching Assistant (DATA STRUCTURES AND ALGORITHMS)

- Provided algorithm design consultation and pseudocode fine-tuning for 150+ students over 6 terms.
- Designed lab materials for Java programming and data structure applications.
- Wrote automatic student code collector for easier plagiarism detection and grading.

WORK EXPERIENCE

OpenRead Mar 2022 - Sep 2022

NLP Engineer (JAVA, PYTHON, PYTORCH)

- Constructed an inference engine for table and figure extraction using vision models from scientific articles.
- Developed document reconstruction program for PDF files using multimodal ensemble neural networks.
- Assembled summarization pipeline for long scholarly documents.

Continental Automotive Singapore

May 2022 - Jul 2022

Software Engineer Intern (PYTHON, SQL, BATCH SCRIPTING)

- Developed an internal tool to track and defer recurring issues on a static code analysis platform with the purpose of reducing manual time.
- Designed heuristic-based algorithm for string matching for issue detection.
- Participated in Agile ceremonies and familiarized Agile workflows.

Taiwan Semiconductor Manufacturing Company (TSMC)

Jul 2021 - Sep 2021

IT Intern (Equipment Edge Computing Team) (JAVA, KUBERNETES, NOSQL)

- Facilitated database transfer from SQL to NoSQL increasing read/write access speed by 10x.
- Created Spring-based backend of the existing dashboard to streamline database accessing procedures.
- Deployed cluster-balanced Cassandra database, reducing reliance on external data services.
- Adapted Prometheus and Grafana interface for easy monitoring of Kubernetes cluster health status.

PROJECTS

Optimized Logical Structure Extraction Network [Python]

- Provided easy-to-access pre-trained deep learning models for logical structure extraction.
- Developed logical structure recovery machine learning pipeline for production usage.

TSMCTalkTalk Discussion Board (Hosted Sample App) [HTML, PYTHON, DJANGO]

- Built a Django-based discussion forum to simulate technical exchanges.
- Designed and implemented discussion thread anonymity and archival functionality.
- Utilized Docker technology for rapid deployment to the in-house Kubernetes platform ensuring data privacy.

Link.me (Project Repo) [JAVA, JAVAFX]

- Built client contact information and meeting schedule management platform with notification features.
- Designed graphic user interface with compatibility for feature expansion.
- Wrote unit tests (JUnit) and maintained developer documentation for all features.

SKILLS

Spoken Languages: Chinese (native), English (professional working proficiency)

Programming Languages: C/C++, Java, Python, SQL, R

Software Frameworks/IT Skills: Arduino, Cassandra, Docker, Django, Git, Gradle, Grafana, Kubernetes, LaTeX, Linux/Unix, Maven, MySQL, NumPy, Pandas, PostgreSQL, Prometheus, PyTorch, Qiskit, Scikit-Learn, SciPy, Spring, Tableau