# **Supply Chain Explorer**

#### By the Emerging Technology Observatory

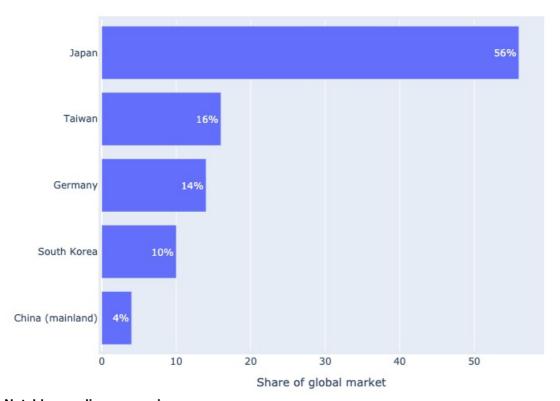
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## Wafer

Silicon wafers are the basic building block for chip production. To produce them, a furnace forms a cylinder of silicon (or other semiconducting materials), which is then cut into disc-shaped wafers. These wafers are then processed, split and packaged into individual chips. Most wafers are made purely of silicon or another material, but others have more complex structures. Dopants, such as boron, aluminum, phosphorous, platinum or other elements, may be added to alter the level of semiconductivity.

300 mm wafers, produced by Japanese, Taiwanese, German, and Korean firms, are used to produce nearly all advanced chips today. Manufacturing the smoothest, high-purity 300 mm wafers-which are necessary for supporting the smallest chip feature sizes-requires considerable tacit know-how. Older-technology wafer sizes include 150 and 200 mm, used for chips with larger feature sizes.

### **Country provision**



## Notable supplier companies

- GlobalWafers Taiwan
- Gritek (negligible market share) China (mainland)
- JRH (negligible market share) China (mainland)
- MCL (negligible market share) China (mainland)
- Okmetic (negligible market share) China (mainland)
- POSHING (negligible market share) China (mainland)
- SK Siltron South Korea
- SUMCO Japan

- Shin-Etsu Japan
- Siltronic Germany
- Simgui (negligible market share) China (mainland)
- Zhonghuan Semiconductor (negligible market share) China (mainland)
- ZingSEMI (negligible market share) China (mainland)