NAIRR Pilot Compute Calculations

We made the following calculations to estimate the compute available through the NAIRR pilot, which encompasses six Federal HPC resources. This includes calculations to (a.) estimate the FLOPS of CPUs, (b.) convert GPU FLOPS to FP16, and (c.) aggregate GPU, CPU, and ASIC FLOPS across nodes. Information on the hardware for each NAIRR pilot resource was sourced primarily from the NAIRR website,[1] information used to convert all FLOPS to FP16 was sourced primarily from TechPowerUp,[2] and information on CPU specifications was sourced from the developers' websites (i.e., Intel and AMD's websites). Note that FP16 FLOPS for some resources were directly provided by sources, and in these cases we did not perform calculations separately.

Delta Resource[3]

GPUs:

1. NVIDIA A100 40GB HBM2e[4] [tensor FP16 without sparsity] = 312TF
    a. [400 NVIDIA A100 40GB HBM2] 400 * 312TF = 124800TF
2. NVIDIA A40 48GB GDDR6 PCIe 4.0[5] = 37.42TF
    a. [400 NVIDIA A40 48GB GDDR6 PCIe 4.0] 400 * 37.42 = 14968TF
3. NVIDIA A100 GPUs[6] [tensor FP16 without sparsity = 312TF
    a. [40 NVIDIA A100 GPUs (assuming 40GB HBM2)] 40 * 312TF= 12480TF
4. AMD MI100[7] = 184.6TF
    a. [8 AMD MI100] 184.6 * 8 = 1476.8TF
5. Total GPU FLOPS: 124800TF + 14968TF + 12480TF + 1476.8TF = 153724.8TF = 153.7248PF

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[1] https://nairrpilot.org/allocations

[2] https://www.techpowerup.com/gpu-specs/

[3] https://www.ncsa.illinois.edu/research/project-highlights/delta/

[4] https://www.nvidia.com/content/dam/en-zz/Solutions/Data-Center/a100/pdf/nvidia-a100-datasheet-nvidia-us-2188504-web.pdf

[5] https://www.techpowerup.com/gpu-specs/a40-pcie.c3700#:~:text=NVIDIA%20has%20paired%2048%20GB,MHz%20(14.5%20Gbps%20effective)

[6] https://www.nvidia.com/content/dam/en-zz/Solutions/Data-Center/a100/pdf/nvidia-a100-datasheet-nvidia-us-2188504-web.pdf

[7] https://www.techpowerup.com/gpu-specs/radeon-instinct-mi100.c3496

CPUs: 200 AMD EPYC 7763 (“Milan”) CPUs[8] with 64-cores/socket (64-cores/node) at 2.55GHz and 256GB of DDR4-3200 RAM

6. AVX 256 [bits] / 16 [FP16] * 2 [multiple and add; 2 operations] * 2 [two AVX 256 blocks per core] * 64 [64 cores per CPU] * 2.55 Ghz = 10444.8 GF/node
    a. 10444.8 GF/node * 200 [total CPUs] = 2088960GF = 2.08896PF

Total FLOPS:

7. 153.7248PF [from GPUs] + 2.08896PF [from CPUs] = 155.81376PF

Frontera Resource[9]

GPUs:

1. The 90 nodes with NVIDIA Quadro RTX 5000 GPUs can do 4PF in FP32 in total.[10] Based on conversions using the FP32/FP16 numbers here,[11] this would be 8.028PF.
    a. Note that we used non-tensor FP16 FLOPS because the Quadro RTX 5000 specs sheet does not indicate the precision of the tensor performance.[12]

CPUs:

2. Intel® Xeon® Platinum 8280 Processors[13]
    a. AVX 512 [bits] / 16 [FP16] * 2 [multiple and add; 2 operations] * 2 [2 AVX 512 blocks per core] * 28 [28 cores per CPU] * 2.7 Ghz = 9676.8GF
        i. 9676.8GF * 2 [2 CPUs per node][14] = 19353.6 GF/node
        ii. 19353.6 GF/node * 8368 [total nodes] = 161950924.8GF = 161.95PF
3. Intel Xeon CPU E5-2620 v4 Processors[15]
    a. 256 [bits] / 16 [FP16] * 2 [multiple and add; 2 operations] * 2 [blocks per core] * 8 [8 cores per CPU] * 2.1 GHz = 1075.2GF/CPU
    b. 1075.2GF/CPU * 360 [360 CPUs] = 387072GF = 387.072TF = 0.387PF

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[8] https://www.amd.com/en/products/cpu/amd-epyc-7763
[9] https://tacc.utexas.edu/systems/frontera/
[10] https://tacc.utexas.edu/systems/frontera/
[11] https://www.techpowerup.com/gpu-specs/quadro-rtx-5000.c3308
[12] https://www.nvidia.com/content/dam/en-zz/Solutions/design-visualization/quadro-product-literature/quadro-rtx-5000-data-sheet-us-nvidia-704120-r4-web.pdf
[13] https://ark.intel.com/content/www/us/en/ark/products/192478/intel-xeon-platinum-8280-processor-38-5m-cache-2-70-ghz.html
[14] https://tacc.utexas.edu/systems/frontera/
[15] https://www.intel.com/content/www/us/en/products/sku/92986/intel-xeon-processor-e52620-v4-20m-cache-2-10-ghz/specifications.html

4. CPUs total
    a. 161.95PF + 0.387PF = 162.337PF

Total FLOPS:

5. 8.028PF [from GPUs] + 162.337PF [from CPUs] = 170.365PF

Lonestar6 Resource[16]

CPU: AMD "Milan" EPYC 7763 processors

1. AVX 256 [256 bits] / 16 [FP16] * 2 [multiple and add; 2 operations] * 2 [2 AVX 256 blocks per core] * 64 [64 cores per CPU] * 2.55 Ghz = 10444.8GF
    a. 10444.8GF * 2 [2 CPUs per node] = 20889.6 GF/node
    b. 20889.6 GF/node * 530 [total nodes] = 11071488GF = 11.071488PF

GPU:

1. 255 A100s * 312TF [312TF per A100 without sparsity][17] = 79560TF = 79.56PF
2. 8 H100s * 756TF [756TF per H100 without sparsity][18] = 6048TF = 6.048PF
3. Total: 79.56PF + 6.048PF = 85.608PF

Total FLOPS:

2. 11.071488PF [from CPUs] + 85.608PF [from GPUs] = 96.68PF

Neocortex Resource[19]

ASICs: Two Cerebras CS-2s[20]

1. 5780TF [per CS-2] * 2 [2 CS-2s] = 11.560PF[21]

CPUs: 32 Intel Xeon Platinum 8280L CPUs[22]

2. AVX 512 [bits] / 16 [FP16] * 2 [multiple and add; 2 operations] * 2 [2 AVX 512 blocks per core] * 28 [28 cores per CPU] * 2.7 Ghz = 9676.8GF

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[16] https://docs.tacc.utexas.edu/hpc/lonestar6/#intro

[17] https://www.nvidia.com/content/dam/en-zz/Solutions/Data-Center/a100/pdf/nvidia-a100-datasheet-nvidia-us-2188504-web.pdf

[18] https://resources.nvidia.com/en-us-tensor-core/nvidia-tensor-core-gpu-datasheet

[19] https://www.cmu.edu/psc/aibd/neocortex/

[20] https://f.hubspotusercontent30.net/hubfs/8968533/CS-2%20Data%20Sheet.pdf

[21] https://www.alcf.anl.gov/alcf-ai-testbed

[22] https://www.intel.com/content/www/us/en/products/sku/192472/intel-xeon-platinum-8280l-processor-38-5m-cache-2-70-ghz/specifications.html

3. 9676.8GF * 32 [32 total CPUs] = 309657.6GF = 0.30966PF

Total FLOPS:

4. 11.560PF [from two Cerebras CS-2s] + 0.30966PF [from CPUs] = 11.87PF

H100 Equivalent to Total NAIRR Compute

1. Total NAIRR compute = 3770.04PF = 3770040TF
    a. Note that some of the FP16 FLOPS numbers in the total NAIRR compute are not included in the above calculations because they are provided directly by sources. See Table 1 in the blog post for more details.
2. 3770040TF / 756TF [756TF per H100 in tensor FP16 without sparsity] = 4986.8 H100s