Georgios Evangelou

Eindhoven, the Netherlands

✉ gevangelou@hotmail.com   Ⓢ live:gevangelou_5   ⓞ georgevangelou   in linkedin.com/in/gevangelou/

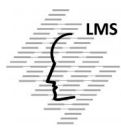| 3 yrs job exp. in S/W Eng.	3 EU projects	2 high-tech projects	2 publications	ECE grad. (top 5%)

EXPERIENCE

TU/e

Software Technology PDEng trainee
Eindhoven University of Technology
[*November 2021 - present*]

− Works as a Professional Doctorate in Engineering trainee (PDEng title: Software Technology).

− Has worked in **2 high-tech industry projects** (**Philips Research, Airbus**).

LMS

R&D Engineer
Lab. for Manufacturing Systems and Automation
[*June 2019 - September 2021*]

− Involved in **3 Horizon 2020 EU founded projects** (THOMAS, SHERLOCK, PENELOPE).

− Worked on a Task Planning and Execution system for Human-Robot Collaboration in **industrial environments**.

− **Hosted discussions and coordinates developments** with R&D and industry partners.

− Authored **1 conference paper** (published) and **1 journal article** (under review) in **2 years**.

DSIP lab

Teaching Assistant
Digital Signal and Image Processing Laboratory
[*October 2019 - January 2020*]

− Provided **teaching assistance** in the "Digital Signal Processing" laboratory (4th year undergraduate) course.

− **Introduced and assisted students** on Assembly & C programming and development on C6713 DSK boards.

EDUCATION

- **Diploma in Electrical and Computer Engineering**
  University of Patras [*Sep 2015 - Aug 2020*]
  − Grade: 8.45/10.00 (Ranking: **TOP 5%**).
  − 5 year studies, BSc & integrated MSc degree (300 ECTS).
  − Specialization in Electronics and Embedded Systems.
  − Thesis: "Architectures and Implementations of the Neural Network LeNet-5 in FPGAs".

- **General Certificate of Secondary Education (GSSE)**
  General High School of Kallipoli, Piraeus, Greece [*2015*]
  − Grade: 19.20/20.00.

AWARDS & RECOGNITION

- **IEEE AP-S Student Design Contest**
  Achieved **4th position (globally)** with his team for their *Radiolocation and Channel Sounding* implementation ($\approx$20 teams in total). [*2018*]

- **Christoulakis Spiritual Institute award**
  Awarded for being among the **highest performing highschool students** in Piraeus for the years 2014-2015. [*2015*]

LANGUAGES

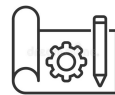| Greek	Native Proficiency	-
English	Professional Proficiency	C2, IELTS score: 8
Dutch	Elementary Proficiency	A2
German	Elementary Proficiency	B1

VOLUNTEERING AND OTHER ACTIVITIES

- **Hands-on experience** in over 8 (6mos.-1yr. duration) university projects concerning programming, systems design, and electronics prototyping.

- Technical assistant in *Meletaki summer camp*, providing **technical support** to the autonomous **electrical power and distribution system** of the establishment for more than 3 years.

- Selected **member of the *Greek Youth Parliament*** (19th summit; 2014), taking part in sessions and giving speeches regarding secondary education in Greece.

- Various activities driven by the ambition to **communicate science** and technology and introduce individuals to relevant concepts.
  * **Assisted students** of relevant science/technology fields, providing **teaching support** in subjects such as programming, electric circuits analysis, embedded systems design and probability theory.
  * Involved in activities of *University of Patras IEEE Student Branch* for **2 years** and **elected board member** of the *Power and Energy Society group*.
  * **Organized, moderated and/or held speeches** in over 5 university seminars.

TECHNICAL SKILLS

- **Programming and Databases:** Java, Python, C/C++, HTML, CSS, JavaScript, MySQL, MongoDB, Docker, ROS, OpenMP, VHDL, (x86, ARM, TI) Assembly, Matlab, Prolog, GNU Radio Companion.
- **Modeling:** BPMN, DMN, UML, SysML.
- **Tools:** IntelliJ, Vivado Design Suite, Code Composer Studio, Camtasia, Git, SVN, LaTeX, C6713 DSK, Arduino, Linux.

HOBBIES & INTERESTS

Prototyping

Bicycling

Byzantine music

PUBLICATIONS

- Evangelou, Georgios & Dimitropoulos, Nikolaos & Michalos, George & Makris, Sotiris. (2021). *An approach for task and action planning in Human–Robot Collaborative cells using AI*. Procedia CIRP. 97. 10.1016/j.procir.2020.08.006.

- Evangelou, Georgios & Aivaliotis, Sotiris & Kousi, Niki & Koukas, Spyros & Makris, Sotiris. (under review). *Model-based task planning and work re-organization in reconfigurable robotic assembly lines*.

CONFERENCES

- **IEEE APS/URSI 2018**
  2018 IEEE International Symposium on Antennas and Propagation and USNC-URSI Radio Science Meeting (8-13 July 2018, Boston, Massachusetts) [*2018*]

- **ECESCON 11**
  11th Electrical and Computer Engineering Students Conference (19-21 April 2019, Thessaloniki, Greece) [*2019*]

- **CIRP-CATS 2020**
  8th CIRP Conference on Assembly Technologies and Systems (29 September - 1 October 2020, Athens, Greece) [*2020*]

- **ECESCON 12**
  12th Electrical and Computer Engineering Students Conference (23-25 April 2021, online) [*2021*]

UNIVERSITY TALKS

- "Electric Cars and Natural Gas", 14 May 2018, University of Patras, Greece.

- "Autonomous Cars", 29 May 2018, University of Patras, Greece.

- "Developments and Modern Perception regarding the operation of the Electricity Grid", 26 November 2018, University of Patras, Greece.

NOTABLE GITHUB PROJECTS

Title	Description	Skills Trained
A framework for developing NNs in H/W accelerators	H/W accelerator which uses LeNet-5 to identify handwritten digits.	High-Level Synthesis, Embedded Systems design, Image Recognition, Neural-Networks
Parallel Programming with OpenMP	Well-known algorithms implemented using OpenMP (Serial and Parallel Programming).	C/C++, Parallel Programming
ALOHA protocol simulator	Simulates the ALOHA communication protocol and gathers performance statistics.	Python, ALOHA protocol
Bringing yoU Spiti (BUS)	Emulates how a commercial bus line application would work.	Python, Databases, GUI design
PhotoStore	Gathers photos, organizes them in albums and provides some editing tools.	Python, Databases, GUI design
DES encrypter/decrypter	H/W accelerator which performs DES encryption/decryption.	VHDL, Encryption Algorithms
Shift/Add multiplier	H/W accelerator which performs shift/add multiplication on customizable bit-length numbers.	VHDL