Time clk=1 Reset_n=1 PC ID_Jump=0 ID_PC_dest[31:0]=xxxxxxxx		X0000045C X00000440	XXXXXXXXXXX 0000045C	\$100 ts \$700000000 \$700000000	Įxxxxxxxxx	\$xxxxxxxxxx			500 ts
Reset_n=1 PC ID_Jump=0 ID_PC_dest[31:0]=xxxxxxxxx	·	(00000440	_)xxxxxxxx	Yxxxxxx			
PC	·	(00000440	_		xxxxxxxx	Txxxxxxx			
ID_Jump=0 ID_PC_dest[31:0]=xxxxxxxxxx	·	(00000440	_]xxxxxxxx	Yxxxxxxx			
ID_PC_dest[31:0]=XXXXXXXXXX	·	(00000440	_		xxxxxxxx	Yxxxxxxx			
EX_PC_Branch_dest[31:0]=0000045C	·	(00000440	_		Į _{AAAAAAA}	Yxxxxxxx			
EX_PC_Branch=1	X0000043C		00000430	,00000000					
<pre>IF_PC[31:0]=00000444</pre>	X 0000043C								
IF_Instruction[31:0]=00000000 one zero,t		X 00000440	00000444	X0000045C	X 00000460	X 00000464	X 00000468	X 0000046C	X 00000470
	2,0x8 (bne zero,gp,0x20		000000111	fence	(li gp,1	(li a7,93	X1i a0,0	/ecall	Xunimp
	,uxo pile zero,gp,uxzu) Nience	(0000000	Vience	VII Ab'I	<u>∧</u> 11 a1,53	<u>∧</u> 11 au,u	Mecali	Динтшр
	X t2		t 6	zero	X t6	X zero			
	Xzero	X gp	/Lo	Vzero	Vro		X a7	X a0	X zero
ID_Rd_addr[4:0]=zero gp ID_RegFile_wr_en=0	Vzeto					/gp		<u></u> A a∪	Yzero
ID_RegFile_wr_en=0 ID_Rs1_data[31:0]=0									
ID_Rs1_data[31:0]=0	X 32	X 24	V ₀						
			^u						
WB_RegFile_wr_en=0 WB_Rd_addr[4:0]=zero zero	VLO	V							V-7
	Xt2	Xgp Vos	zero	Vo				Xgp Va	Xa7
Rd_wr_data[31:0]=1 83		X 25	<u></u>	X ^U				X1	X 93
ID_Flush=1									
EX									V
ID_ALU_source_sel[1:0]=R-R	XR-R					R-I			R-R
ID_ALU_op[3:0]=ADD ADD	SEQ		ADD						
ALU_op1[31:0]=0									
ALU_op2[31:0]=0 25	X0	X 25	X0			<u> </u>	X 93	χ0	
EX_ALU_result[31:0]=00000000 00000000	00000019	00000001	00000000				00000001	X0000005D	X 00000000
ForwardA[1:0]=00 00									
ForwardB[1:0]=00 00	V 01		X 00						
Addrs									
MEM_RegFile_wr_en=0									
EX_RegFile_wr_en=0									
MEM_Rd_addr[4:0]=zero zero	t2	/gp	zero					X gp	(a7
EX_Rd_addr[4:0]=zero t2	/gp	zero					X gp	\ a7	X a0