

ZEUS MONITOR Nov. 2021

8-BIT HOBBY COMPUTER BOOTSTRAP CODE

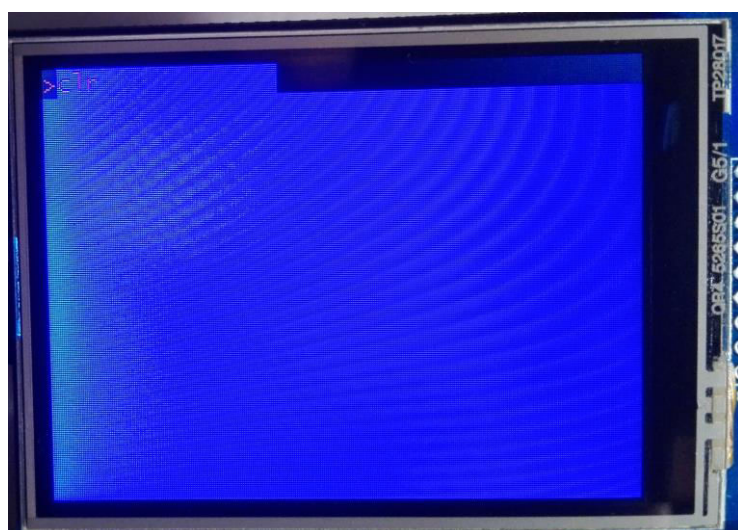
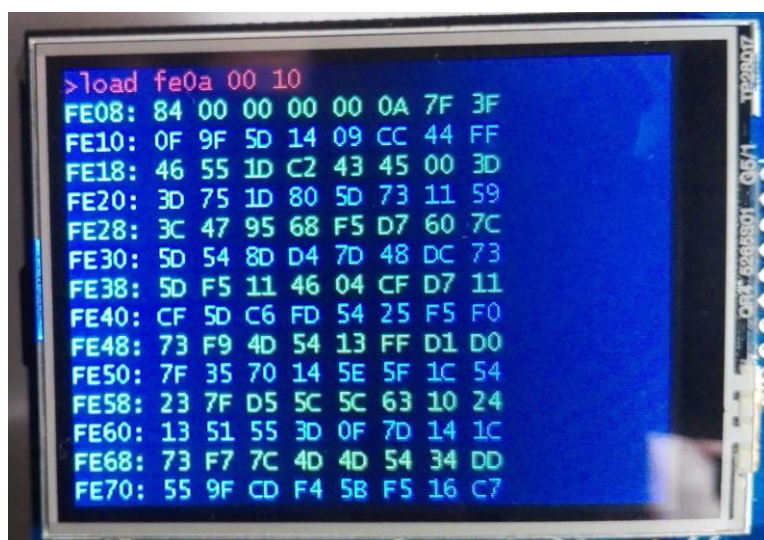
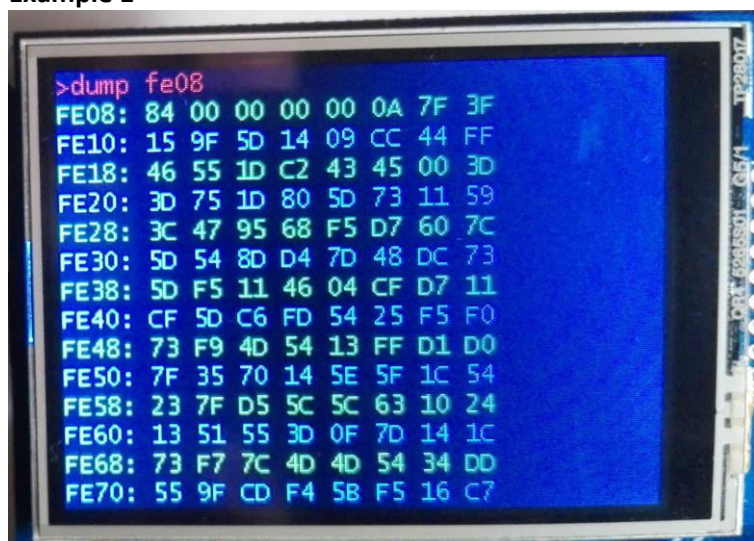
Bootup

At the bootup the Monitor copies memory block \$0000-\$3000 from bank F (EEPROM) to address \$8000 (highmem), then jumps to \$8000 and switch to bank E.

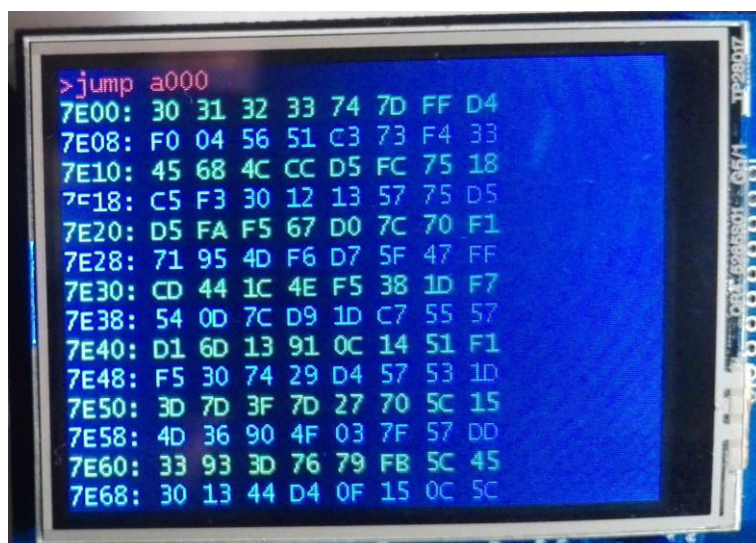
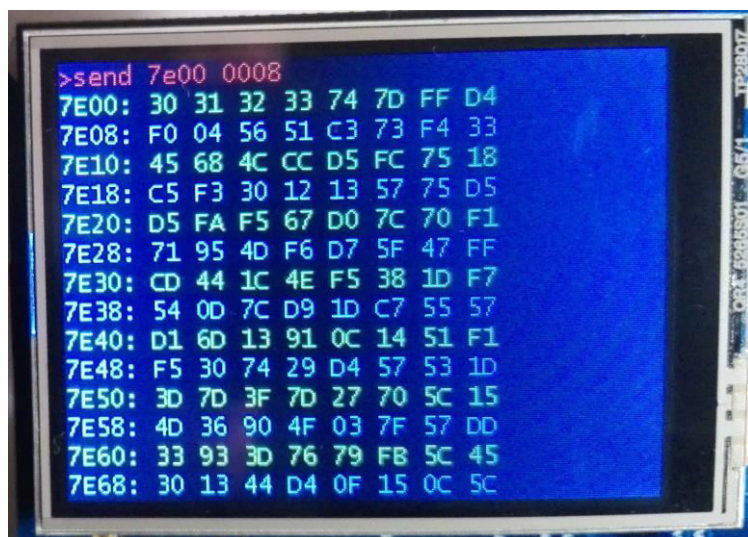
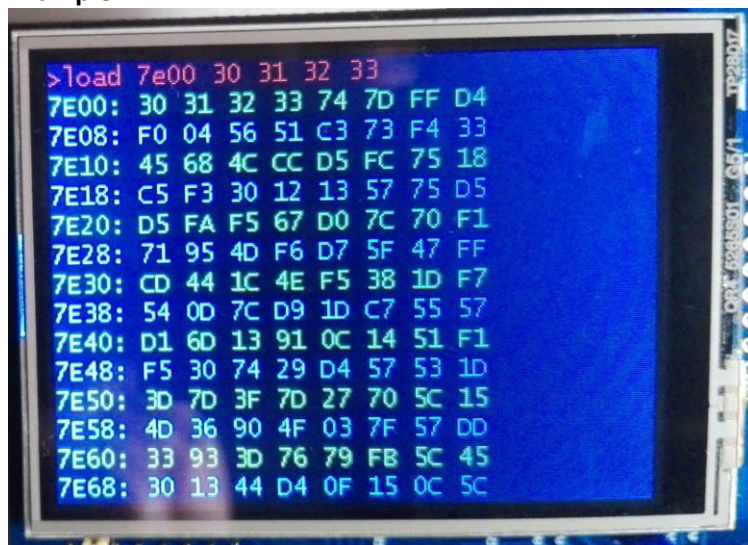
List of commands

- **>clr**
Fills most of TFT screen with background color.
- **>dump**
Dumps on TFT screen 0x70 bytes starting from specified memory address.
- **>send**
Sends to UART specified amount of bytes starting from specified memory address.
- **>sysr**
Sets system register value. Low nibble of sys reg is used by mem128 module for bank switching.
- **>load**
Writes specified byte set starting from specified memory address.
- **>jump**
Sets Program Counter register (jumps) to specified memory address.
- **>exec**
Executes specified byte set ended with 0xC9 (RET instruction).
- **>crc7**
Calculates crc7 sum augmented with bit0 = 1 (check SD phys layer specs) for specified byte set.

Example 1



Example 2



Example 3

