

# ZEUS MONITOR Feb. 2022

## 8-BIT HOBBY COMPUTER BOOTSTRAP CODE

### Bootup

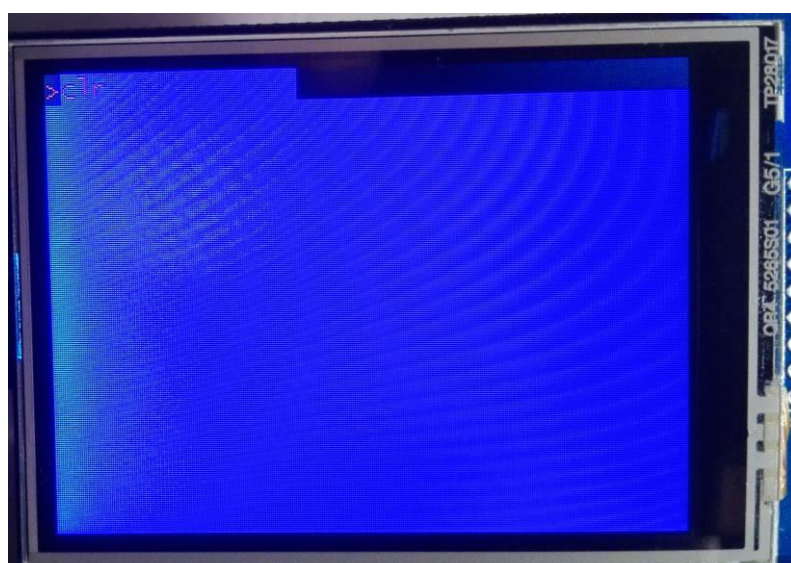
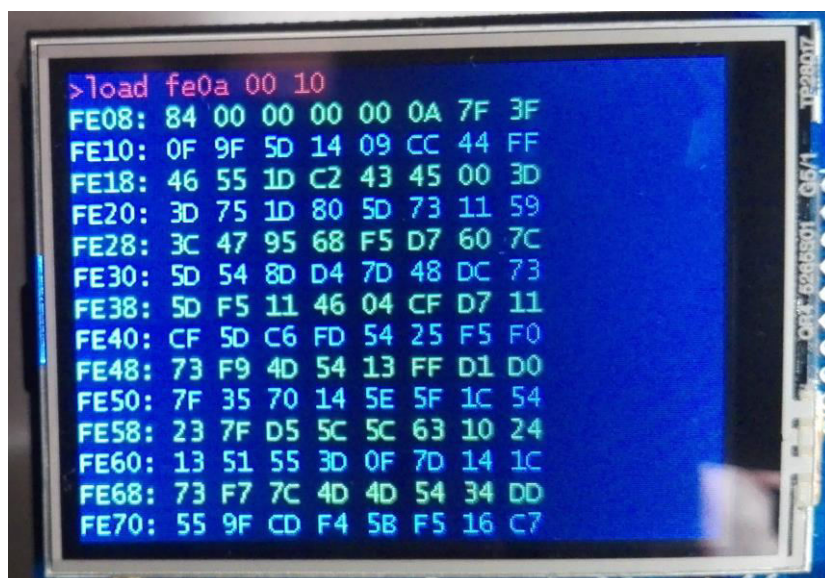
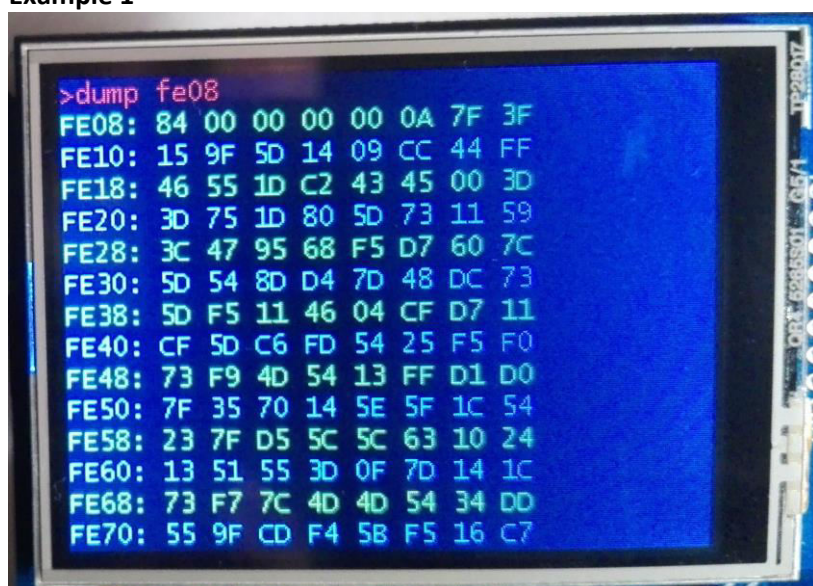
At the bootup the Monitor

1. copies memory block \$0000-\$3000 from bank F (EEPROM) to address \$8000 (highmem);
2. counts number of memory banks in the system and enumerates them (address \$0007);
3. jumps to \$8000, configures system, switches to bank E and initiates it (writes reboot jump command to address \$0000).

### List of commands

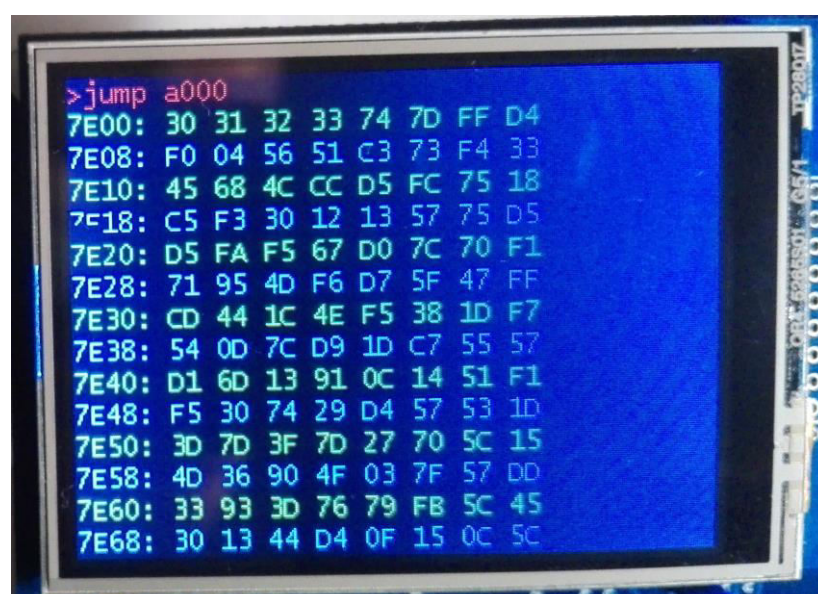
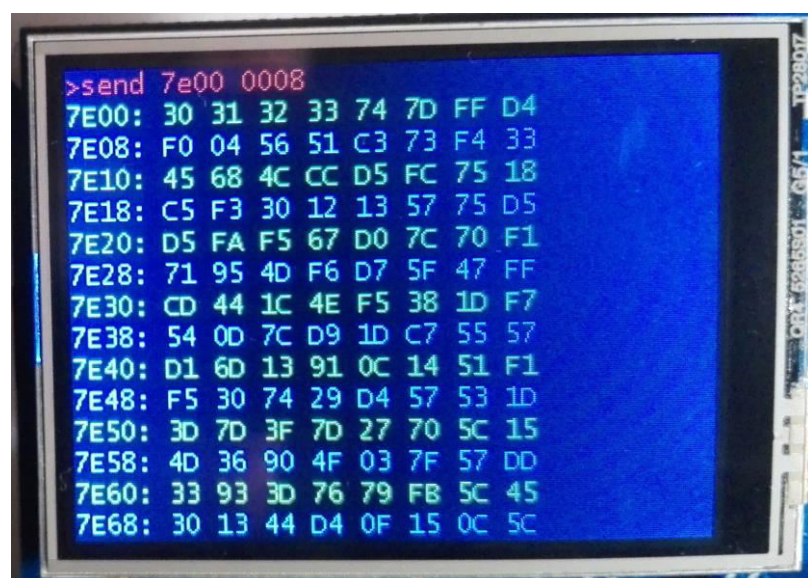
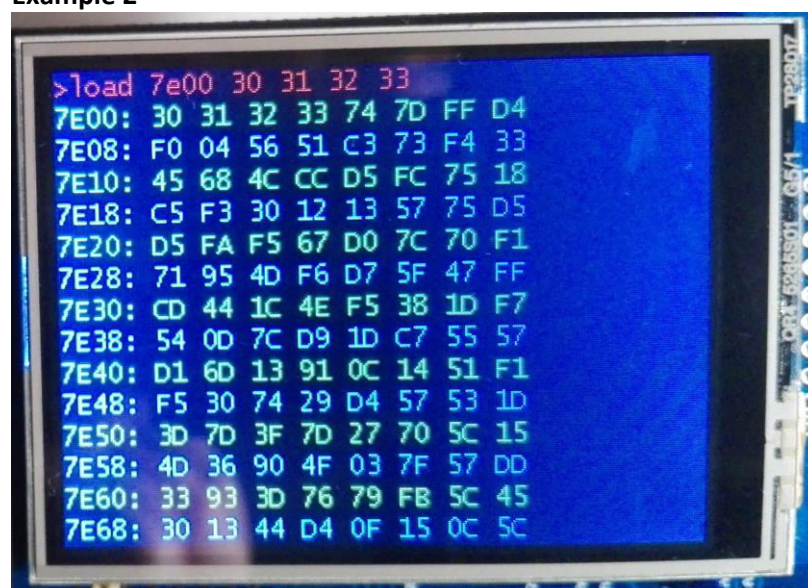
- **>clr**  
Fills most of TFT screen (everything except command line) with background color.
- **>dump (source)**  
Dumps on TFT screen 0x70 bytes starting from specified memory address.
- **>send (source) (bytes)**  
Sends to UART specified number of bytes starting from specified memory address.
- **>sysr (value)**  
Sets system register value. Low nibble of sys reg is used by mem128 module for bank switching. Right after bank switch this new bank is initiated by writing corresponding jump instruction to address \$0000 (in order to keep RESET function operational).  
Take it into consideration before closing EEPROM write enable jumper on the MEM module or else your EEPROM becomes unbootable after power off. So close the jumper ONLY after "SYSR XF" command or fix the jump address with use of LOAD command before power off.
- **>load (dest) (byte1) (byte2)...**  
Writes specified byte set starting from specified memory address.
- **>move (dest) (source) (bytes)**  
Writes specified number of bytes starting from specified memory address to another memory address.
- **>jump (source)**  
Sets Program Counter register (jumps) to specified memory address.
- **>exec (source)**  
Executes specified byte set ended with 0xC9 (RET instruction).
- **>crc7 (byte1) (byte2)...**  
Calculates crc7 sum augmented with bit0 = 1 (check SD phys layer specs) for specified byte set.

### Example 1





## Example 2



### Example 3

