

ZEUS MONITOR Nov. 2021

8-BIT HOBBY COMPUTER BOOTSTRAP CODE

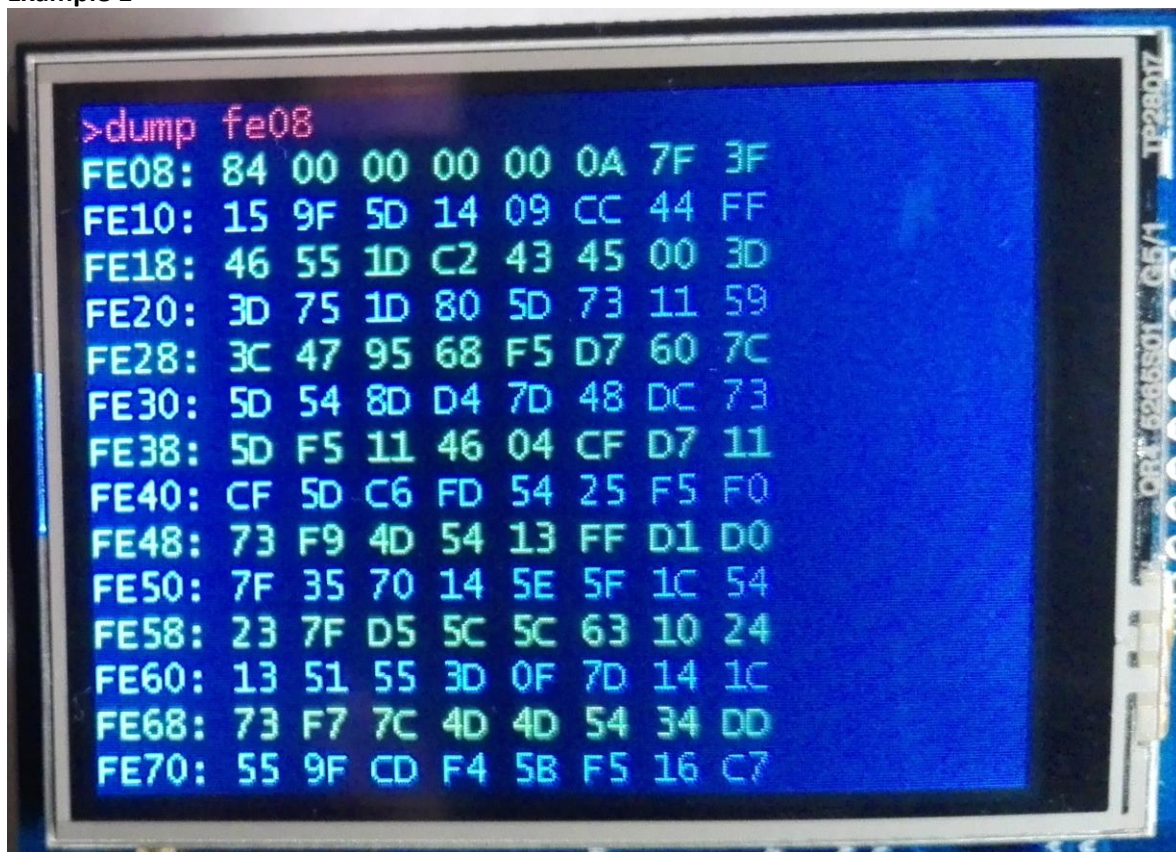
Bootup

At the bootup the Monitor copies memory block \$0000-\$3000 from bank F (EEPROM) to address \$8000 (highmem), then jumps to \$8000 and switch to bank E.

List of commands

- **>clr**
Fills most of TFT screen with background color.
- **>dump**
Dumps on TFT screen 0x70 bytes starting from specified memory address.
- **>send**
Sends to UART specified amount of bytes starting from specified memory address.
- **>sysr**
Sets system register value. Low nibble of sys reg is used by mem128 module for bank switching.
- **>load**
Writes specified byte set starting from specified memory address.
- **>jump**
Sets Program Counter register (jumps) to specified memory address.
- **>exec**
Executes specified byte set.
- **>crc7**
Calculates crc7 sum augmented with bit0 = 1 (check SD phys layer specs).

Example 1

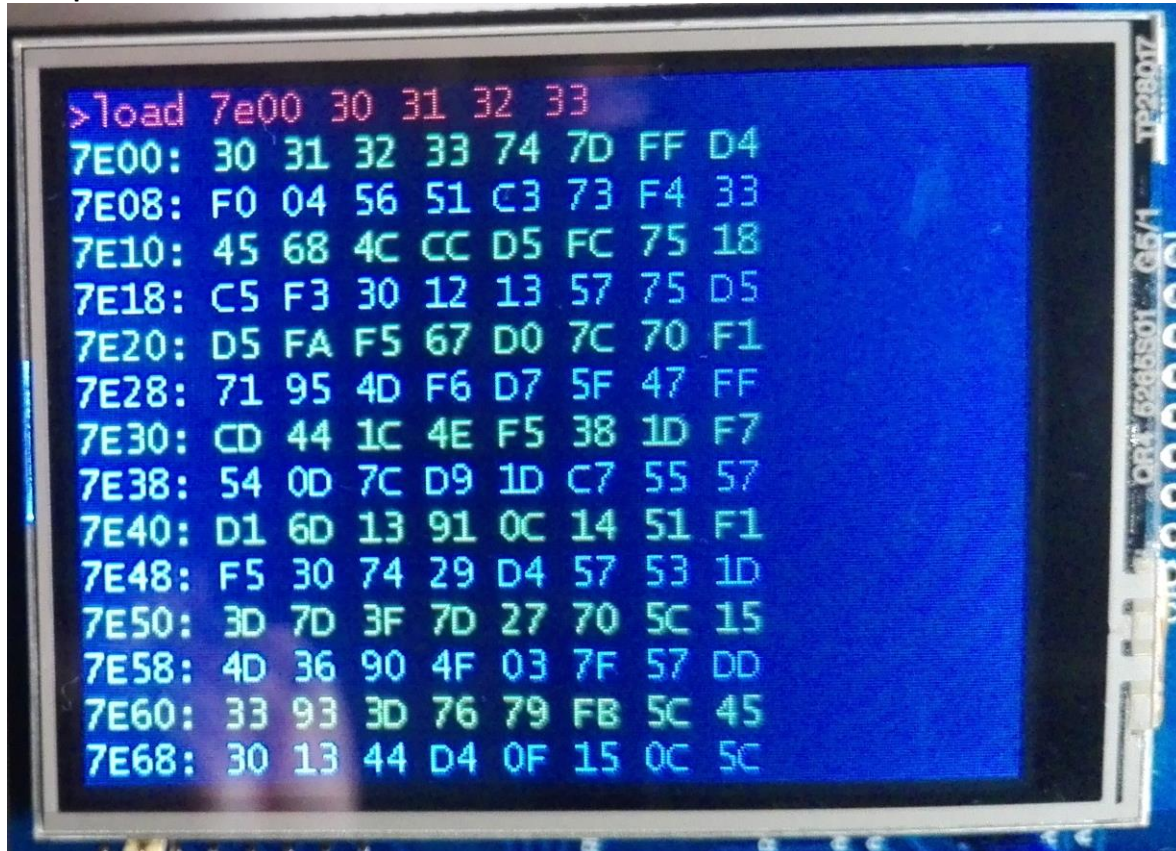


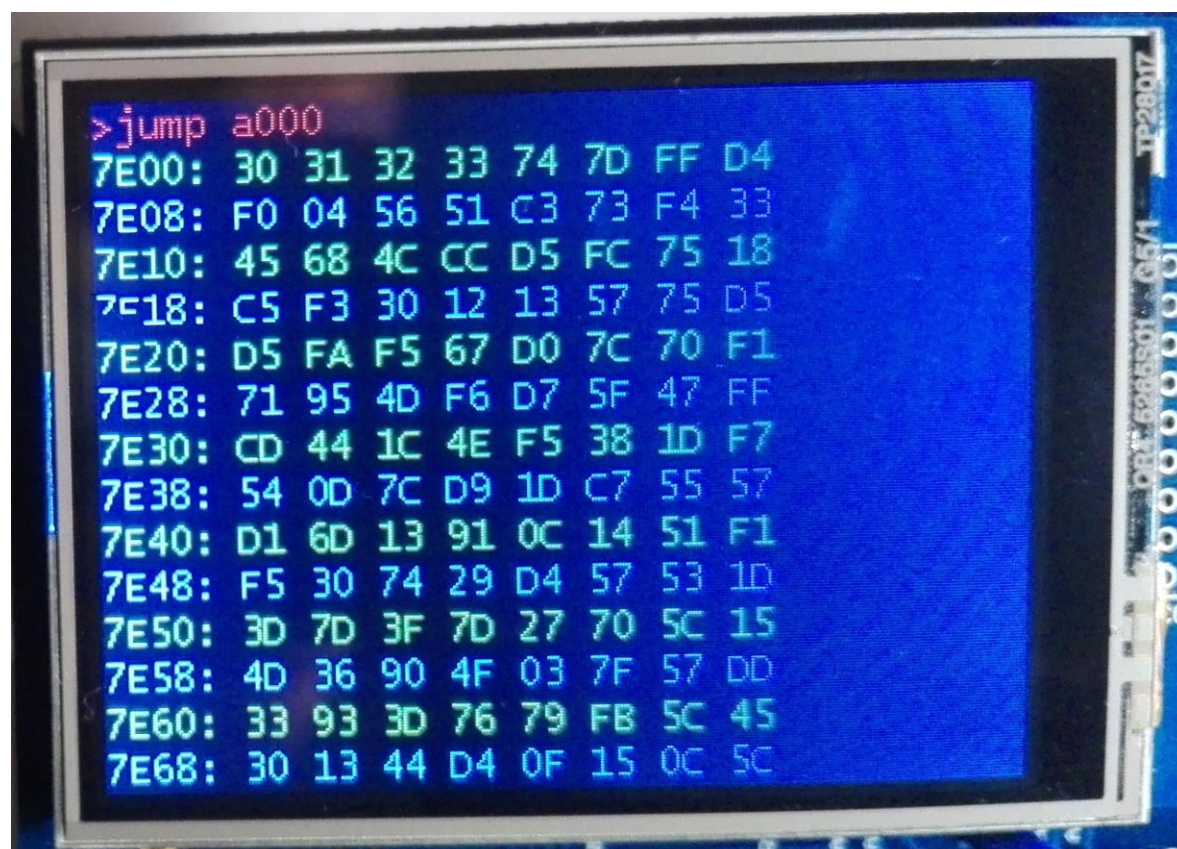
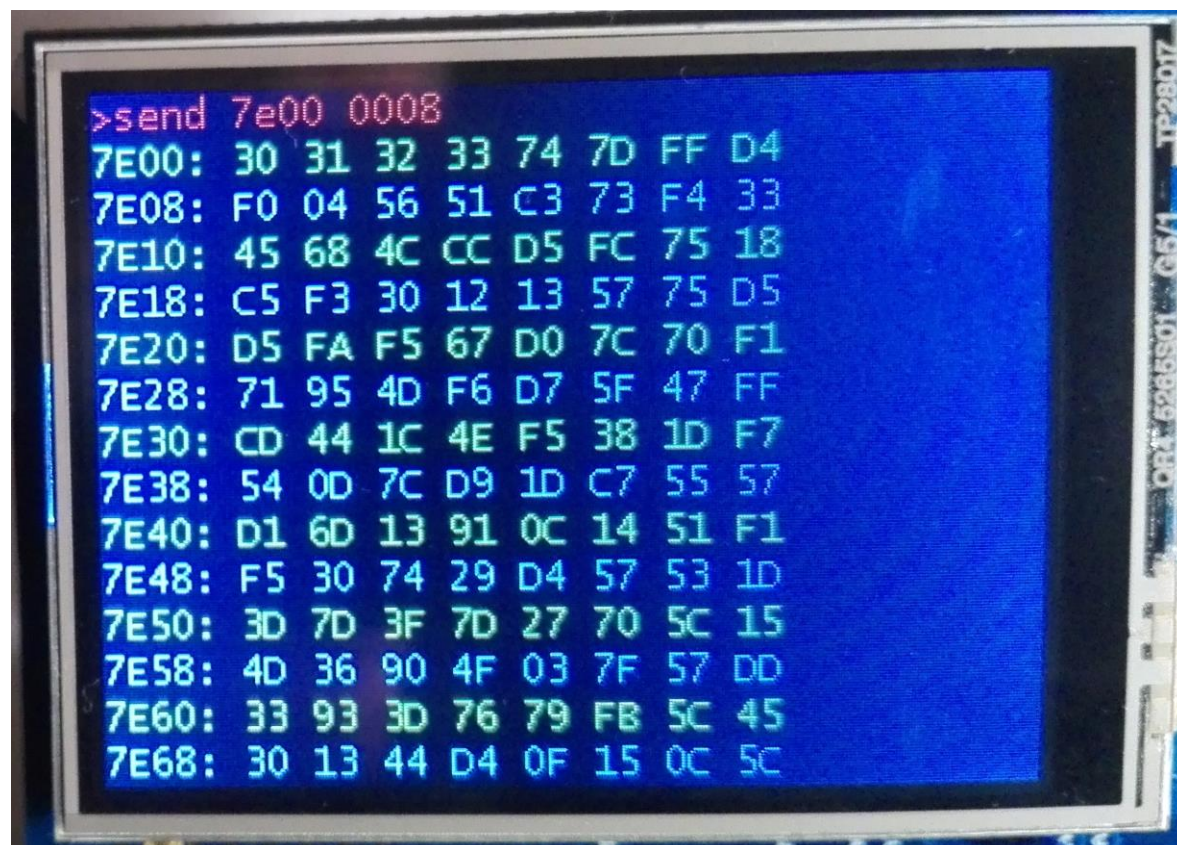
>load fe0a 00 10

FE08:	84	00	00	00	0A	7F	3F
FE10:	0F	9F	5D	14	09	CC	44
FE18:	46	55	1D	C2	43	45	00
FE20:	3D	75	1D	80	5D	73	11
FE28:	3C	47	95	68	F5	D7	60
FE30:	5D	54	8D	D4	7D	48	DC
FE38:	5D	F5	11	46	04	CF	D7
FE40:	CF	5D	C6	FD	54	25	F5
FE48:	73	F9	4D	54	13	FF	D1
FE50:	7F	35	70	14	5E	5F	1C
FE58:	23	7F	D5	5C	5C	63	10
FE60:	13	51	55	3D	0F	7D	14
FE68:	73	F7	7C	4D	4D	54	34
FE70:	55	9F	CD	F4	5B	F5	16

>clr

Example 2





Example 3



>dump 0100

0100:	00	01	02	03	45	F7	FB	BF
0108:	6B	AC	C8	4F	08	8E	E3	FA
0110:	2F	C3	A8	0F	43	B8	BB	FE
0118:	CF	9A	84	29	38	20	EB	0E
0120:	B4	23	2C	8E	AF	C2	20	AB
0128:	8F	46	AA	34	F8	E2	3A	3E
0130:	8A	EF	EB	27	0A	C0	70	B8
0138:	FA	4A	C8	08	22	3E	BE	0C
0140:	B4	CC	C7	C4	A3	72	CE	FA
0148:	2B	2B	F2	38	96	4B	81	B8
0150:	8F	0B	3A	FB	F7	E2	3B	BA
0158:	F3	3E	C8	EA	D3	AA	68	9A
0160:	3E	4C	2A	0F	2C	A0	AE	8F
0168:	28	CE	C3	0F	CC	0D	B8	0E