

# Hybrid Distributed Sort with Bitonic Interchanges

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## 1 Introduction

Sorting is one of the most fundamental operations in computer science, with broad applications in data processing, scientific computing, and distributed systems. As dataset sizes continue to grow, leveraging parallel and distributed architectures becomes essential to achieve performance and scalability. Bitonic Sort is a well-known comparison-based sorting algorithm that is especially amenable to parallelization due to its regular structure and predictable communication pattern.

This project focuses on the implementation and benchmarking of a hybrid distributed sorting algorithm that employs Bitonic Sort for inter-process sorting. The dataset to be sorted consists of  $N = 2^{p+q}$  integers, distributed across  $2^p$  MPI processes. Each process receives a chunk of  $2^q$  elements. The algorithm first performs a local sort and then enters a series of bitonic merging phases that involve both local and inter-process communication steps.

The goal of this work is to evaluate the performance of the parallel Bitonic Sort implementation under various configurations. We study how execution time and communication cost scale with the number of processes, data size, and the degree of communication granularity controlled by the parameter  $s$ , which represents the buffer splitting depth. Benchmarks are conducted on a high-performance computing cluster, and results are visualized to identify performance trends and bottlenecks.

## 2 Algorithm Overview

The implemented algorithm is a hybrid distributed sorting method that combines local sorting with parallel Bitonic merging across multiple MPI processes. Each of the  $2^p$  MPI processes receives a block of  $2^q$  integers. The parameter  $s$  controls the communication buffer size, dividing each local array into  $2^{q-s}$  chunks for non-blocking communication during the pairwise exchange phase.

### 2.1 Stages of the Algorithm

The algorithm proceeds in three main stages:

1. **Initial Alternating Sort:** Each process locally sorts its data. Processes with even ranks sort in ascending order, while processes with odd ranks sort in descending order, thus preparing a “bitonic” sequence across the distributed data.
2. **Bitonic Merging with Pairwise Exchanges:** The global merge is performed in  $\log_2(2^p) = p$  stages. At each stage, every process exchanges data with a dynamically selected “partner” process, computed using bitwise XOR:  $partner = rank \oplus 2^k$ , where  $k$  depends on the step within the stage. This XOR operation flips the  $k$ -th bit of the rank, effectively connecting processes whose ranks differ by a power of two. The process graph formed in this way is a  $p$ -dimensional hypercube, where each edge represents communication between processes at Hamming distance 1, 2, 4, etc. As the algorithm progresses, the sorting propagates along these hypercube edges, gradually enforcing global order.

3. **Elbow Sort:** After each merging stage, each process performs an “elbow sort” to fully merge its local array into a sorted sequence, starting from the smallest (or largest) element and expanding outward using a two-pointer merging technique.

The algorithm records timing information for the local sort, communication phases, elbow sort, and total execution time. Synchronization between processes is enforced via `MPI_Barrier` calls to ensure correct timing measurements and data consistency.

## 2.2 Pseudocode

The following pseudocode summarizes the main steps of the distributed bitonic sort:

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### Algorithm 1 Distributed Sort with Bitonic Interchanges

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**Require:** Local data array *local\_data* of size  $2^q$

**Require:** Number of processes  $P = 2^p$ , buffer size  $B = 2^s$ , process rank  $r$

**Ensure:** Globally sorted data distributed across all processes

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1: if  $r \bmod 2 = 0$  then
2:   SORTASCENDING(local_data)
3: else
4:   SORTDESCENDING(local_data)
5: end if
6: for  $stage = 1$  to  $\log_2(P)$  do
7:    $chunk \leftarrow \lfloor r / (P / 2^{stage}) \rfloor$ 
8:    $ascending \leftarrow (chunk \bmod 2 = 0)$ 
9:   for  $step = stage - 1$  to 0 do
10:     $partner \leftarrow r \oplus (1 \ll step)$ 
11:    if  $r \geq partner$  then
12:      NONBLOCKINGCOMMUNICATION(local_data, partner, B)
13:    else
14:      ASYNCRECEIVEANDEXCHANGE(local_data, partner, B, ascending)
15:    end if
16:    BARRIER
17:  end for
18:  ELBOWSORT(local_data, ascending)
19:  BARRIER
20: end for

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## 2.3 Communication and Data Exchange

The core of the distributed Bitonic Sort lies in the communication and data exchange between MPI processes during the merging phases. At each step within a stage, a process determines its partner via the XOR operation  $partner = rank \oplus 2^k$ . This operation reflects a structured traversal of a hypercube topology, where each dimension corresponds to one communication round. The result is a classification network where processes interact with others that differ in exactly one bit position of their binary rank. This structure enables a scalable and regular communication pattern.

Data is exchanged in chunks of size  $2^s$ , resulting in  $2^{q-s}$  buffer splits per process. Non-blocking primitives (`MPI_Isend`, `MPI_Irecv`) are used to overlap communication and computation. The abstract functions `NONBLOCKINGCOMMUNICATION` and `ASYNCRECEIVEANDEXCHANGE` encapsulate these steps, enabling higher-ranked processes to initiate sending early, while lower-ranked processes perform receives and apply directional merging logic.

This design ensures bandwidth-efficient data exchange while preserving the required bitonic structure for correct merges at each stage. Global consistency is maintained via barriers that synchronize all processes before moving to the next stage of the algorithm.

## 3 Performance Overview

All benchmarks were conducted on the Aristotelis HPC cluster using 8 compute nodes. This section presents a detailed breakdown of the algorithm’s execution time and identifies bottlenecks that motivate optimization efforts.

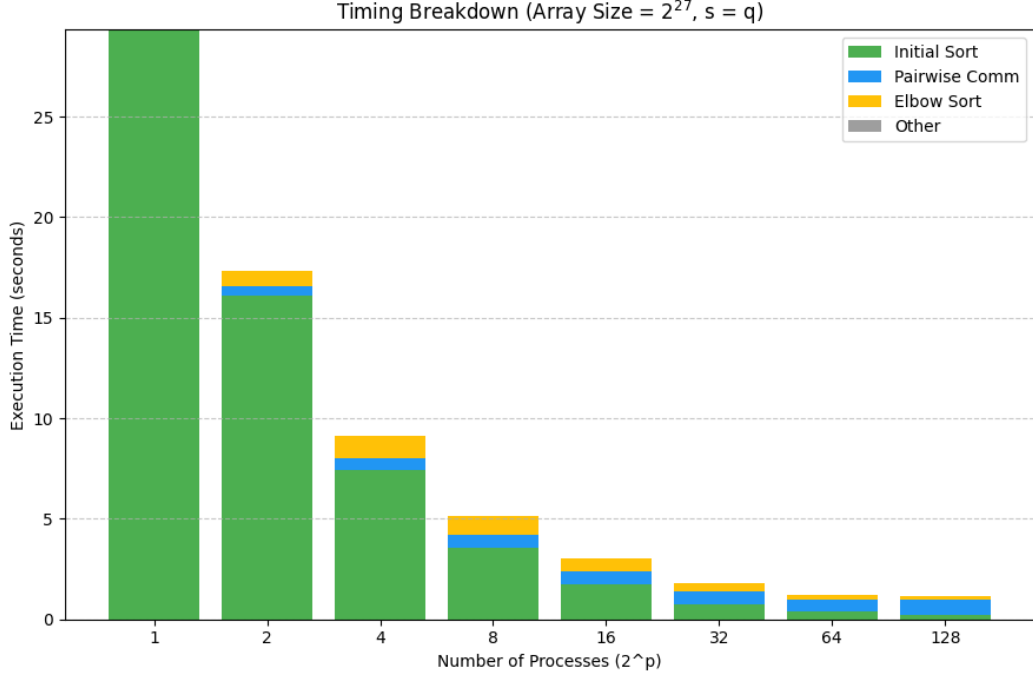


Figure 1: Execution time breakdown per stage for total size  $2^{27}$  with no buffer splitting ( $s = q$ ).

### 3.1 Time Breakdown

Before applying any optimizations, we first analyze how the total execution time is distributed across the different stages of the algorithm. Figure 1 shows the execution time versus the number of processes  $2^p$ , for a total input size of  $2^{p+q} = 2^{27}$  integers. In this setup, the communication buffer is not split ( $s = q$ ).

We observe that when the number of processes is small, the majority of time is spent on the *initial sort* phase. However, as the number of processes increases, the cost of *pairwise communication* grows substantially and eventually dominates total execution time once  $2^p \geq 64$ . This trend is also evident in Table 1. Despite this, distributing the workload still leads to a significant reduction in overall runtime.

Table 1: Execution Time Breakdown by Stage (total size:  $2^{p+q} = 2^{27}$ ,  $s = q$ )

p	q	Initial Sort (%)	Pairwise Sort (%)	Elbow Sort (%)	Other (%)
0	27	<b>100.00</b>	0.00	0.00	0.00
1	26	<b>92.79</b>	2.54	4.66	0.01
2	25	<b>81.27</b>	6.79	11.93	0.01
3	24	<b>69.53</b>	12.42	18.04	0.01
4	23	<b>58.09</b>	21.84	20.06	0.02
5	22	<b>43.02</b>	35.50	21.45	0.03
6	21	29.92	<b>50.97</b>	19.08	0.03
7	20	17.07	<b>66.08</b>	16.81	0.05

### 3.2 Optimizing Pairwise Communication

As shown in the previous subsection, *pairwise communication* becomes the dominant performance bottleneck for large process counts. To mitigate this overhead, we experimented with splitting the communication buffer into smaller batches. This technique aims to overlap communication with computation and improve scalability.

Figure 2 shows the impact of different buffer split configurations, defined by  $B = 2^{q-s}$ , on the pairwise communication time. We observe that buffer splitting reduces communication time slightly for moderate process counts. However, when the number of processes becomes too large, splitting becomes counterproductive – particularly when  $p$  is large and  $q$  is small,

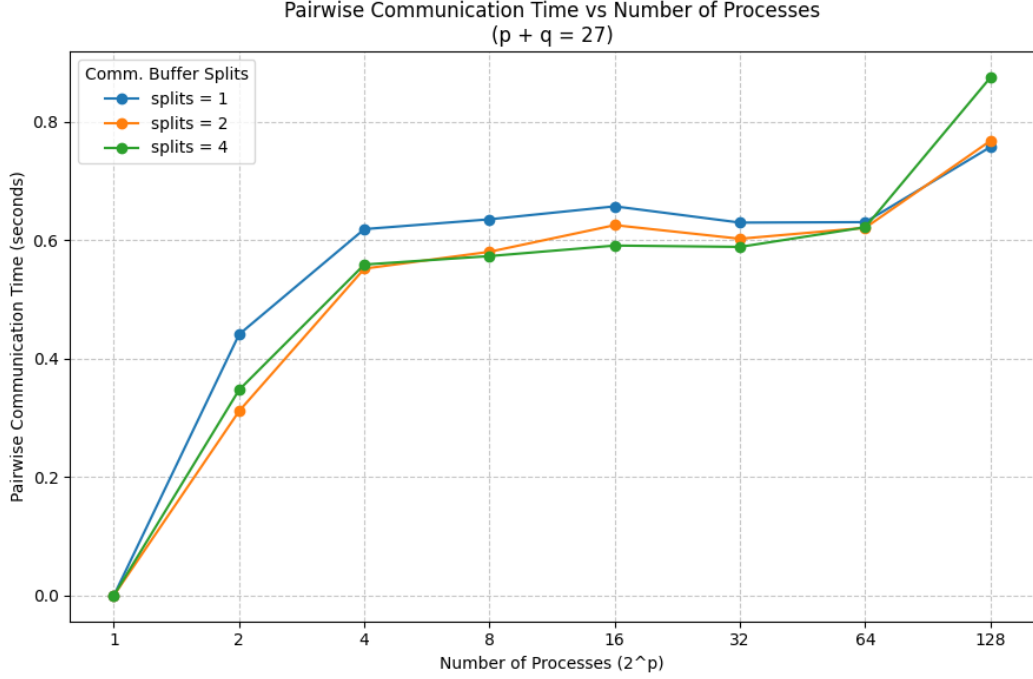


Figure 2: Pairwise communication time vs number of processes for varying communication buffer splits ( $B = 2^{q-s}$ ).

resulting in limited data per process. In such cases, the overhead of managing multiple small transfers outweighs any benefits from overlap.

Therefore, while buffer splitting can be marginally beneficial in some configurations, it is not sufficient to scale communication performance at large process counts. This observation motivates further work toward optimizing *local sorting*, which increasingly dominates as communication optimizations saturate.

### 3.3 Scalability with Respect to Total Data Size

Finally, Fig. 3 presents the total execution time as a function of the total array size for all tested configurations of  $p$  and  $q$ . For each array size  $2^{p+q}$ , we observe that increasing the number of processes leads to a consistent reduction in execution time. This clearly demonstrates the effectiveness of the parallelization strategy in improving performance as the problem size scales.

## 4 Conclusions and Future Work

In this work, we developed and evaluated a hybrid distributed sorting algorithm that combines local sorting with inter-process Bitonic merging. The design leverages the structured nature of Bitonic Sort and the communication regularity provided by the hypercube topology formed through bitwise XOR partner selection.

Our benchmarks show that the algorithm scales well with increasing problem size and number of processes. When the number of processes is relatively small, performance is limited by the cost of local sorting. However, as the number of processes increases, pairwise communication emerges as the dominant bottleneck. We explored the effect of buffer splitting as a strategy to mitigate this cost, finding that it provides marginal benefits only in specific configurations, and may become counterproductive in high-parallelism regimes due to increased message overhead.

Future work will focus on optimizing the local sorting phase by implementing Bitonic Sort on GPUs using CUDA. Since Bitonic Sort is inherently parallel and well-suited for GPU architectures, this approach can significantly accelerate local sorting, reduce the overall execution time, and better balance the computation-communication ratio in the distributed algorithm.

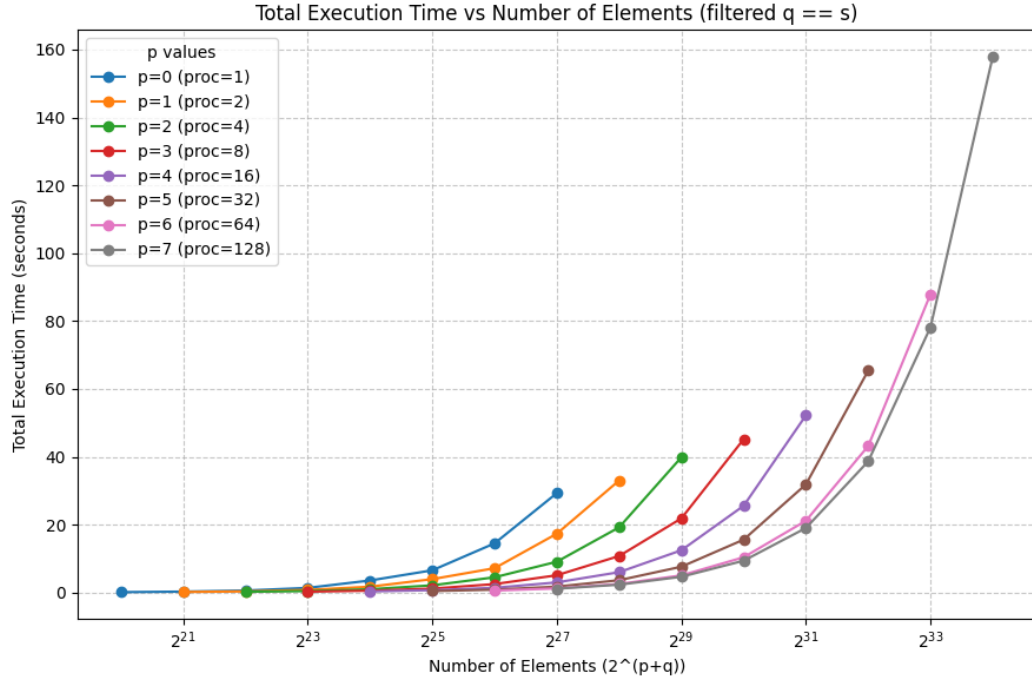


Figure 3: Total execution time vs total array size for all  $(p, q)$  combinations.

Overall, the results validate that distributed Bitonic Sort can serve as a scalable backbone for parallel sorting tasks, provided that communication and local computation are carefully balanced.

## Acknowledgments

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