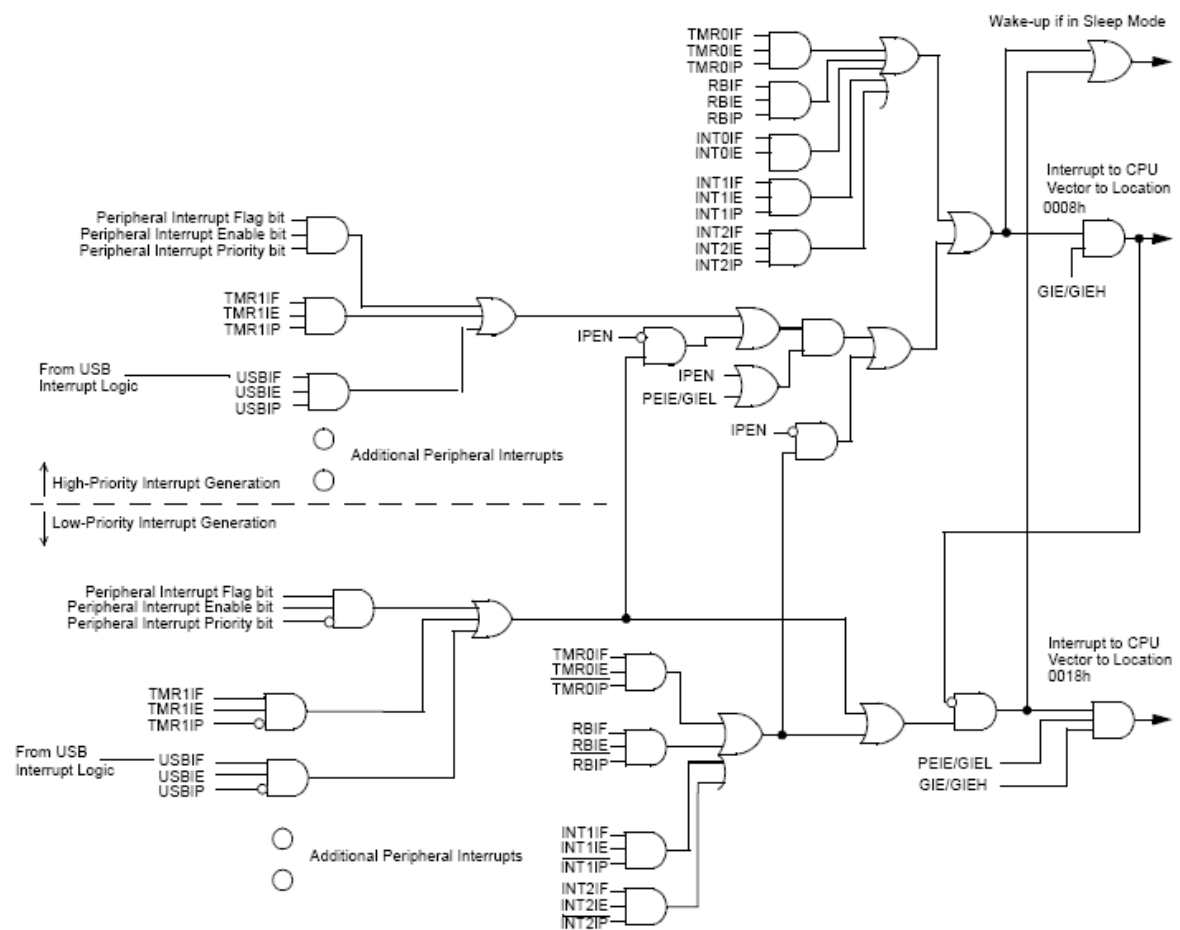
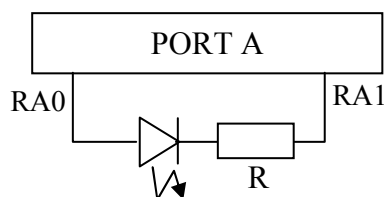


- 5) Malgrat el vist a la pregunta 2, podríem fer que una única petició d'interrupció arribada per INT1IF provoqués una interrupció d'alta prioritat i DESPRÉS una de baixa prioritat? Si és possible indica com.

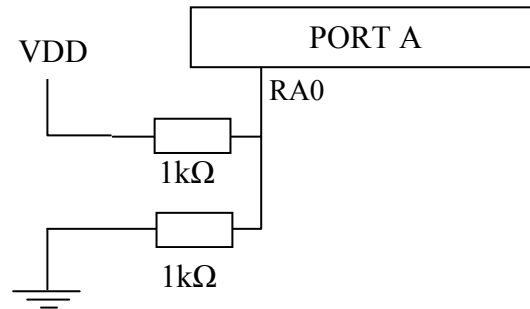


- 6) Indiqueu els valors amb els quals cal configurar el port A (TRISA) i quins valors cal escriure en el seu registre de dades (PORTA) per a que el led de la figura següent s'encengui.



Nom i Cognoms: _____

- 7) A partir de quina tensió d'alimentació VDD garantitzem que al bit 0 del port A llegirem un "1" lògic? Justifica la resposta.



DC CHARACTERISTICS			Standard Operating Conditions (unless otherwise stated) Operating temperature $-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$ for industrial			
Param No.	Symbol	Characteristic	Min	Max	Units	Conditions
D030 D030A D031 D032 D032A D033	V_{IL}	Input Low Voltage I/O Ports (except RC4/RC5 in USB mode): with TTL Buffer with Schmitt Trigger Buffer RB0 and RB1 MCLR OSC1 and T10SI OSC1	V_{SS} — V_{SS} V_{SS} V_{SS} V_{SS}	$0.15 V_{DD}$ 0.8 $0.2 V_{DD}$ $0.3 V_{DD}$ $0.2 V_{DD}$ $0.3 V_{DD}$	V V V V V V	$V_{DD} < 4.5\text{V}$ $4.5\text{V} \leq V_{DD} \leq 5.5\text{V}$ When in $I^2\text{C}^{\text{TM}}$ mode XT, HS, HSPLL modes ⁽¹⁾ EC mode ⁽¹⁾
D040 D040A D041 D042 D042A D043	V_{IH}	Input High Voltage I/O Ports (except RC4/RC5 in USB mode): with TTL Buffer with Schmitt Trigger Buffer RB0 and RB1 MCLR OSC1 and T10SI OSC1	$0.25 V_{DD} + 0.8\text{V}$ 2.0 $0.8 V_{DD}$ $0.7 V_{DD}$ $0.8 V_{DD}$ $0.7 V_{DD}$	V_{DD} V_{DD} V_{DD} V_{DD} V_{DD} V_{DD}	V V V V V V	$V_{DD} < 4.5\text{V}$ $4.5\text{V} \leq V_{DD} \leq 5.5\text{V}$ When in $I^2\text{C}$ mode XT, HS, HSPLL modes ⁽¹⁾ EC mode ⁽¹⁾

- 8) Necessitem generar un nou caràcter a la LCD emprada en pràctiques per representar el símbol de l'euro. Mostra la seqüència d'instruccions a fer per poder visualitzar a la posició 0,0 de la LCD aquest símbol. Podeu emprar les ordres XLCDCommand(int x) i XLCDPut(BYTE b).

0	0	0	0	0
0	0	1	1	1
0	1	0	0	0
1	1	1	1	0
0	1	0	0	0
1	1	1	1	0
0	1	0	0	0
0	0	1	1	1

Table 7.6 HD44780 instruction set

Instruction	Code										Description	Execution time
	RS	R/W	B7	B6	B5	B4	B3	B2	B1	B0		
Clear display	0	0	0	0	0	0	0	0	0	1	Clears display and returns cursor to the home position (address 0).	1.64 ms
Cursor home	0	0	0	0	0	0	0	0	1	*	Returns cursor to home position (address 0). Also returns display being shifted to the original position. DDRAM contents remain unchanged.	1.64 ms
Entry mode set	0	0	0	0	0	0	0	1	I/D	S	Set cursor move direction (I/D), specifies to shift the display (S). These operations are performed during data read/write.	40 µs
Display on/off control	0	0	0	0	0	0	1	D	C	B	Sets on/off of all display (D), cursor on/off (C) and blink of cursor position character (B).	40 µs
Cursor /display shift	0	0	0	0	0	1	S/C	R/L	*	*	Sets cursor-move or display-(S/C), shift direction (R/L). DDRAM contents remains unchanged.	40 µs
Function set	0	0	0	0	1	DL	N	F	*	*	Sets interface data length (DL), number of display line (N) and character font (F).	40 µs
Set CGRAM address	0	0	0	1	CGRAM address						Sets the CGRAM address. CGRAM data is sent and received after this setting.	40 µs
Set DDRAM address	0	0	1	DDRAM address							Sets the DDRAM address. DDRAM data is sent and received after this setting.	40 µs
Read busy flag and address counter	0	1	BF	CGRAM/DDRAM address							Reads busy flag (BF) indicating internal operation is being performed and reads CGRAM or DDRAM address counter contents (depending on previous instruction).	0 µs
Write to CGRAM or DDRAM	1	0	write data								Writes data to CGRAM or DDRAM.	40 µs
Read from CGRAM or DDRAM	1	1	read data								Reads data from CGRAM or DDRAM.	40 µs