




Gerin Amalaraj


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Education

- **University of Toronto** Toronto, CA
BASc in Computer Engineering Sep. 2016 - Present

Work Experience

- **Elecssoft Consulting Inc.** Toronto, CA
ESP(Engineering Strategies and Practices) Intern 01/2017 - 04/2017
 - Designed a 'smart weight lifting machine' to allow users to count their reps and calories burned on their phone by signing in with a QR code.

Personal Projects

- **Inventory** 07/2018 - 08/2018
Android Application (Java, XML, SQLite), Andoid OS
 - Developed an inventory app that displays a catalogue of your inventory. Created SQLite database structures that integrates with application allowing to add, edit and delete items. Implemented a search bar with auto-complete and auto-suggestions.
- **Alarm** 06/2018 - 07/2018
Android Application (Java, XML), Andoid OS
 - Developed all in one clock app allowing users to set alarms, add timers and customize alarm sounds.
 - Includes buttons to stop, snooze and reset buttons as well as alarm notifications.
- **Music Player** 05/2018 - 06/2018
Android Application (Java, XML), Andoid OS
 - Music Player offers full media controls, a repeat button, a volume bar and a seek bar.
- **TourPlan** 01/2018 - 04/2018
C++ Application, Linux (Debian)
 - Developed a GIS featuring intuitive controls and color scheme. View roads, landmarks, intersections and points of interests. Click or search for a location in the search bar for more detailed information.
 - Implemented pathfinding capabilities to offer step-by-step directions and estimated travel time.
- **Password Authenticated Garage System** 03/2018 - 04/2018
NIOS II Assembly language hardware project (FPGA, LEGO motors, sensors)
 - Uses a PS2 keyboard to set up a secure password and 7-segment displays to indicate when it is locked.
 - Custom LEGO controller provide by UofT is used to interface between the NIOS II board to open a LEGO garage door or sound an alarm depending on whether the password is correct.
- **Circuit Solver** 10/2017 - 11/2017
C++ Application, Linux (Red Hat)
 - Created program making use of "Kirchoff's Laws" to create a circuit consisting of a resistor network and a voltage source to calculate the voltage at each node in the circuit.

Skills

Languages: C/C++, Java, Assembly, Verilog

Tools and Technologies: SQLite, XML, Git, Github, MatLab, Simulink, NI MultiSim, ModelSim

Operating Systems: Linux (Debian, Redhat, CentOS), Android, Windows 10

Qualities: Time Management, Prioritization, Team Player, Resourceful, Strategic Thinker, Motivated, Attention to Detail, Natural Leader, Resiliency