

# realtime-embedded-assignment-2

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Repository for the second assignment of CU Boulder's Real Time Embedded Systems course

## Problem 1

A: Do a quick "sudo whoami" to demonstrate success for account creation. B: Logout and test your login, then logout. Use Alt+Print-Screen to capture your desktop and save as proof you set up your account.

Since I am running the headless version of Raspberry Pi OS I do not have access to the regular UI and instead am using SSH for the bulk of my development. For small tweaks to the code I am using `vim` but am doing larger changes on my laptop and then sync them to the Pi either through git or via `scp`. For more intensive projects I may install the [VSCode Remote Development Extension](#) so that I can code via VSCode on my laptop directly on the Pi through SSH to save time. Below is an image showing my custom user using `sudo` and proof of the ability to log out + log in.

```
gerritt@arthur:~/repos/realtime-embedded-assignment-2/Feasibility $ sudo whoami
root
gerritt@arthur:~/repos/realtime-embedded-assignment-2/Feasibility $ exit
logout
Connection to arthur.local closed.
repositories $ ssh gerritt@arthur.local
gerritt@arthur.local's password:
Linux arthur 6.12.25-rpt-rpi-v8 #1 SMP PREEMPT Debian 1:6.12.25-1+rpt1 (2025-04-30) aarch64

The programs included with the Debian GNU/Linux system are free software;
the exact distribution terms for each program are described in the
individual files in /usr/share/doc/*/copyright.

Debian GNU/Linux comes with ABSOLUTELY NO WARRANTY, to the extent
permitted by applicable law.
Last login: Wed Jun 18 17:10:31 2025 from 192.168.5.212
gerritt@arthur:~ $
```

C: Make sure you can access graphical tools with MobaXterm or VNC and show that tools you may need in the future work such as editors like "geany" or "eom" (`sudo apt-get install geany`, or `sudo apt-get install eom`), a tool to display graphics in PPM or PGM format. For example, show that our class web page on Firefox (or default browser) and set your home page to <https://sites.google.com/colorado.edu/ecen5623-summer>. Overall, make sure you are comfortable with development, debug, compiler general native or cross-development tools and document and demonstrate that you know them.

For the most part my plan is to develop entirely headless. I understand that the final project involves using a webcam to take pictures but if I need to view the taken pictures I will enable X-11 forwarding so that I can view the images from my laptop.

## Problem 2

Read the paper "Architecture of the Space Shuttle Primary Avionics Software System" [available on Canvas], by Gene Carlow.

A: Provide an explanation and critique of the frequency executive architecture.

The Shuttle PASS was divided up into 8 individual phase/functionality operational sequences (OPS) which are loaded into a mass memory module on the bus. This is due to the fact that PASS was so large that it couldn't fit in the main memory of the flight computer. OPS were loaded into main memory and used so that there would be no more loading during flight/safety critical moments in time. Once loaded, the OPS are managed by the Flight Computer Operating System (FCOS) that manages process/task runtime, I/O, synchronization, redundancy, etc. Part of this operating system is a frequency executive which controls the initiation and phasing of principal functions and I/O. A table-driven dispatching design was used to enable the phasing and sequencing of the OPS tasks to be altered between major mode changes via an update to the dispatcher table. There were a total of 3 frequency executors running with the high priority executor at 25Hz, medium priority at 6.25Hz, and low priority at 0.25Hz. The system was built for high repeatability but once created was difficult to change. The difficulty to change is less of an issue as all human-rated software must go through extensive qualification and the PASS was designed to ease the process of verification.

B: What advantages and disadvantages does the frequency executive have compared to the real-time threading and tasking implementation methods for real-time software systems? Please be specific about the advantages and disadvantages and provide at least three advantages as well as three disadvantages.

### Advantages

1. The tasks within the frequency executive run with precise intervals introducing minimal jitter and leading to highly deterministic behavior. Since all tasks use their entire execution timeslots without any preemption the tasks behave as if they were a harmonic set of tasks in RMS following the same set of task orders.
2. Since the executive is table based, the frequency of each of the tasks is easily changed/tuned based off the mission state allowing for fast reconfiguration of the software. This is made possible by a dispatcher table update (DTU) that allows for tasks that were considered higher priority in another mission phase to be moved to the lower priority executor and vice versa.
3. Since the running tasks in the frequency executive are constrained to their time slots, the executive has a form of built in fault containment that prevents tasks in a fault state from accidentally consuming the CPU. Any form of an overrun in a task is ended at the end of that task's time slot whereas in an RTOS an overrun could bleed over into the window of other tasks causing a cascade of failures.

### Disadvantages

1. One of the strengths listed above can also be considered a disadvantage. The use of a dispatcher table configured for specific mission phases allows for reconfiguration based off mission phase but it does not provide flexibility at run time. If a new service would need to be added/ran at run time, it would be cumbersome or even impossible to pull off in a verified way.
2. Since the frequency executive is not interrupt based, asynchronous events are only processed when the responsible task is ran. This means asynchronous events may not be serviced for a while if the responsible task isn't constantly polling/checkign for the event.
3. The architecture relies on a lot of up front analysis to verify the functionality and feasibility of every configuration of the dispatcher table. Especially to end up with deterministic and highly repeatable

behavior, the software must go through verification during the concept, design, and development phases at a higher level.

## Problem 3

Download feasibility example code and build it on your Raspberry Pi or alternate system of your choice and execute the code. Note that the C code already implements the RM LUB and RM exact feasibility analysis using scheduling point and completion test methods, but we have not studied methods to automate EDF or LLF feasibility beyond hand analysis methods, so for EDF and LLF, rely upon Cheddar and your hand analysis.

A: Compare the tests provided to analysis using Cheddar for the first 4 examples tested by the code.

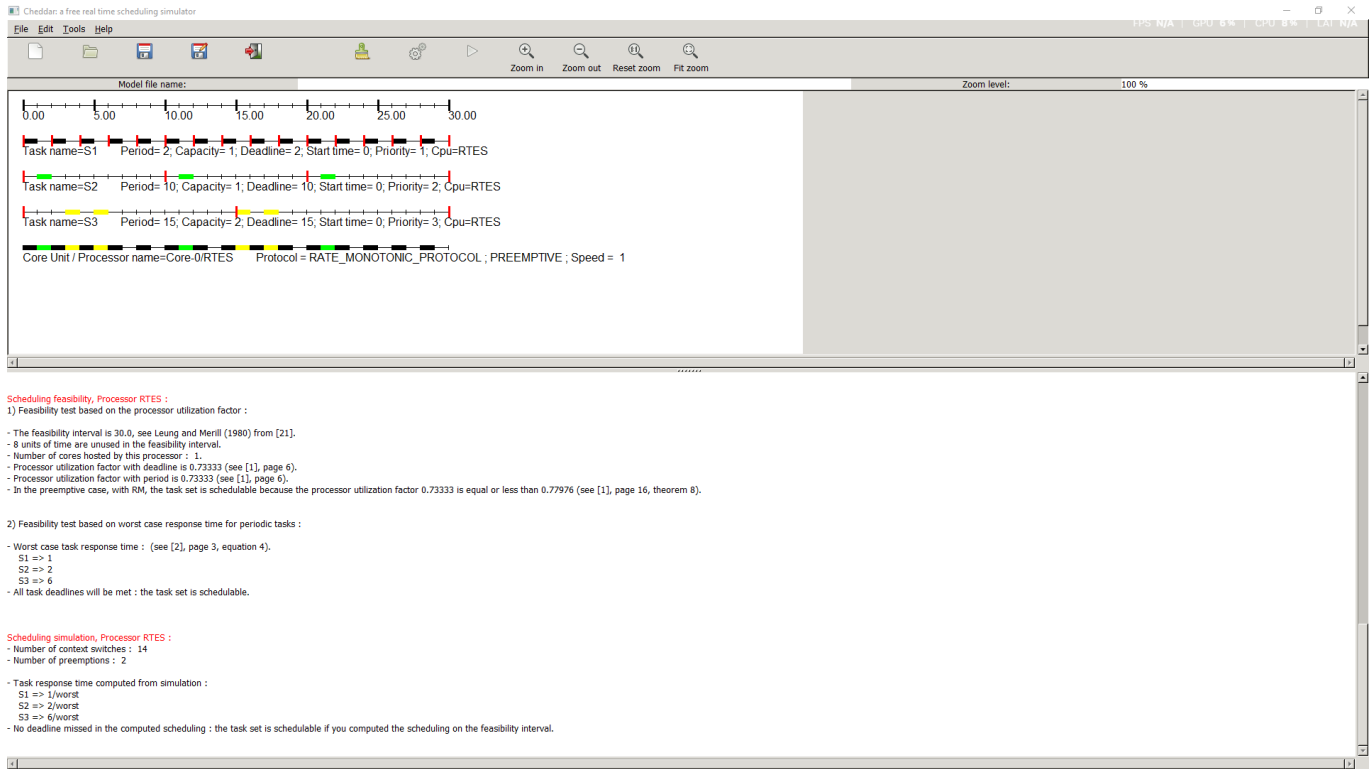
For the first code example provided in the original `feasibility_tests.c` file, we have a set of services, *S1*, *S2*, and *S3* with the following settings: *C1=1*, *C2=1*, *C3=2*; *T1=2*, *T2=10*, *T3=15*; *T=D*. The provided output from the test program is:

```
***** Completion Test Feasibility Example
Ex-0 U=73.33% (C1=1, C2=1, C3=2; T1=2, T2=10, T3=15; T=D): CT test FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=2.000000, utility_sum = 0.500000
for 1, wcet=1.000000, period=10.000000, utility_sum = 0.600000
for 2, wcet=2.000000, period=15.000000, utility_sum = 0.733333
utility_sum = 0.733333
LUB = 0.779763
RM LUB FEASIBLE

***** Scheduling Point Feasibility Example
Ex-0 U=73.33% (C1=1, C2=1, C3=2; T1=2, T2=10, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=2.000000, utility_sum = 0.500000
for 1, wcet=1.000000, period=10.000000, utility_sum = 0.600000
for 2, wcet=2.000000, period=15.000000, utility_sum = 0.733333
utility_sum = 0.733333
LUB = 0.779763
RM LUB FEASIBLE
```

Since the total utility of these services, 73.33%, is less than the LUB, 77.976%, they are schedulable. They are also feasible due to both the Completion Test and Scheduling Point test.

Below is a screenshot of the output from running analysis using Cheddar which also calculates the LUB while performing the Completion and Scheduling Point tests. The cheddar analysis agrees with the provided tests for these services.



The second set of services is a set of 3 with the following definition:  $C1=1$ ,  $C2=1$ ,  $C3=2$ ;  $T1=2$ ,  $T2=5$ ,  $T3=7$ ;  $T=D$ . Below is the output of the second set:

#### \*\*\*\*\* Completion Test Feasibility Example

Ex-1  $U=98.57\%$  ( $C1=1$ ,  $C2=1$ ,  $C3=2$ ;  $T1=2$ ,  $T2=5$ ,  $T3=7$ ;  $T=D$ ): INFEASIBLE

for 3, utility\_sum = 0.000000

for 0, wcet=1.000000, period=2.000000, utility\_sum = 0.500000

for 1, wcet=1.000000, period=5.000000, utility\_sum = 0.700000

for 2, wcet=2.000000, period=7.000000, utility\_sum = 0.985714

utility\_sum = 0.985714

LUB = 0.779763

RM LUB INFEASIBLE

#### \*\*\*\*\* Scheduling Point Feasibility Example

Ex-1  $U=98.57\%$  ( $C1=1$ ,  $C2=1$ ,  $C3=2$ ;  $T1=2$ ,  $T2=5$ ,  $T3=7$ ;  $T=D$ ): INFEASIBLE

for 3, utility\_sum = 0.000000

for 0, wcet=1.000000, period=2.000000, utility\_sum = 0.500000

for 1, wcet=1.000000, period=5.000000, utility\_sum = 0.700000

for 2, wcet=2.000000, period=7.000000, utility\_sum = 0.985714

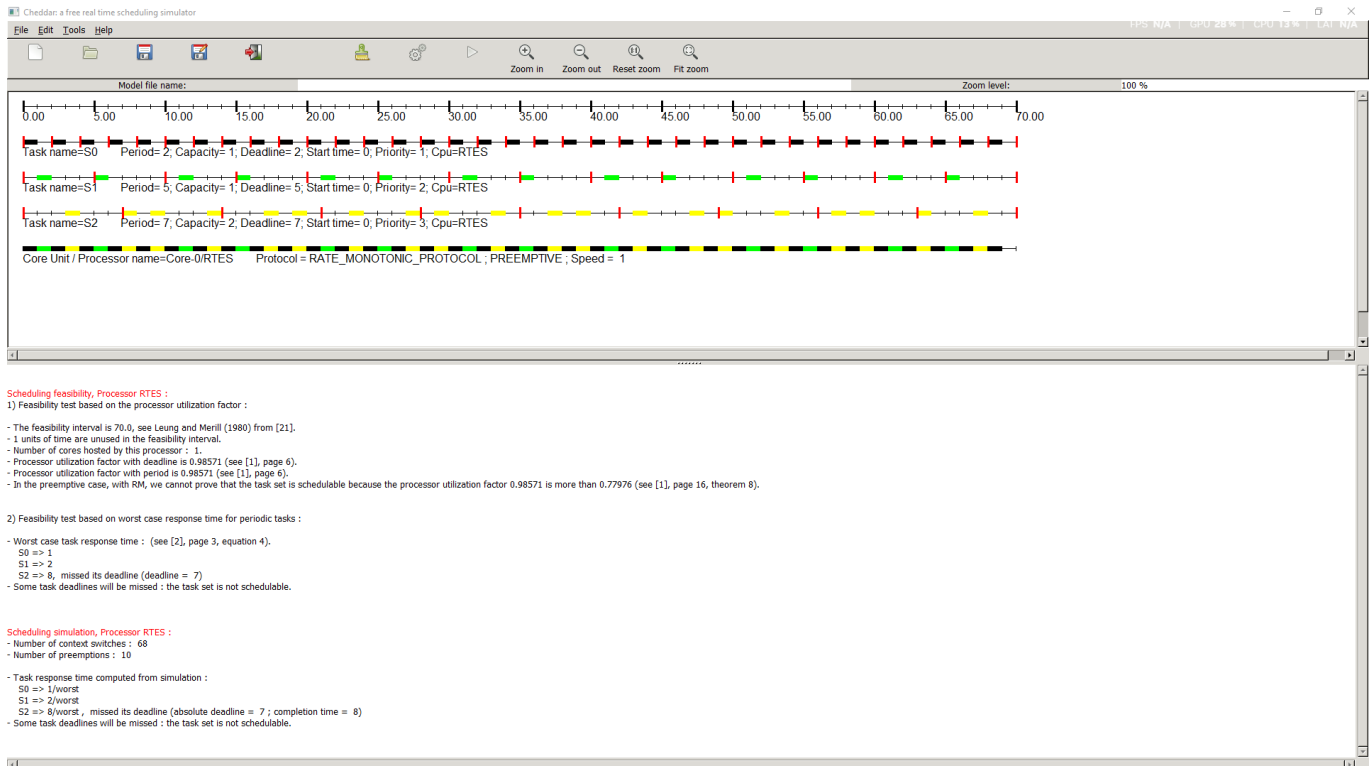
utility\_sum = 0.985714

LUB = 0.779763

RM LUB INFEASIBLE

With all three of the tests that are performed, the set of services fails all 3. The LUB fails due to the the total utility being  $98.57\%$  while the calculated LUB for 3 services is  $77.97\%$ . This alone does not mean the services are unschedulable but since the Completion and Scheduling Point tests fail as well, the examples are infeasible.

The Cheddar analysis agrees with this test as well. It acknowledges that the utility is greater than the LUB and that it is not feasible due to the other two tests.



The third example, with the service settings  $C1=1$ ,  $C2=1$ ,  $C3=1$ ,  $C4=2$ ;  $T1=2$ ,  $T2=5$ ,  $T3=7$ ,  $T4=13$ ;  $T=D$ , can be found below:

#### \*\*\*\*\* Completion Test Feasibility Example

Ex-2  $U=99.67\%$  ( $C1=1$ ,  $C2=1$ ,  $C3=1$ ,  $C4=2$ ;  $T1=2$ ,  $T2=5$ ,  $T3=7$ ,  $T4=13$ ;  $T=D$ ): INFEASIBLE

for 4, utility\_sum = 0.000000

for 0, wcet=1.000000, period=2.000000, utility\_sum = 0.500000

for 1, wcet=1.000000, period=5.000000, utility\_sum = 0.700000

for 2, wcet=1.000000, period=7.000000, utility\_sum = 0.842857

for 3, wcet=2.000000, period=13.000000, utility\_sum = 0.996703

utility\_sum = 0.996703

LUB = 0.756828

RM LUB INFEASIBLE

#### \*\*\*\*\* Scheduling Point Feasibility Example

Ex-2  $U=99.67\%$  ( $C1=1$ ,  $C2=1$ ,  $C3=1$ ,  $C4=2$ ;  $T1=2$ ,  $T2=5$ ,  $T3=7$ ,  $T4=13$ ;  $T=D$ ): INFEASIBLE

for 4, utility\_sum = 0.000000

for 0, wcet=1.000000, period=2.000000, utility\_sum = 0.500000

for 1, wcet=1.000000, period=5.000000, utility\_sum = 0.700000

for 2, wcet=1.000000, period=7.000000, utility\_sum = 0.842857

for 3, wcet=2.000000, period=13.000000, utility\_sum = 0.996703

utility\_sum = 0.996703

LUB = 0.756828

RM LUB INFEASIBLE

With this set of 4 services, the utility is 99.67% which is greatly exceeding the LUB of 75.68%. Because of this, the other two tests must be performed which both fail for these services.

Cheddar: a free real time scheduling simulator

File Edit Tools Help

Model file name: example2 Zoom in Zoom out Reset zoom Fit zoom Zoom level: 100 %

Task name=S0 Period= 2, Capacity= 1, Deadline= 2, Start time= 0, Priority= 1, Cpu=RTES

Task name=S1 Period= 5, Capacity= 1, Deadline= 5, Start time= 0, Priority= 2, Cpu=RTES

Task name=S2 Period= 7, Capacity= 1, Deadline= 7, Start time= 0, Priority= 3, Cpu=RTES

Task name=S3 Period= 13, Capacity= 2, Deadline= 13, Start time= 0, Priority= 4, Cpu=RTES

Core Unit / Processor name=Core-0/RTES Protocol=RATE\_MONOTONIC\_PROTOCOL, PREEMPTIVE, Speed= 1

Scheduling feasibility, Processor RTES :

1) Feasibility test based on the processor utilization factor :

- The feasibility interval is 910.0, see Leung and Merill (1980) from [21].
- 3 units of time are unused in the feasibility interval.
- Number of cores hosted by this processor : 1.
- Processor utilization factor with deadline is 0.99670 (see [1], page 6).
- Processor utilization factor with period is 0.99670 (see [1], page 6).
- In the preemptive case, with RM, we cannot prove that the task set is schedulable because the processor utilization factor 0.99670 is more than 0.75683 (see [1], page 16, theorem 8).

2) Feasibility test based on worst case response time for periodic tasks :

- Worst case task response time : (see [2], page 3, equation 4).
- S0 => 1
- S1 => 2
- S2 => 4
- S3 => 16, missed its deadline (deadline = 13)
- Some task deadlines will be missed : the task set is not schedulable.

Scheduling simulation, Processor RTES :

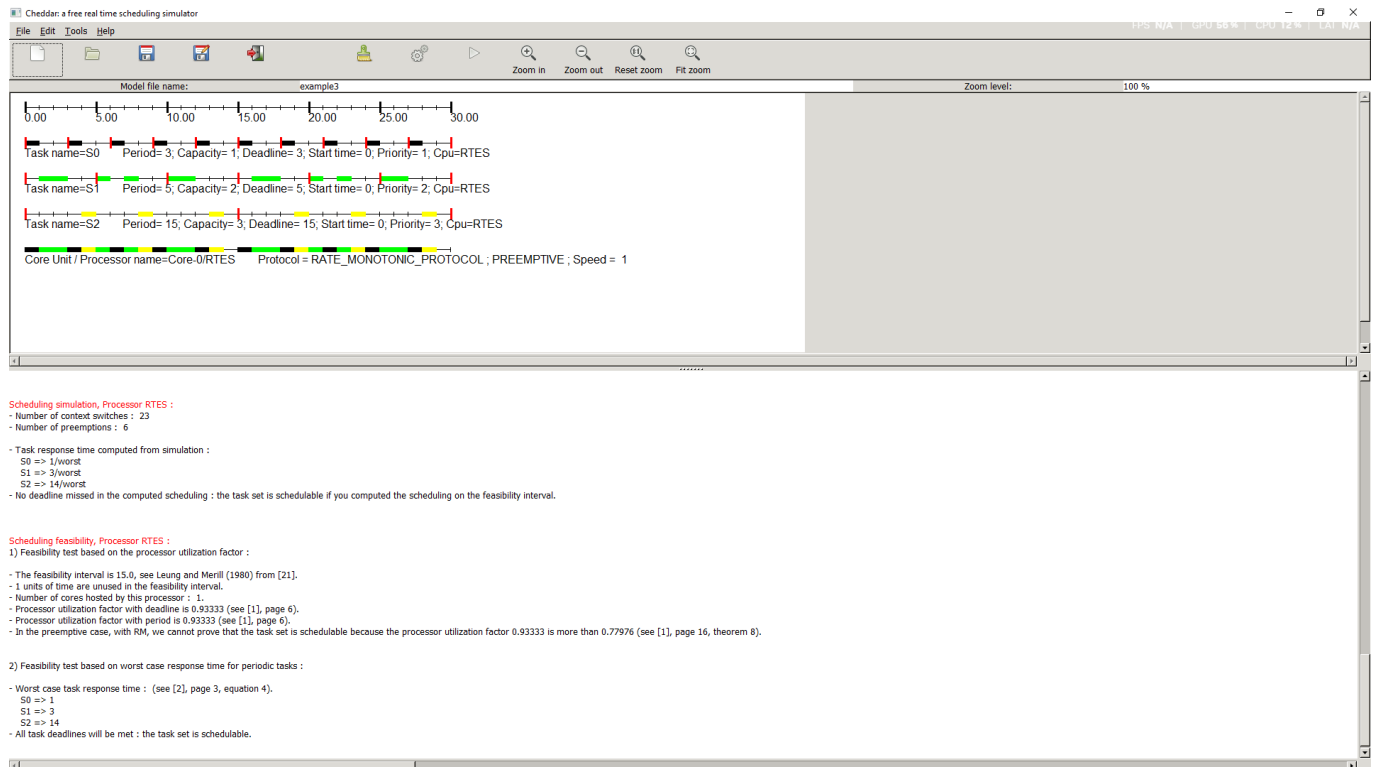
- Number of context switches : 904
- Number of preemptions : 70
- Task response time computed from simulation :
  - S0 => 1/worst
  - S1 => 2/worst
  - S2 => 4/worst
  - S3 => 16/worst, missed its deadline (absolute deadline = 13 ; completion time = 14), missed its deadline (absolute deadline = 26 ; completion time = 28), missed its deadline (absolute deadline = 39 ; completion time = 40), missed its deadline (absolute deadline = 52 ; completion time = 54), missed its deadline (absolute deadline = 65 ; completion time = 66), missed its deadline (absolute deadline = 78 ; completion time = 79), missed its deadline (absolute deadline = 91 ; completion time = 92), missed its deadline (absolute deadline = 104 ; completion time = 105), missed its deadline (absolute deadline = 117 ; completion time = 118), missed its deadline (absolute deadline = 130 ; completion time = 131), missed its deadline (absolute deadline = 143 ; completion time = 144), missed its deadline (absolute deadline = 156 ; completion time = 157), missed its deadline (absolute deadline = 169 ; completion time = 170), missed its deadline (absolute deadline = 182 ; completion time = 183), missed its deadline (absolute deadline = 195 ; completion time = 196), missed its deadline (absolute deadline = 208 ; completion time = 209), missed its deadline (absolute deadline = 221 ; completion time = 222), missed its deadline (absolute deadline = 234 ; completion time = 235), missed its deadline (absolute deadline = 247 ; completion time = 248), missed its deadline (absolute deadline = 260 ; completion time = 261), missed its deadline (absolute deadline = 273 ; completion time = 274), missed its deadline (absolute deadline = 286 ; completion time = 287), missed its deadline (absolute deadline = 299 ; completion time = 300), missed its deadline (absolute deadline = 312 ; completion time = 313), missed its deadline (absolute deadline = 325 ; completion time = 326), missed its deadline (absolute deadline = 338 ; completion time = 339), missed its deadline (absolute deadline = 351 ; completion time = 352), missed its deadline (absolute deadline = 364 ; completion time = 365), missed its deadline (absolute deadline = 377 ; completion time = 378), missed its deadline (absolute deadline = 390 ; completion time = 391), missed its deadline (absolute deadline = 403 ; completion time = 404), missed its deadline (absolute deadline = 416 ; completion time = 417), missed its deadline (absolute deadline = 429 ; completion time = 430), missed its deadline (absolute deadline = 442 ; completion time = 443), missed its deadline (absolute deadline = 455 ; completion time = 456), missed its deadline (absolute deadline = 468 ; completion time = 469), missed its deadline (absolute deadline = 481 ; completion time = 482), missed its deadline (absolute deadline = 494 ; completion time = 495), missed its deadline (absolute deadline = 507 ; completion time = 508), missed its deadline (absolute deadline = 520 ; completion time = 521), missed its deadline (absolute deadline = 533 ; completion time = 534), missed its deadline (absolute deadline = 546 ; completion time = 547), missed its deadline (absolute deadline = 559 ; completion time = 560), missed its deadline (absolute deadline = 572 ; completion time = 573), missed its deadline (absolute deadline = 585 ; completion time = 586), missed its deadline (absolute deadline = 598 ; completion time = 599), missed its deadline (absolute deadline = 611 ; completion time = 612), missed its deadline (absolute deadline = 624 ; completion time = 625), missed its deadline (absolute deadline = 637 ; completion time = 638), missed its deadline (absolute deadline = 650 ; completion time = 651), missed its deadline (absolute deadline = 663 ; completion time = 664), missed its deadline (absolute deadline = 676 ; completion time = 677), missed its deadline (absolute deadline = 689 ; completion time = 690), missed its deadline (absolute deadline = 702 ; completion time = 703), missed its deadline (absolute deadline = 715 ; completion time = 716), missed its deadline (absolute deadline = 728 ; completion time = 729), missed its deadline (absolute deadline = 741 ; completion time = 742), missed its deadline (absolute deadline = 754 ; completion time = 755), missed its deadline (absolute deadline = 767 ; completion time = 768), missed its deadline (absolute deadline = 780 ; completion time = 781), missed its deadline (absolute deadline = 793 ; completion time = 794), missed its deadline (absolute deadline = 806 ; completion time = 807), missed its deadline (absolute deadline = 819 ; completion time = 820), missed its deadline (absolute deadline = 832 ; completion time = 833), missed its deadline (absolute deadline = 845 ; completion time = 846), missed its deadline (absolute deadline = 858 ; completion time = 859), missed its deadline (absolute deadline = 871 ; completion time = 872), missed its deadline (absolute deadline = 884 ; completion time = 885), missed its deadline (absolute deadline = 897 ; completion time = 898), missed its deadline (absolute deadline = 910 ; completion time = 911), missed its deadline (absolute deadline = 923 ; completion time = 924), missed its deadline (absolute deadline = 936 ; completion time = 937), missed its deadline (absolute deadline = 949 ; completion time = 950), missed its deadline (absolute deadline = 962 ; completion time = 963), missed its deadline (absolute deadline = 975 ; completion time = 976), missed its deadline (absolute deadline = 988 ; completion time = 989), missed its deadline (absolute deadline = 1001 ; completion time = 1002), missed its deadline (absolute deadline = 1014 ; completion time = 1015), missed its deadline (absolute deadline = 1027 ; completion time = 1028), missed its deadline (absolute deadline = 1040 ; completion time = 1041), missed its deadline (absolute deadline = 1053 ; completion time = 1054), missed its deadline (absolute deadline = 1066 ; completion time = 1067), missed its deadline (absolute deadline = 1079 ; completion time = 1080), missed its deadline (absolute deadline = 1092 ; completion time = 1093), missed its deadline (absolute deadline = 1105 ; completion time = 1106), missed its deadline (absolute deadline = 1118 ; completion time = 1119), missed its deadline (absolute deadline = 1131 ; completion time = 1132), missed its deadline (absolute deadline = 1144 ; completion time = 1145), missed its deadline (absolute deadline = 1157 ; completion time = 1158), missed its deadline (absolute deadline = 1170 ; completion time = 1171), missed its deadline (absolute deadline = 1183 ; completion time = 1184), missed its deadline (absolute deadline = 1196 ; completion time = 1197), missed its deadline (absolute deadline = 1209 ; completion time = 1210), missed its deadline (absolute deadline = 1222 ; completion time = 1223), missed its deadline (absolute deadline = 1235 ; completion time = 1236), missed its deadline (absolute deadline = 1248 ; completion time = 1249), missed its deadline (absolute deadline = 1261 ; completion time = 1262), missed its deadline (absolute deadline = 1274 ; completion time = 1275), missed its deadline (absolute deadline = 1287 ; completion time = 1288), missed its deadline (absolute deadline = 1300 ; completion time = 1301), missed its deadline (absolute deadline = 1313 ; completion time = 1314), missed its deadline (absolute deadline = 1326 ; completion time = 1327), missed its deadline (absolute deadline = 1339 ; completion time = 1340), missed its deadline (absolute deadline = 1352 ; completion time = 1353), missed its deadline (absolute deadline = 1365 ; completion time = 1366), missed its deadline (absolute deadline = 1378 ; completion time = 1379), missed its deadline (absolute deadline = 1391 ; completion time = 1392), missed its deadline (absolute deadline = 1404 ; completion time = 1405), missed its deadline (absolute deadline = 1417 ; completion time = 1418), missed its deadline (absolute deadline = 1430 ; completion time = 1431), missed its deadline (absolute deadline = 1443 ; completion time = 1444), missed its deadline (absolute deadline = 1456 ; completion time = 1457), missed its deadline (absolute deadline = 1469 ; completion time = 1470), missed its deadline (absolute deadline = 1482 ; completion time = 1483), missed its deadline (absolute deadline = 1495 ; completion time = 1496), missed its deadline (absolute deadline = 1508 ; completion time = 1509), missed its deadline (absolute deadline = 1521 ; completion time = 1522), missed its deadline (absolute deadline = 1534 ; completion time = 1535), missed its deadline (absolute deadline = 1547 ; completion time = 1548), missed its deadline (absolute deadline = 1560 ; completion time = 1561), missed its deadline (absolute deadline = 1573 ; completion time = 1574), missed its deadline (absolute deadline = 1586 ; completion time = 1587), missed its deadline (absolute deadline = 1599 ; completion time = 1599), missed its deadline (absolute deadline = 1612 ; completion time = 1613), missed its deadline (absolute deadline = 1625 ; completion time = 1626), missed its deadline (absolute deadline = 1638 ; completion time = 1639), missed its deadline (absolute deadline = 1651 ; completion time = 1652), missed its deadline (absolute deadline = 1664 ; completion time = 1665), missed its deadline (absolute deadline = 1677 ; completion time = 1678), missed its deadline (absolute deadline = 1690 ; completion time = 1691), missed its deadline (absolute deadline = 1703 ; completion time = 1704), missed its deadline (absolute deadline = 1716 ; completion time = 1717), missed its deadline (absolute deadline = 1729 ; completion time = 1730), missed its deadline (absolute deadline = 1742 ; completion time = 1743), missed its deadline (absolute deadline = 1755 ; completion time = 1756), missed its deadline (absolute deadline = 1768 ; completion time = 1769), missed its deadline (absolute deadline = 1781 ; completion time = 1782), missed its deadline (absolute deadline = 1794 ; completion time = 1795), missed its deadline (absolute deadline = 1807 ; completion time = 1808), missed its deadline (absolute deadline = 1820 ; completion time = 1821), missed its deadline (absolute deadline = 1833 ; completion time = 1834), missed its deadline (absolute deadline = 1846 ; completion time = 1847), missed its deadline (absolute deadline = 1859 ; completion time = 1860), missed its deadline (absolute deadline = 1872 ; completion time = 1873), missed its deadline (absolute deadline = 1885 ; completion time = 1886), missed its deadline (absolute deadline = 1898 ; completion time = 1899), missed its deadline (absolute deadline = 1911 ; completion time = 1912), missed its deadline (absolute deadline = 1924 ; completion time = 1925), missed its deadline (absolute deadline = 1937 ; completion time = 1938), missed its deadline (absolute

```
***** Completion Test Feasibility Example
Ex-3 U=93.33% (C1=1, C2=2, C3=3; T1=3, T2=5, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=3.000000, utility_sum = 0.333333
for 1, wcet=2.000000, period=5.000000, utility_sum = 0.733333
for 2, wcet=3.000000, period=15.000000, utility_sum = 0.933333
utility_sum = 0.933333
LUB = 0.779763
RM LUB INFEASIBLE

***** Scheduling Point Feasibility Example
Ex-3 U=93.33% (C1=1, C2=2, C3=3; T1=3, T2=5, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=3.000000, utility_sum = 0.333333
for 1, wcet=2.000000, period=5.000000, utility_sum = 0.733333
for 2, wcet=3.000000, period=15.000000, utility_sum = 0.933333
utility_sum = 0.933333
LUB = 0.779763
RM LUB INFEASIBLE
```

6 / 16

Cheddar also shows that the services will never miss a deadline despite being over the LUB.



TODO:

B: Now, implement additional examples for practice [6 more] of your interest from those that we reviewed in class (found here). Complete analysis using Cheddar RM. In cases where RM fails (fixed priority does not work), test EDF or LLF dynamic priority policies using hand analysis or Cheddar to see if either of those succeeds and if so, explain why. Cheddar uses both service simulations over the LCM of the periods as well as feasibility analysis based on the RM LUB and scheduling-point/completion-test algorithms, referred to as "Worst Case Analysis."

The first in class example that I checked was `sched-example-0-feasible-above-LUB-disharmonic` which is a set of 3 services with the the following definition:  $C1=1, C2=1, C3=2; T1=2, T2=5, T3=15; T=D$ . The services have a total utility of **83.33%** which is over the LUB of **77.976%**. When using Cheddar to analyze the services, it shows that fixed priority is feasible despite being over the LUB and the services being disharmonic.

Below is the output of the feasibility tests and a capture of the cheddar analysis for fixed priority:

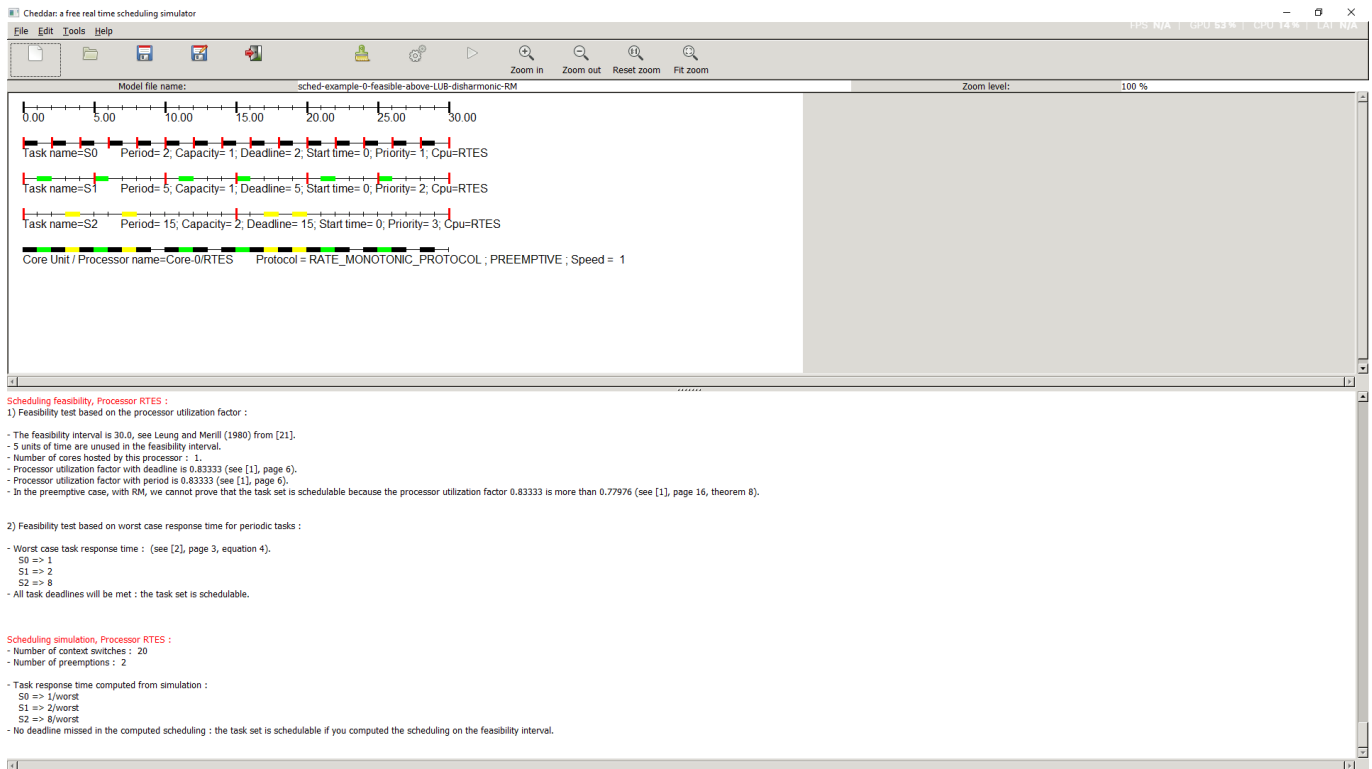
```
***** Completion Test Feasibility Example
sched-example-0-feasible-above-LUB-disharmonic
U=83.33% (C1=1, C2=1, C3=2; T1=2, T2=5, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=2.000000, utility_sum = 0.500000
for 1, wcet=1.000000, period=5.000000, utility_sum = 0.700000
for 2, wcet=2.000000, period=15.000000, utility_sum = 0.833333
utility_sum = 0.833333
LUB = 0.779763
RM LUB INFEASIBLE

***** Scheduling Point Feasibility Example
```

```

sched-example-0-feasible-above-LUB-disharmonic
U=83.33% (C1=1, C2=1, C3=2; T1=2, T2=5, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=2.000000, utility_sum = 0.500000
for 1, wcet=1.000000, period=5.000000, utility_sum = 0.700000
for 2, wcet=2.000000, period=15.000000, utility_sum = 0.833333
utility_sum = 0.833333
LUB = 0.779763
RM LUB INFEASIBLE

```



It can be seen in the Cheddar output that the worst case response time for all of the services is less than or equal to all of the periods of the services meaning they are feasible.

The second set of services that I chose to analyze is the `sched-example-3-above-LUB-harmonic` set with the following settings: `C1=1, C2=2, C3=3; T1=3, T2=5, T3=15; T=D`. The utility of the services, `93.33%`, is above the LUB for a set of 3 services, `77.97%`. When performing worst case analysis, it can be seen that the services are still feasible despite being over the LUB. The output can be seen here:

```

***** Completion Test Feasibility Example
sched-example-3-above-LUB-harmonic
U=93.33% (C1=1, C2=2, C3=3; T1=3, T2=5, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=3.000000, utility_sum = 0.333333
for 1, wcet=2.000000, period=5.000000, utility_sum = 0.733333
for 2, wcet=3.000000, period=15.000000, utility_sum = 0.933333
utility_sum = 0.933333
LUB = 0.779763
RM LUB INFEASIBLE

***** Scheduling Point Feasibility Example

```

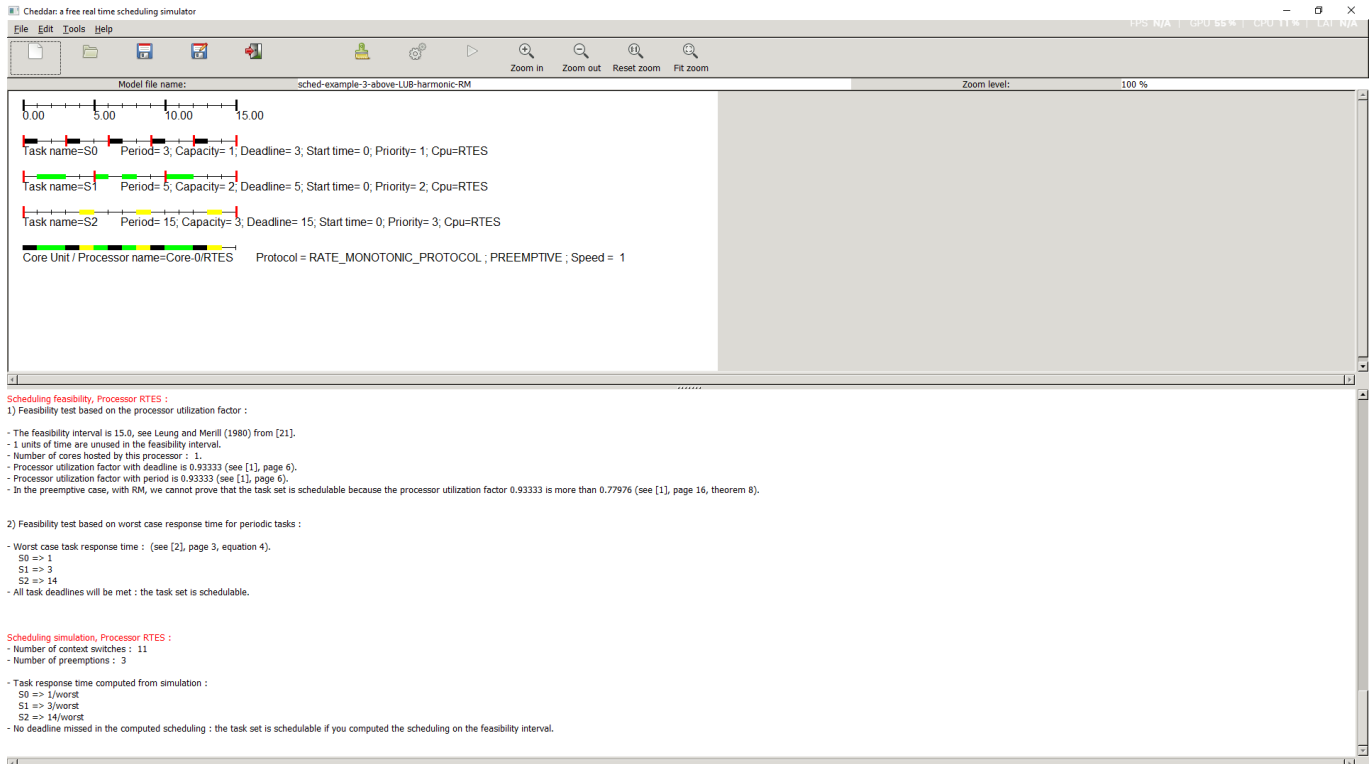


```

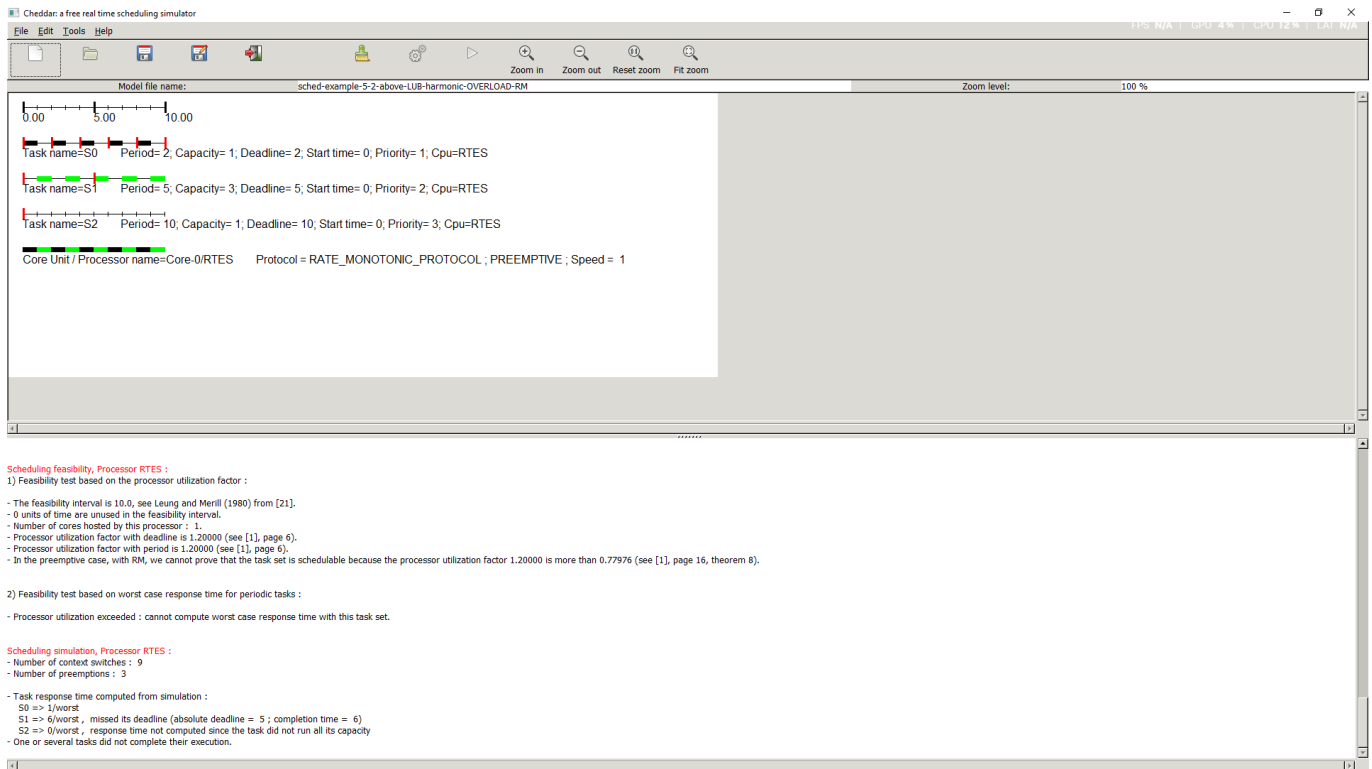
sched-example-3-above-LUB-harmonic
U=93.33% (C1=1, C2=1, C3=2; T1=2, T2=5, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=3.000000, utility_sum = 0.333333
for 1, wcet=2.000000, period=5.000000, utility_sum = 0.733333
for 2, wcet=3.000000, period=15.000000, utility_sum = 0.933333
utility_sum = 0.933333
LUB = 0.779763
RM LUB INFEASIBLE

```

The Cheddar analysis agrees with the tests which is seen here:



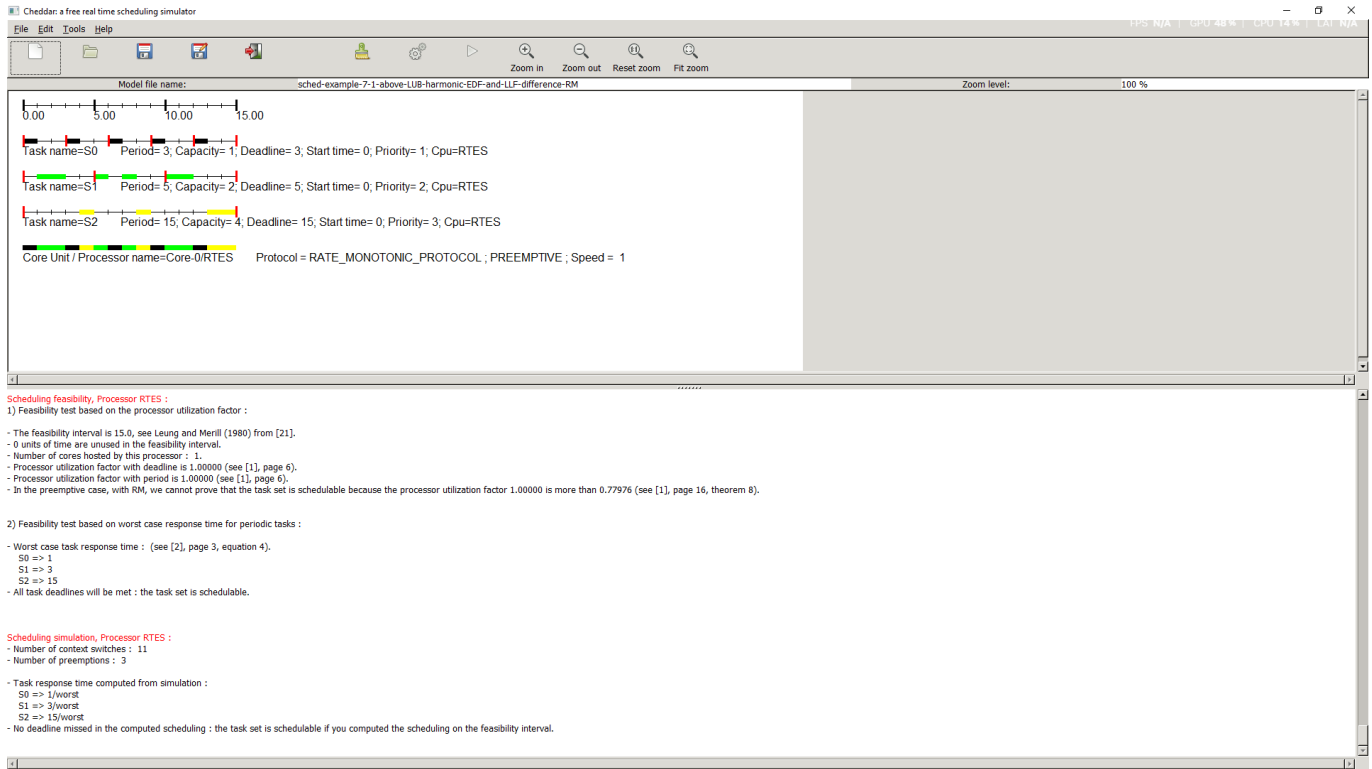
The third example that I picked was **sched-example-5-2-above-LUB-harmonic-OVERLOAD** which is an example of overloading the CPU. The services are defined with **C1=1, C2=1, C3=2; T1=2, T2=5, T3=15; T=D** which has a total utility of **120%**. Because of this overload of the CPU, fixed priority will always fail to schedule the tasks. Unfortunately, dynamic priority scheduling will also fail because it is unable to schedule any set of services with a utility > 100%. Below is the cheddar output for fixed priority scheduling. All scheduling methods fail due to the over 100% utility:



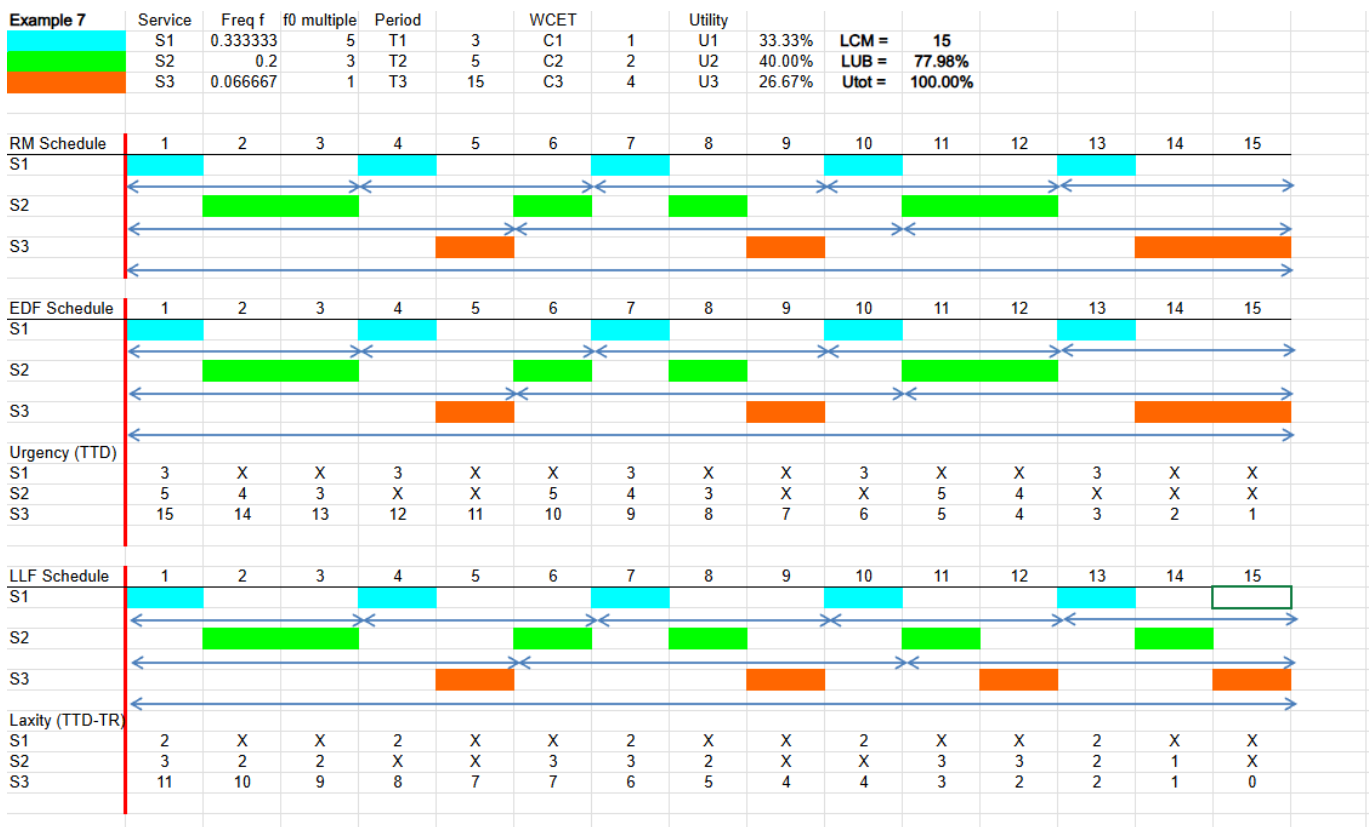
The fourth example that I picked was `sched-example-7-1-above-LUB-harmonic-EDF-and-LLF-difference` with services defined with:  $C1=1$ ,  $C2=1$ ,  $C3=2$ ;  $T1=2$ ,  $T2=5$ ,  $T3=15$ ;  $T=D$ . This is an example of where RM can pass despite having a 100% utility. This is also an example of how LLF and EDF can have a different order of task execution. The LUB is **77.97%** while the total utility of the services is 100%. The testing output and Cheddar analysis can be found below for fixed priority showing that despite failing the LUB check, the tasks are schedulable:

```
***** Completion Test Feasibility Example
sched-example-7-1-above-LUB-harmonic-EDF-and-LLF-difference
U=100.00% (C1=1, C2=2, C3=4; T1=3, T2=5, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=3.000000, utility_sum = 0.333333
for 1, wcet=2.000000, period=5.000000, utility_sum = 0.733333
for 2, wcet=4.000000, period=15.000000, utility_sum = 1.000000
utility_sum = 1.000000
LUB = 0.779763
RM LUB INFEASIBLE

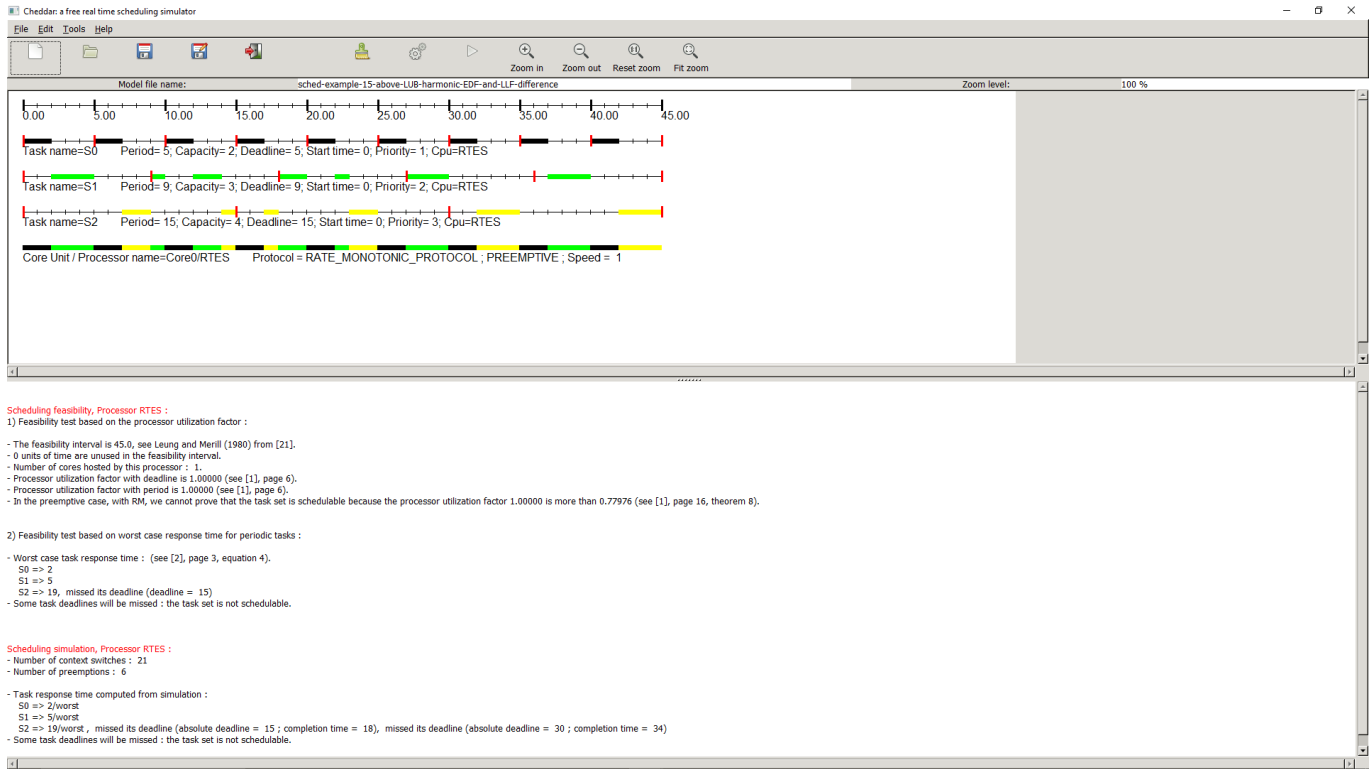
***** Scheduling Point Feasibility Example
sched-example-7-1-above-LUB-harmonic-EDF-and-LLF-difference
U=100.00% (C1=1, C2=1, C3=2; T1=2, T2=5, T3=15; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=3.000000, utility_sum = 0.333333
for 1, wcet=2.000000, period=5.000000, utility_sum = 0.733333
for 2, wcet=4.000000, period=15.000000, utility_sum = 1.000000
utility_sum = 1.000000
LUB = 0.779763
RM LUB INFEASIBLE
```



EDF and LLF diverge at the 12th time slice when S3 having a lower Laxity than S2 causes it to preempt S2. It causes a new sequence of execution for the remainder of the LCM and can be seen below:



For the fifth example I picked I chose `sched-example-15-above-LUB-harmonic-EDF-and-LLF-difference` which defines 3 tasks as such:  $C1=2$ ,  $C2=3$ ,  $C3=4$ ;  $T1=5$ ,  $T2=9$ ,  $T3=15$ ;  $T=D$ . The total utility of the tasks is **100%** while the LUB is **77.97%**. When running the test RM tests you find that they are not schedulable as the third service, S3, misses its deadline by having a worst case response time of 18 while its deadline is 15. This can be seen in the Cheddar output below.

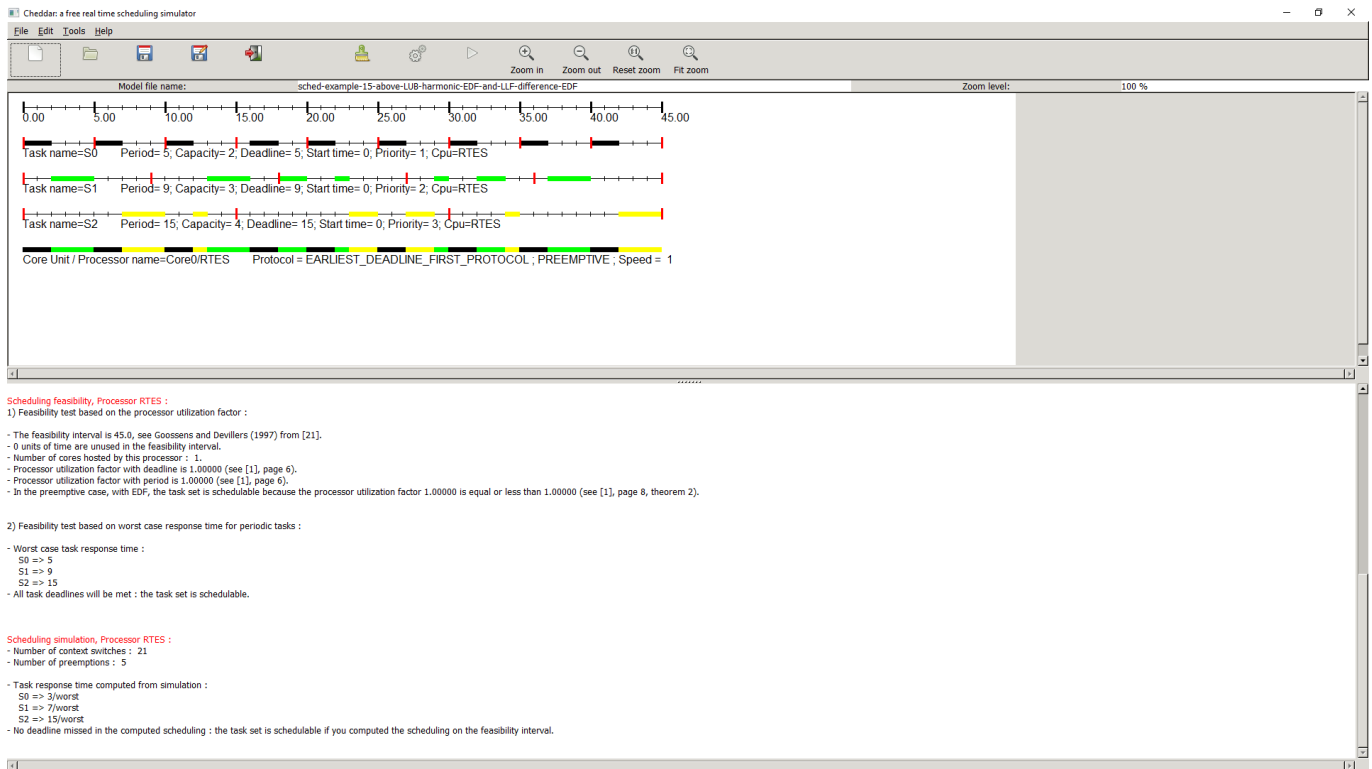


The result of the altered feasibility test set agrees with this analysis with the following output:

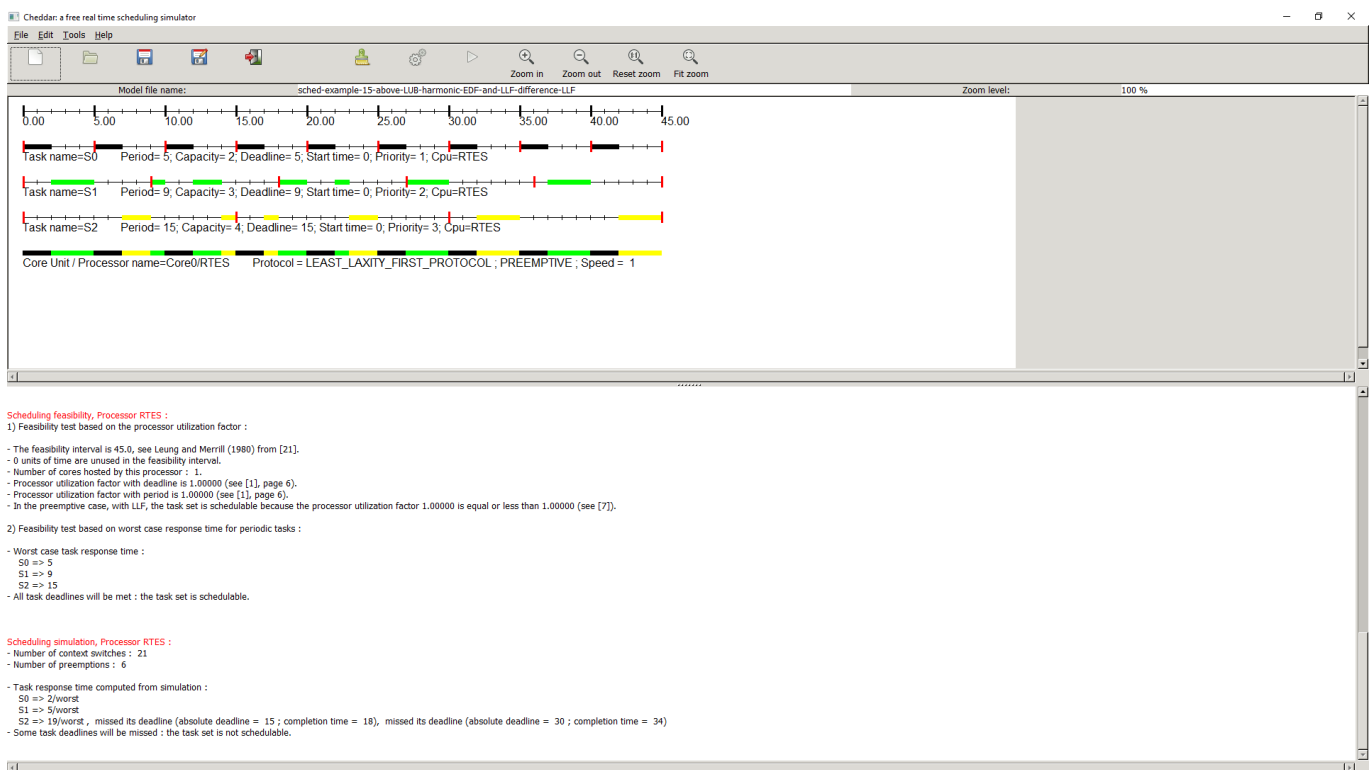
```
***** Completion Test Feasibility Example
sched-example-15-above-LUB-harmonic-EDF-and-LLF-difference
U=100.00% (C1=2, C2=3, C3=4; T1=5, T2=9, T3=15; T=D): INFEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=2.000000, period=5.000000, utility_sum = 0.400000
for 1, wcet=3.000000, period=9.000000, utility_sum = 0.733333
for 2, wcet=4.000000, period=15.000000, utility_sum = 1.000000
utility_sum = 1.000000
LUB = 0.779763
RM LUB INFEASIBLE

***** Scheduling Point Feasibility Example
sched-example-15-above-LUB-harmonic-EDF-and-LLF-difference
U=100.00% (C1=2, C2=3, C3=4; T1=5, T2=9, T3=15; T=D): INFEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=2.000000, period=5.000000, utility_sum = 0.400000
for 1, wcet=3.000000, period=9.000000, utility_sum = 0.733333
for 2, wcet=4.000000, period=15.000000, utility_sum = 1.000000
utility_sum = 1.000000
LUB = 0.779763
RM LUB INFEASIBLE
```

When using cheddar to perform analysis on the feasibility of EDF, it is found that over the LCM of the system that it is schedulable. The worst case response time of each task is found to be their periods. This can be seen below:



When using Cheddar to perform analysis on the feasibility of LLF, it reports that the system is sechedulable based on worst case response time but when performing a simulation of the schedule, S3 is unschedulable due to its worst response time in the simulation being 19 while its deadline is 15. This can be seen below in the image:



The sixth and final example that I picked was **sched-example-14-1-above-LUB** with a set of services defined with: **C1=1, C2=1, C3=1; T1=2, T2=4, T3=7; T=D**. The services have a total utility of **89.28%** which is above the LUB of **77.97%**. Despite being over the LUB, the services pass both the Scheduling Point and the Completion test with an output seen below:

```

***** Completion Test Feasibility Example
sched-example-14-1-above-LUB
U=89.29% (C1=1, C2=1, C3=1; T1=2, T2=4, T3=7; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=2.000000, utility_sum = 0.500000
for 1, wcet=1.000000, period=4.000000, utility_sum = 0.750000
for 2, wcet=1.000000, period=7.000000, utility_sum = 0.892857
utility_sum = 0.892857
LUB = 0.779763
RM LUB INFEASIBLE

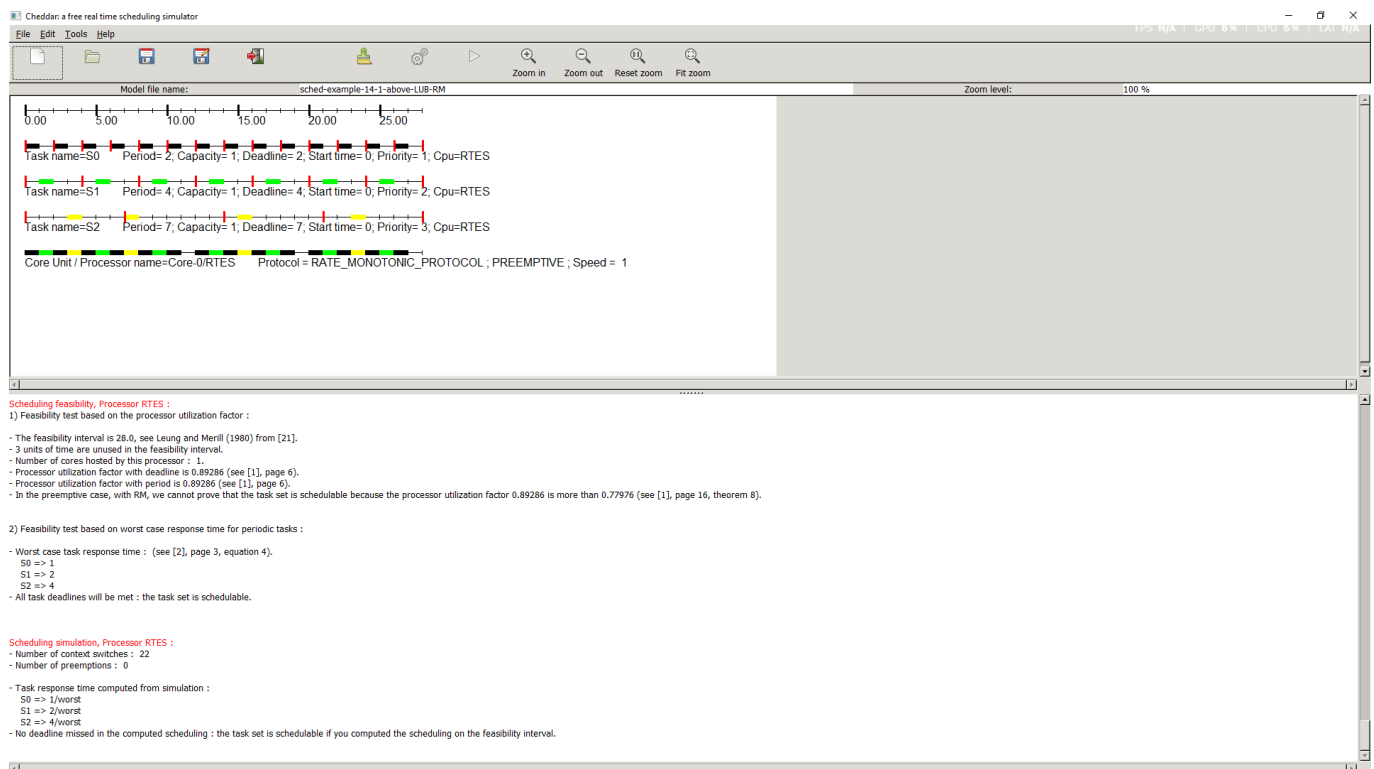
```

```

***** Scheduling Point Feasibility Example
sched-example-14-1-above-LUB
U=89.29% (C1=1, C2=1, C3=1; T1=2, T2=4, T3=7; T=D): FEASIBLE
for 3, utility_sum = 0.000000
for 0, wcet=1.000000, period=2.000000, utility_sum = 0.500000
for 1, wcet=1.000000, period=4.000000, utility_sum = 0.750000
for 2, wcet=1.000000, period=7.000000, utility_sum = 0.892857
utility_sum = 0.892857
LUB = 0.779763
RM LUB INFEASIBLE

```

The RM Cheddar analysis agrees with the output of the feasibility tests showing that the worst case response times of the services are 1, 2, and 4, respectively. There is also enough time laxity in the system for it to have slack as seen in the cheddar image below:



C: Does your modified feasibility code agree with Cheddar analysis for additional cases? Why or why not?

The Cheddar analysis appears to agree with the modified `feasibility_tests.c` file. On all of the chosen examples the utilization, LUB, and worst case analysis result in the same findings.

Read Chapter 3 of the textbook.

A: Provide 3 constraints that are made in the RM LUB derivation and 3 assumptions as documented in the Liu and Layland paper and in Chapter 3 of RTECS with Linux and RTOS. Describe whether you think each is reasonable for actual practice or whether you think each is only applicable to an idealized model of practice

- Assumptions
  - All services requested on periodic basis, the period is constant
    - This assumption is very reasonable for some a mathematical verification formula/conservative check. For the majority of embedded software that I have worked on the work is fairly periodic aside from some interrupt processing so this is applicable.
  - Completion-time < period
    - This assumption is the basis of realtime software verification. We have X amount of work that must be completed in Y amount of time on a periodic basis. If it ever runs long we will lose the real time status and possibly lose property or life.
  - Runtime is known and deterministic (WCET may be used)
    - This seems to be more of the idealized model. The frequencies of the bus can change depending on environmental factors, interrupts can fire at times that are unexpected, and runtime constraints can be far from ideal.
- Constraints
  - Deadline = period by definition
    - This once again makes sense due to the desire to validate the absolute worst case of the software. The deadline must be the period in the software model so that if the runtime of that service is equal to its period, it is still within its deadline.
  - Fixed-priority, preemptive, run-to-completion scheduling
    - Once again this is understandable. The LUB is a very basic check that is used as a first check. It cannot be used in a more complex manner testing out dynamic priority like EDF or LLF.

B: Finally, list three key derivation steps in the RM LUB derivation that you either do not understand or that you would consider "tricky" math. Attempt to describe the rationale for those steps as best you can do based upon reading in Chapter 3 of RTECS with Linux and RTOS.

1. Step 3.1 is a little tricky but I believe I am understanding it. What this step is saying is that the execution time of S1 must be less than or equal to the difference between period of T2 and amount of time that T1 can be active during the period of T2. If this wasn't the case, then S1 would be consuming too much of the CPU and would be starving S2 leading to missed deadlines.
2. The steps to get to equation 3.5 are definitely a little tricky. They require having already solved for C2 with equation 2 but it isn't shown how that is achieved. If you use equation 3 to solve for C2 you will end up with a different looking equation. The algebraic steps to go from 3.4 to 3.5 are definitely hard if you haven't done algebra in a while but it is solvable.
3. Step 3.12 is some fairly extensive algebra that needs to be done. It requires solving for both C1 and C2 in previous steps based off the assumptions and constraints made. It makes sense that you want to solve down to as few of variables as possible to simplify the equation but what confuses me on it is

how do you go from 3.12 to 3.13 where the  $f$  variable is defined. I am unsure how the assumption is made that  $f$  is the value that is given to it.