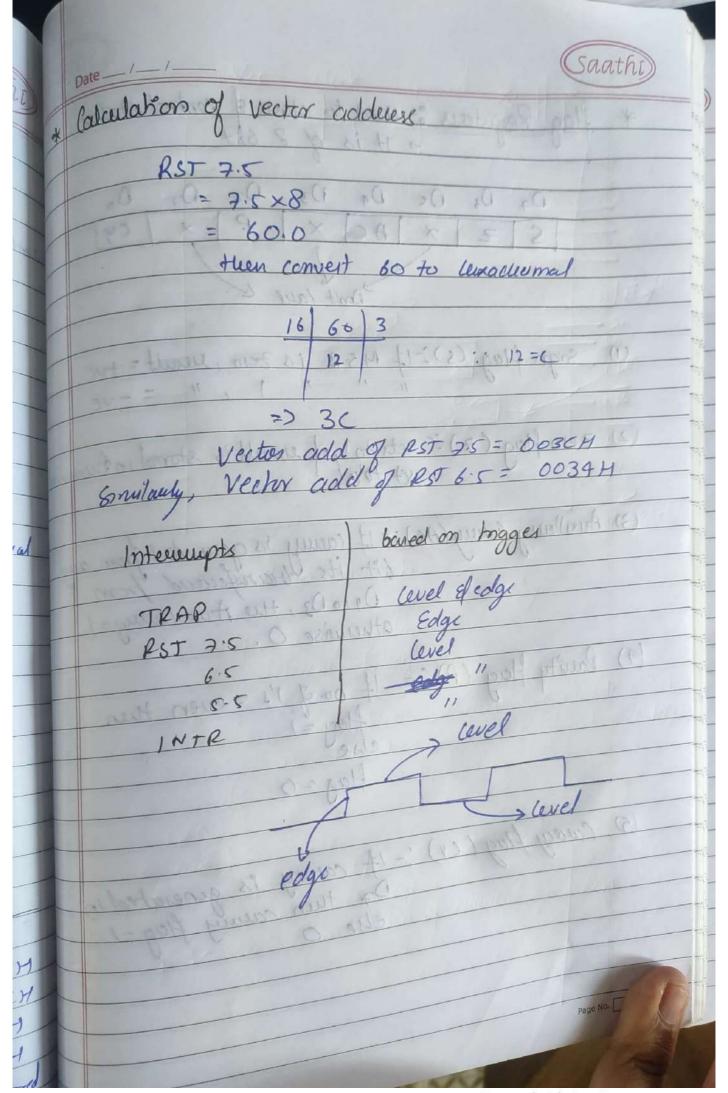
pate / /	
* Nicumprocessor Its a contracto	Gaathi
Nicumprocessos of Its a controlling unit of capable of pentanning to a consists of an ALU Register A	rulous-conjute.
It consists of an ALU Register Array	and contral
* ALU -> Performs Assthmatic of lagical operation data electeved from 40 memory	ation on the
Super device.	Ou on the
CU -3 Conterole the flow of dato & 90 within the compeder	shruckens -
Register avoing >> 17 9s used to execute	
* Registeers over made of flip-flop	
SUPPLIED OF SELECTION OF THE PARTY OF THE PA	258 4
WORKING OF UP (1) fetch Instructions (2) decode (3) e	xecute
A TONE STATE OF THE PARTY OF TH	1300 2
313C of meeting	1062 6
(' 0 0 1 111	
16 69+ Addeuss bus G in 8085 par 8 6it data bus	210 = 1210
Journala = 2 ^A × D A = addeun bus size 0 = desta bus size	20= Mega 230= Giga 240= Texa

37	
	Date_/_/_
	Auclitecture
100-19	county of peuthonism Van
340	1 true of cel = (1) Cost effective
*	fectures of cel :- (1) Cost effective (2) Reliability (3) Venestald (4) Cow power Consumption
	(2) Kelicibiolog Sight and Anti-
100	1 + A 2085 UP
*	features of 8085 MP
	A C A September 1949 A March 1964 C 18
0	40 pm 10
(2)	16 oig frauces
(3)	NMOS technology
(9)	frequency = (3 MHz - 5MHZ)
(0)	nexadecimal number system
(6)	WE KULLEUMAN.
1	- decidence at books of the Et mounts 19th - 1
6	Architectuel
	The state of the s
F	- External Architecture Internal Brown Brown
-	2025 UP (40 p?n)
12	Douta bus (ADo to ADa)
17	Addeus bus (AD. to ADIS)
7	Control & states signal (RD, WR, ALE, So, S, Jo/H)
P	Power supply of frequency signal (Vec, Clock, Ground, X, LX) Externally liviticated signal
7	externally (whoved signed
	2 637 dota bus /
241	esc.
aM =	ose salutani
E PE	A address bus six 200

Date / /_	Vec Clock Coard	Caatha
	Ground XIII	(Saathi)
SID		12
500 -	AQUE AQUE	
MS (TRAP -	AD	so lead to
RST 7.5	ADB-	1
QST 6-5 QST 5-5		1 / (E)
INTR	-2 8085 Ao.	To be found.
INTA	2 July 11P	Pol M X Hung
HOLD	Addeun ladel Energy	50
	Data bus o	ALD -
HLDA	Add bus 1 = 1	- WR
Reset In	2 Resets all periphens	+ RO
	O devices	1995
Resetout	a summer of both a come	5
Ready	200 200 0 7 D 100 P 100 P	Read (feth dala)
1	> Used to establish	10 4
	connections of sync	
	Javren diselle	stor
	devices stisself	1. + 0 het
	15 3-(ADO - AD) Couver heart signing	nificant & bit(26)
* Data bu	us 3- (ADO - AD) Couver least Significant 3- (ADO - AD) Couver meet significant of the nature	Ham & on Col
N (A a a a 1 1 1 1 /)	11 Martin Tul Martin	y rights
* Steatus 8ic	mal: Used to Identy the nature (5):- Read operation (fetch) (7):- Write "	
+ Read CR	(6) 3-1 state "	wid de
* write (u	5R) ~ Wrte "	
* TOLM	of 1 -> 2/0 Memory of water	erahon.
201.	He states of another of	1001
+ So S1 5	?- Tells the status of aument of	
30/3	Non-4-1101/101	
111100		
119000	(010)	
1000	11	

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	Date / /			
44.	Date//_	1-010-12	Vec	Saath,
1	externally	Inihated Signa	ls	
(1)				(0)
(2)	101500	Handware Inter Interrupt pag.	unips (RST 2.5, 6	5,50
(3)	- 13 A	N. S. C.		16079
(4)	INTA:	Interrupt Acknowl	egenero.	41
Can Fill	TIOCO !-	Externol peripheral	divices sends a	HOLD AUG
10		The second	Tu awa	911011
	TON C	- :- up to ex	tunal devices (m	nechans.
650		J. LO		Tild
*	Clock :-	Used for synch	autestian.	144
*	Reset In :-	- It 9s used to e	resot Accumulatory	s/ Au
*	Reset out "	- wed to exert a	other external se	ling during
7	Ready 5	Used to establish	sh a conn blew	slow conjuly
1999	SID 0 7	SID & General I/o parts		
*	SOD 3 9	Security Por	of lovels to	
		med same	Electrical Property	41.
-		programmed .		
-	Interrup			-
10.1	1-0A		askable	
TIO W	tanderes t	+ Vector		
	too t undo		on tougges	
	1 9	* Based	on Provity.	Inc. d
		- Super	well of Coul	4-(2-(0)
10	Interrupts	Baned on Provity	Bared on Husking	bound on vector add
		Managery	TOO CHANGE	
	TRAP (RST 4.5)	wigh .	Mankeuble	0029H
	RST 7.5	()	Non-Markable	003CH
	6-5		"	00 34H
	5-5	/2	11	Non-vectored
	INTR	low	Saannad with CamSa	Page No.



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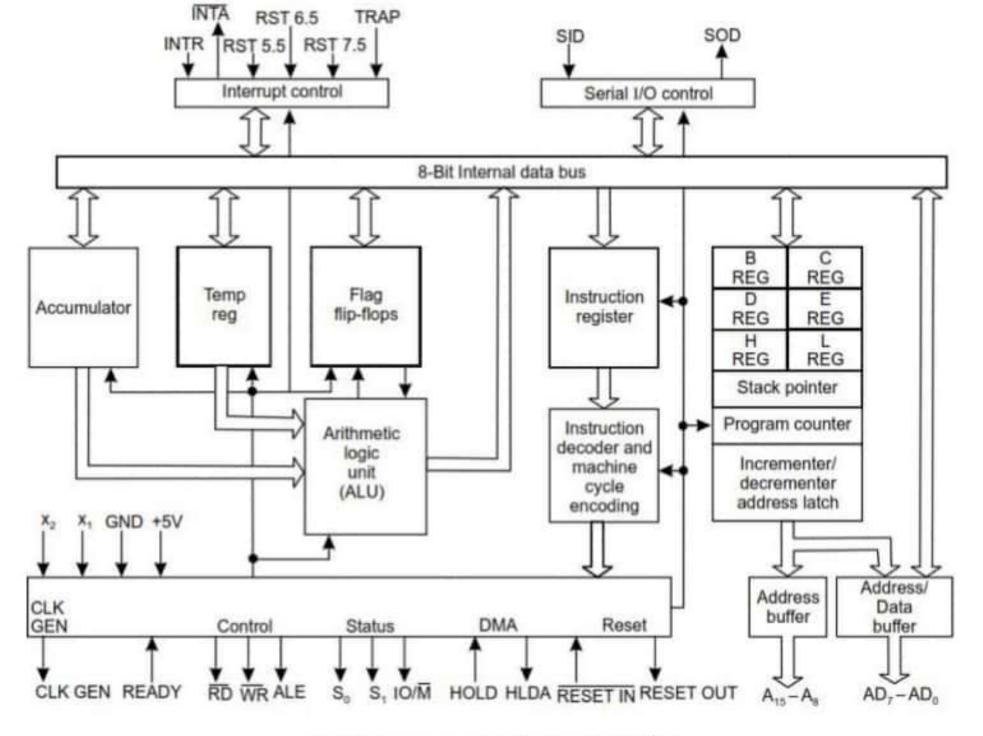
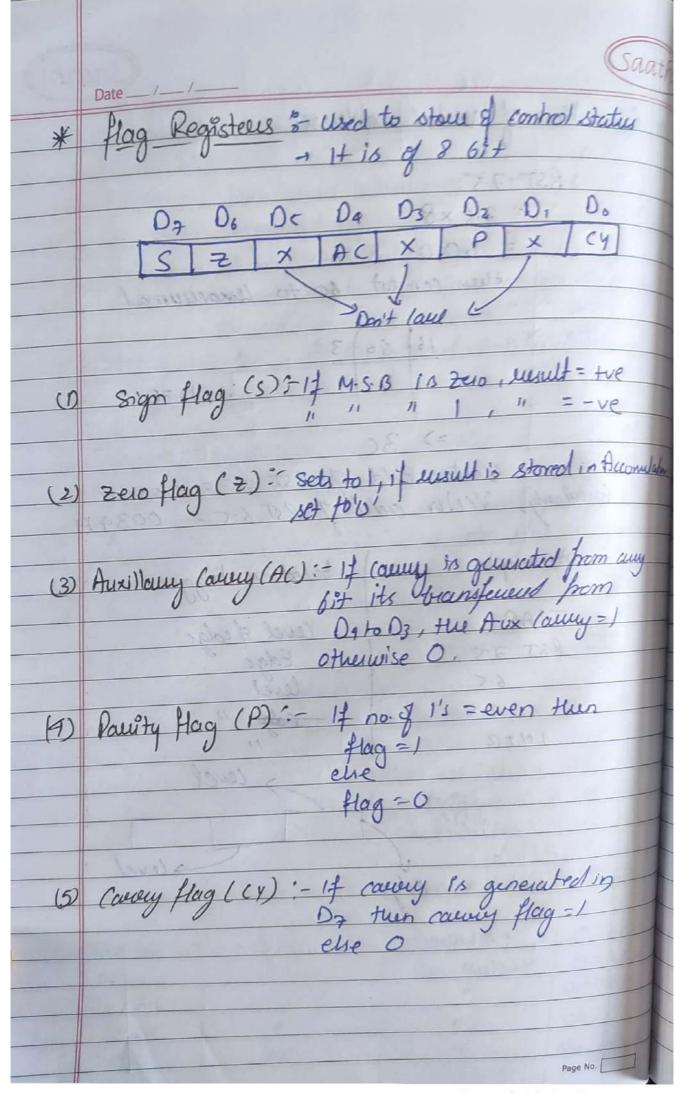


Fig 1.1 Hardware Architecture of 8085



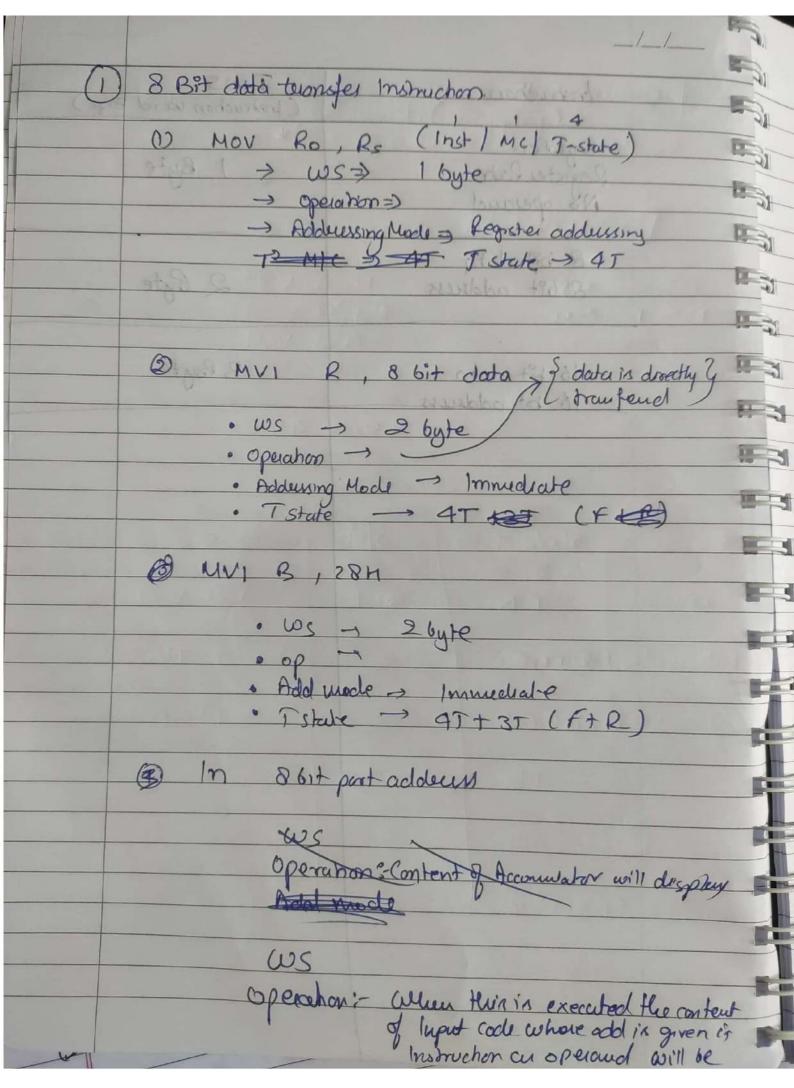
The same of the sa	Interwent	
277	Interrupts Garnke	28.
-	Internal Ander	
	Internal Anchitecture	
	a late To take of the state promotion	
T	Instructions: Commands to the necessary to perform tast on specific data.	
-3	to perform task on specific data	
TIL	the state of the s	
1	O Opcode ?- Task which is to be performed.	
	audits to be performed.	
	O operand: Data which is to be operated upon.	
1	alate (a Atamos) March and cal logo (logo).	
	@ Instruction word Size :- No. of byte taken by	the
	Instruction word Style: No of byte taken by	the
	operation.	
	The state of the s	
	@ Machine Cercle = It is the bone take to be	0
3	Machine Cycle :- It is the bone taken by the state on Brotruction	1
-7	and such at the second of the line	
	> Fetch Machin Cycli	
	-> Memony frad Machine Cycle	
	-> Memon unite Machiene Cycle	
国	-> Remon write Machine Cycle -> I/o machine cycle -> Bus Idle Machine cycle	
7	-> Bus Idle Marchine cuch	
4	Just the state of	
4	* 1 Madein cycle consist of a 3T to 6T	
	A Maria oga	
1		
	* Tstate->	

"Bus"		BR
<u>C</u>	Petch Madling Cucle = County of AT 11	-
	Petch Madin Cycle: - Consist of 4T state	
	Memory Reced M/c :- Consist 3T state	
0		
	Memory write M/c: - Consist 3T state	
6		
-54	The state of the s	
<u> </u>	Bus Idle M/C: - Only used for OAD instruction :- 3T starte Soluble Add	
N.	= 3T steve double Add	
	E Open 18 - Tak which is to be performed	
0	or opened ? Data which is to be opened up	-
#	Special Instruction (consists 6 T state)	E
		E .
	R: Restart	-
	I 5- INX & DNX	-
	S :- SPML & PCML	1
	P: BUSH	F
911 114		
	the executer our instructions	E
	* Total time to execute Instruction	-
	Total home = nx teix	80%
	the many offers many st.	
	tax = 1 le victore 1 0 12	P
	the finded del mis co	
	* 1 Madarus eggle constal of Cl. ST to 6T	T.
	7 7	-
4		Application of the last of the

- 0	
- Cau	Legured to execute an instruction is 1.4 user
	engured to execute an instruction is 1.4 user
	men men of 1 - 1 to accept of for
	inimuchen is state millier for executing the
	0) 1
	6) 6
	1) 2
200	d) 8
24	Stell & march 16/4 & Doall proposable Admitted to
	+= SMH+
	T= 1.4 usec.
	1-4×10-6
-3	M. A. VOH EX
6	Addressing Mode
	The transfer of the post of the sunt
	The method by which the address of source of
	dota or oddenses of decomation of account one
	given in Primuchons are called addressing.
	Register Addrusing: Pata is Stored in engister and
	of name of a sugester and
	aguar in mitruction as operand.
	quest a bourger on + way later today
The state of the s	MOV ABO
	Got · · · · · · · · · · · · · · · · · · ·
7	Derect Addressing & @ No engrister es enquired Derect Addressing & @ Prosetty data given en the Immul
3 5	Direct Additions of Directly data given on the inhall
1	ar socialid
	MVIA 24H
7	Ex: MVIA, 24H
	(w+2+4) E 6 7/N
	(100) 100) (100)

		- Pal
- Tank (0.31=	Terrort Addisserver 12 All I	FRI
- Dag	Derect Addressing mode: - Address of data directly gover	1
itee	in manichen as operand	THE STATE OF THE S
	Ex: LDA 2 LD A 2000	- 1
		100
	(Coad into accurrentator)	RE-DI
	The state of the s	
3	1 2 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1	85-1-1
	Indirect Addressing Mode: Address of data is stored in sugister pair, and name of a sugister given in Instruction as operand.	177
	pair, and name of a legister given in Instruction	» = 3
	as operand.	F 73
		-
	Ext Mov B, M	
	New will alway point in Hz par (will	
De angua	niove to B	E
esult law	date on allever of declination of the	
-	H = 90 9010 B	-
U	L = 20	-
13-1-1-1033	as bord 97 ? told a groundship is again to	
was inter	to the saint of a land	
	I aller to technichen an apainud.	-
2)	Implicit Add Mode :- No operand is enquired	
	BEIG WOLL	
	Ex: NOP	
	word of relations of a Heteroughland of the surger	
weeks alt	19° mule intelled fill and a	-
	Os operoud	
	Mou D, M 2 bytes	Room
	MIC + 3 (F+R+W)	-
	QT) (3T (3T)	
		-
		HERE !

Instruction
Register Rain 1 Byte No operand 8 bit data
Rogestes (marrichon word 8720)
Register Pair
No operand
The State of the S
8 bit data
8 69t address 2 Byte
16 bit data 10 8 1/3 Byte
16 bit addeus
stud & Dict 2000 .
t mathema a
* Addresser & Oboth mounted *
ENTRY DISTRICT
4585 4 50 1411 85
57,08 × 580 0
THE PART OF THE PA
advalance of alm at later a
(S+3) (TE+3D = 3603 = 1.00)
translation to 61 (2)
The set of the North North And Front Company of the
A Charleston
200
1.1.000



V			_/_/_
V I	the Visit		
	Stored in	accamulator.	Suche Machines &
(0	ALL CAR	muth (som	Parl orde
	and wode	or Direct.	SXAUX
	1-State	5- 4T+3T	+35 (F+R+R)
14	tood HE Plan Con		
	(D) 0.1 0	10700	107137
10000	@ out, 8	bit Kost add	ues
406 10	27.	0 101	(A V 3):3
	ws	- 2 Oyl	e van
	of	- 1	3 VOI4
	op Add	mode - chill	+3T+3T (F+R+R)
	1 + 3		
			asH2
	G	+	THE
	6 HLT		
	ws-	- 1 byte	1 acroidouriosa
Mary bloo	91 pt - 19	1 2/0	CAMPINE TO SERVICE TO
		1 - CUTA	fetch & vom
Pegaster	MIC	- (41)	recon
		1	1405,0 IVM
musikato	6 NOR	e	b 19/31 11
	ws		fetch M MM
A whole	MOTOLMC	·) (41)	rerow)
100100			90U
	47		
			NOS MI
- 12	Or 100 1 TOU	2	NO3
Assert	(D) (6-2) (D)		
	5 0 3 1 7ml	8	H 85 70.0
And	(4+9+3) TO		

			//_	E.
				FA
6	AIM: To exchou	ign the conter	ocipping) shay se manglena	
	Store the	wesult (Su	eapping)	The state of the s
100		e aggn ka od	hay so mangena	3
0	The state of the s			3
	1 41.5	2050	[Lood HL Par Load]	
	1 de	4) / /_	() Ye	
	Mov	AH	(+) 2080 →03 H 3 2051 →04	-
	Mov	(04)	H 3 2051 304	
	Mov	(04)A 00	* 101A	
Per und	37-37 (8+1	+TP - 91	sf2 T	F .
1,24	SHLD			Fig. 17
	HUT		A	100
			TIH A	15-
	1	MIC	J-state add uno	de
	Instructions	1710		
	MOV BICHA	FTH (4)	47 Rogostes	
	1.10			
	MVI 0,20M	2	7 T (FIR) / vumeble	
	10 11 218	120	10 Topens Indirect	
	MV1 M1 4014	11-15	(O) (Professional Professional	
	NOP	,	4T	
	1N 20H	3	10T (FARTR) Dreet	
		0	1	
	007 20 H	3	(Ftere) Direct.	E
9				
	THE RESERVE	HH		E
1				11

1		_/_/_
*	write an instruction to display the suggester H at output OOH	content of
1	MOV A, H SULD OUT OON	
Ru	A=97H B= A9H	
<u> </u>	Country = 1	10 15 100/
•	AND A, B	1000001 8 1 H
	81 H $5ngn = 1$ $2 = 0$	
	AC = 0 $PF = 1$ $C = 0$	