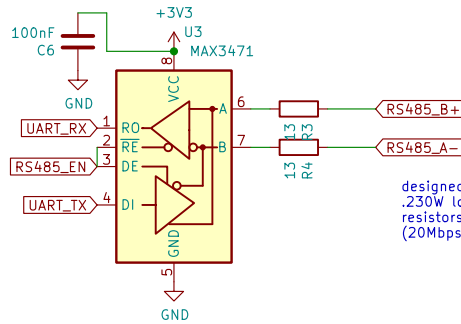
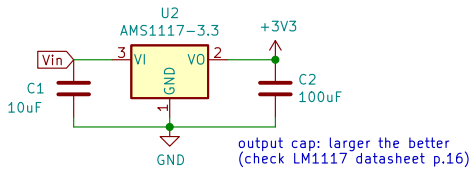


REPLACE with MPM3610 on V1

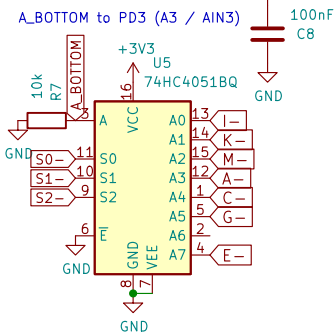
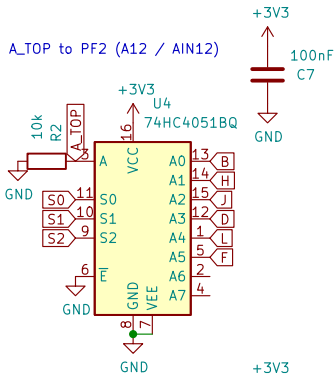


On-chip IEC ESD protection is sufficient for laboratory surge transients occurring in industrial environments. The use of external transient protection devices

these are 10 ohms in the datasheet, but seed of

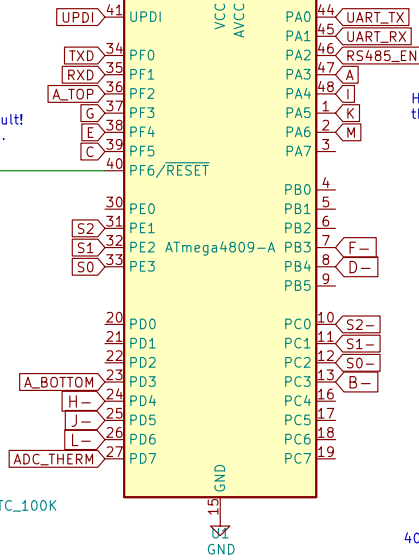
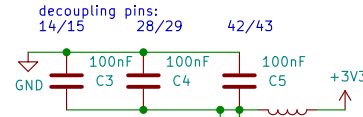
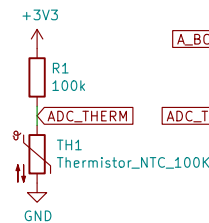
designed for SN65HVD75: .230W load at 20Mbps, 50% duty cycle test conditions (datasheet) resistors should be absolutely minimum rated to 1/4W (20Mbps is faster than we will be communicating, and we're not

JLC actually stocks a smaller housing... but i already laid it out for SOIC-8, which makes testing and troubleshooting easier anyway.



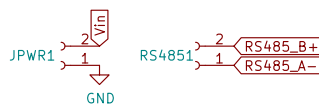
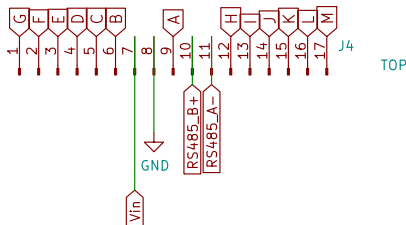
important note: it's the HC version (3v3), not HCT (5v device)!

reset requires no extra hardware – actually, the pin is a GPIO by default! resetting is done thru the UPDI pin.

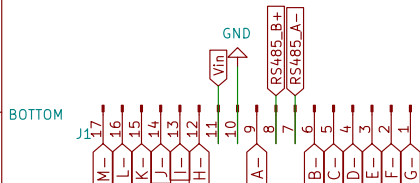
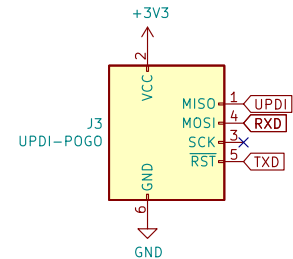


Half of the pins on the connector are for V+/0V the other half are for analog measurements.

40mA GPIO sink/source current



UPDI programming interface (for use with pogo pins, custom setup)



Essential revisions:
replace regulator / dc converter
thermistor placement not near heat sources

Sheet: /
File: TFRAS_TOP.sch

Title: TFRAS Sensor board V0

Size: A4 Date:
KiCad E.D.A. kicad (6.0.6-0)

Rev:
Id: 1/1