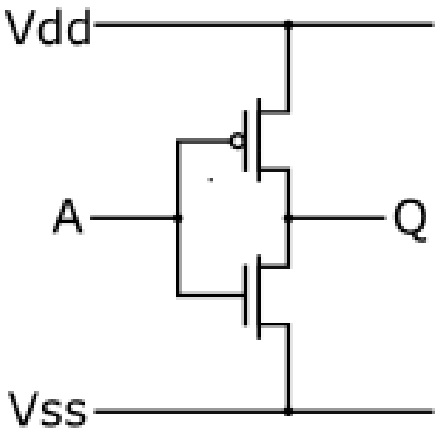
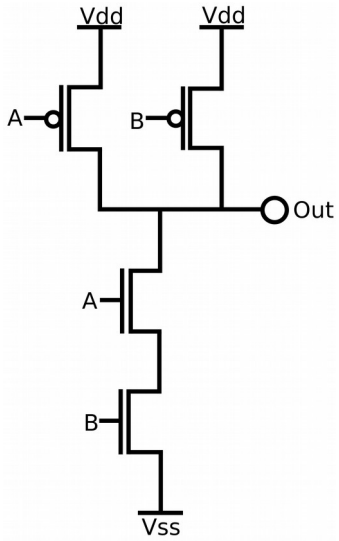


(A)

Circuit A

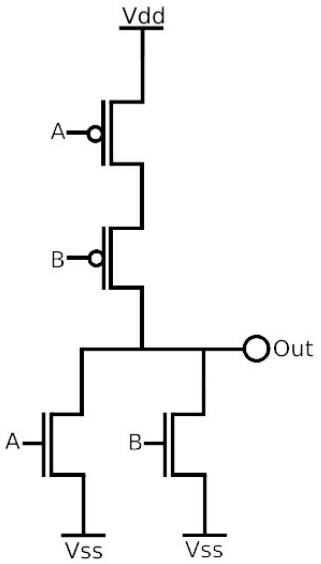
A	Q
0	
1	



(B)

Circuit B

A	B	Out
0	0	
0	1	
1	0	
1	1	



(C)

Circuit C

A	B	Out
0	0	
0	1	
1	0	
1	1	