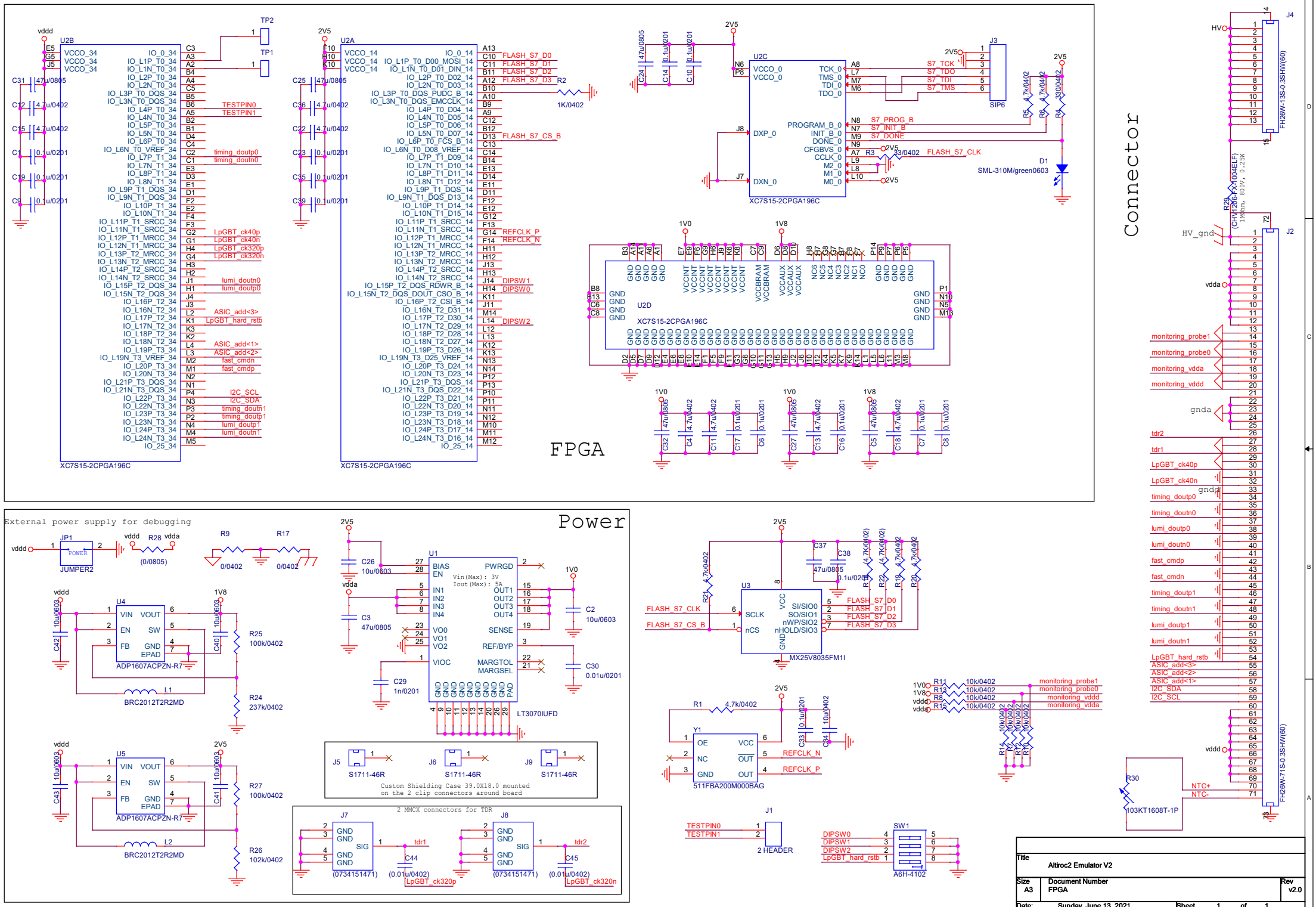
FPGA
Connector
Power
U2B
U2A
U2C
U2D
XC7S15-2CPGA196C
J3
SIP6
J4
FH26W-13S-0.3SHW(60)
J2
FH26W-71S-0.3SHW(60)
D1
SML-310M/green0603
External power supply for debugging
JP1
JUMPER2
U1
LT3070IUFD
Vin(Max): 3V
Iout(Max): 5A
U3
MX25V8035FM1I
U4
U5
ADP1607ACPZN-R7
BRC2012T2R2MD
J5
J6
J9
S1711-46R
Custom Shielding Case 39.0X18.0 mounted on the 2 clip connectors around board
2 MMCX connectors for TDR
J7
J8
(0734151471)
Y1
511FBA200M000BAG
J1
2 HEADER
SW1
A6H-4102
103KT1608T-1P
Title
Altiroc2 Emulator V2
Document Number
FPGA
Rev
v2.0
Date:
Sunday, June 13, 2021
Sheet 1 of 1