30.06.2024		Tang N	ano 9k FPGA Base	e board		Pascal G.	
Feature	I/O NAME	PIN	INFO	PIN	I/O NAME	Feature	
P	MOD 1				PMOD	2	
1.1	IOB33A	39	‡ (	28	IOB11B	1.1	
1.2	IOB29B	36	Shared with TF CARD	27	IOB11A	1.2	
1.3	IOB31A	37	ared F.C.	26	IOB8B	1.3	
1.4	IOB31B	38	Sh	25	IOB8A	1.4	
1.5	IOR24A	49	<u> </u>	34	IOB23B	1.5	
1.6	IOR24B	48	1 wi CD	33	IOB23A	1.6	
1.7	ІОТ37В	76	Shared with SPI LCD	30	IOB13B	1.7	
1.8	IOT37A	77	S S	29	IOB13A	1.8	
			<del></del>				
PMOD 3				PMOD 4			
1.1	IOB41B	42		32	IOB15B	1.1	
1.2	IOB41A	41		31	IOB15A	1.2	
1.3	IOB29A	35		57	IOR13A	1.3	
1.4	IOB33B	40		56	IOR14A	1.4	
1.5	IOR14B	55	,				
1.6	IOR15A	54			SerialR	GB	
1.7	IOR15B	53		63	IOR5A	RGBLED	
1.8	IOR17B	51					
			-	JOYSTICK		СК	
EN	ICODER			83	IOT10B	Center	
Α	IOT10A	84		82	IOT11A	Left	
В	IOT8B	85		81	IOT11B	Up	
Center	IOT8A	86		80	IOT12A	Right	
				79	IOT12B	Down	
LEDs							
LED1	IOL15A	10			SPILO	D	
LED2	IOL16B	11	vith 1	49	IOR24A	R/S	
LED3	IOL21B	13	d w	77	IOT37A	MOSI	
LED4	IOL22B	14	Shared with	76	IOT37B	SCLK	
LED5	IOL25B	15	N	48	IOR24B	!CS	
LED6	IOL26B	16		47	IOB43B	!Reset	
			•				
BUTTONS					TF CAF	₹D	
S1	IOT2A	3	ith 1	38	IOB31B	!CS	
S2	IOL5A	4		37	IOB31A	MOSI	
			 Shared with PMOD 1	36	IOB29B	SCLK	
SPIFLASH			- LS	39	IOB33A	MISO	
MOSI	IOR11B	61					
MISO	IOR11A	62		XTAL Clock			
SCLK	IOR12B	59		52	IOR17A	27 MHz	
!CS	IOR12A	60					
					PC UART		
lote: All Buttons, Joysticks, Encoders or non-				17	IOB2A	FPGA-TX	
RGB LE	RGB LEDs are "Active-Low"!				IOB2B	FPGA-RX	