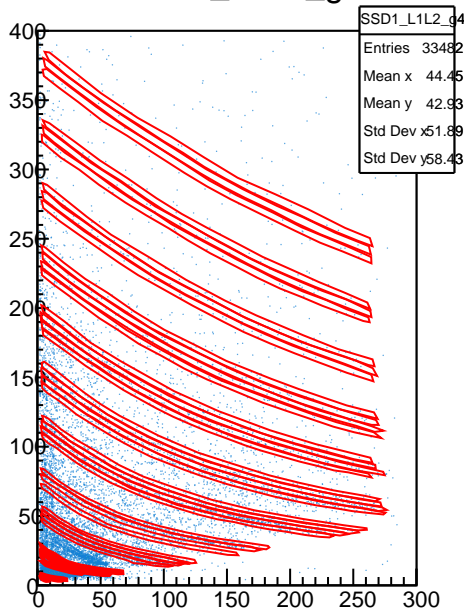
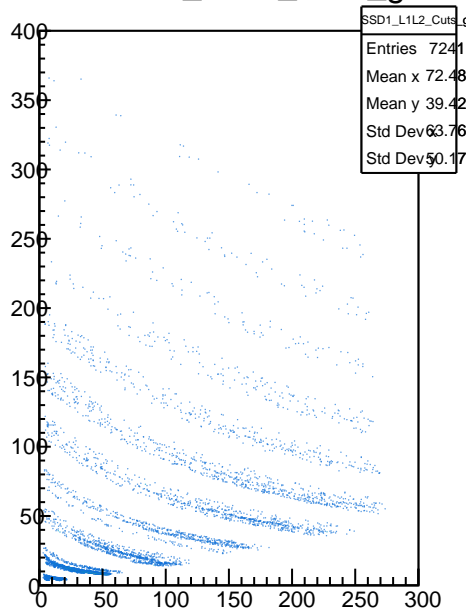


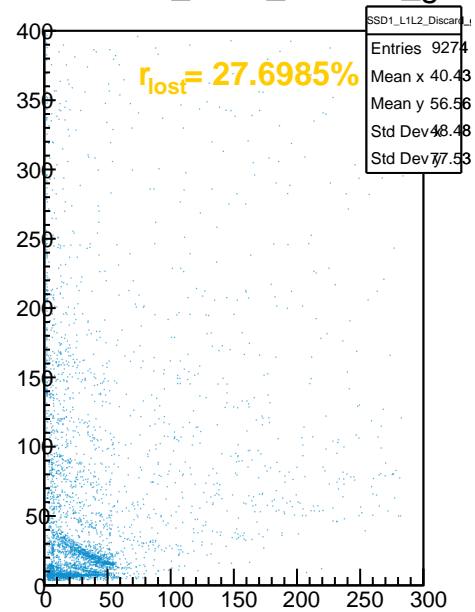
SSD1_L1L2_g4-110



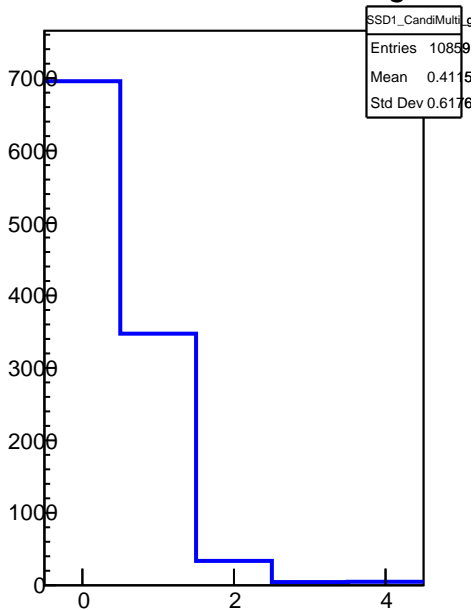
SSD1_L1L2_Cuts_g4-11



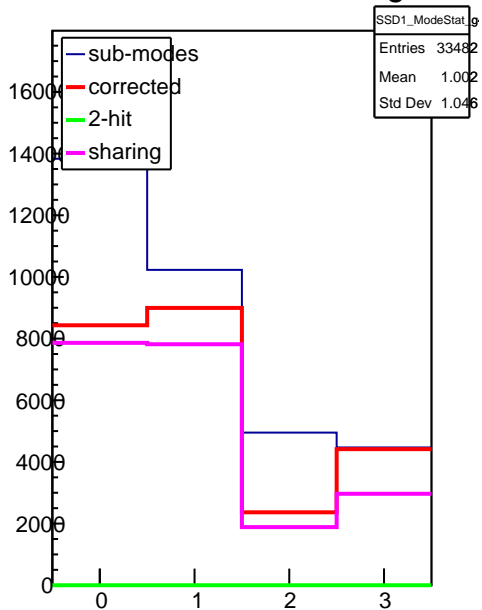
SSD1_L1L2_Discard_g4-11



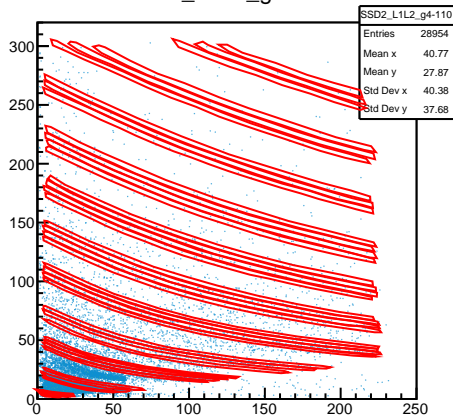
SSD1_CandiMulti_g4-11



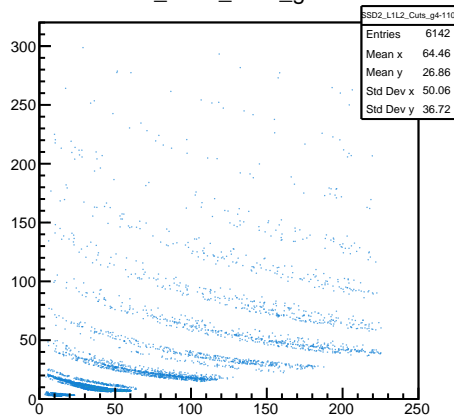
SSD1_ModeStat_g4-11



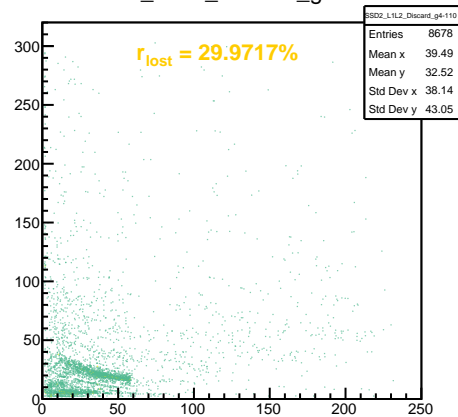
SSD2_L1L2_g4-110



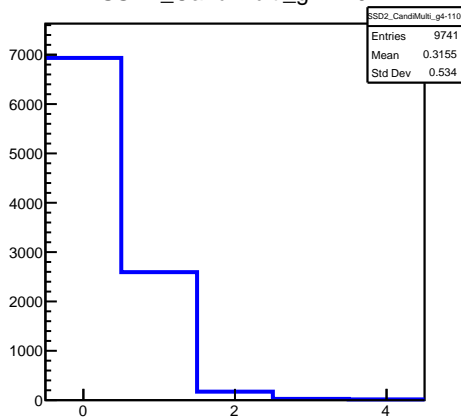
SSD2_L1L2_Cuts_g4-110



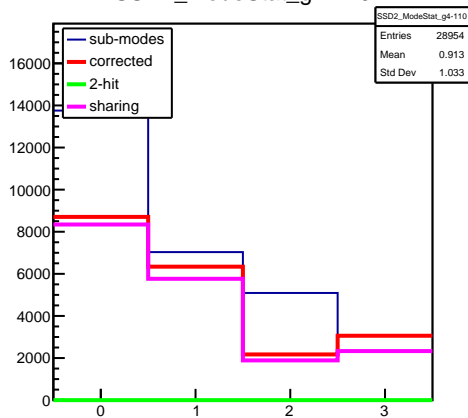
SSD2_L1L2_Discard_g4-110



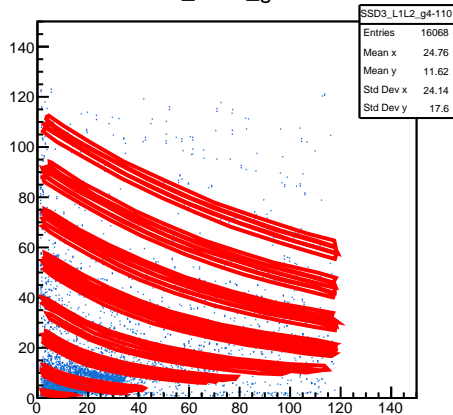
SSD2_CandiMulti_g4-110



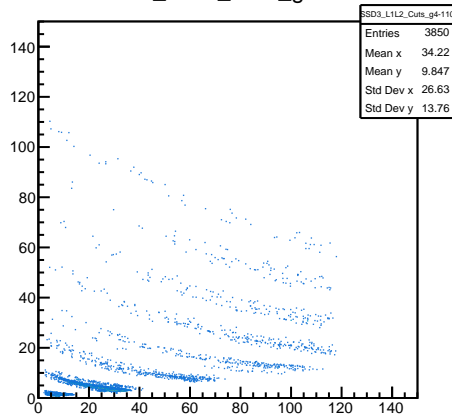
SSD2_ModeStat_g4-110



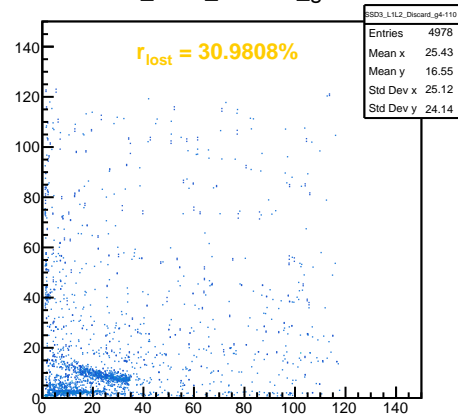
SSD3_L1L2_g4-110



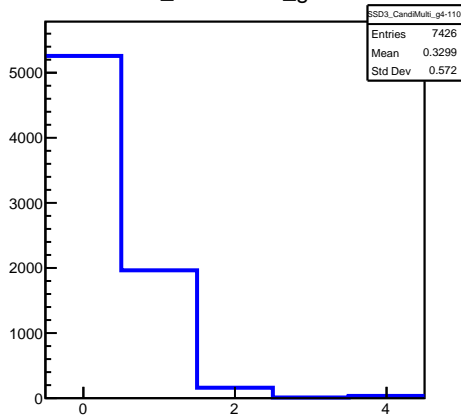
SSD3_L1L2_Cuts_g4-110



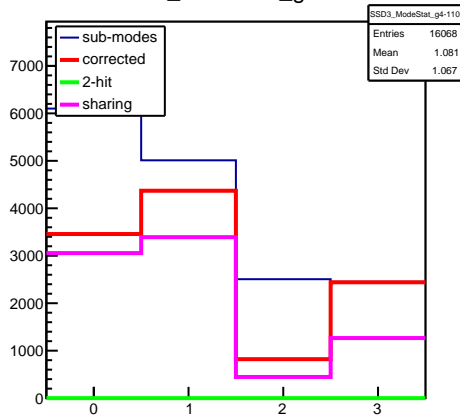
SSD3_L1L2_Discard_g4-110



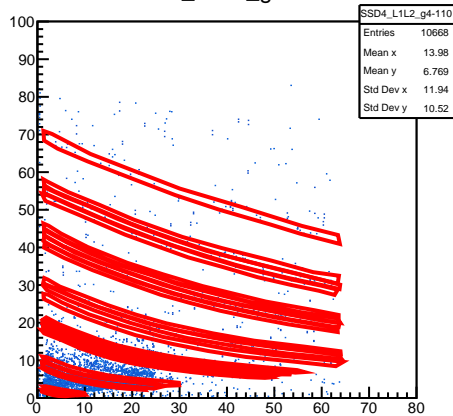
SSD3_CandiMulti_g4-110



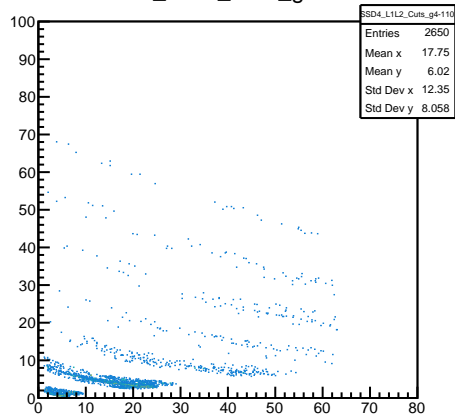
SSD3_ModeStat_g4-110



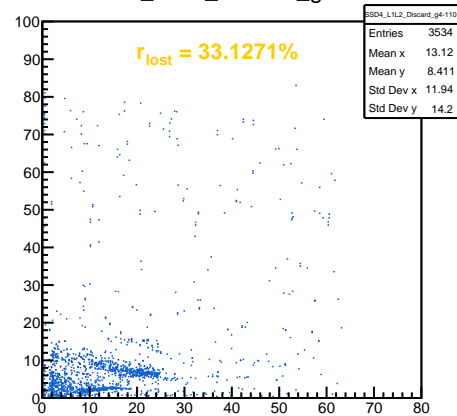
SSD4_L1L2_g4-110



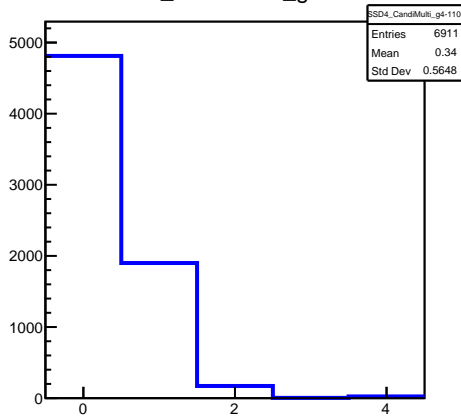
SSD4_L1L2_Cuts_g4-110



SSD4_L1L2_Discard_g4-110



SSD4_CandiMulti_g4-110



SSD4_ModeStat_g4-110

