

h\_L2B\_L3\_SSD1\_Csl6 h\_L2B\_L3\_SSD1\_Csl6 300 23183 **Entries** Mean x 997.5 Mean y 19.6 250 601.1 Std Dev x Std Dev y 22.05 200 EMeV\_L2B 150 100 50

ECh\_CsI

2500

3000

3500

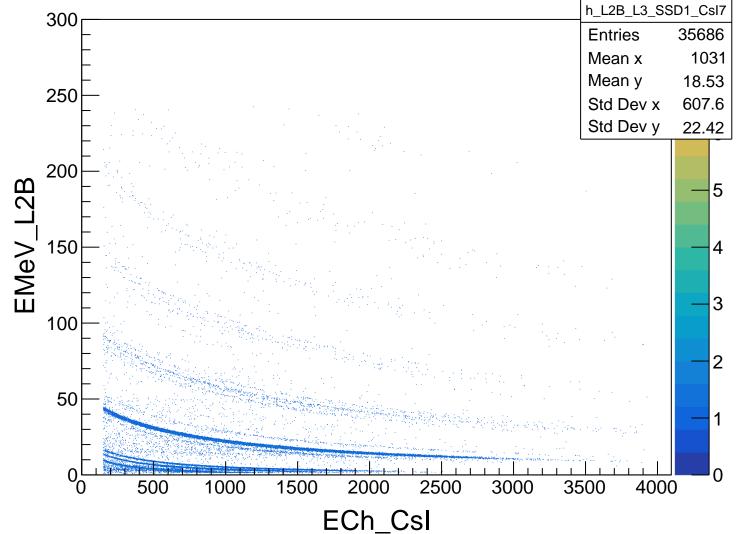
2000

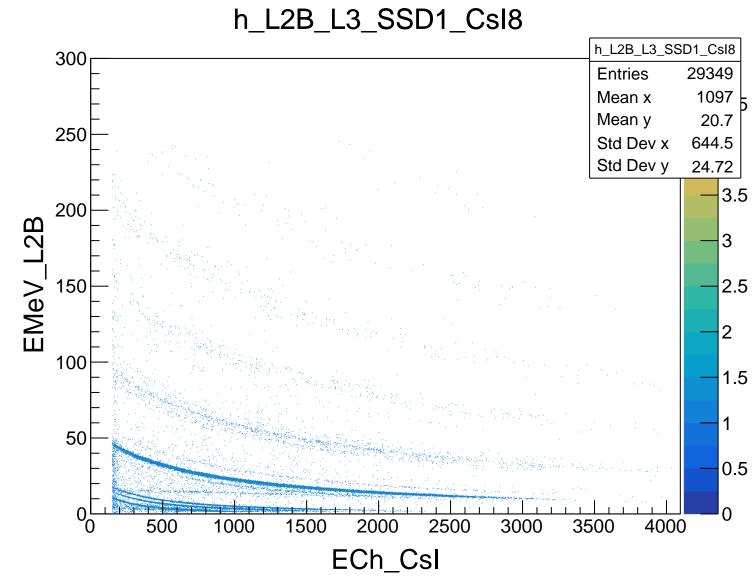
1500

1000

500

4000





h\_L2B\_L3\_SSD2\_CsI0 h\_L2B\_L3\_SSD2\_CsI0 250 19048 **Entries** Mean x 960.9 Mean y 18.55 Std Dev x 616.7 200 Std Dev y 17.18 EMeV\_L2B 150 100 50 500 4000

ECh\_CsI

2000

2500

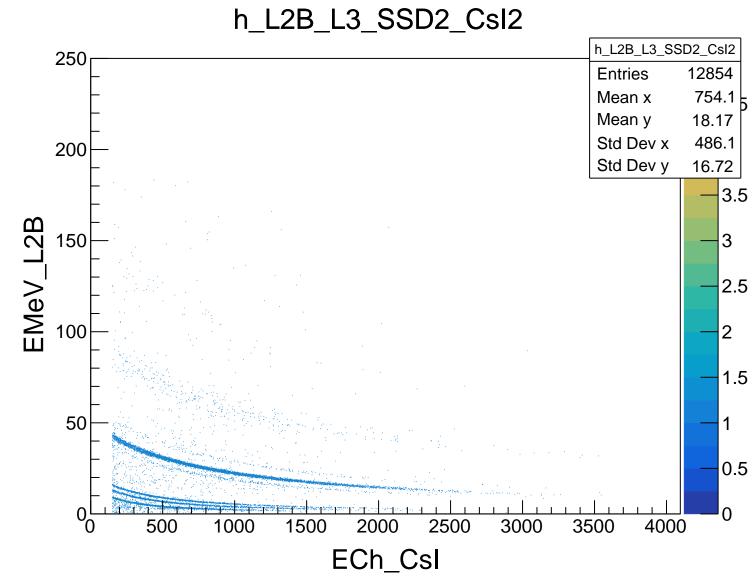
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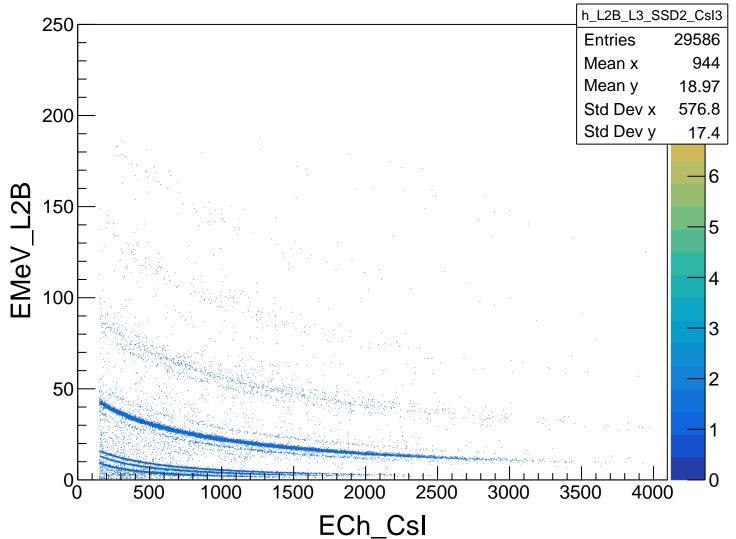
3500

1500

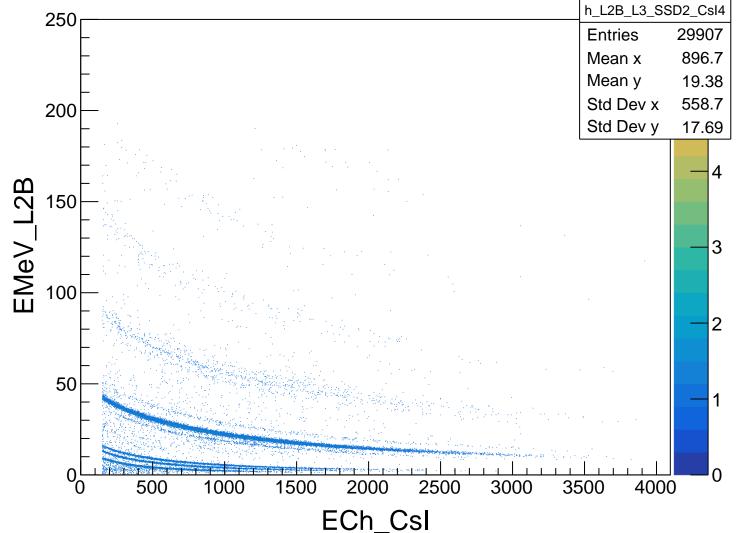
1000

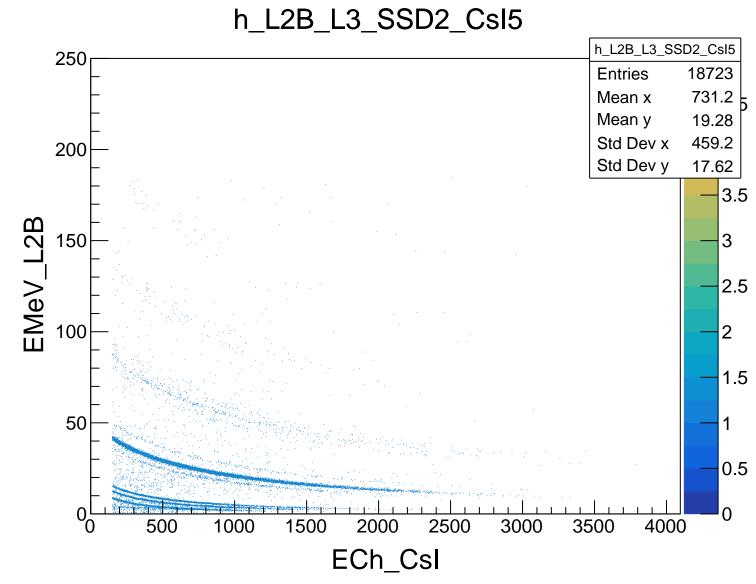
h\_L2B\_L3\_SSD2\_Csl1 h\_L2B\_L3\_SSD2\_Csl1 250 18728 **Entries** Mean x 820.2 Mean y 18.32 521.9 Std Dev x 200 Std Dev y 16.48 EMeV\_L2B 150 100 50 3000 4000 500 1500 2000 2500 3500 1000 ECh\_CsI

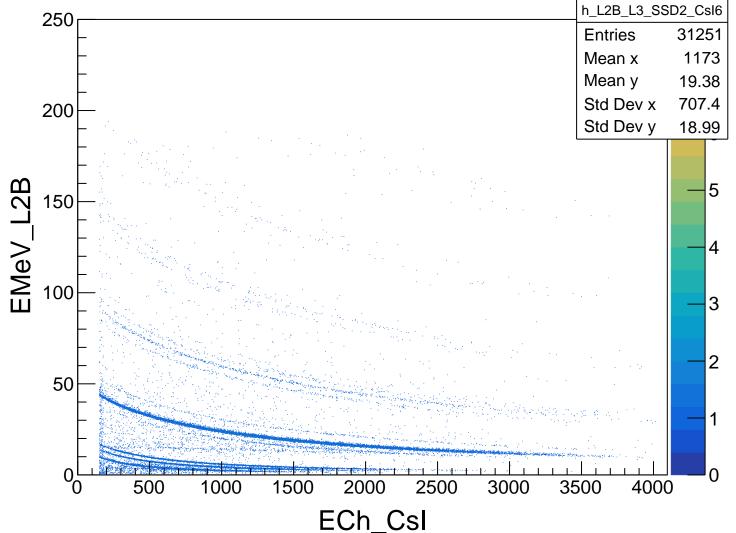




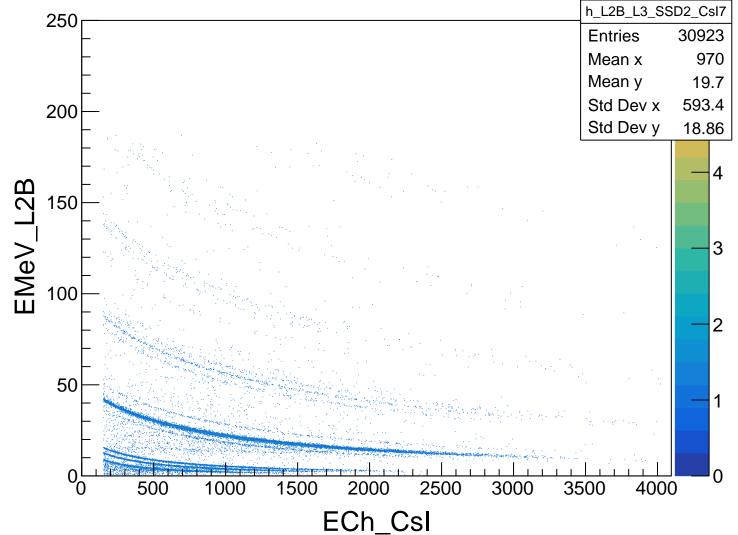
h\_L2B\_L3\_SSD2\_CsI4

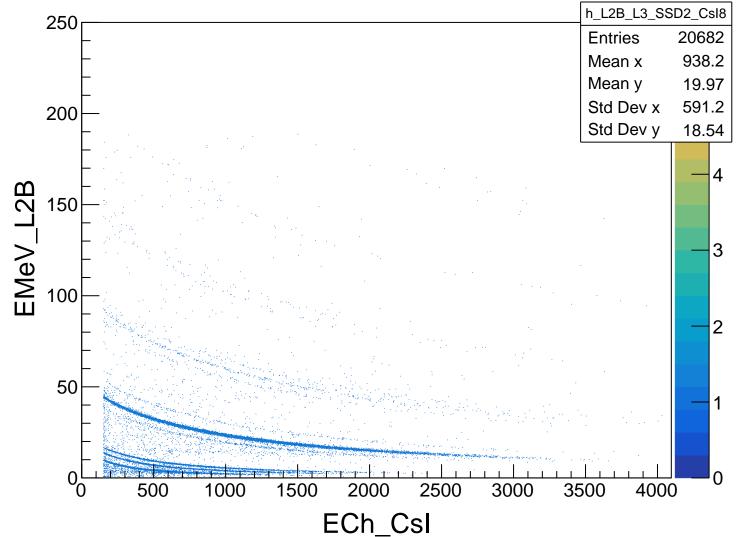




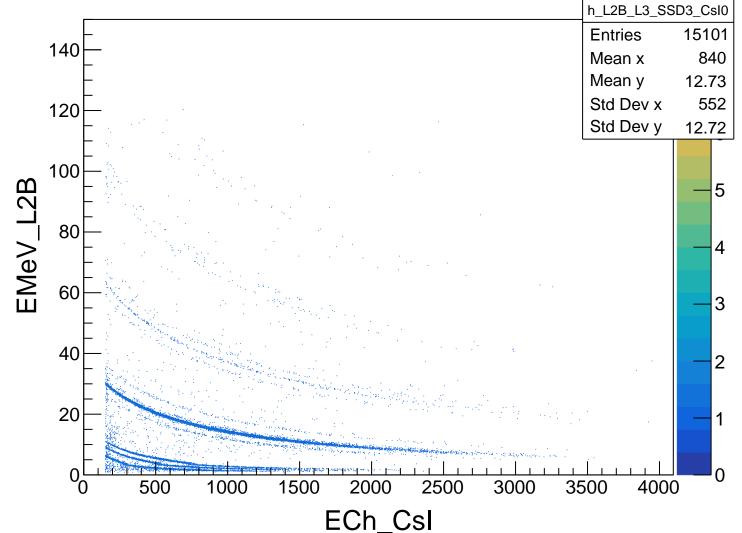


h\_L2B\_L3\_SSD2\_CsI7

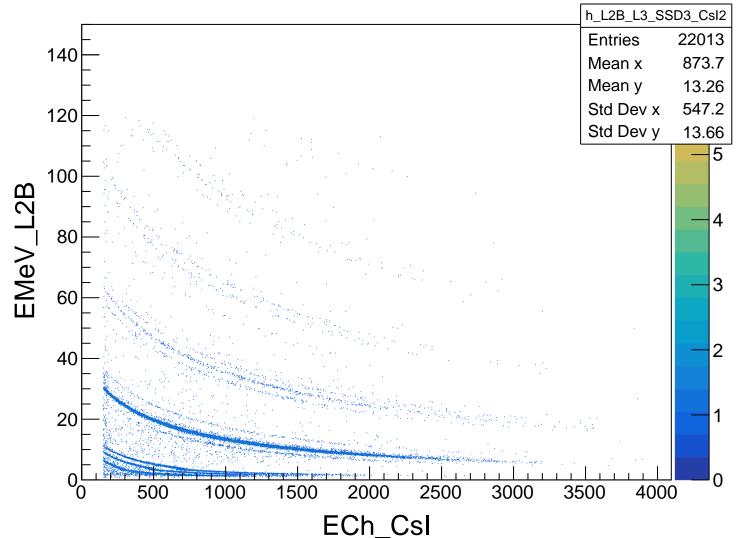


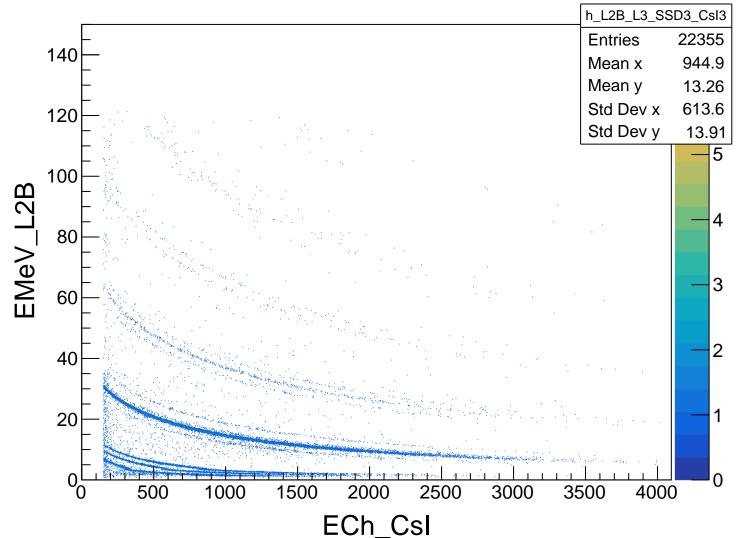


#### h\_L2B\_L3\_SSD3\_CsI0

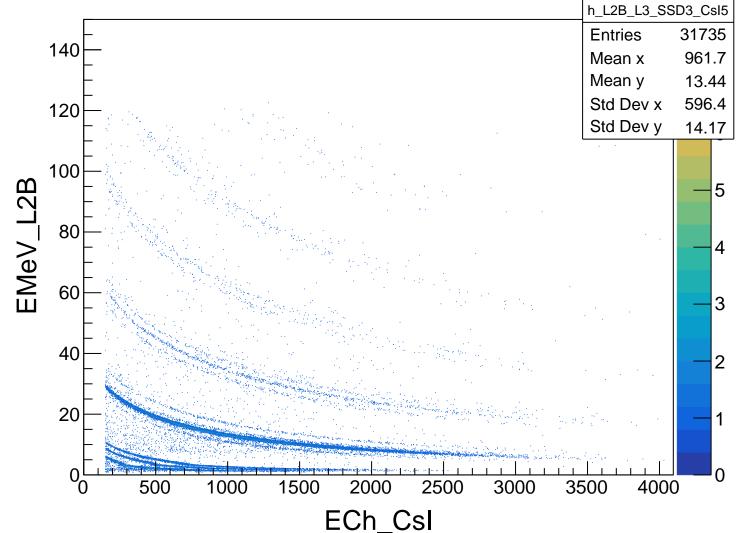


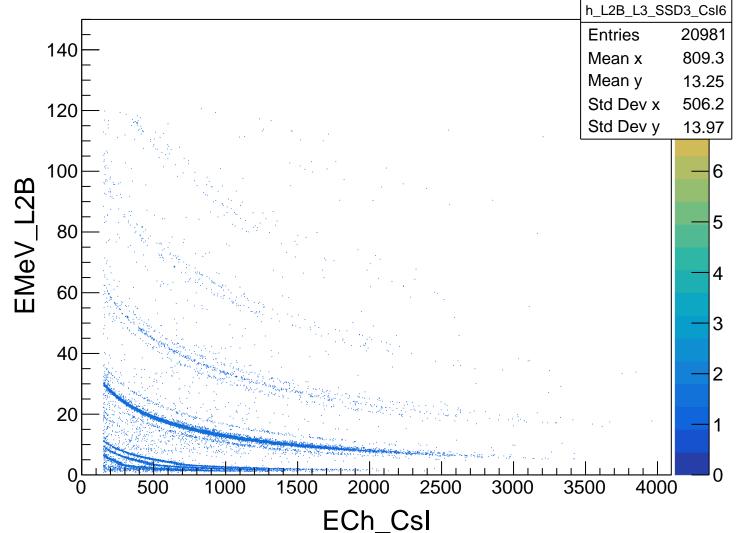
h\_L2B\_L3\_SSD3\_Csl1 h\_L2B\_L3\_SSD3\_Csl1 **Entries** Mean x Mean y 12.9 Std Dev x 496.1 Std Dev y 13.02 EMeV\_L2B ECh\_Csl



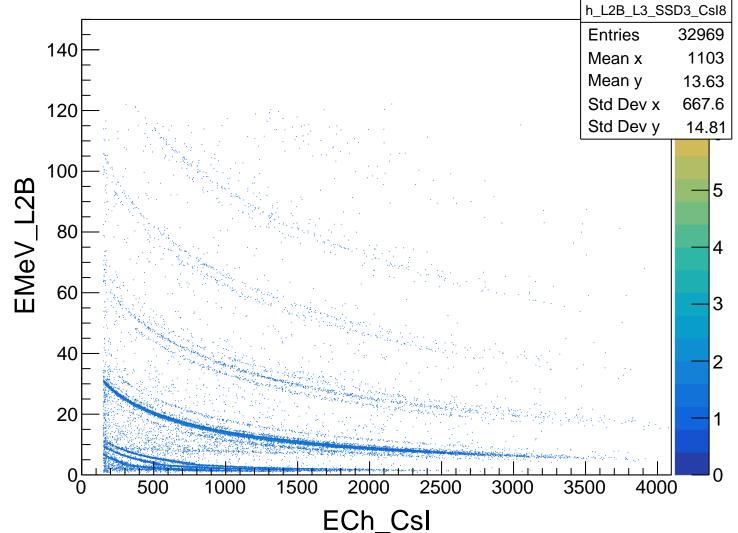


h\_L2B\_L3\_SSD3\_CsI4 h\_L2B\_L3\_SSD3\_Csl4 33282 **Entries** 140 Mean x 998.5 Mean y 13.73 Std Dev x 646.4 120 Std Dev y 14.25 100 6 EMeV\_L2B 5 80 60 40 20 4000 500 1500 2500 3000 3500 1000 2000 ECh\_Csl

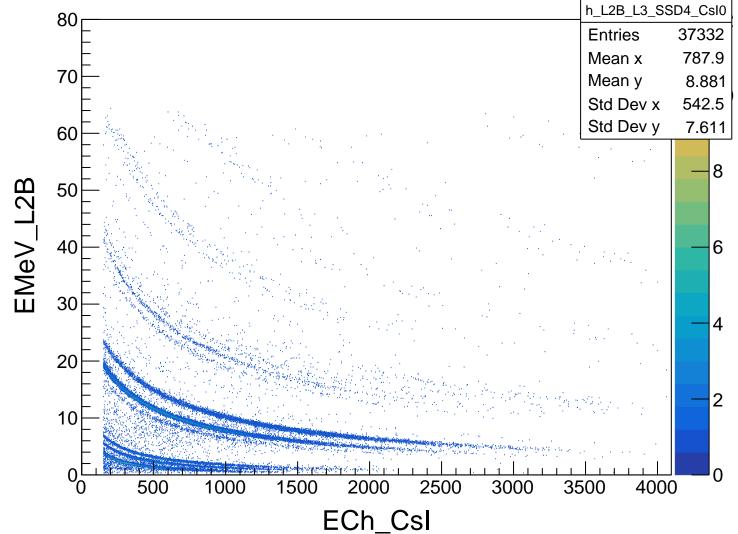




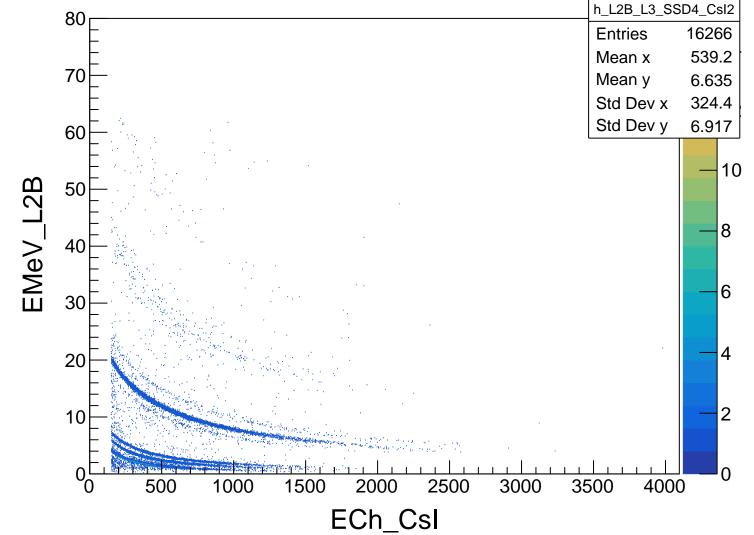
h\_L2B\_L3\_SSD3\_Csl7 h\_L2B\_L3\_SSD3\_Csl7 32378 **Entries** 140 Mean x 998.7 Mean y 13.55 628.1 Std Dev x 120 Std Dev y 14.33 100 EMeV\_L2B 5 80 60 40 20 4000 500 1000 1500 2000 2500 3000 3500 ECh\_CsI

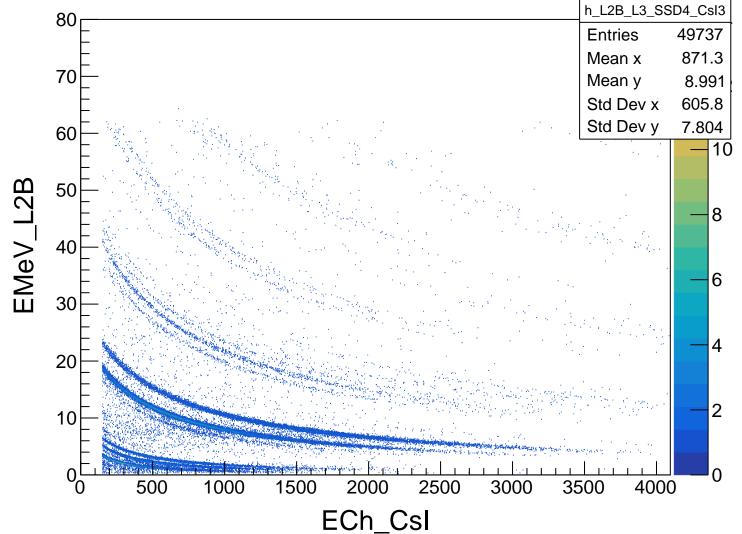


### h\_L2B\_L3\_SSD4\_CsI0

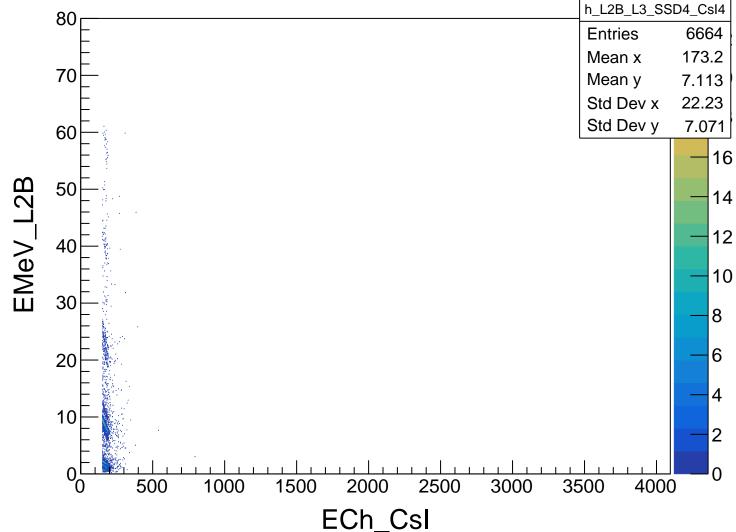


h\_L2B\_L3\_SSD4\_CsI1 h\_L2B\_L3\_SSD4\_Csl1 **Entries** 683.7 Mean x Mean y 7.232 Std Dev x 495.4 Std Dev y 7.145 EMeV\_L2B ECh\_Csl





# h\_L2B\_L3\_SSD4\_CsI4



#### h\_L2B\_L3\_SSD4\_Csl5 h\_L2B\_L3\_SSD4\_Csl5 **Entries** 195.7 Mean x Mean y 6.61 Std Dev x 36.41 Std Dev y 6.962 EMeV\_L2B ECh\_Csl

h\_L2B\_L3\_SSD4\_Csl6 h\_L2B\_L3\_SSD4\_Csl6 80 34540 **Entries** Mean x 647.5 70 Mean y 8.71 392.1 Std Dev x Std Dev y 7.959 60 EMeV\_L2B 50 40 6 30 20 10 3000 4000 500 1500 2500 3500 1000 2000 ECh\_Csl

#### h\_L2B\_L3\_SSD4\_CsI7

