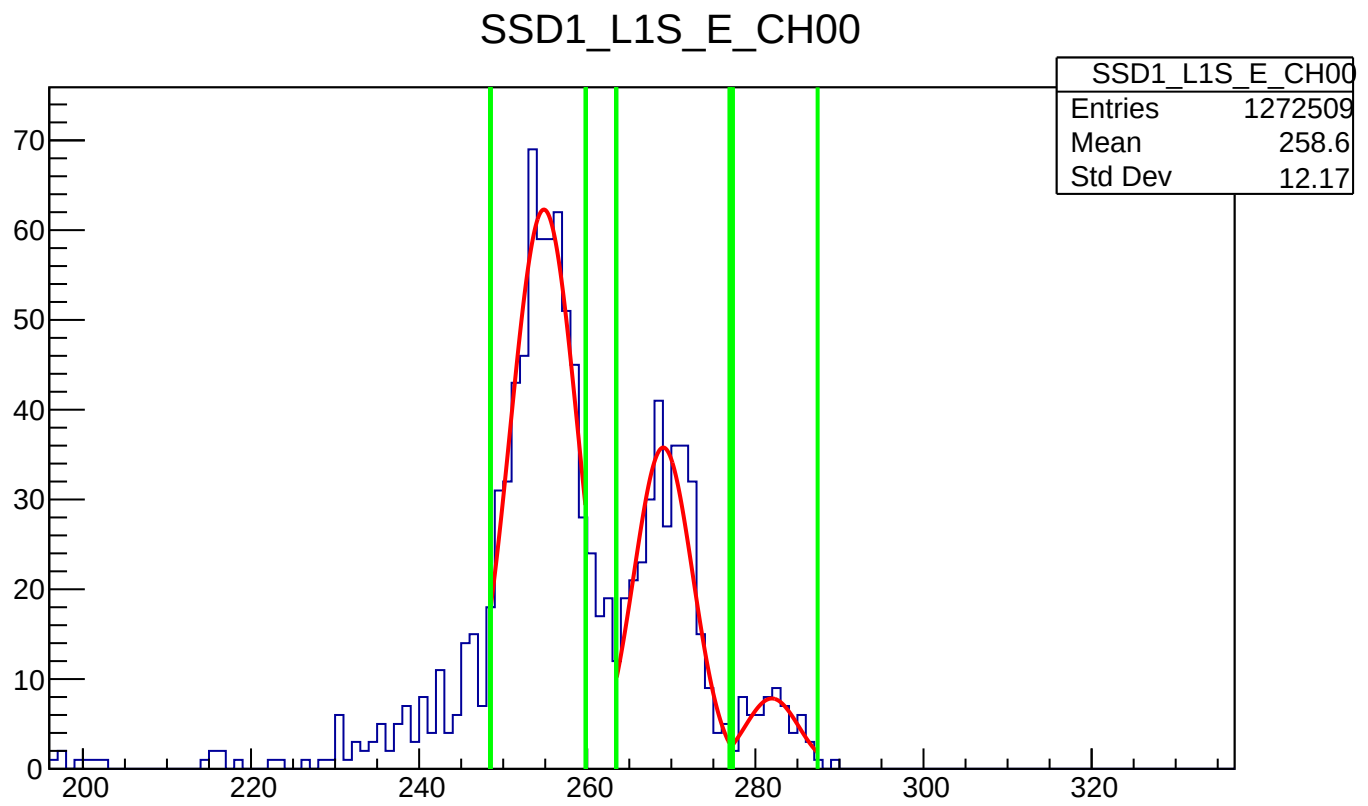
SSD1_L1S_E_CH00
SSD1_L1S_E_CH00
Entries 1272509
Mean 258.6
Std Dev 12.17
70
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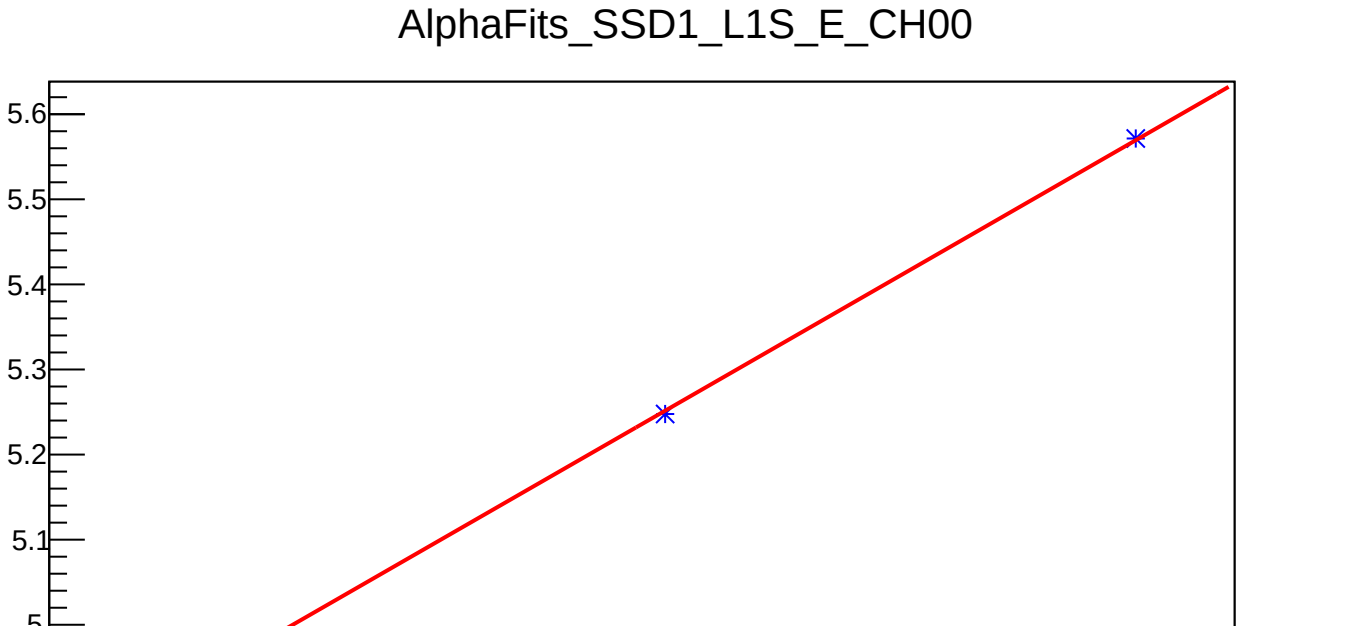
AlphaFits_SSD1_L1S_E_CH00
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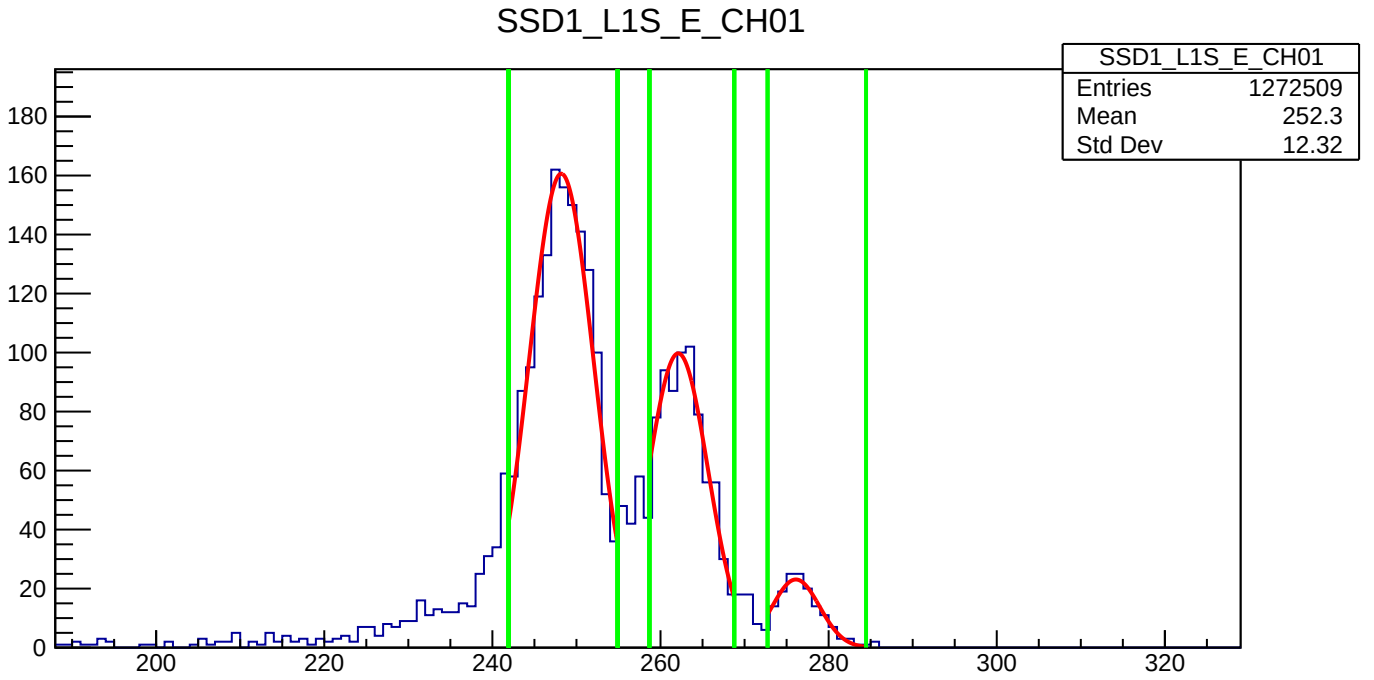
5.2
5.1


SSD1_L1S_E_CH01
SSD1_L1S_E_CH01
Entries 1272509
Mean 252.3
Std Dev 12.32
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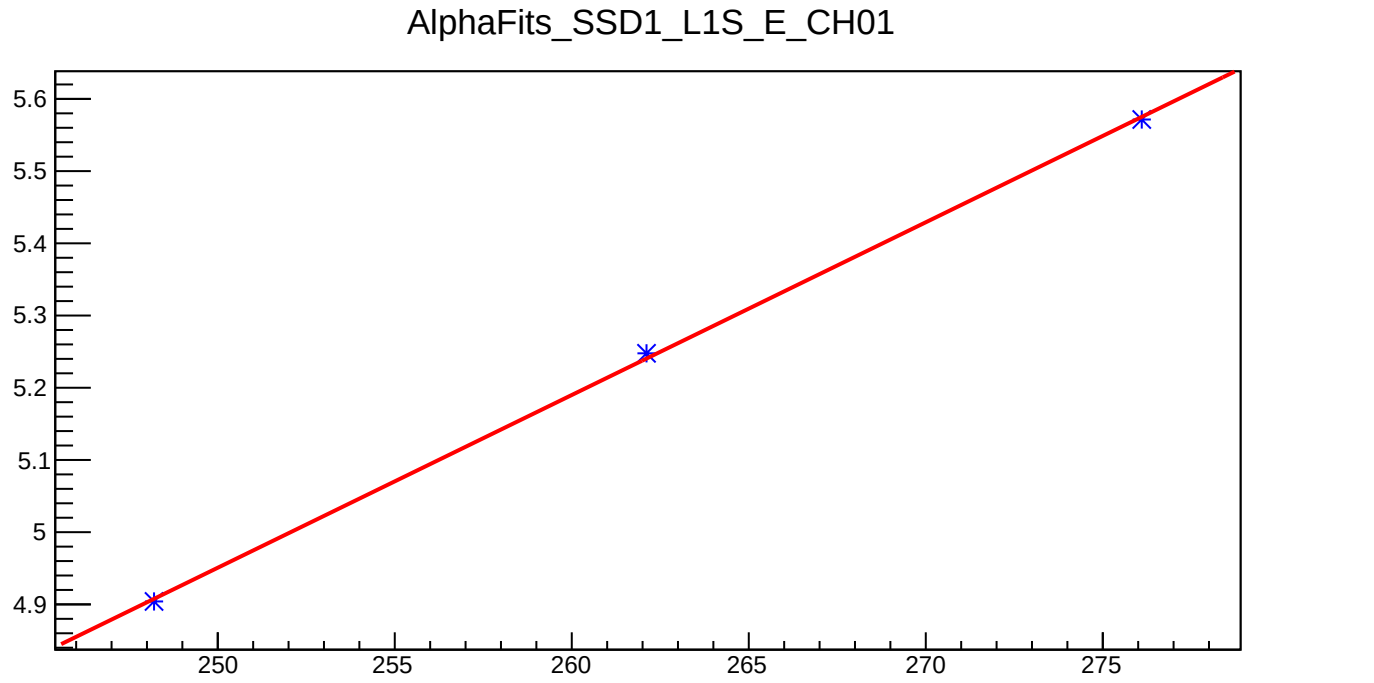
AlphaFits_SSD1_L1S_E_CH01
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5.4
5.3
5.2
5.1
5
4.9
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270
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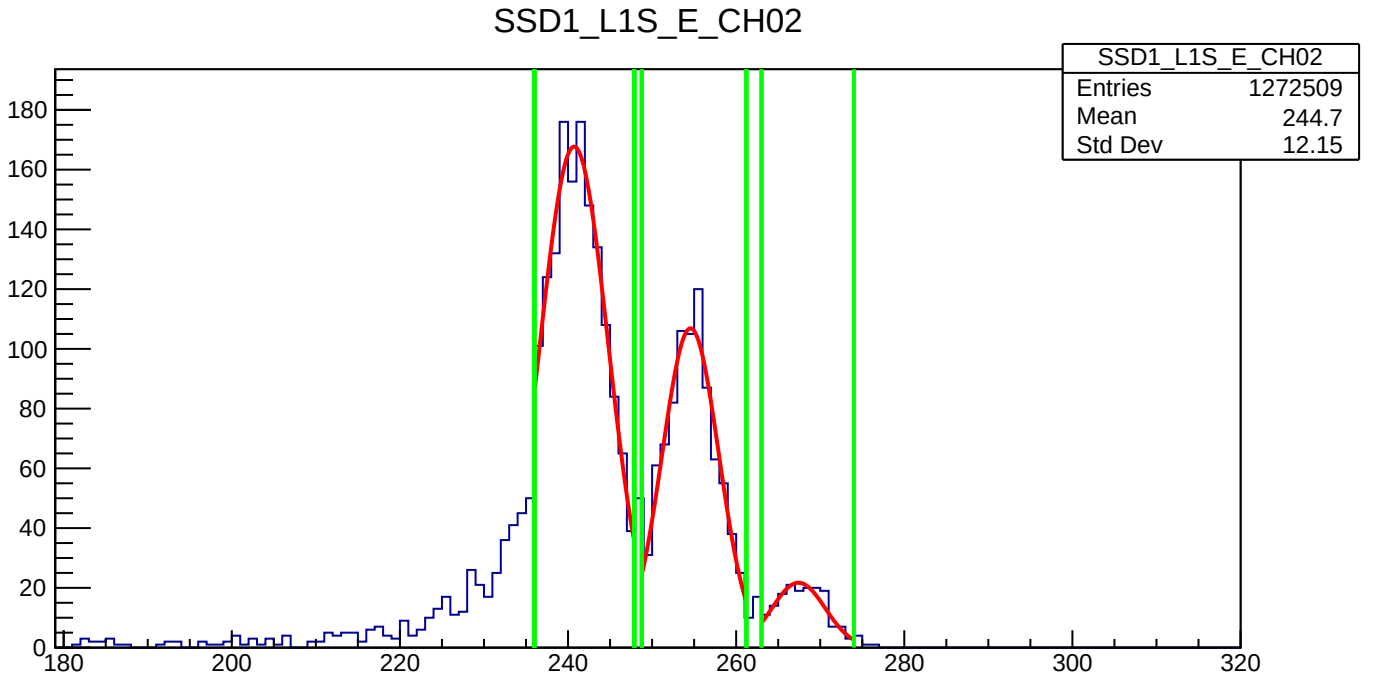
SSD1_L1S_E_CH02
SSD1_L1S_E_CH02
Entries 1272509
Mean 244.7
Std Dev 12.15
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60
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300
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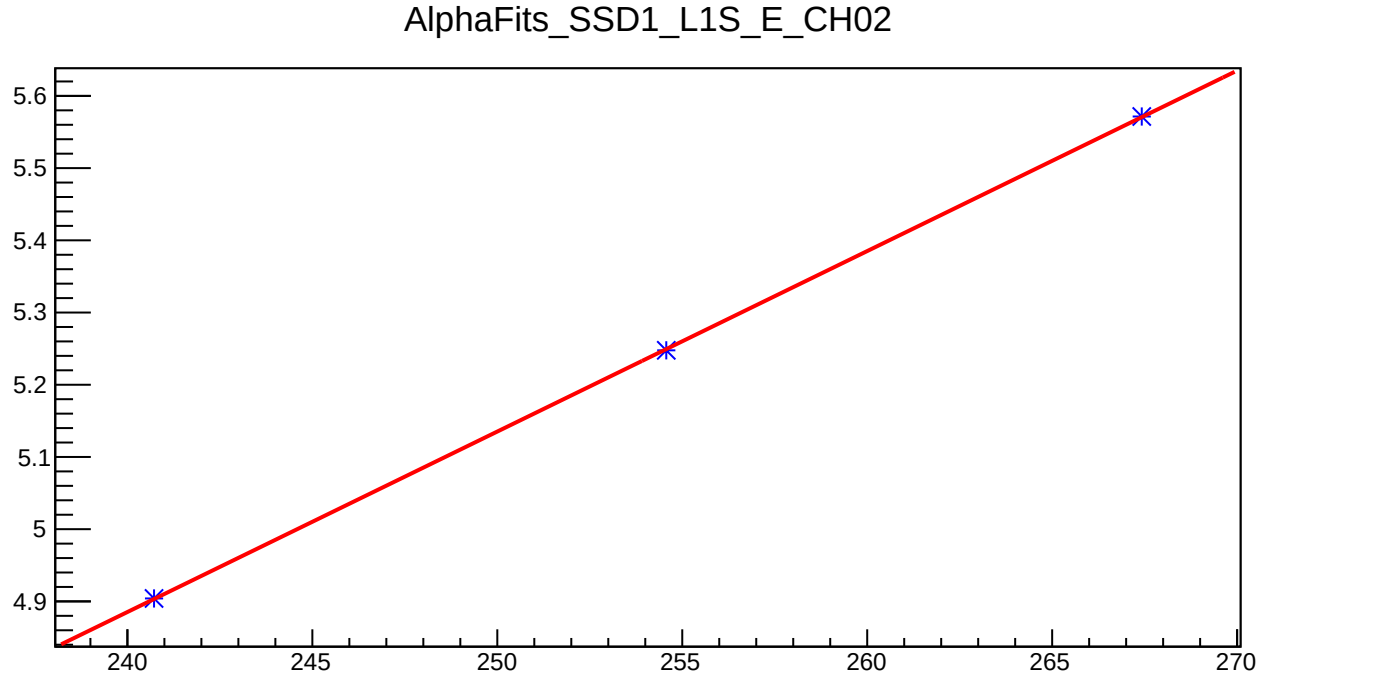
AlphaFits_SSD1_L1S_E_CH02
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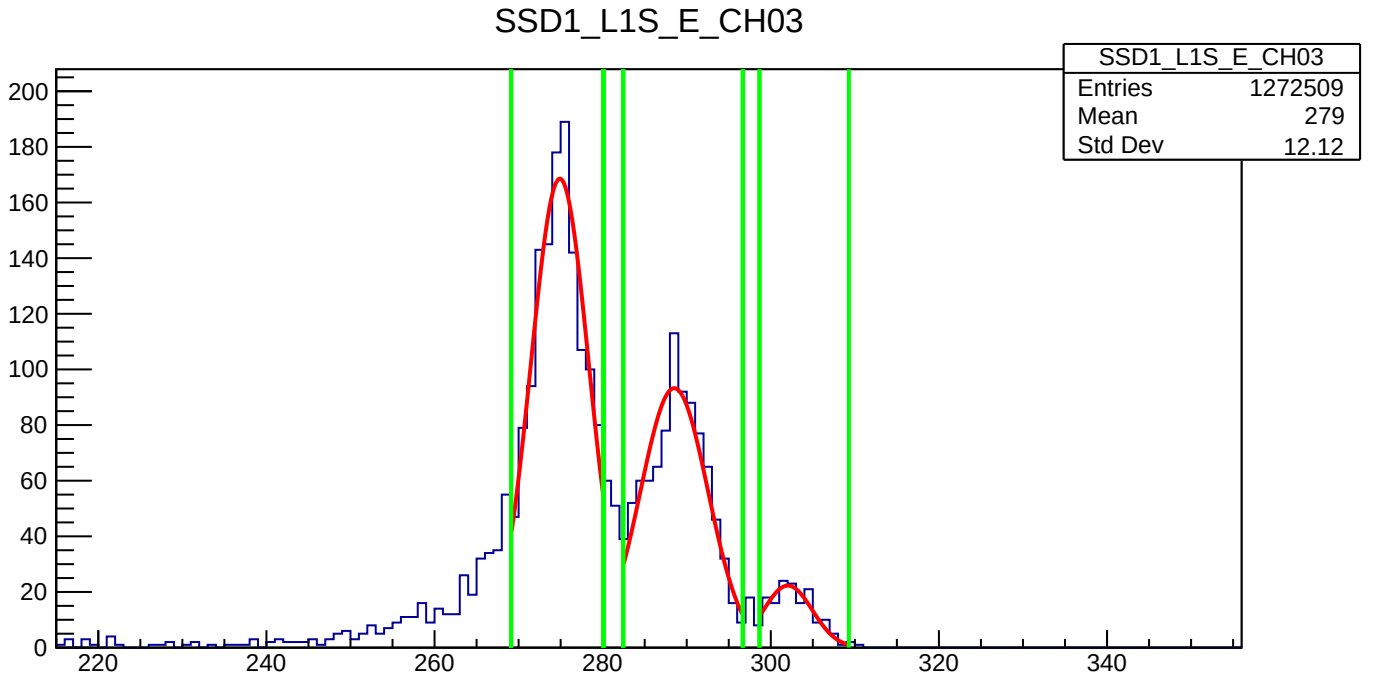
SSD1_L1S_E_CH03
SSD1_L1S_E_CH03
Entries 1272509
Mean 279
Std Dev 12.12
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140
120
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80
60
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0
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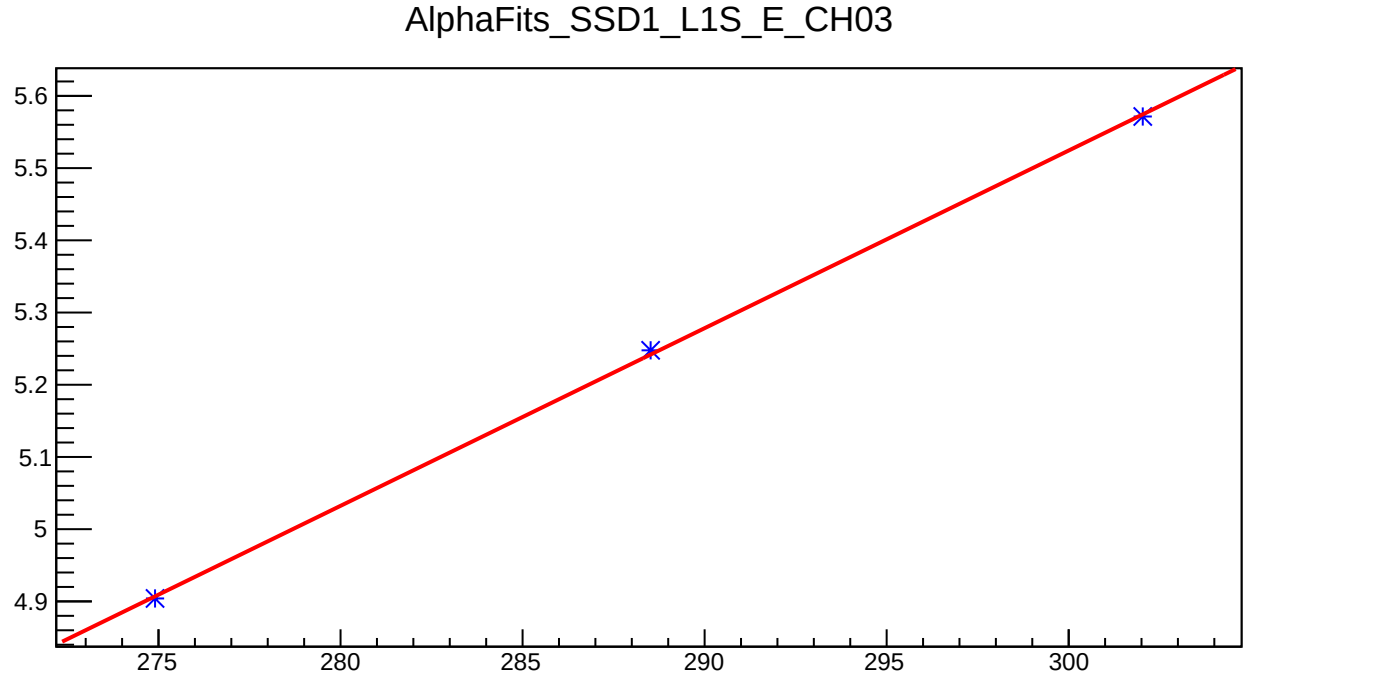
300
320
340


AlphaFits_SSD1_L1S_E_CH03
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5.4
5.3
5.2
5.1
5
4.9
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295
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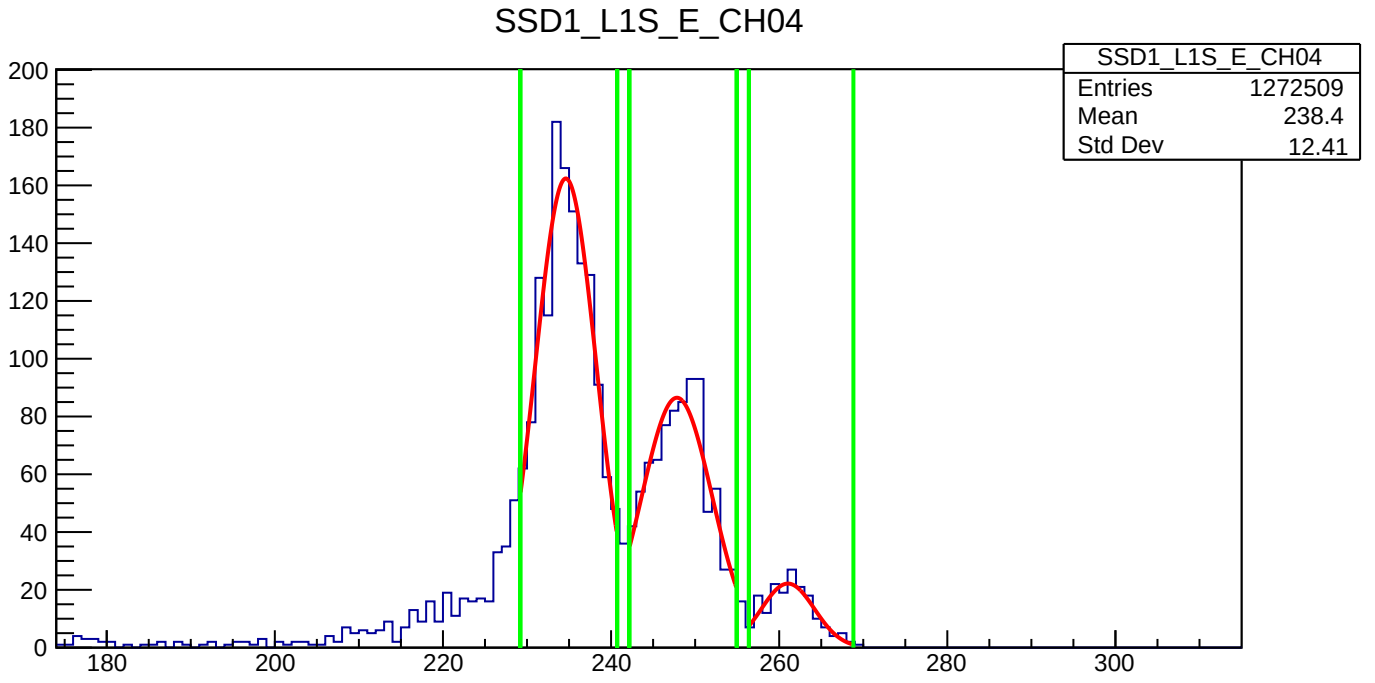
SSD1_L1S_E_CH04
SSD1_L1S_E_CH04
Entries 1272509
Mean 238.4
Std Dev 12.41
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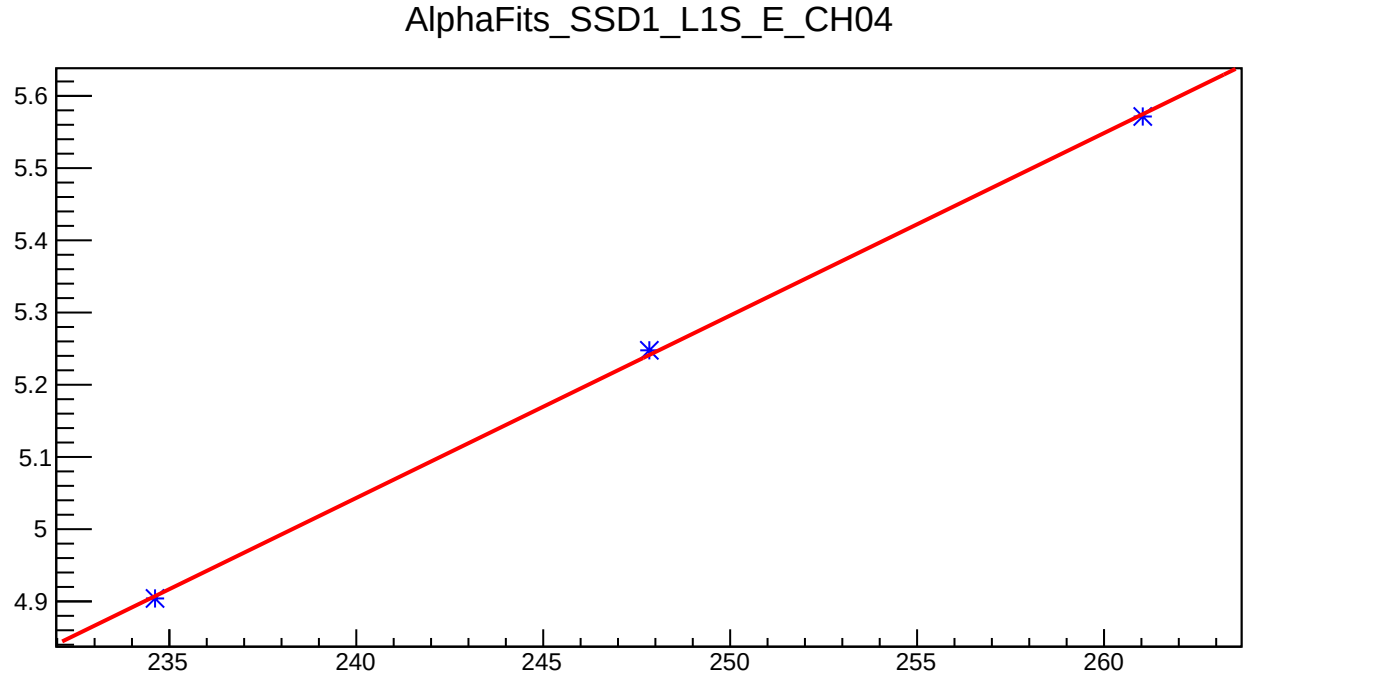
AlphaFits_SSD1_L1S_E_CH04
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5.4
5.3
5.2
5.1
5
4.9
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255
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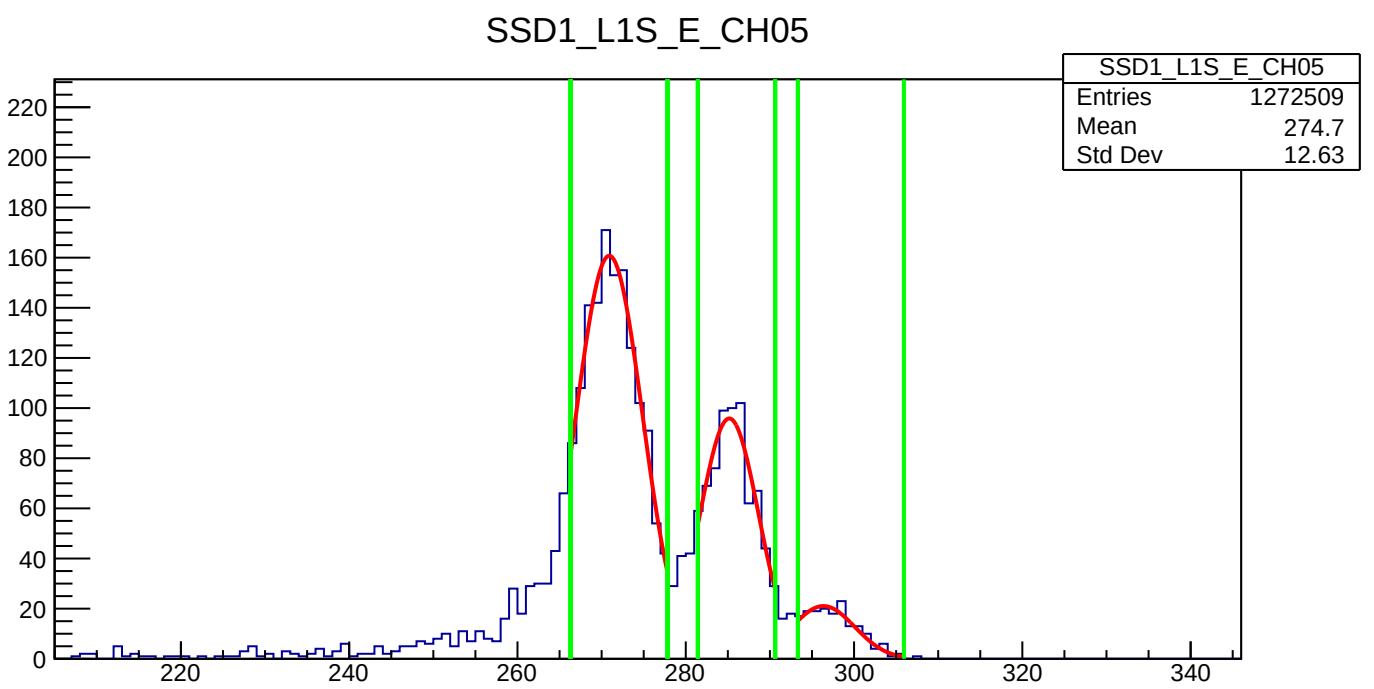
SSD1_L1S_E_CH05
SSD1_L1S_E_CH05
Entries 1272509
Mean 274.7
Std Dev 12.63
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280
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320
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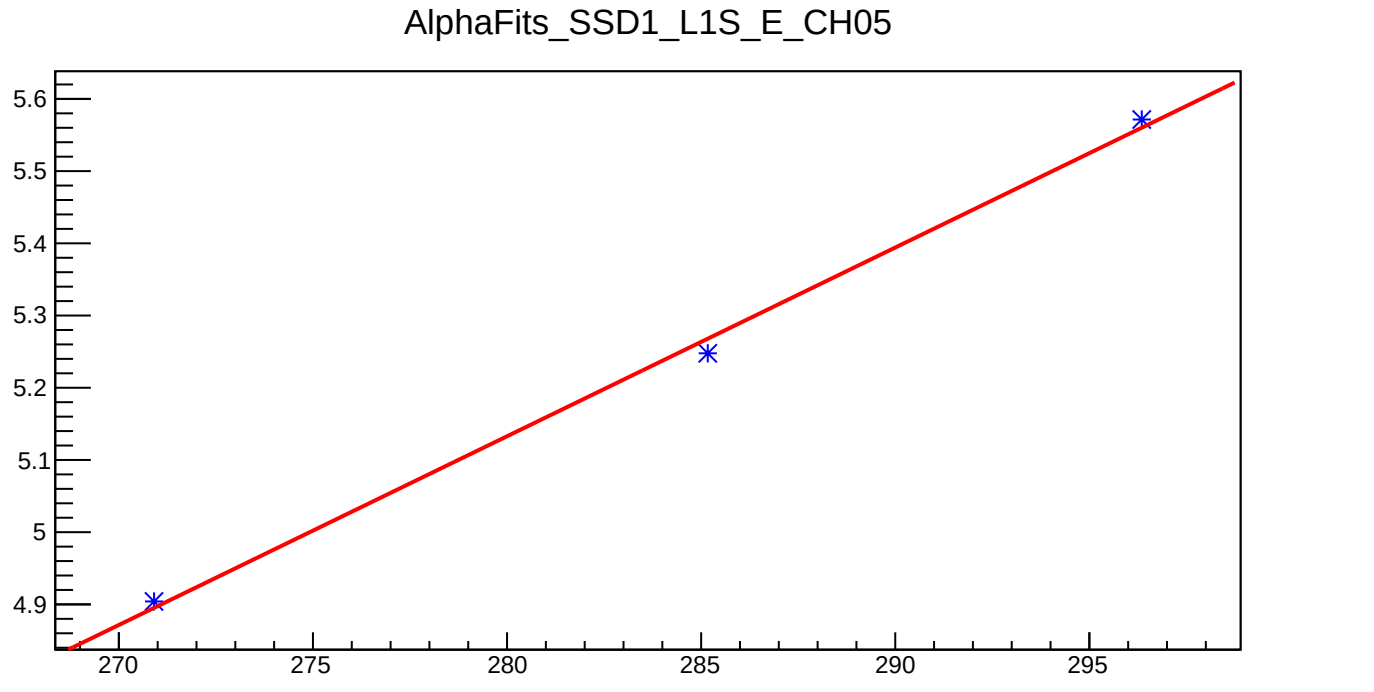
AlphaFits_SSD1_L1S_E_CH05
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5.1
5.2
5.3
5.4
5.5
5.6
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280
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290
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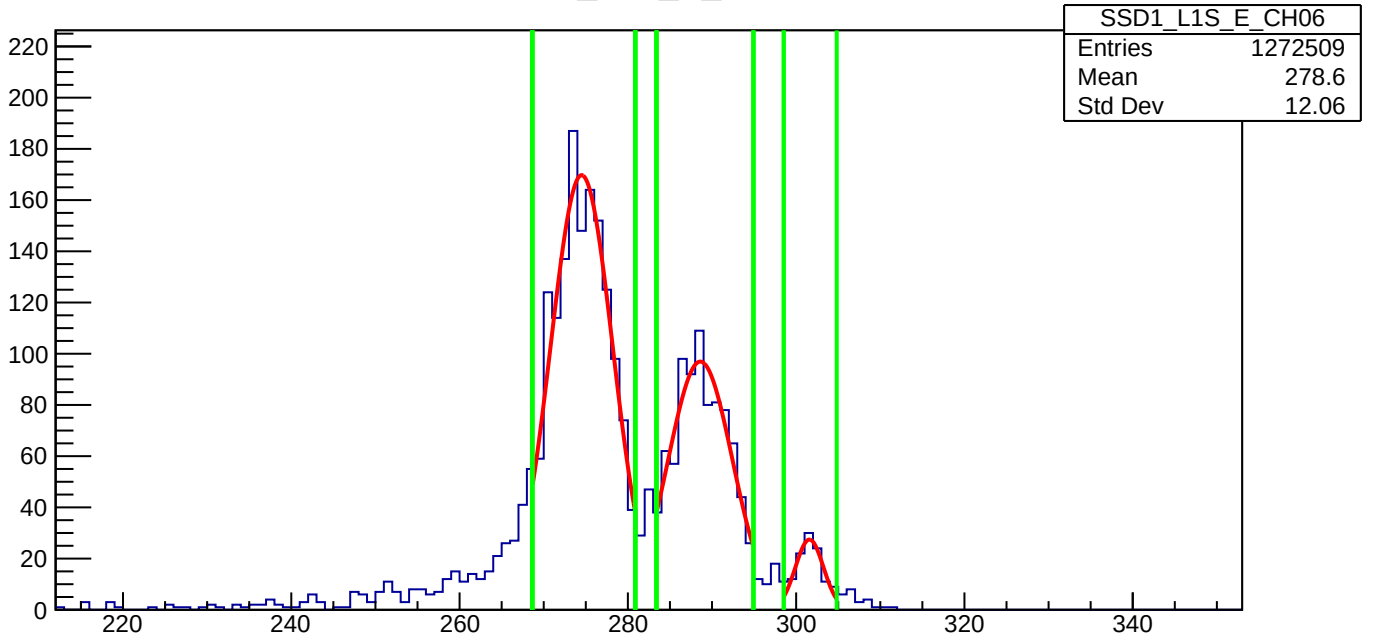
SSD1_L1S_E_CH06
SSD1_L1S_E_CH06
Entries 1272509
Mean 278.6
Std Dev 12.06
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140
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80
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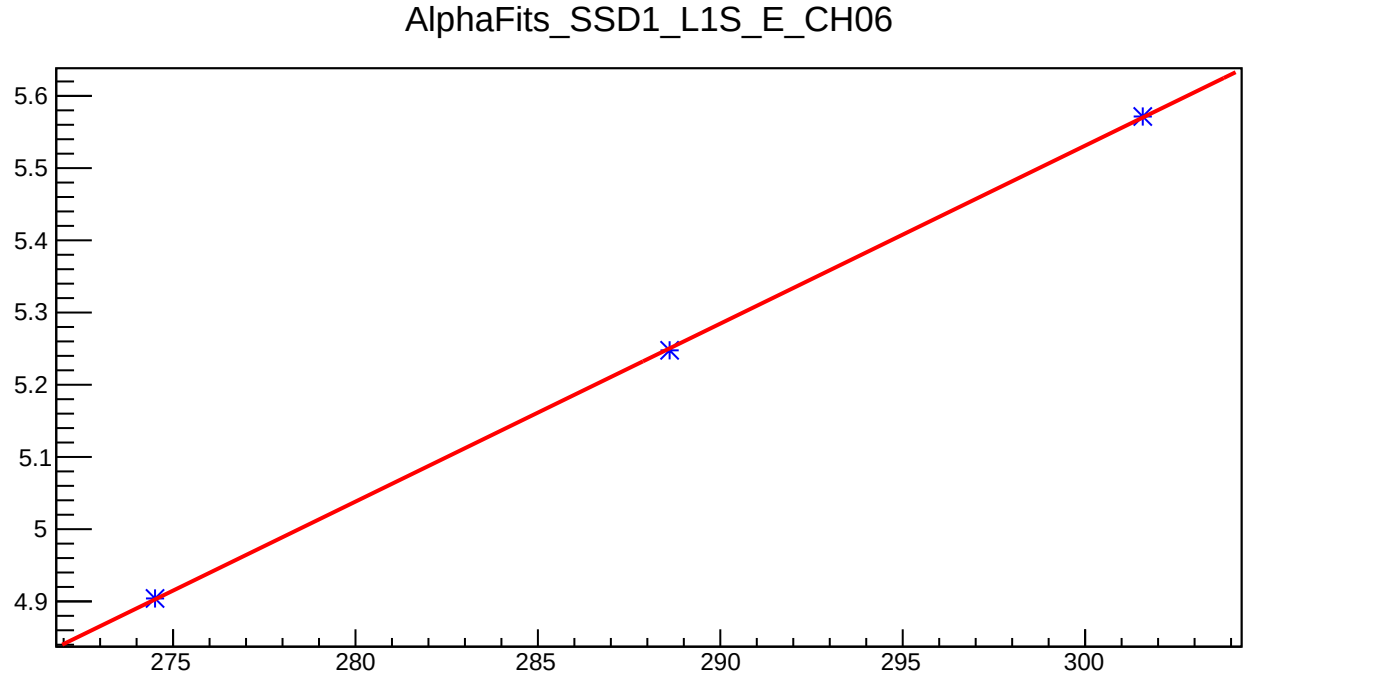
AlphaFits_SSD1_L1S_E_CH06
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5.2
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5
4.9
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295
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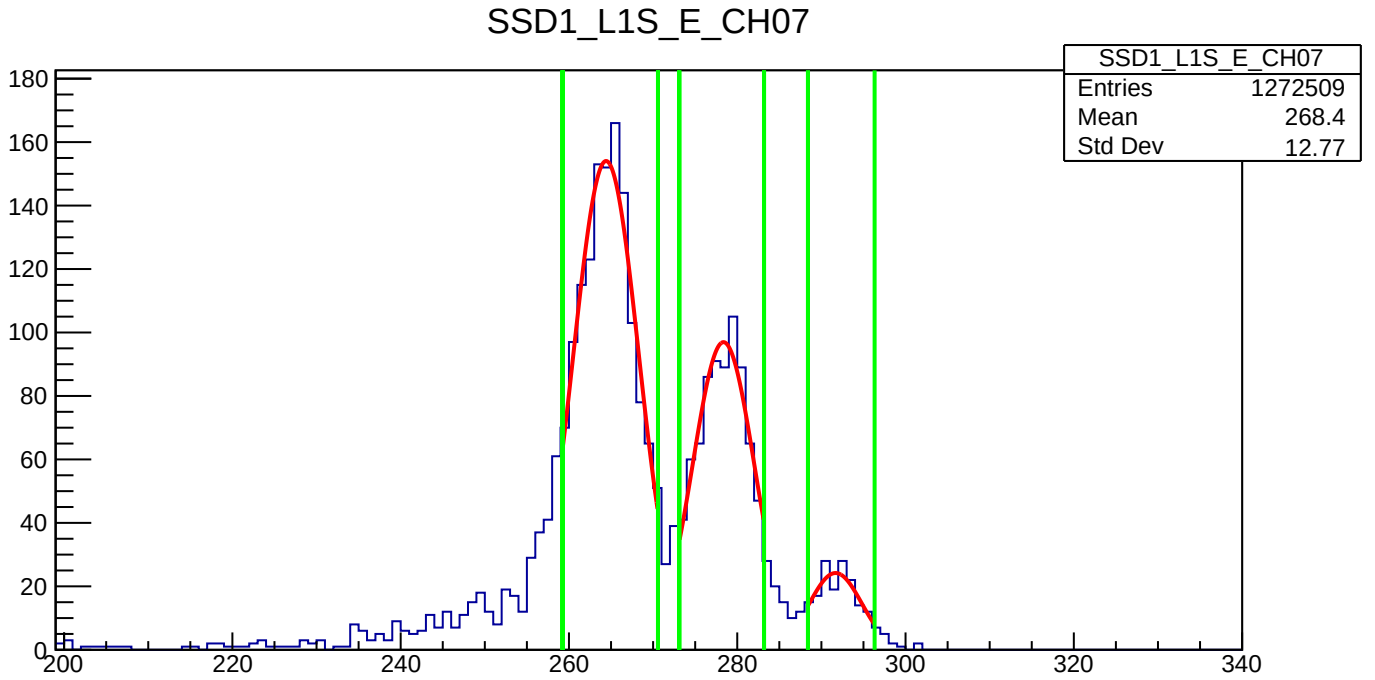
SSD1_L1S_E_CH07
SSD1_L1S_E_CH07
Entries 1272509
Mean 268.4
Std Dev 12.77
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320
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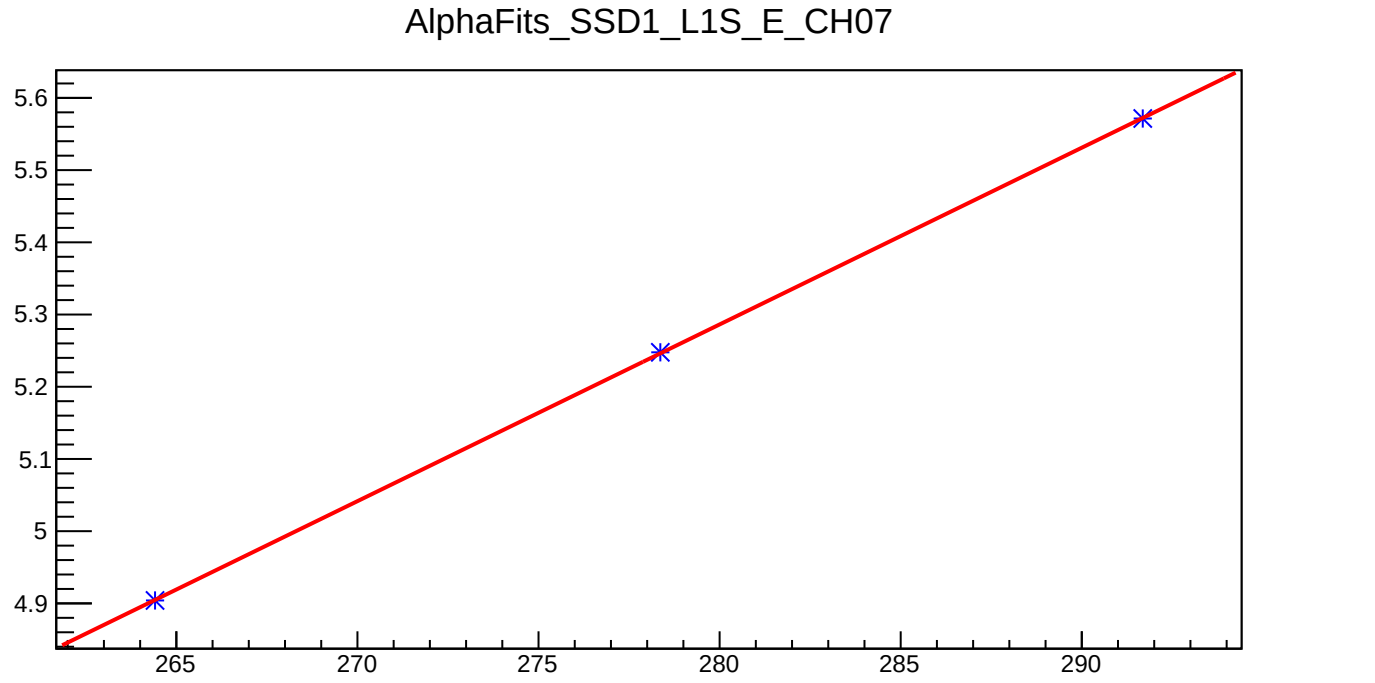
AlphaFits_SSD1_L1S_E_CH07
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5.5
5.4
5.3
5.2
5.1
5
4.9
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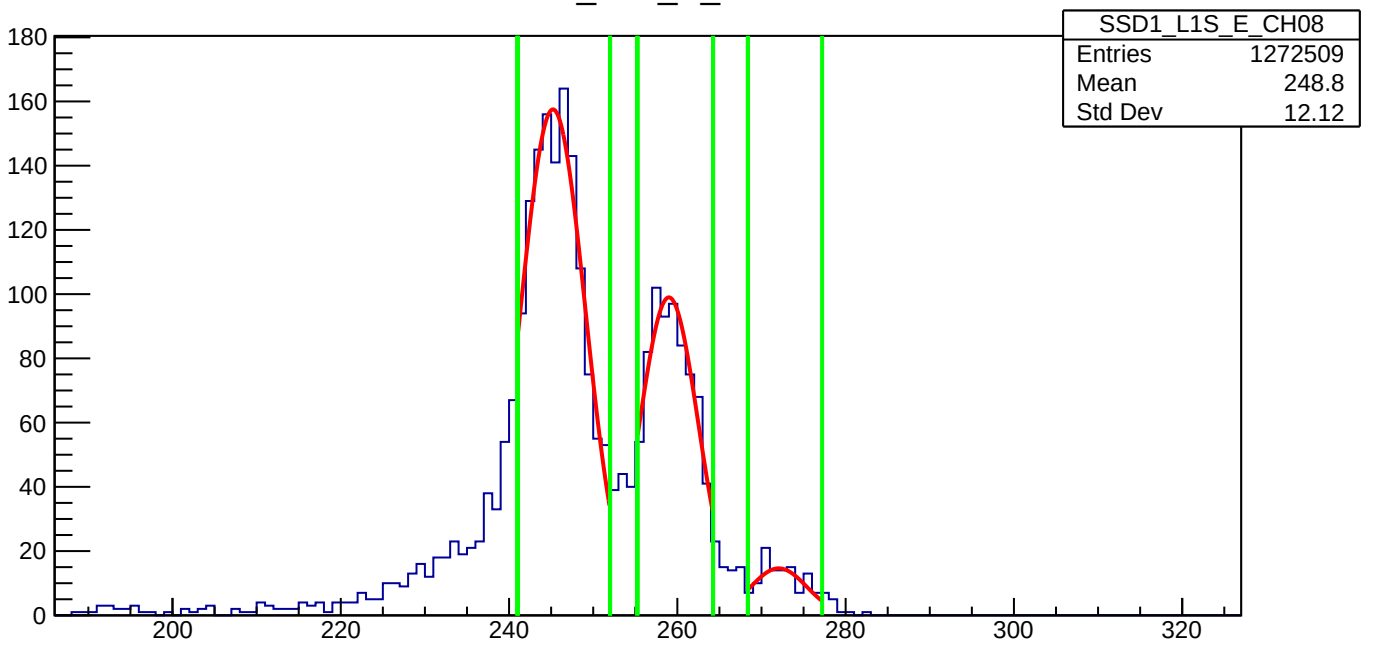
275
280
285
290


SSD1_L1S_E_CH08
SSD1_L1S_E_CH08
Entries 1272509
Mean 248.8
Std Dev 12.12
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0
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300
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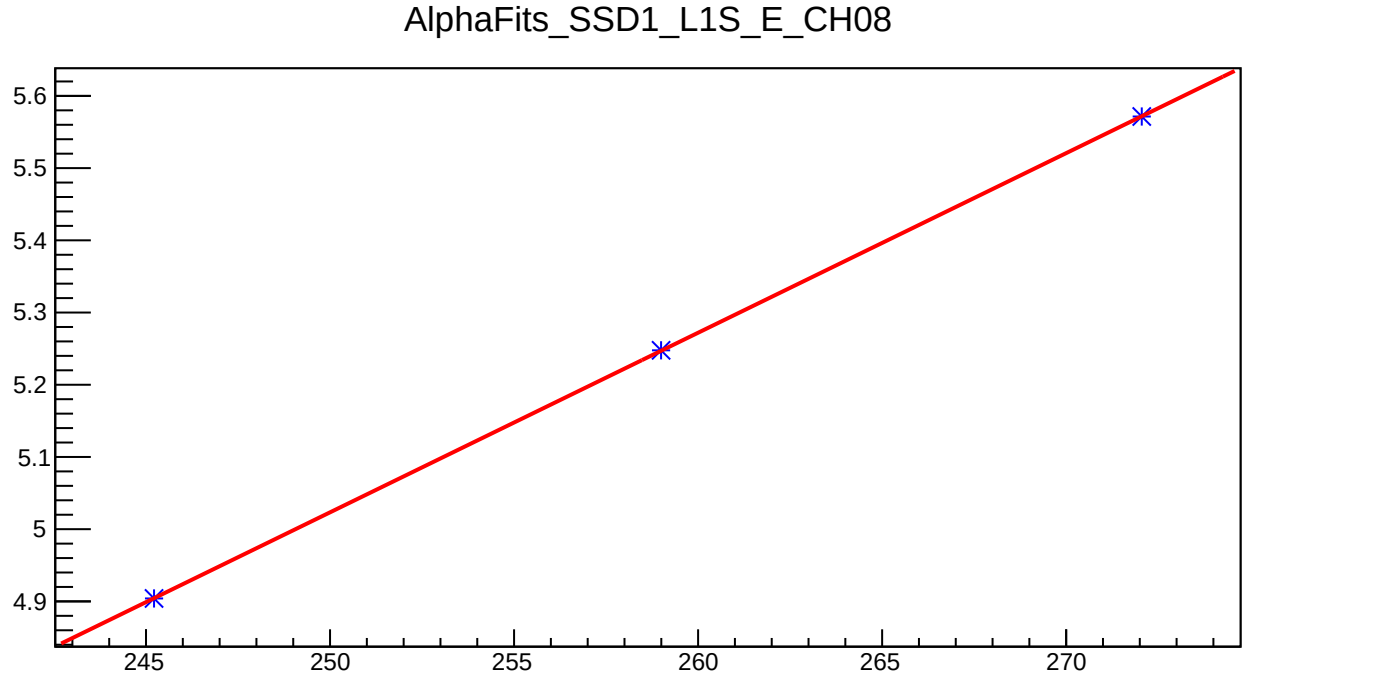
AlphaFits_SSD1_L1S_E_CH08
5.6
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5.4
5.3
5.2
5.1
5
4.9
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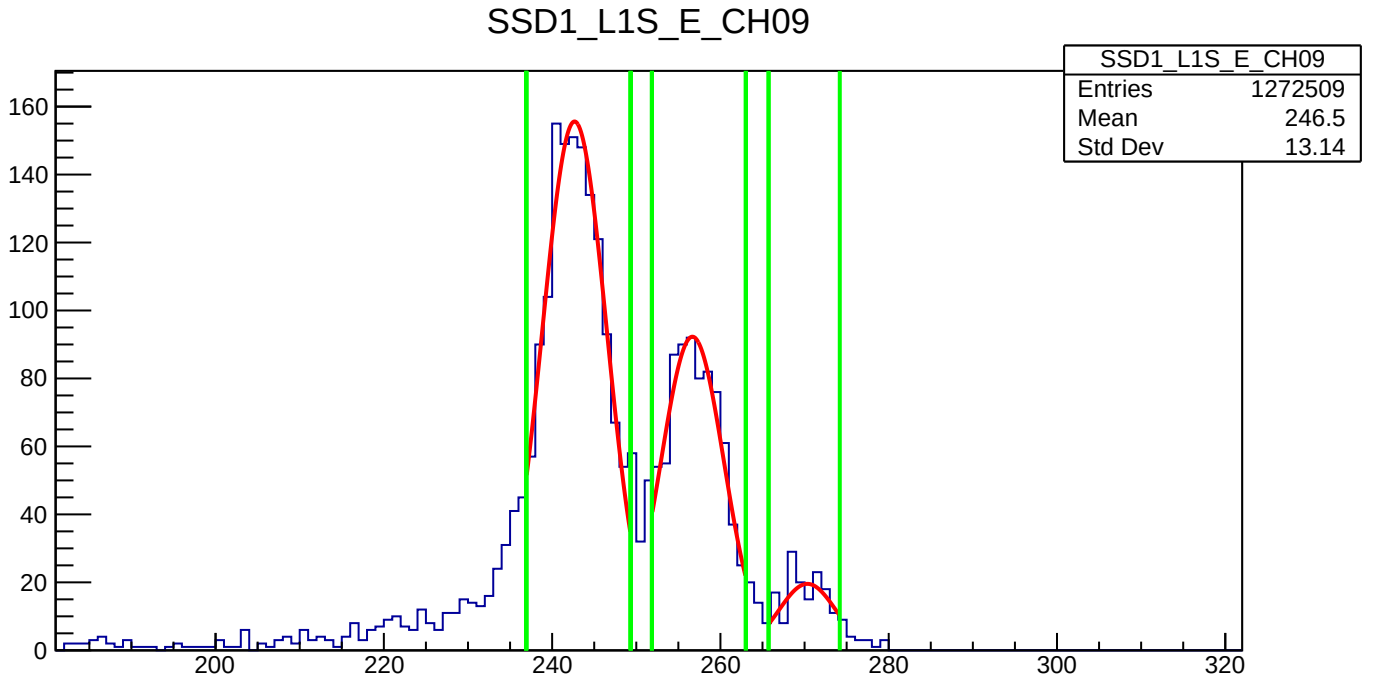
260
265
270


SSD1_L1S_E_CH09
SSD1_L1S_E_CH09
Entries 1272509
Mean 246.5
Std Dev 13.14
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0
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300
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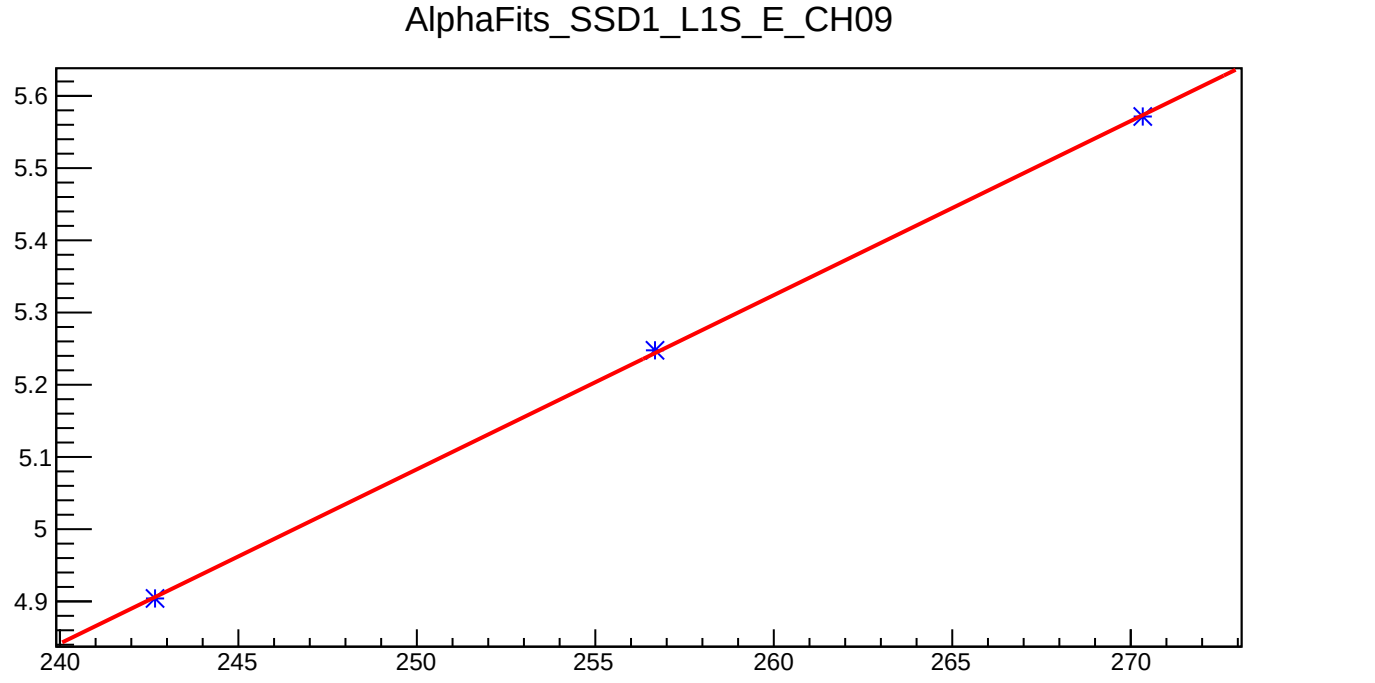
AlphaFits_SSD1_L1S_E_CH09
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5.4
5.3
5.2
5.1
5
4.9
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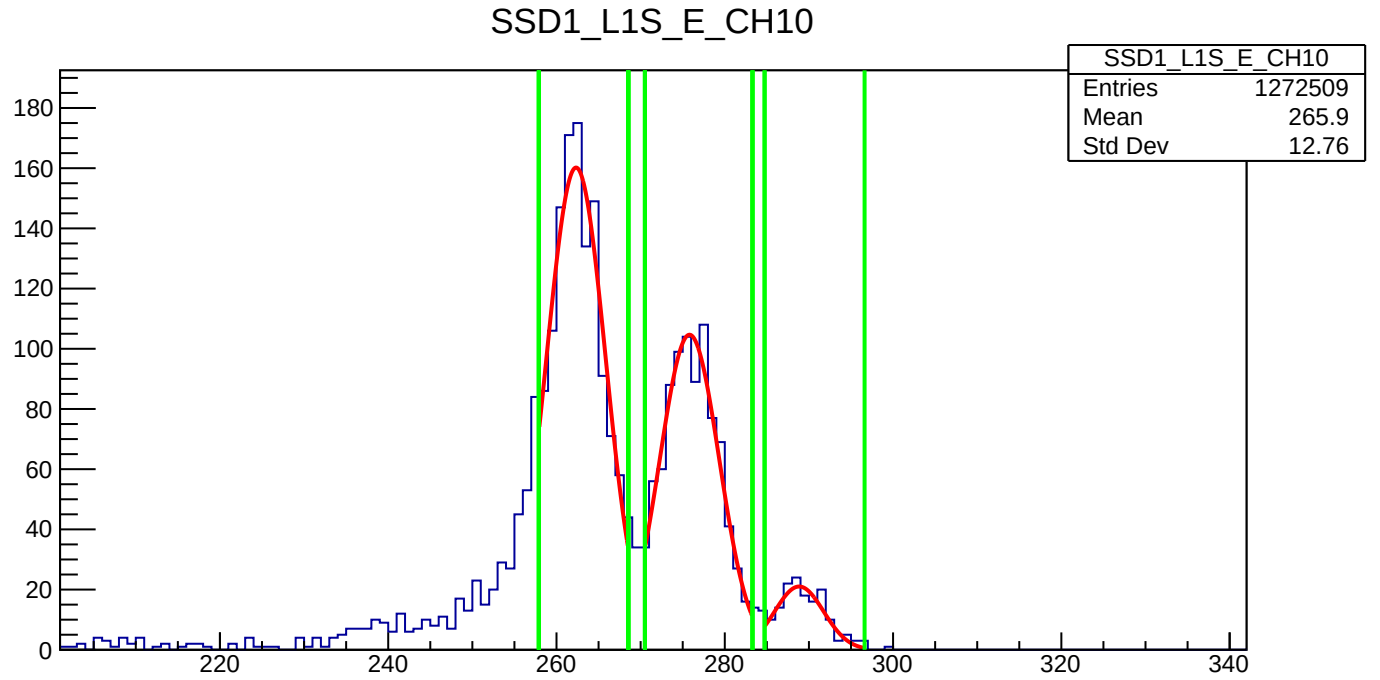
250
255
260
265
270


SSD1_L1S_E_CH10
SSD1_L1S_E_CH10
Entries 1272509
Mean 265.9
Std Dev 12.76
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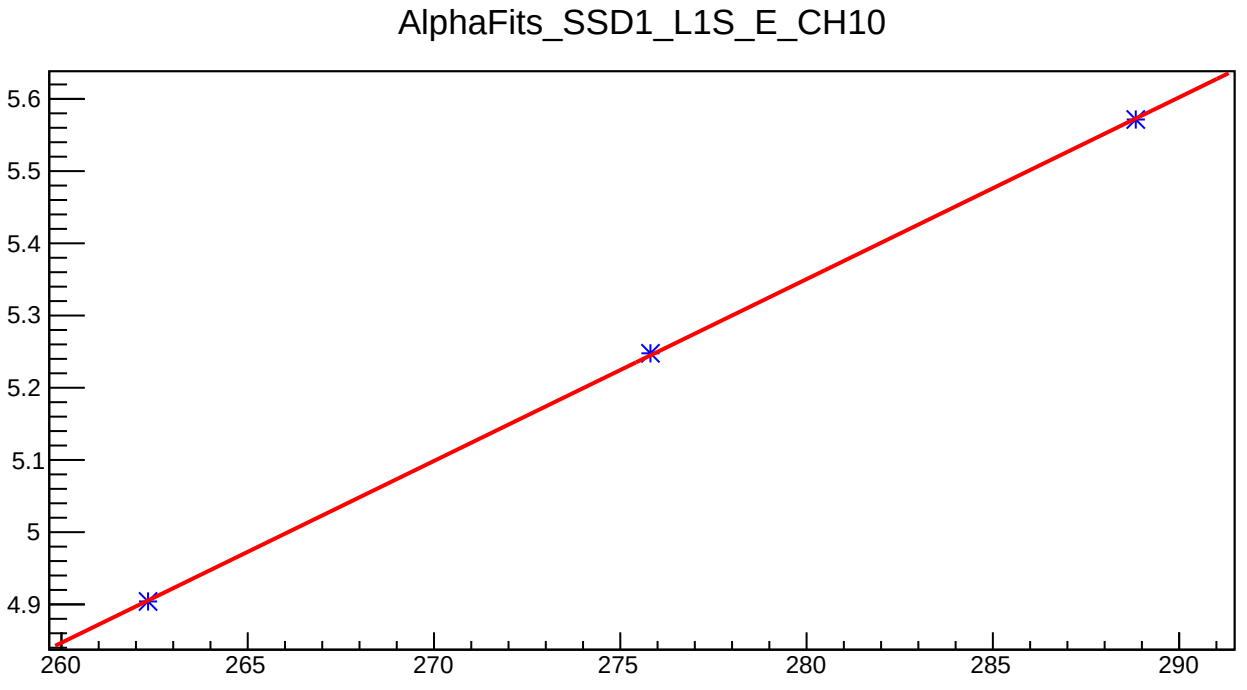
300
320
340


AlphaFits_SSD1_L1S_E_CH10
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5.4
5.3
5.2
5.1
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4.9
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275
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285
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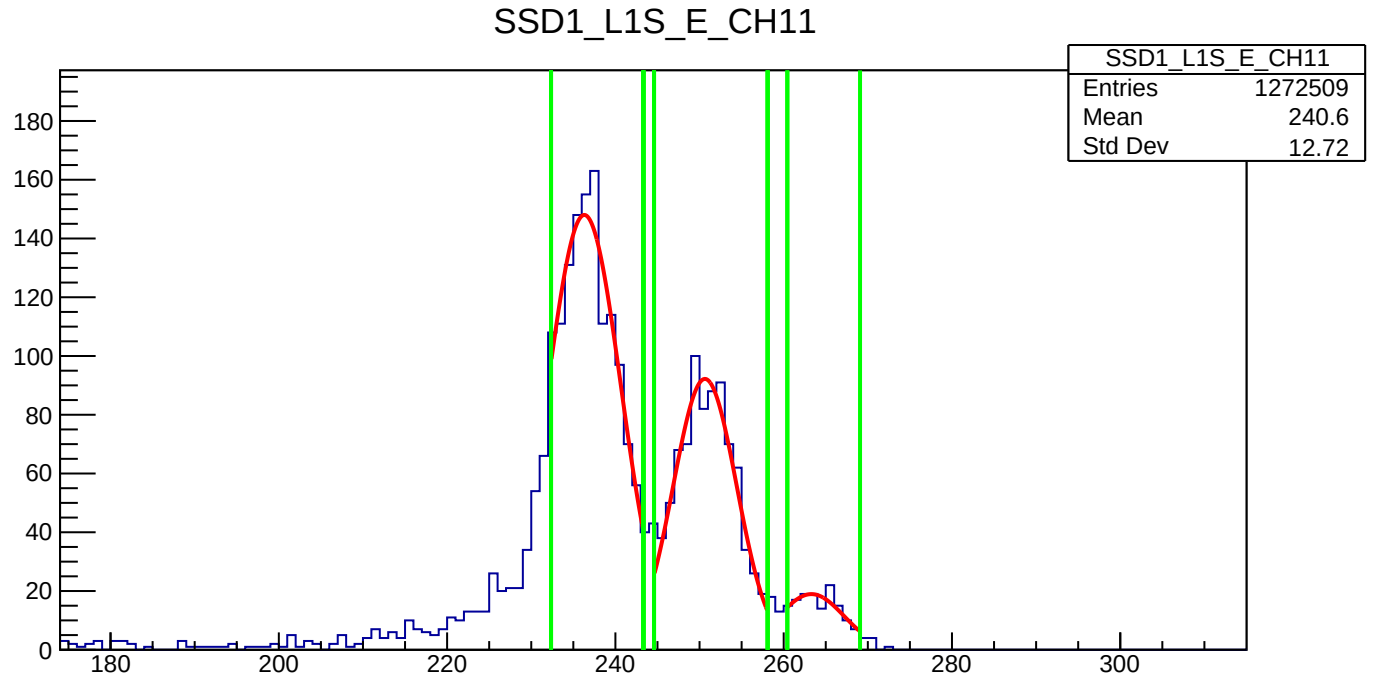
SSD1_L1S_E_CH11
SSD1_L1S_E_CH11
Entries 1272509
Mean 240.6
Std Dev 12.72
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280
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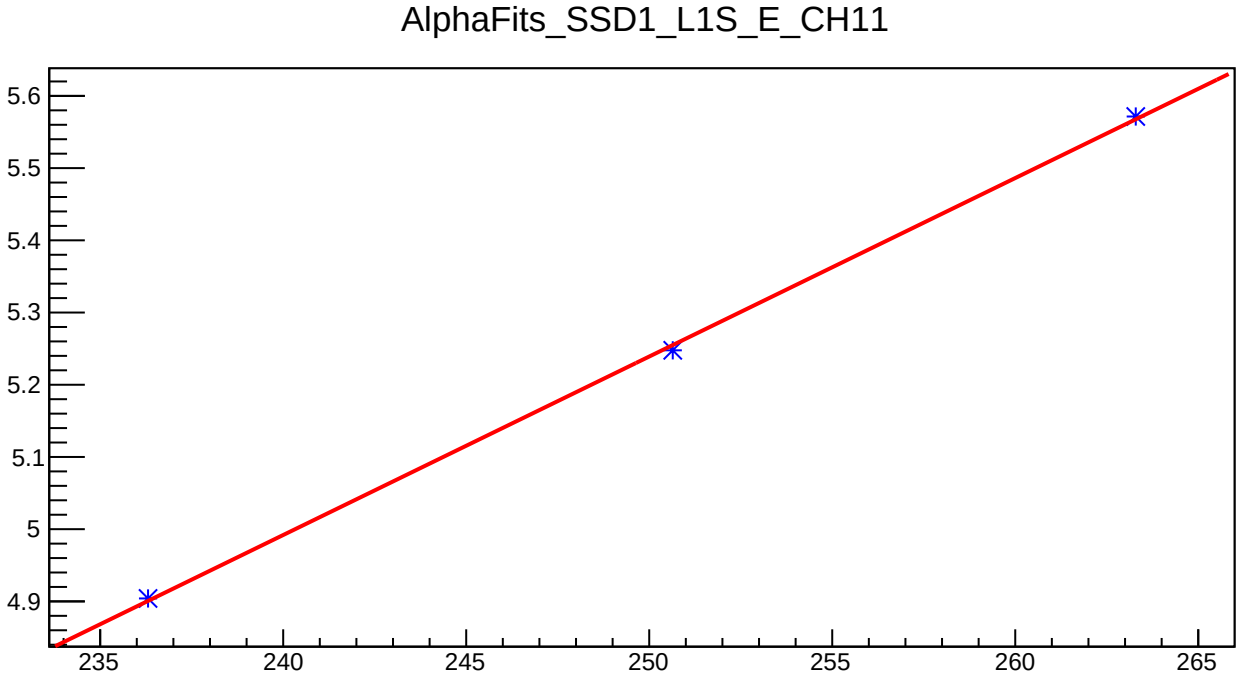
AlphaFits_SSD1_L1S_E_CH11
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5.2
5.3
5.4
5.5
5.6
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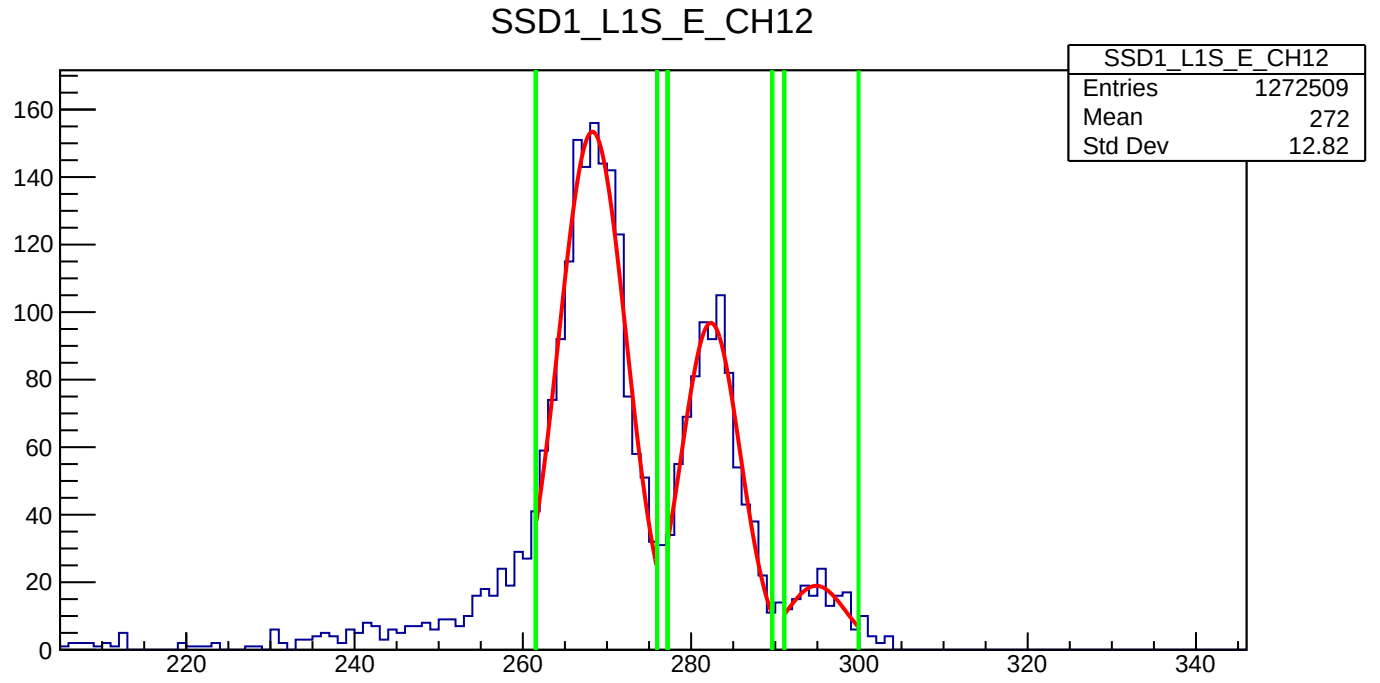
260
265


SSD1_L1S_E_CH12
SSD1_L1S_E_CH12
Entries 1272509
Mean 272
Std Dev 12.82
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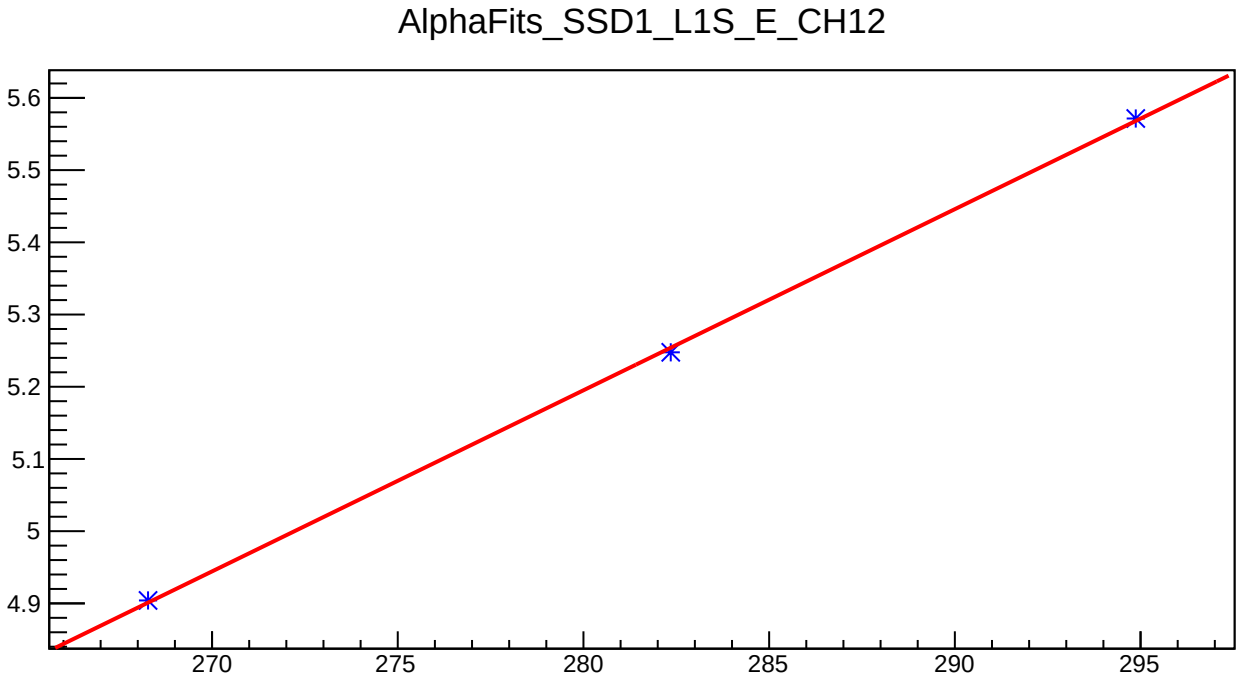
320
340


AlphaFits_SSD1_L1S_E_CH12
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5.5
5.4
5.3
5.2
5.1
5
4.9
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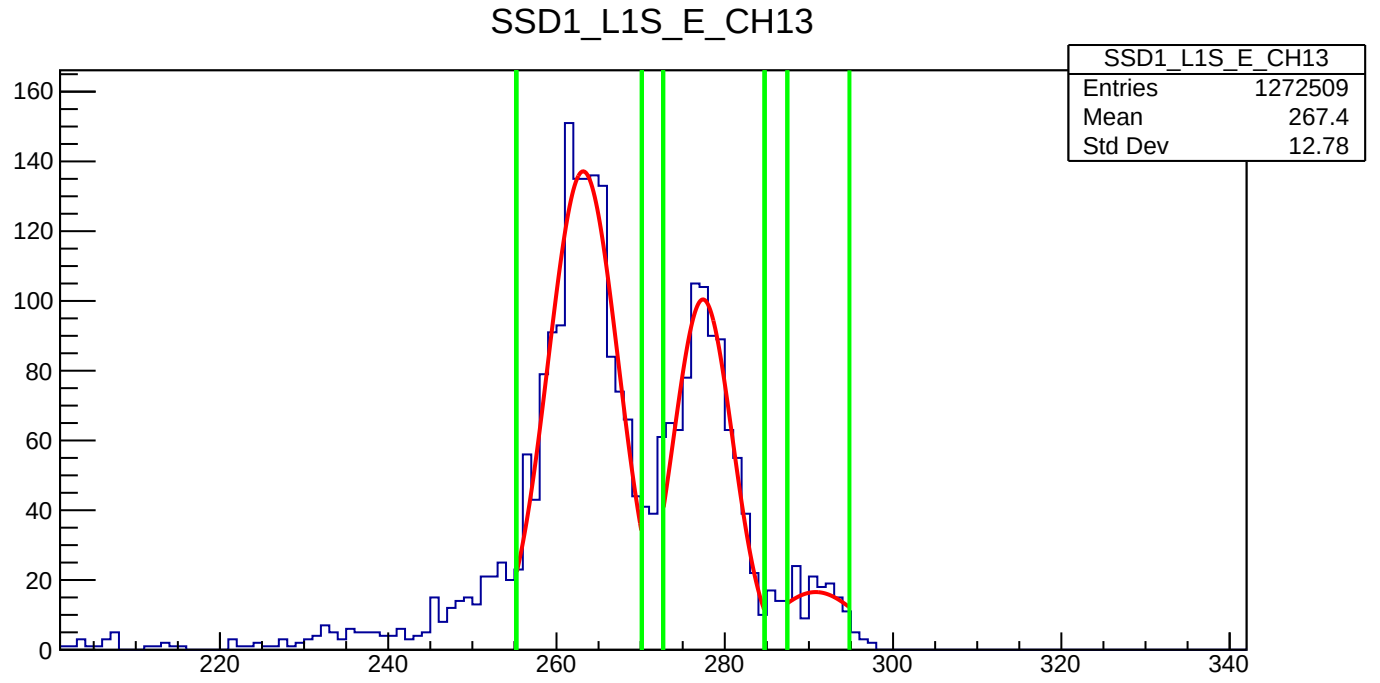
275
280
285
290
295


SSD1_L1S_E_CH13
SSD1_L1S_E_CH13
Entries 1272509
Mean 267.4
Std Dev 12.78
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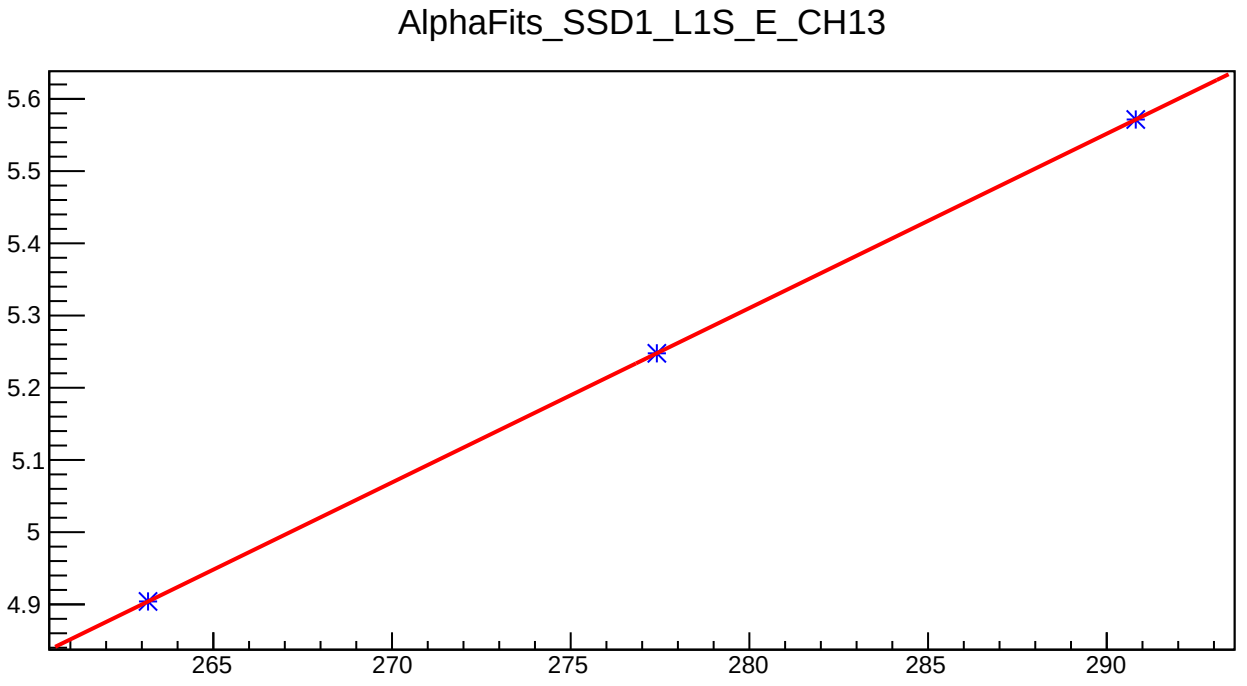
300
320
340


AlphaFits_SSD1_L1S_E_CH13
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5.5
5.4
5.3
5.2
5.1
5
4.9
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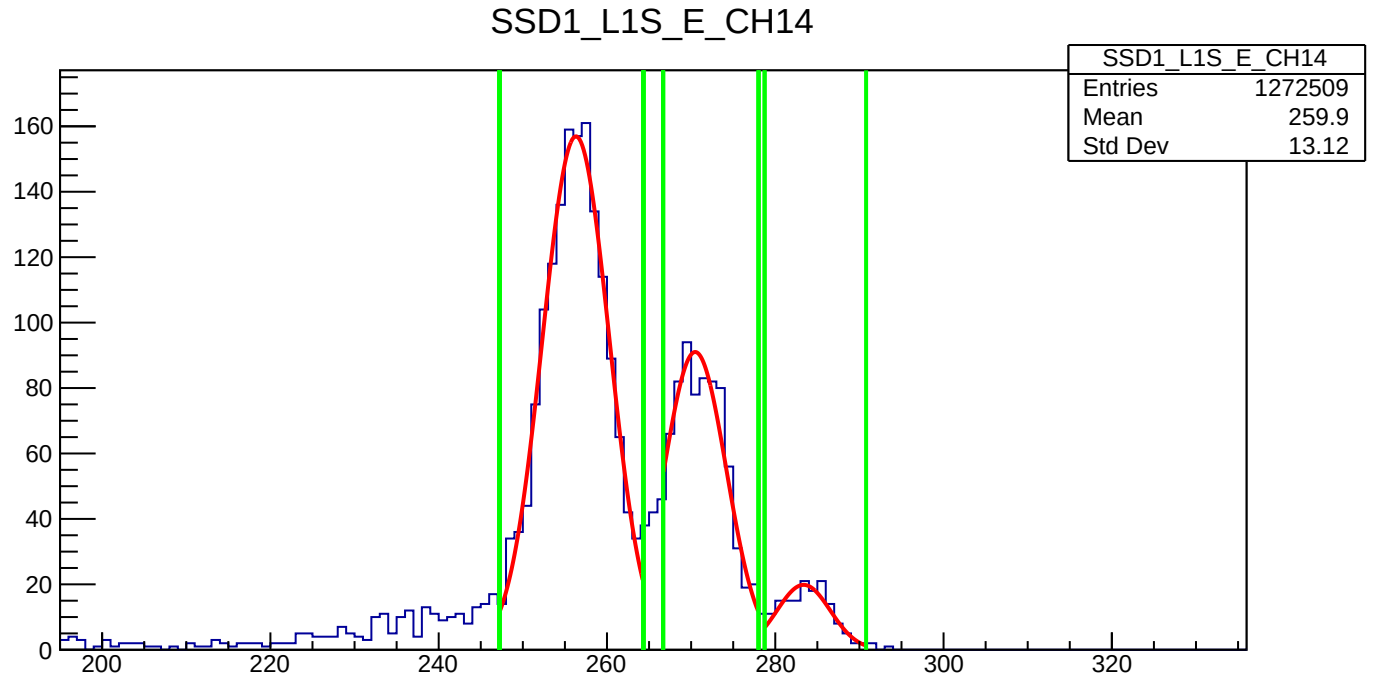
270
275
280
285
290


SSD1_L1S_E_CH14
SSD1_L1S_E_CH14
Entries 1272509
Mean 259.9
Std Dev 13.12
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300
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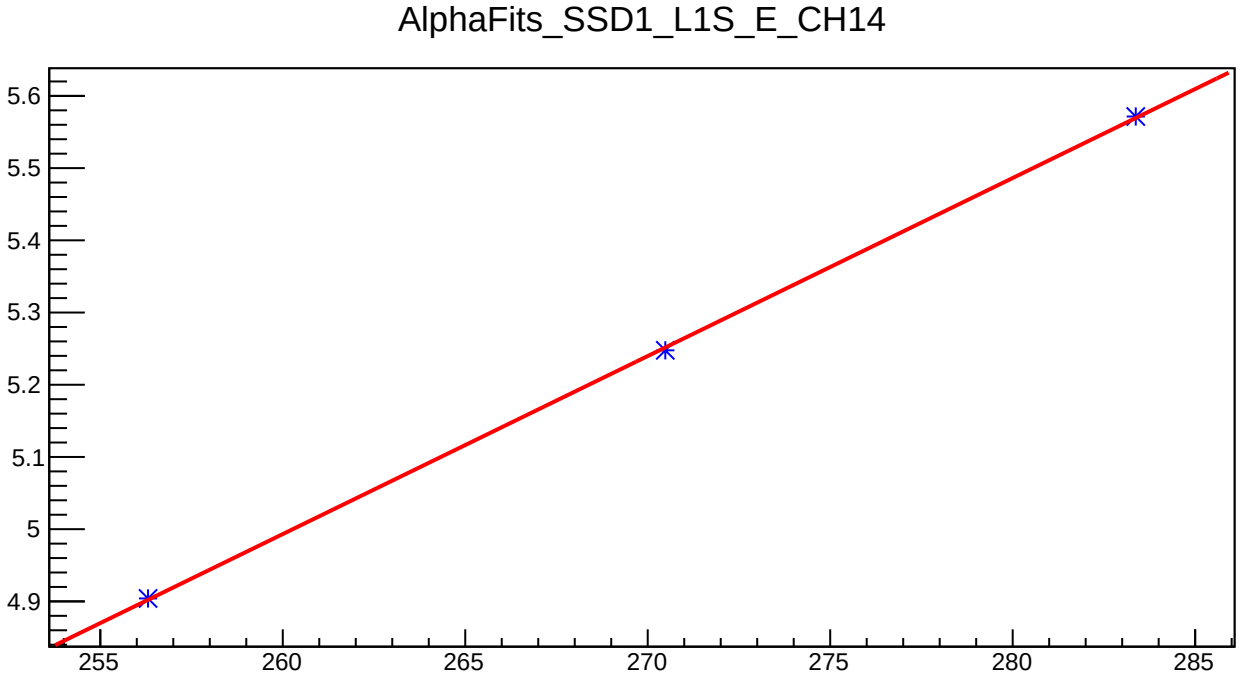
AlphaFits_SSD1_L1S_E_CH14
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5.2
5.3
5.4
5.5
5.6
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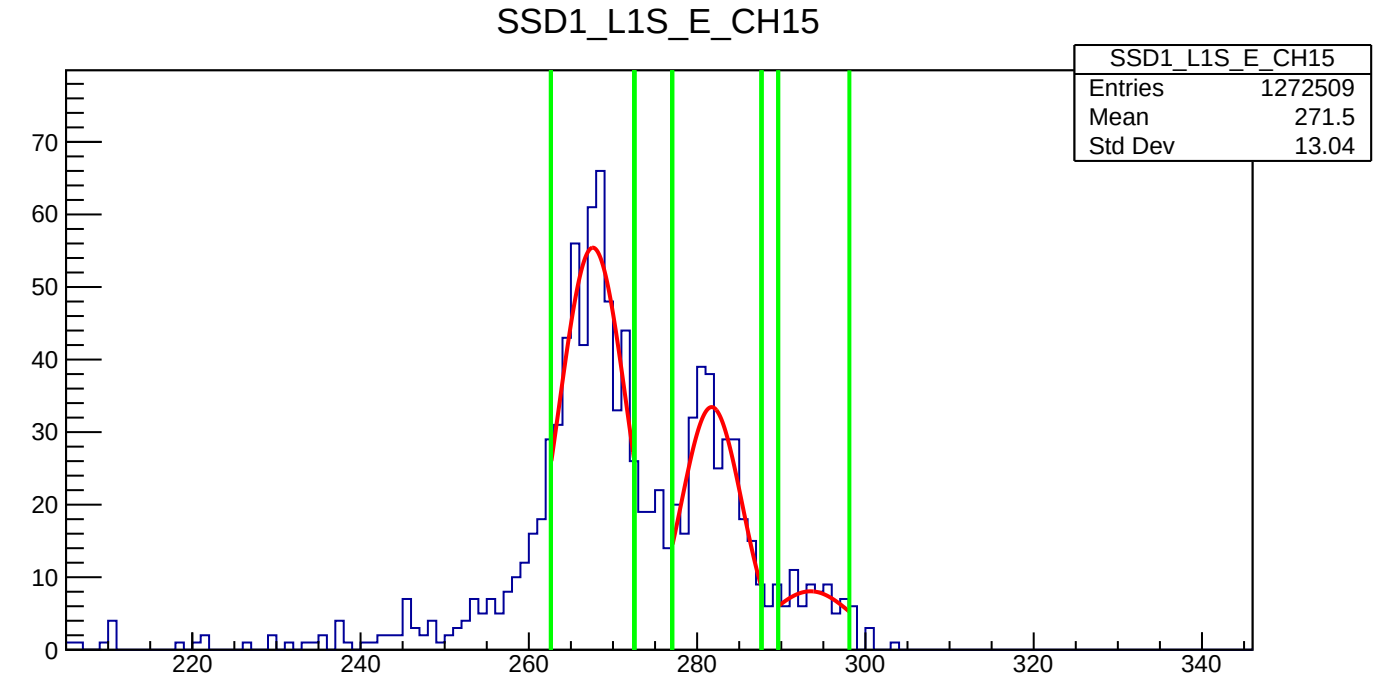
265
270
275
280
285


SSD1_L1S_E_CH15
SSD1_L1S_E_CH15
Entries 1272509
Mean 271.5
Std Dev 13.04
0
10
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30
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70
220
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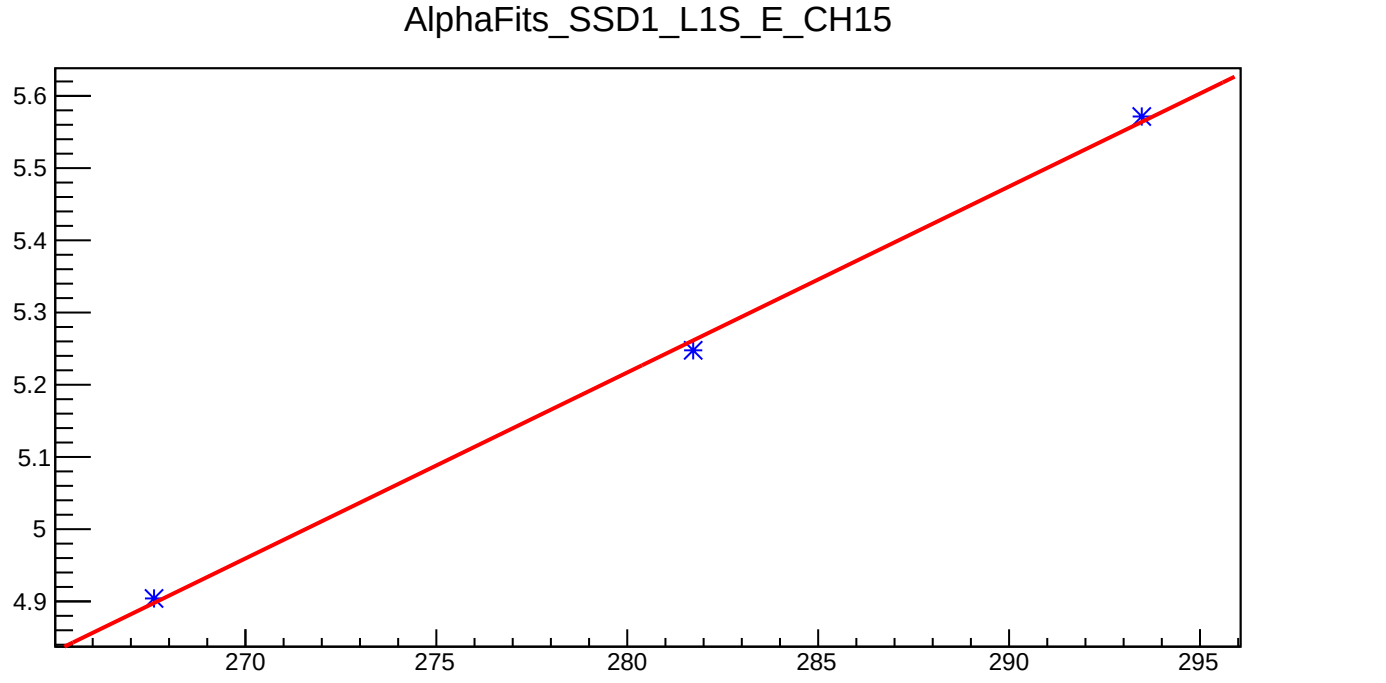
280
300
320
340


AlphaFits_SSD1_L1S_E_CH15
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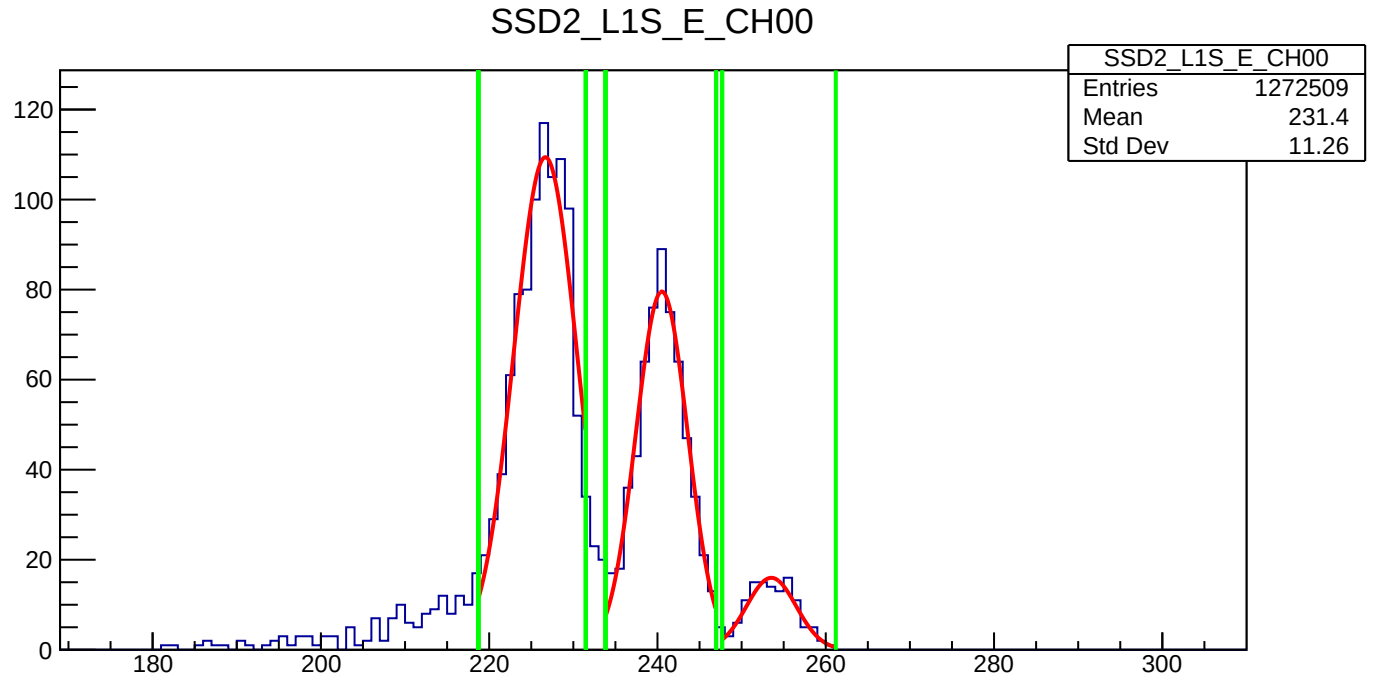
5.1
5.2
5.3
5.4
5.5
5.6
270
275
280
285
290
295


SSD2_L1S_E_CH00
SSD2_L1S_E_CH00
Entries 1272509
Mean 231.4
Std Dev 11.26
120
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20
0
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200
220
240
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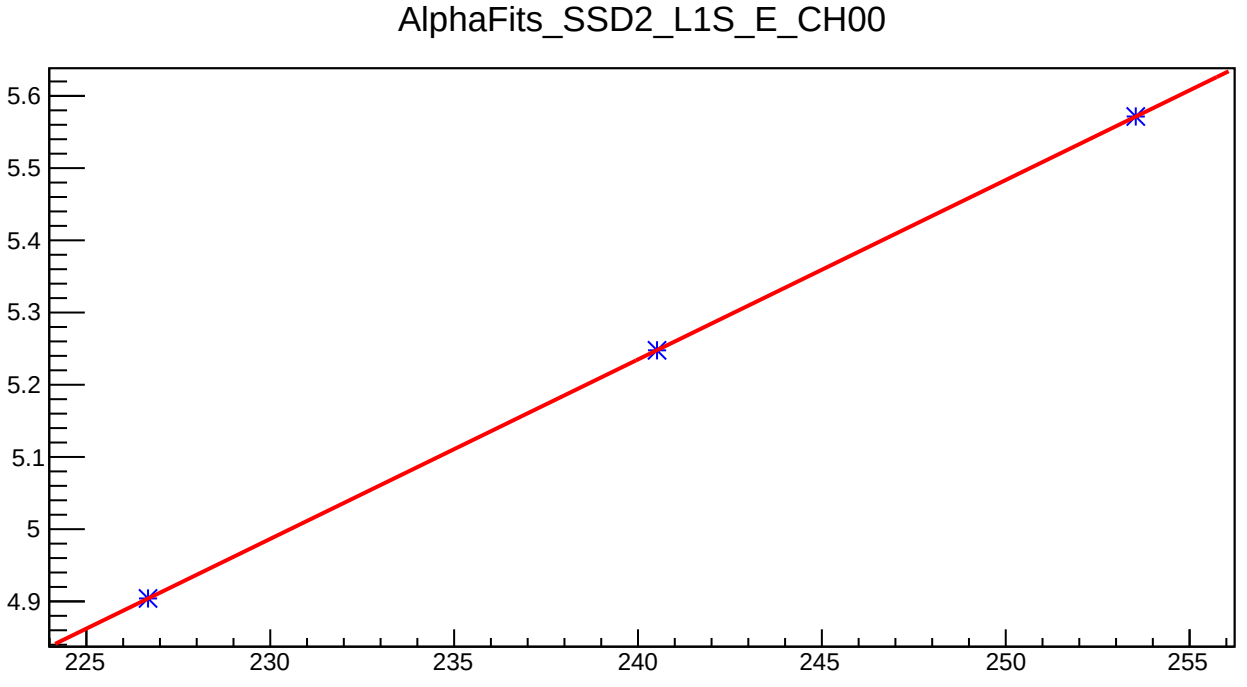
280
300


AlphaFits_SSD2_L1S_E_CH00
5.6
5.5
5.4
5.3
5.2
5.1
5
4.9
225
230
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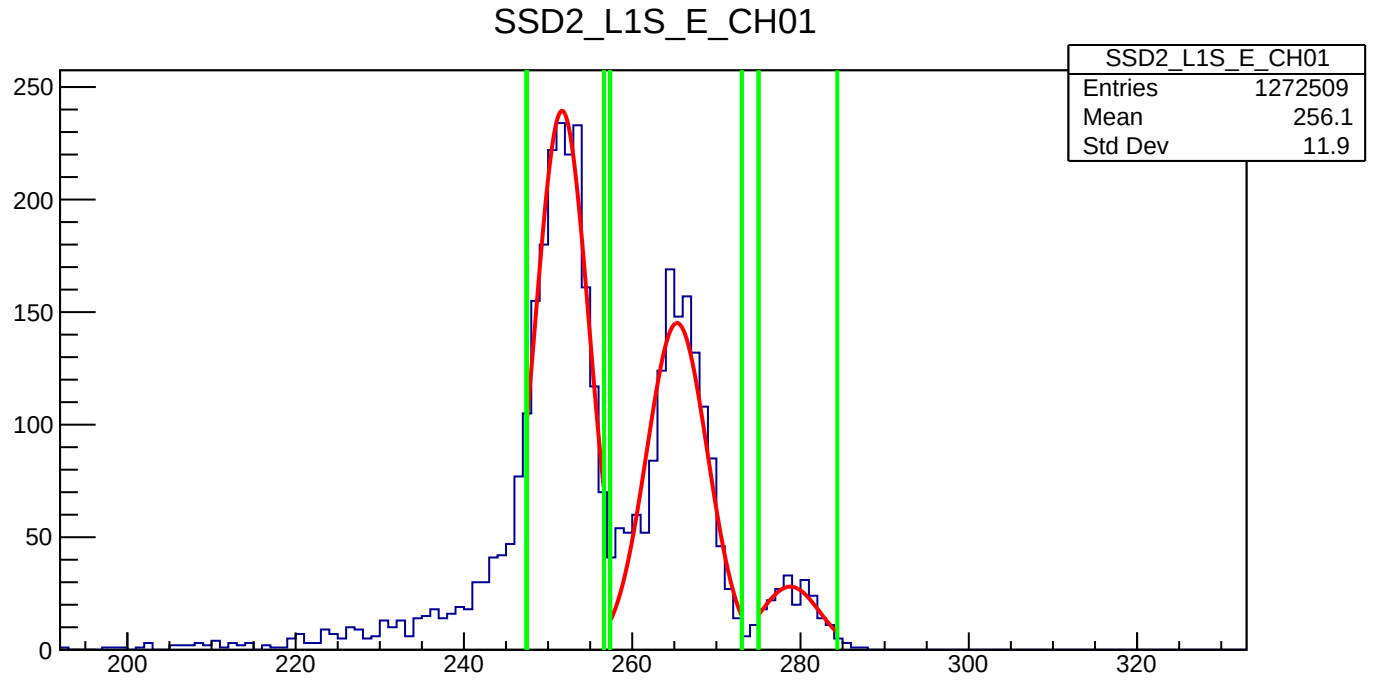
240
245
250
255


SSD2_L1S_E_CH01
SSD2_L1S_E_CH01
Entries 1272509
Mean 256.1
Std Dev 11.9
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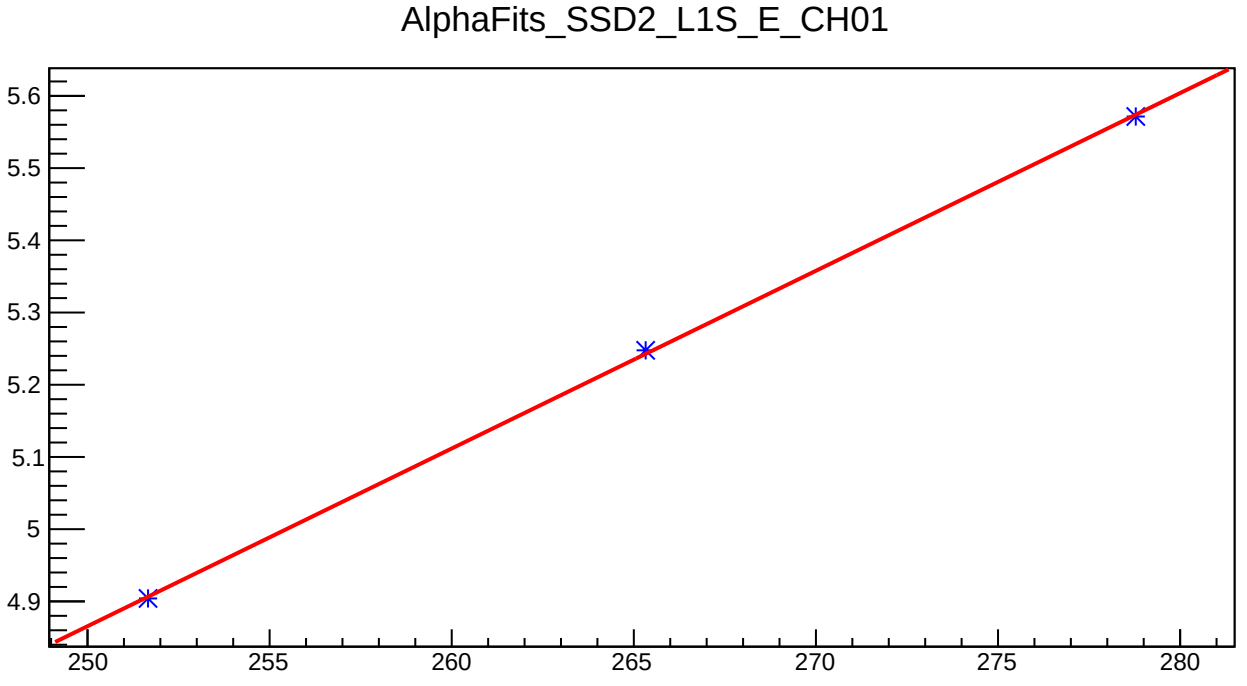
260
280
300
320


AlphaFits_SSD2_L1S_E_CH01
5.6
5.5
5.4
5.3
5.2
5.1
5
4.9
250
255
260
265
270
275
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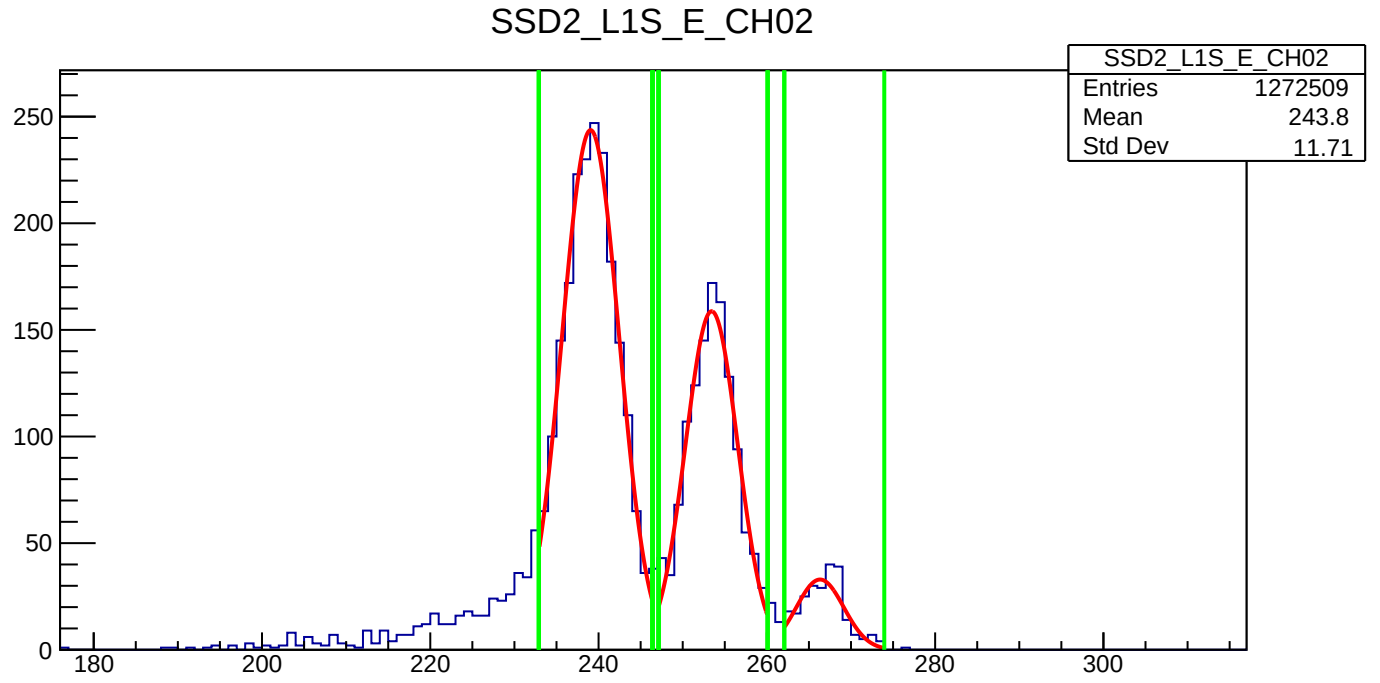
SSD2_L1S_E_CH02
SSD2_L1S_E_CH02
Entries 1272509
Mean 243.8
Std Dev 11.71
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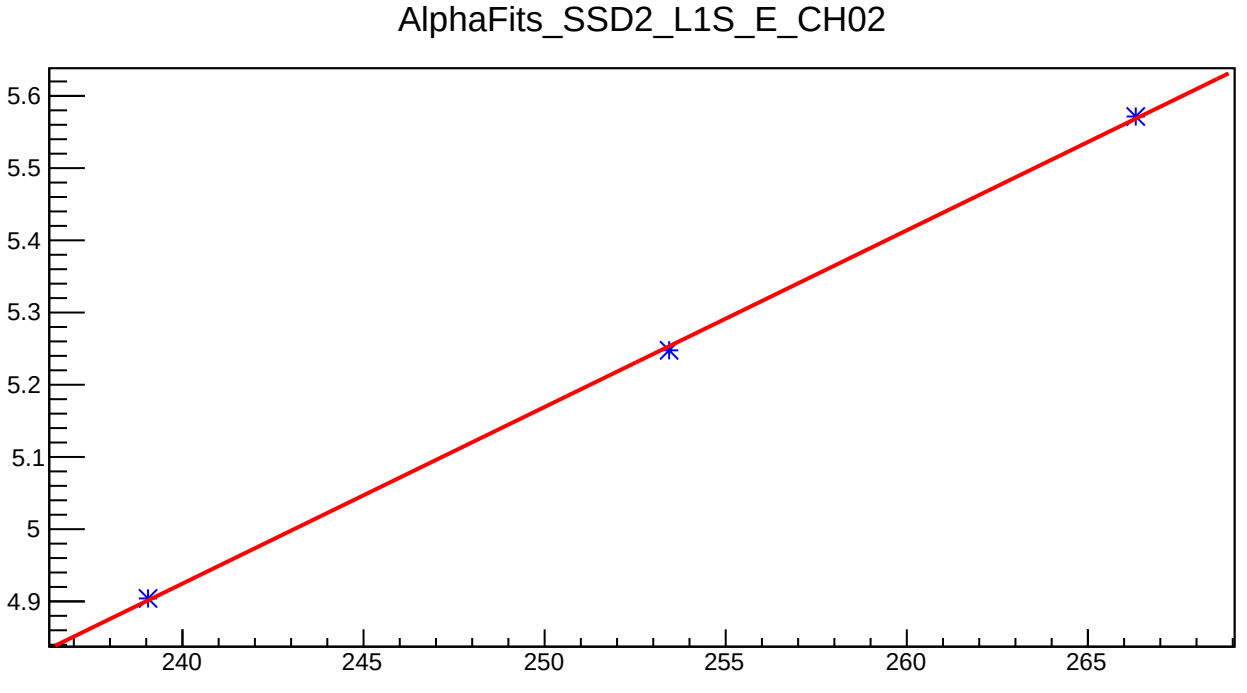
AlphaFits_SSD2_L1S_E_CH02
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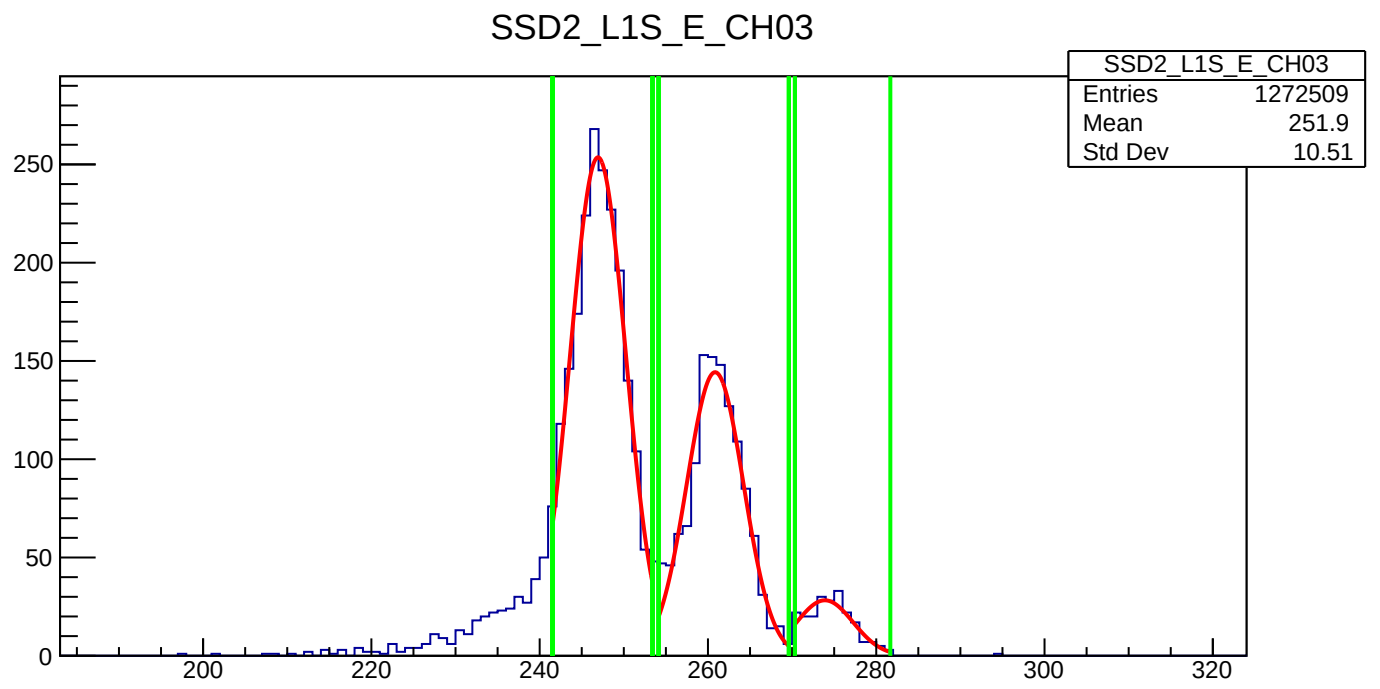
SSD2_L1S_E_CH03
SSD2_L1S_E_CH03
Entries 1272509
Mean 251.9
Std Dev 10.51
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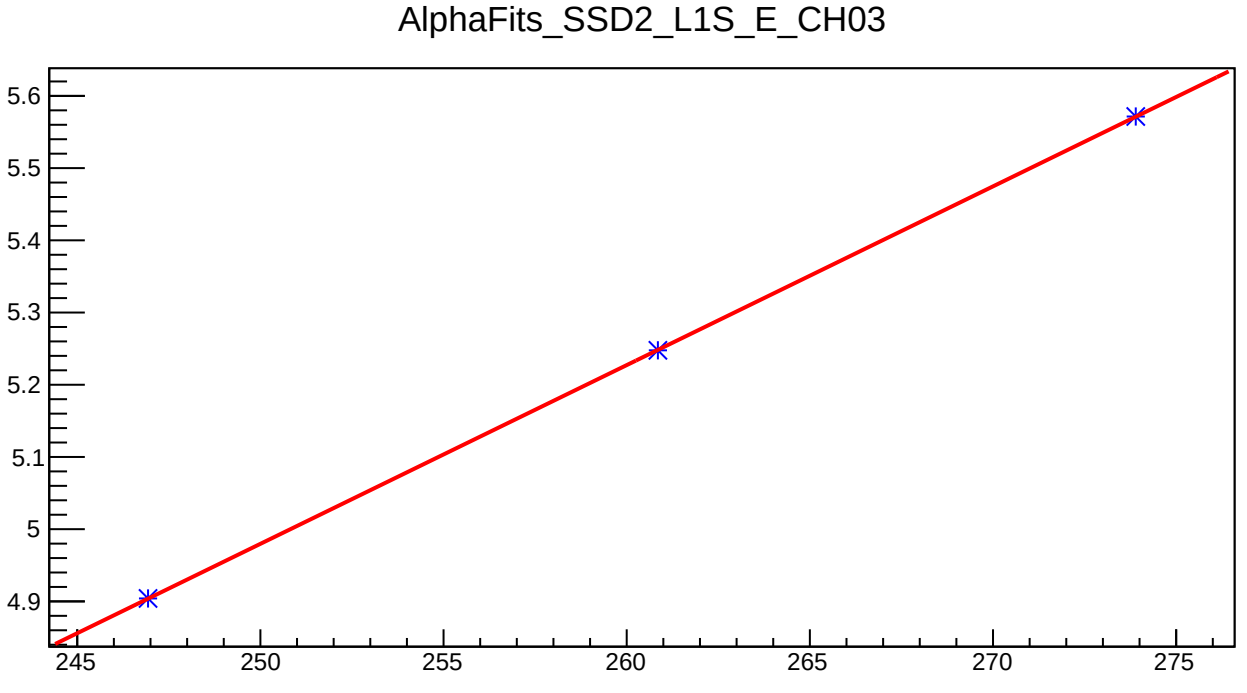
AlphaFits_SSD2_L1S_E_CH03
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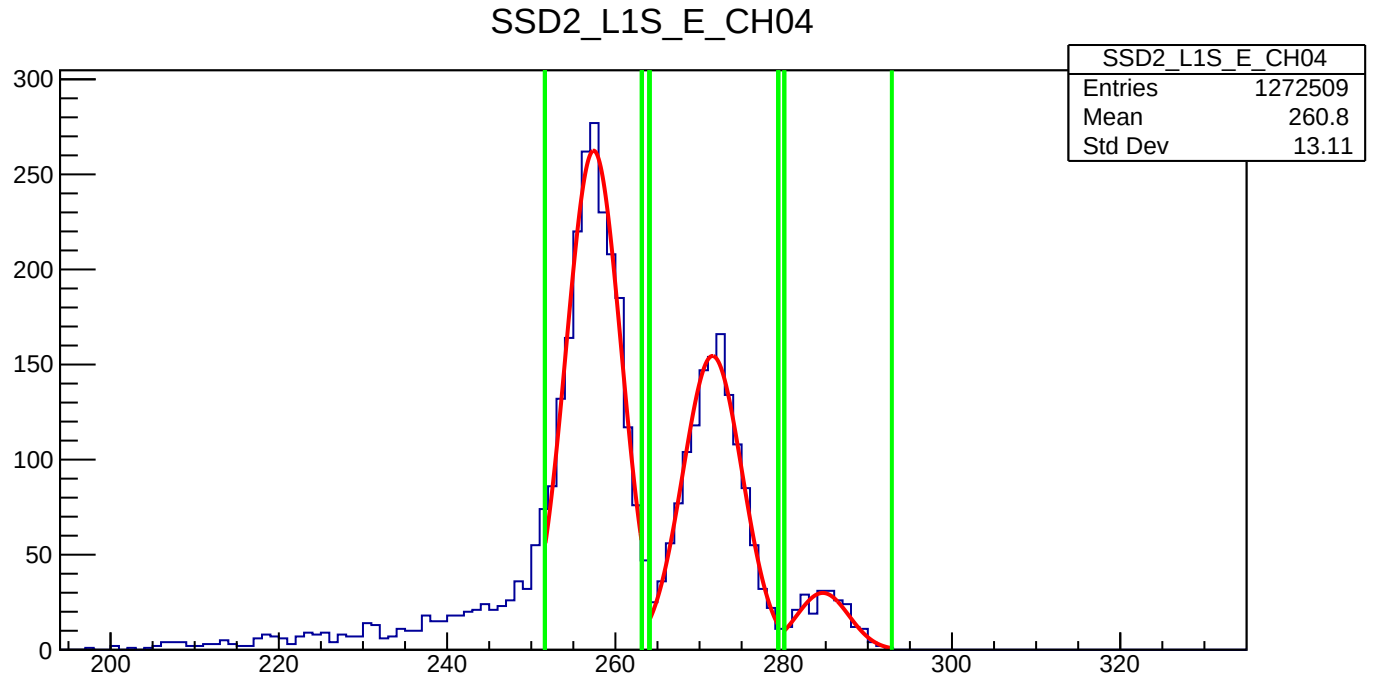
SSD2_L1S_E_CH04
SSD2_L1S_E_CH04
Entries 1272509
Mean 260.8
Std Dev 13.11
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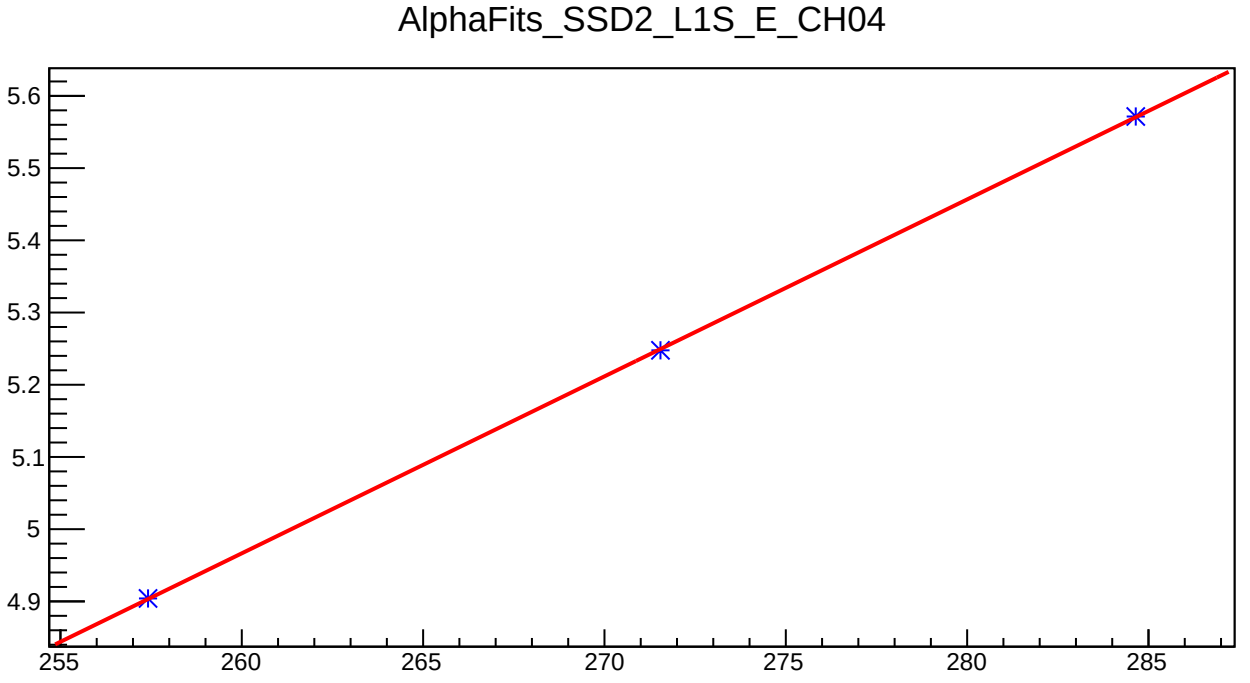
AlphaFits_SSD2_L1S_E_CH04
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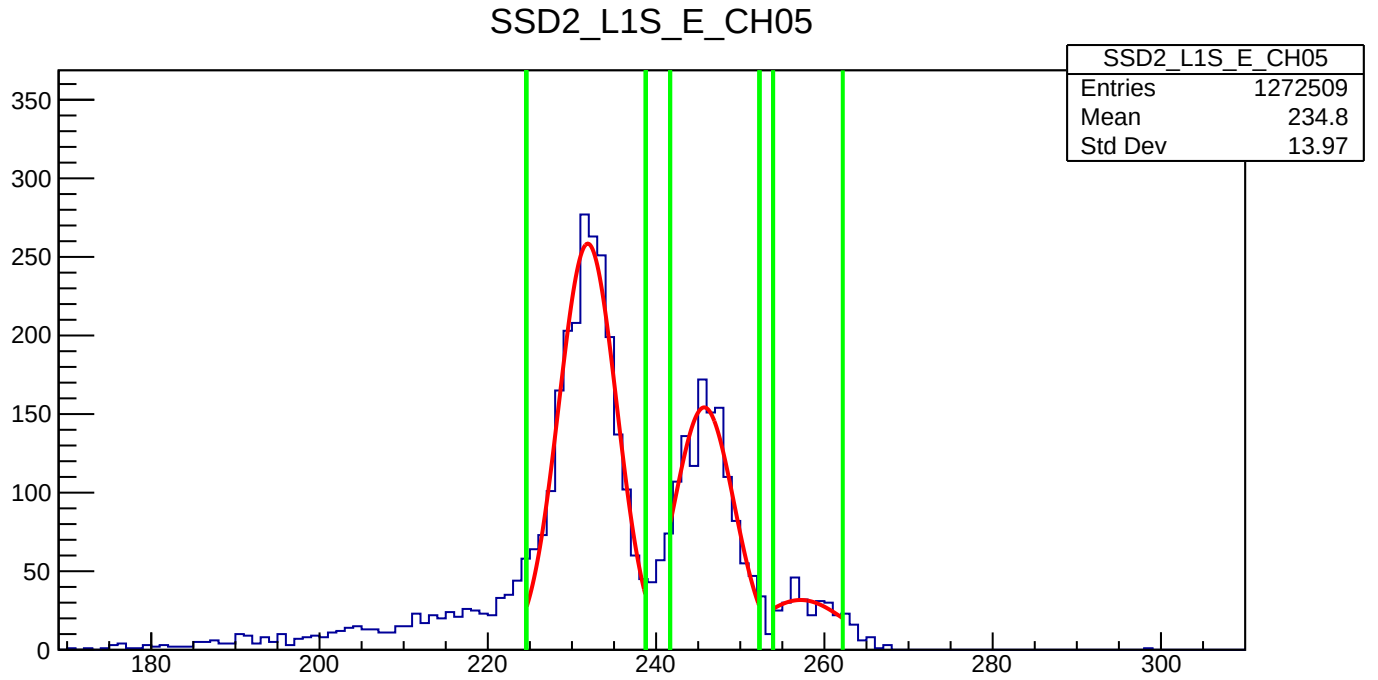
SSD2_L1S_E_CH05
SSD2_L1S_E_CH05
Entries 1272509
Mean 234.8
Std Dev 13.97
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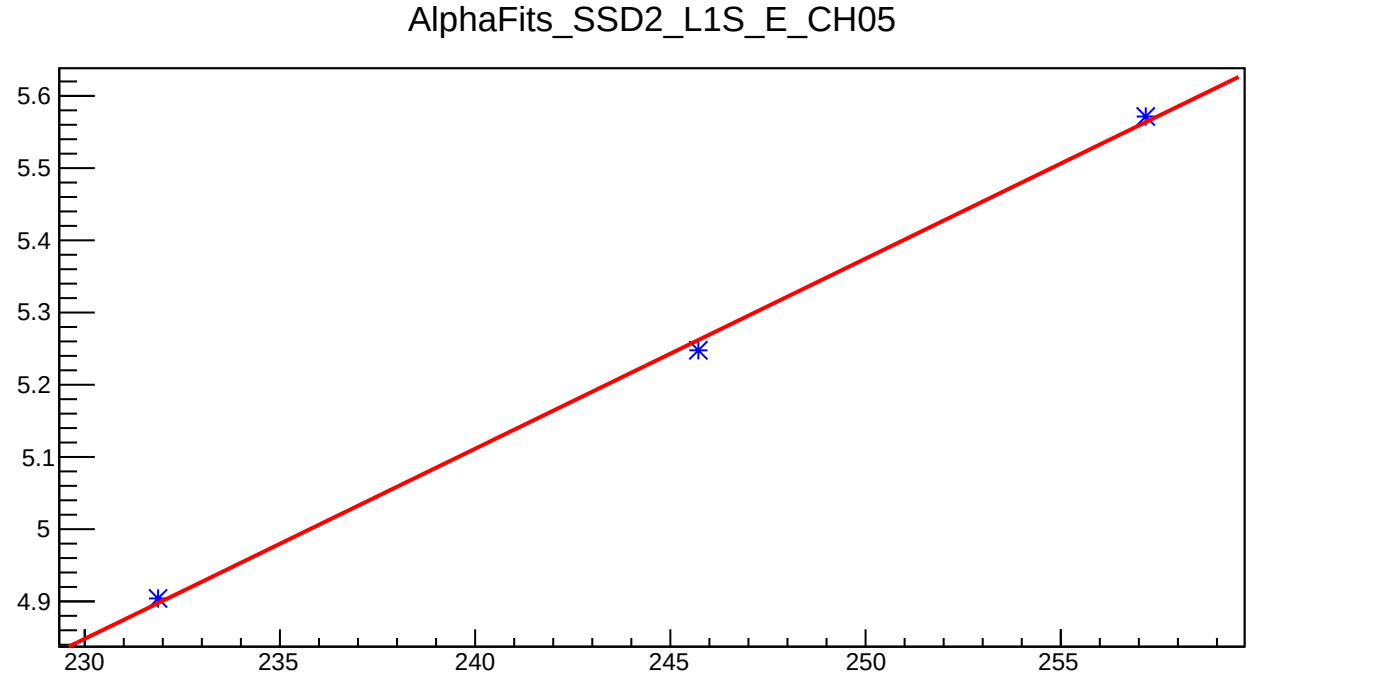
AlphaFits_SSD2_L1S_E_CH05
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4.9
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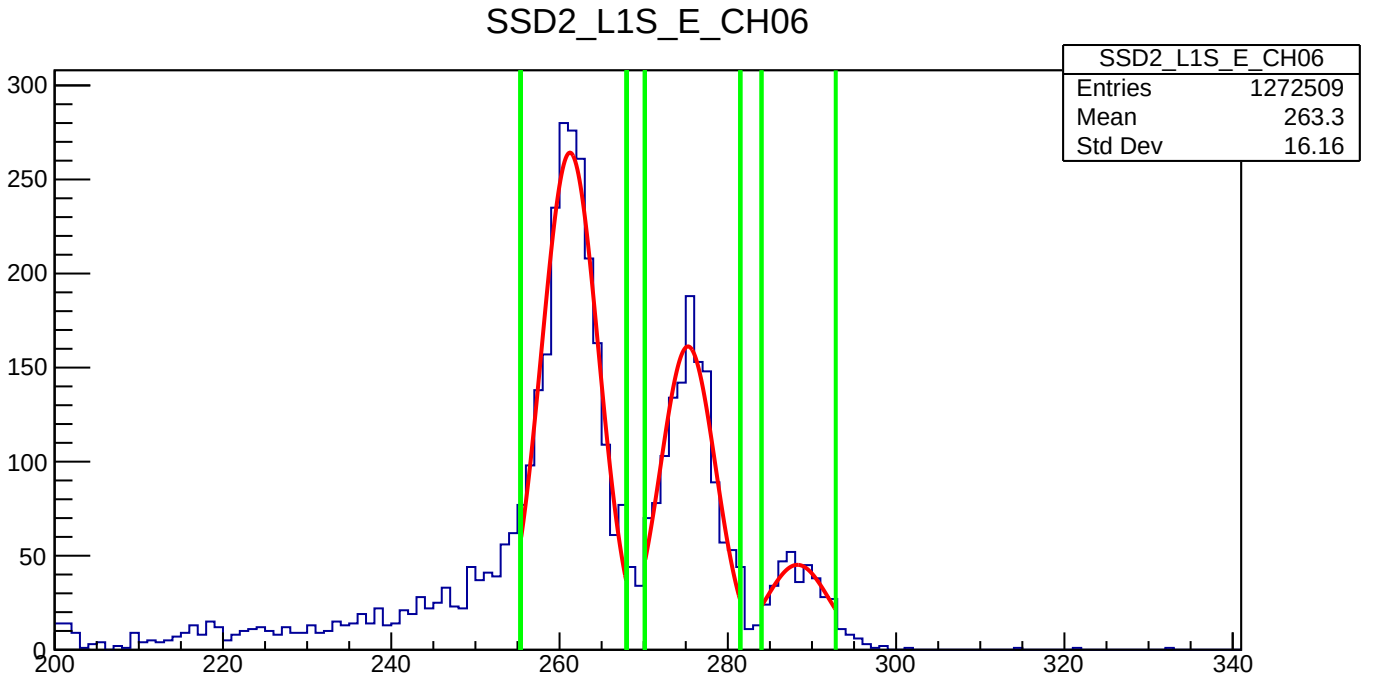
SSD2_L1S_E_CH06
SSD2_L1S_E_CH06
Entries 1272509
Mean 263.3
Std Dev 16.16
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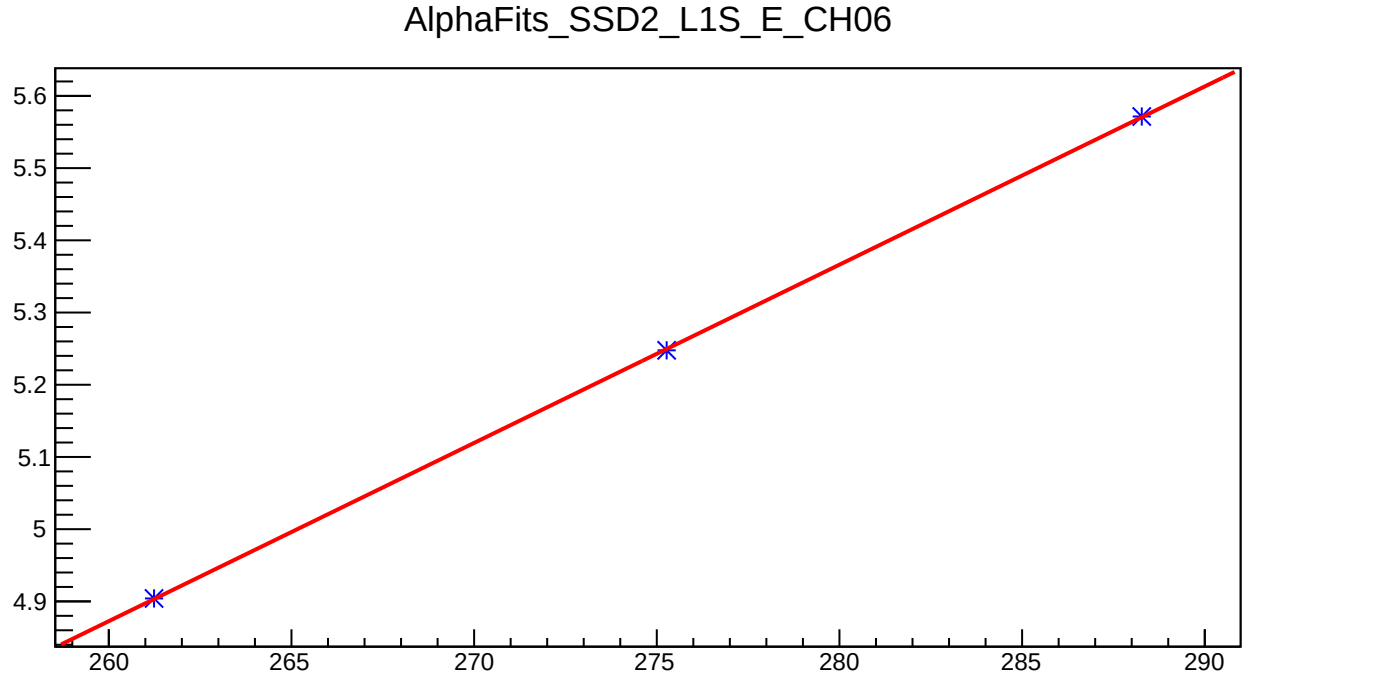
AlphaFits_SSD2_L1S_E_CH06
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4.9
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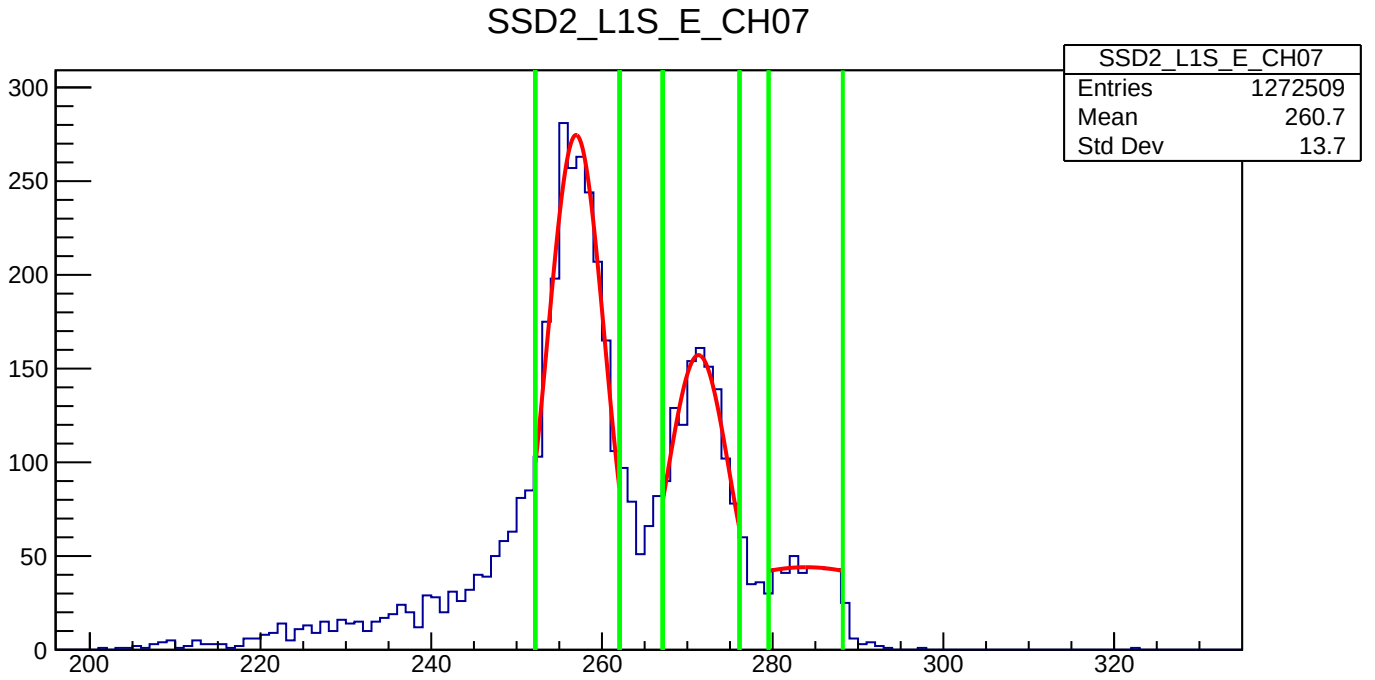
SSD2_L1S_E_CH07
SSD2_L1S_E_CH07
Entries 1272509
Mean 260.7
Std Dev 13.7
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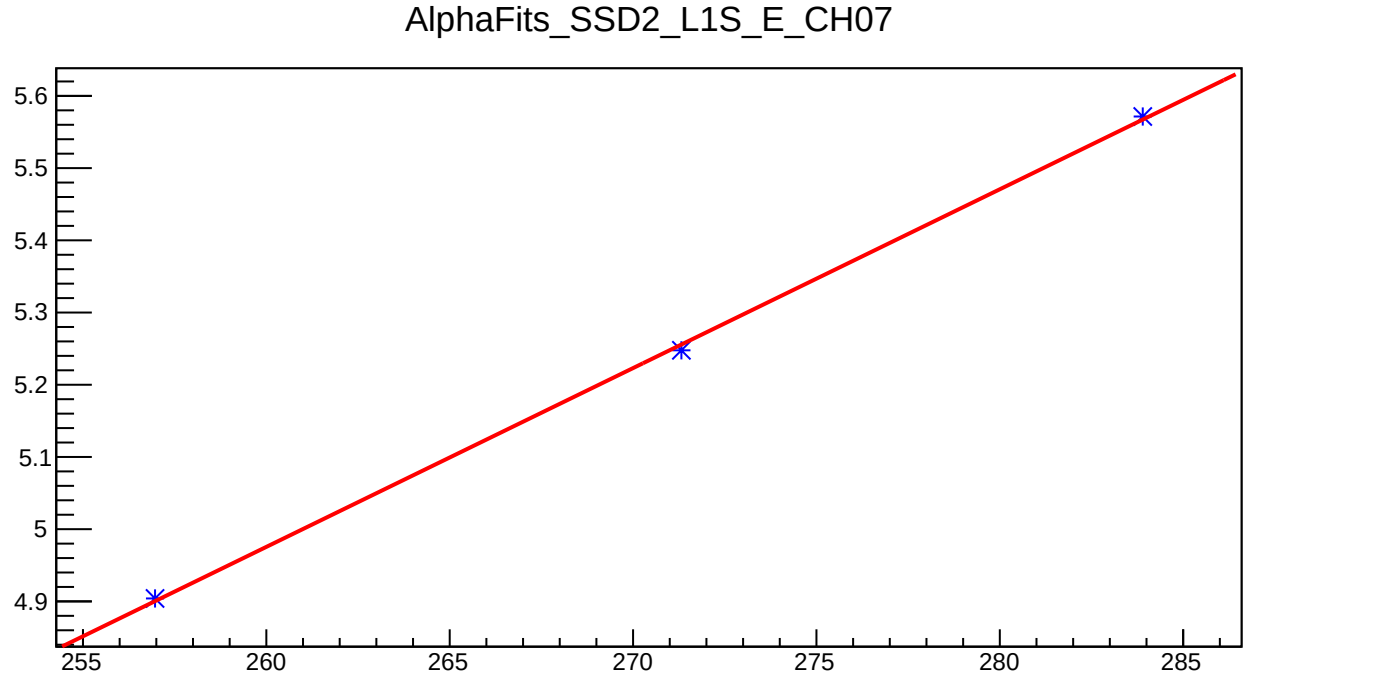
AlphaFits_SSD2_L1S_E_CH07
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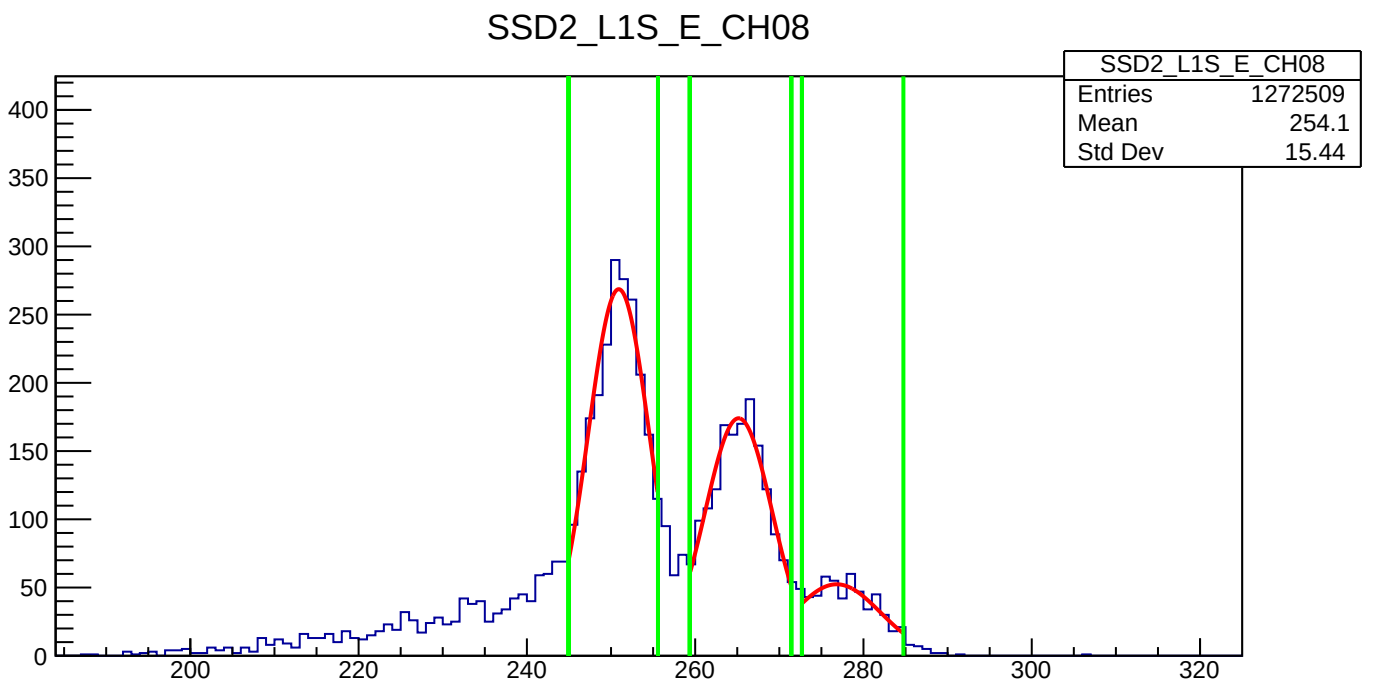
SSD2_L1S_E_CH08
SSD2_L1S_E_CH08
Entries 1272509
Mean 254.1
Std Dev 15.44
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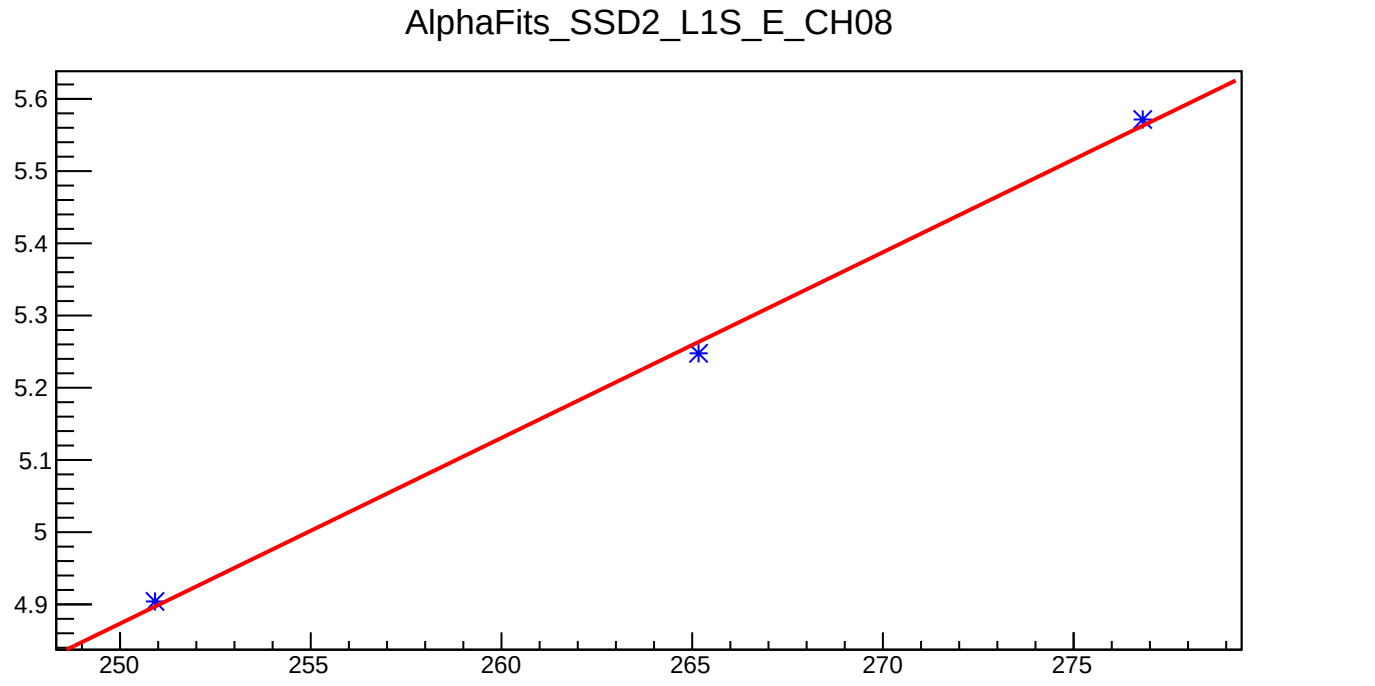
AlphaFits_SSD2_L1S_E_CH08
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4.9
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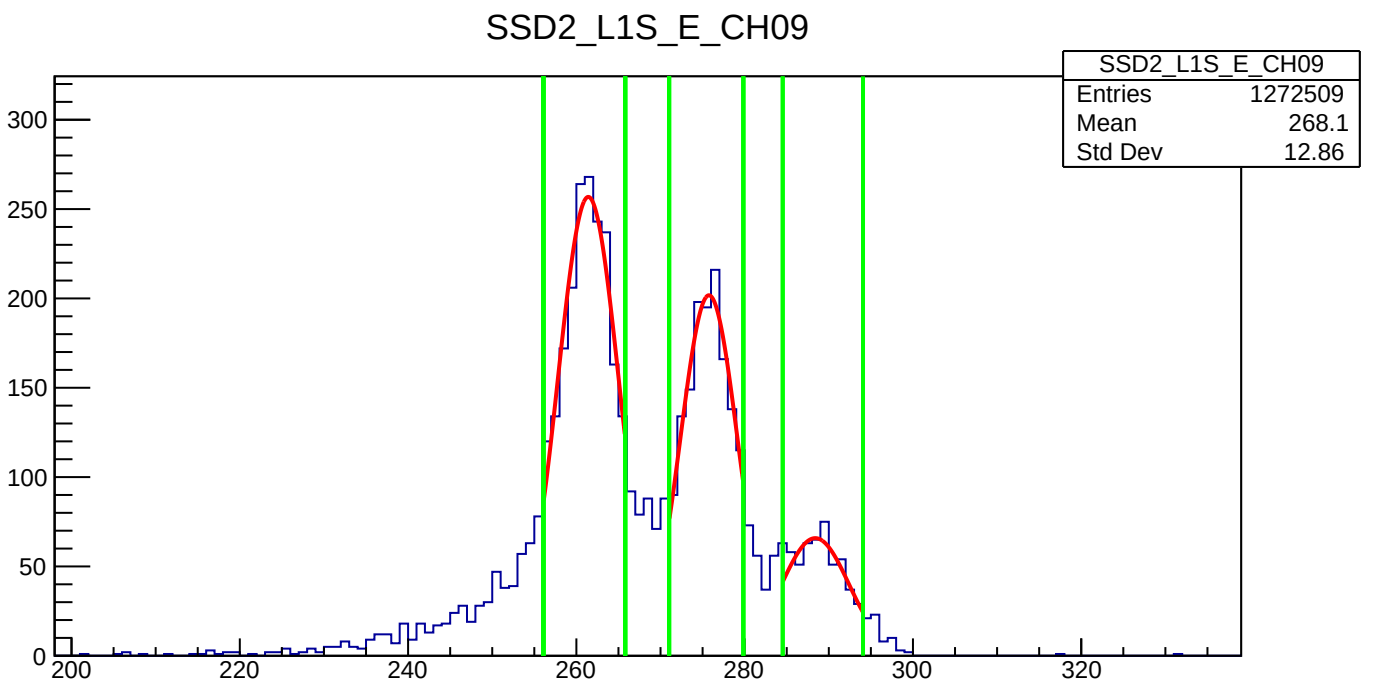
SSD2_L1S_E_CH09
SSD2_L1S_E_CH09
Entries 1272509
Mean 268.1
Std Dev 12.86
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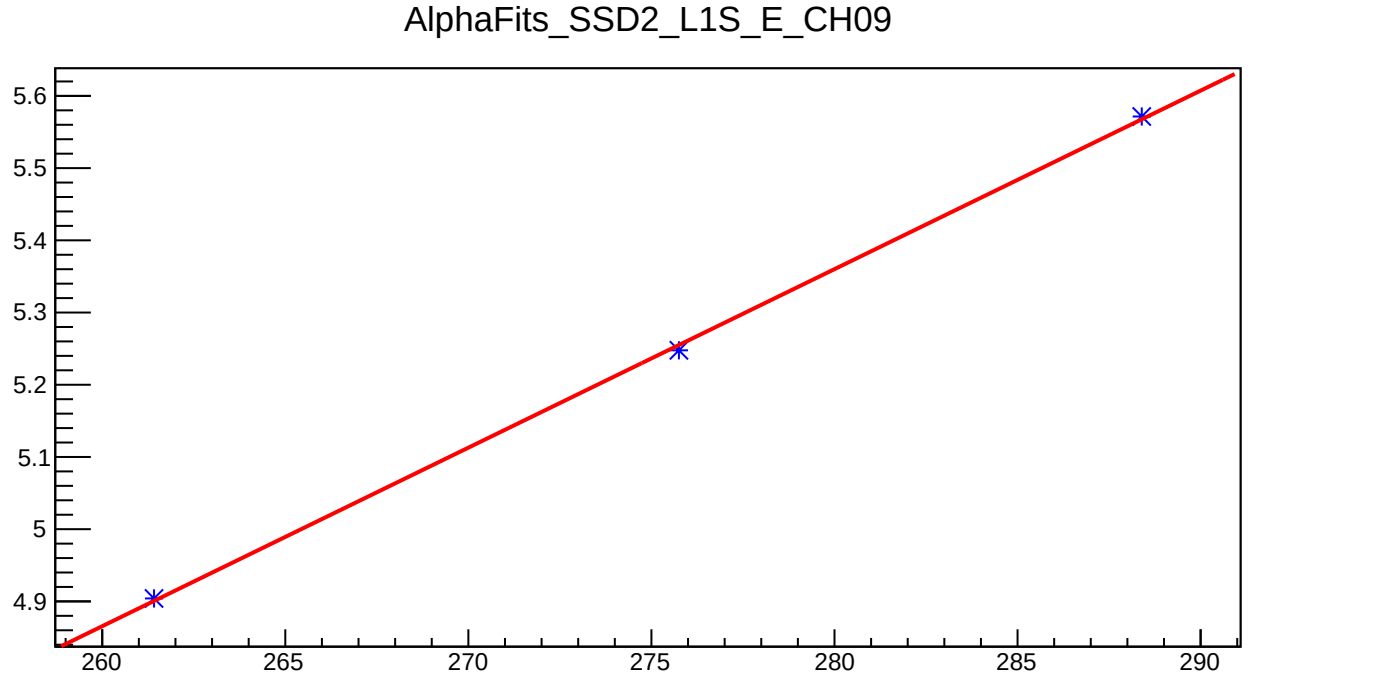
AlphaFits_SSD2_L1S_E_CH09
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5.2
5.1
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4.9
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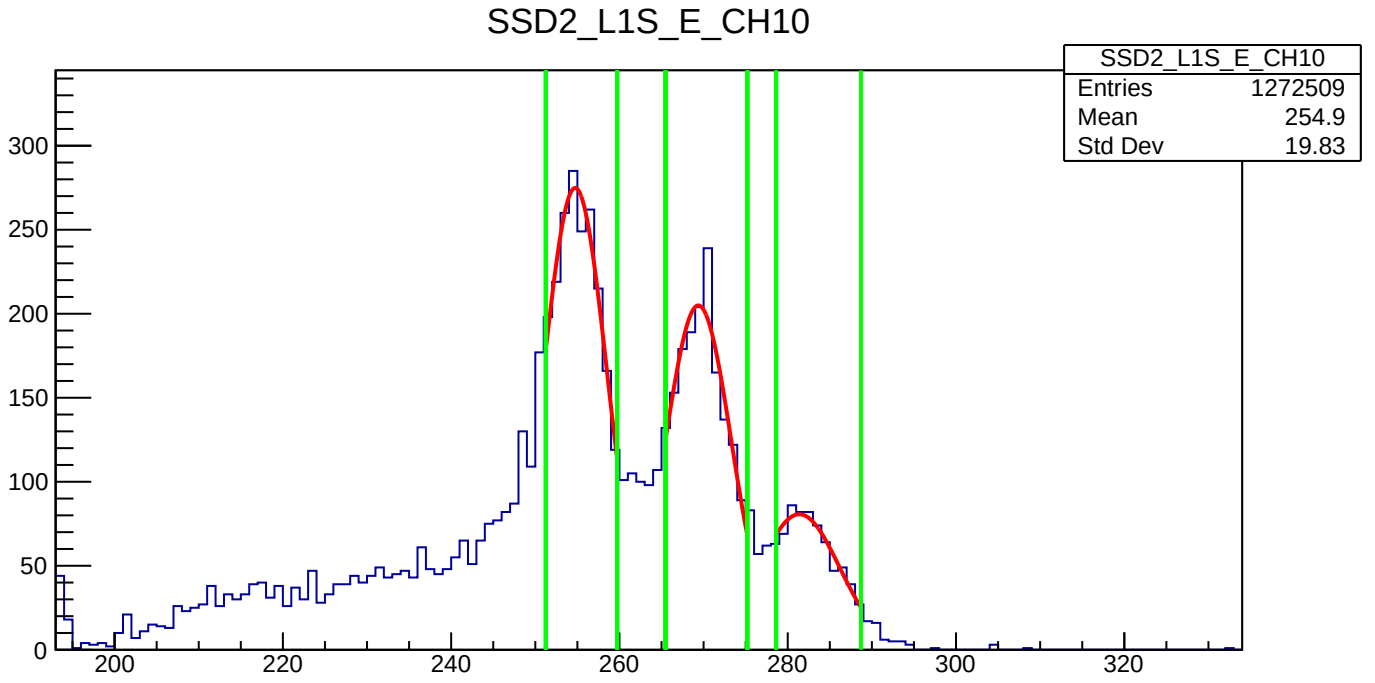
SSD2_L1S_E_CH10
SSD2_L1S_E_CH10
Entries 1272509
Mean 254.9
Std Dev 19.83
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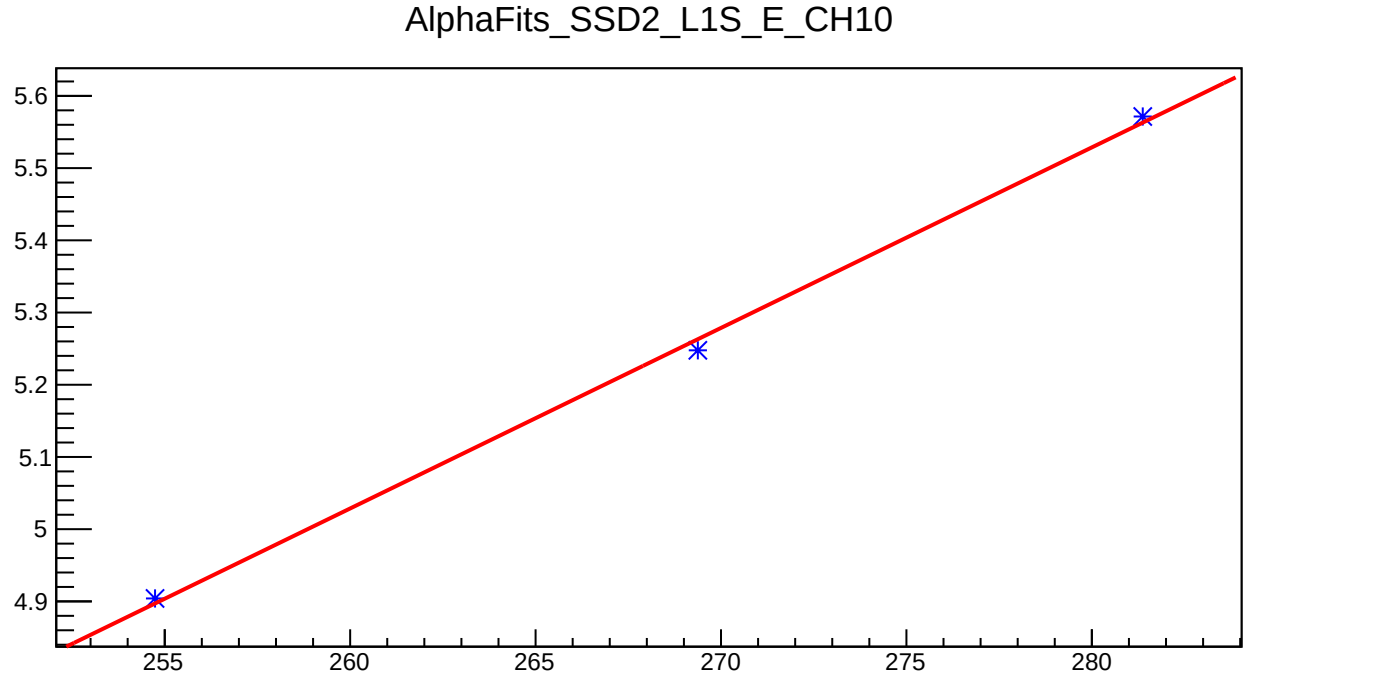
AlphaFits_SSD2_L1S_E_CH10
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4.9
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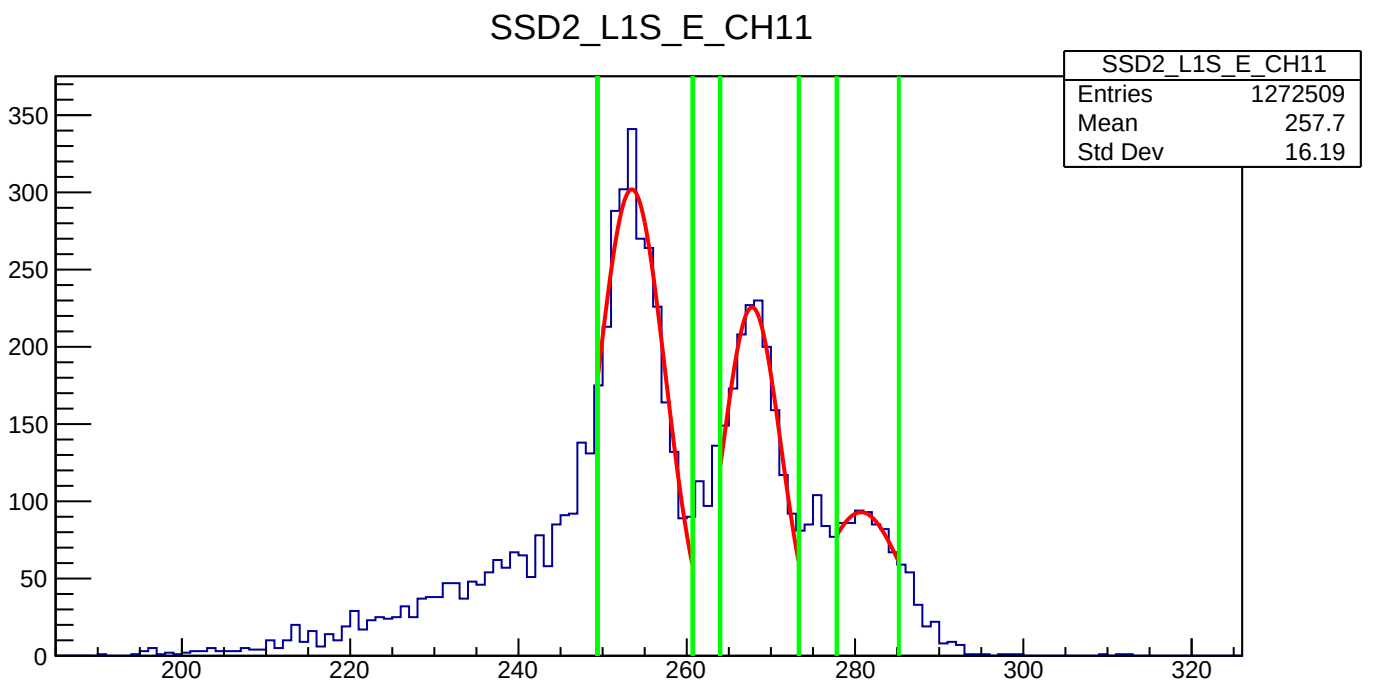
SSD2_L1S_E_CH11
SSD2_L1S_E_CH11
Entries 1272509
Mean 257.7
Std Dev 16.19
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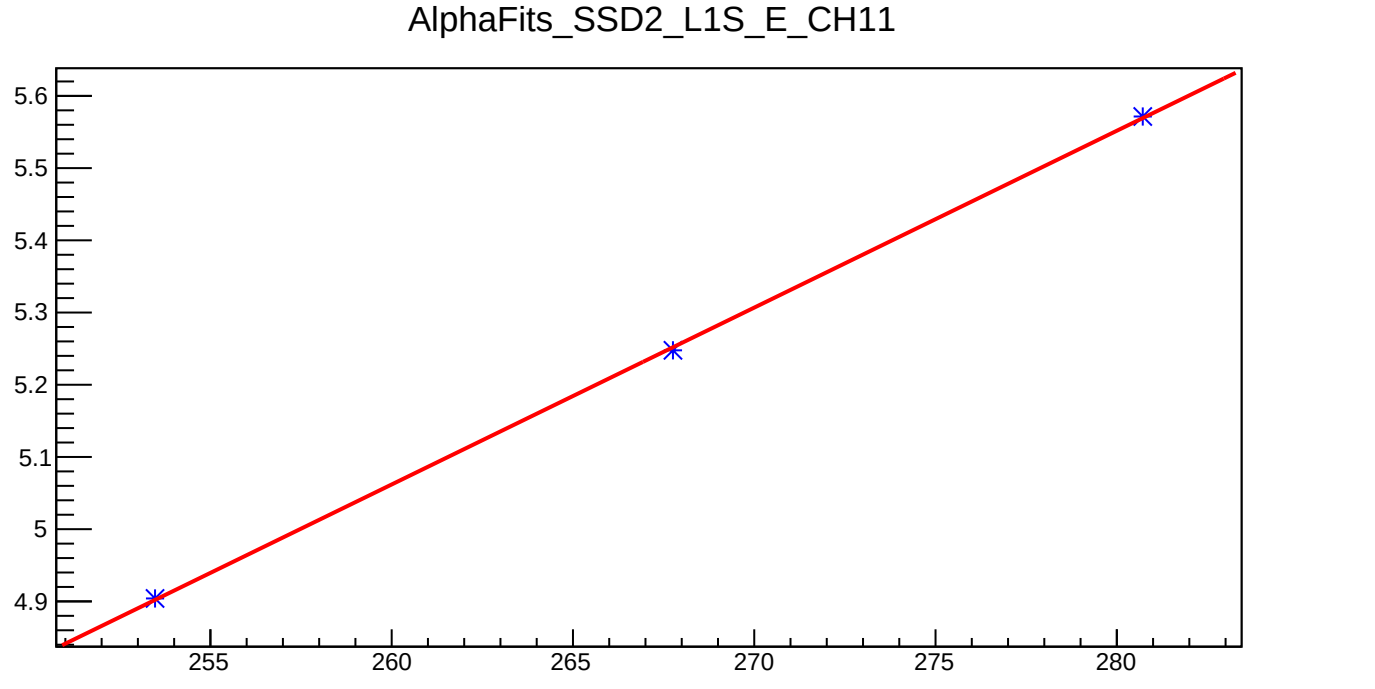
AlphaFits_SSD2_L1S_E_CH11
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5.2
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4.9
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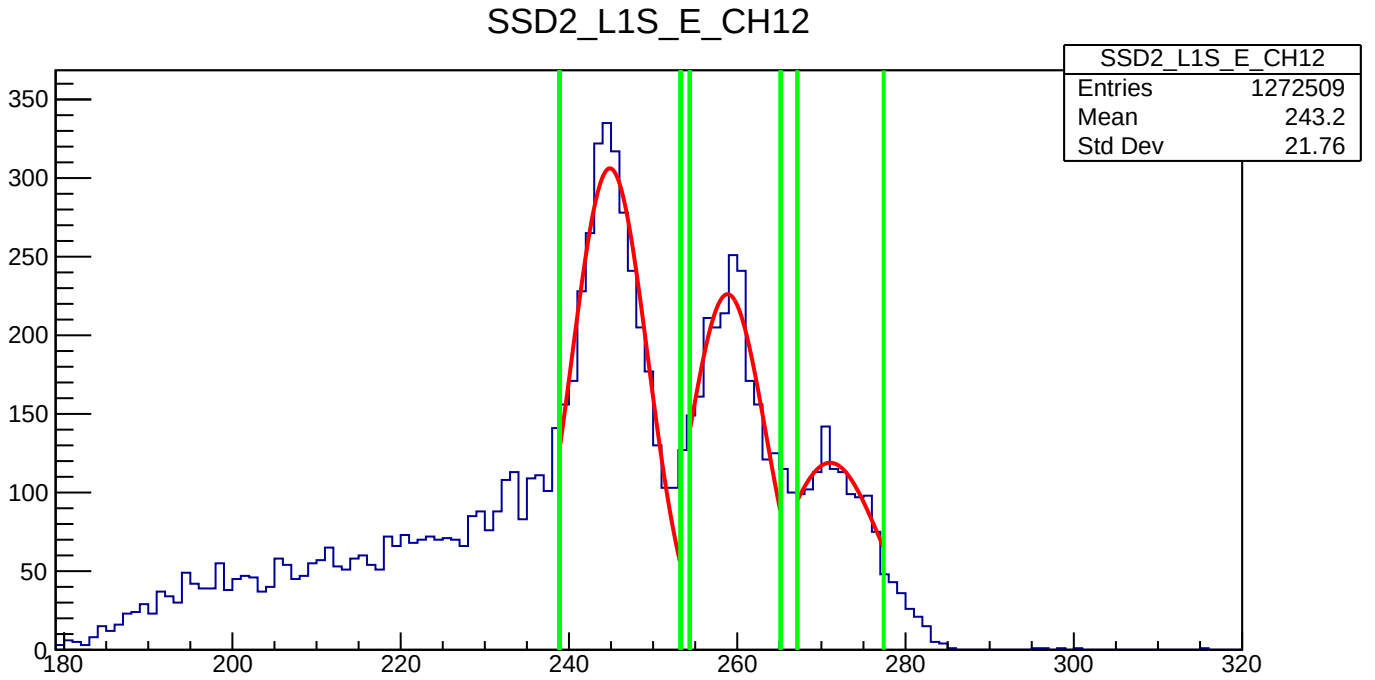
SSD2_L1S_E_CH12
SSD2_L1S_E_CH12
Entries 1272509
Mean 243.2
Std Dev 21.76
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300
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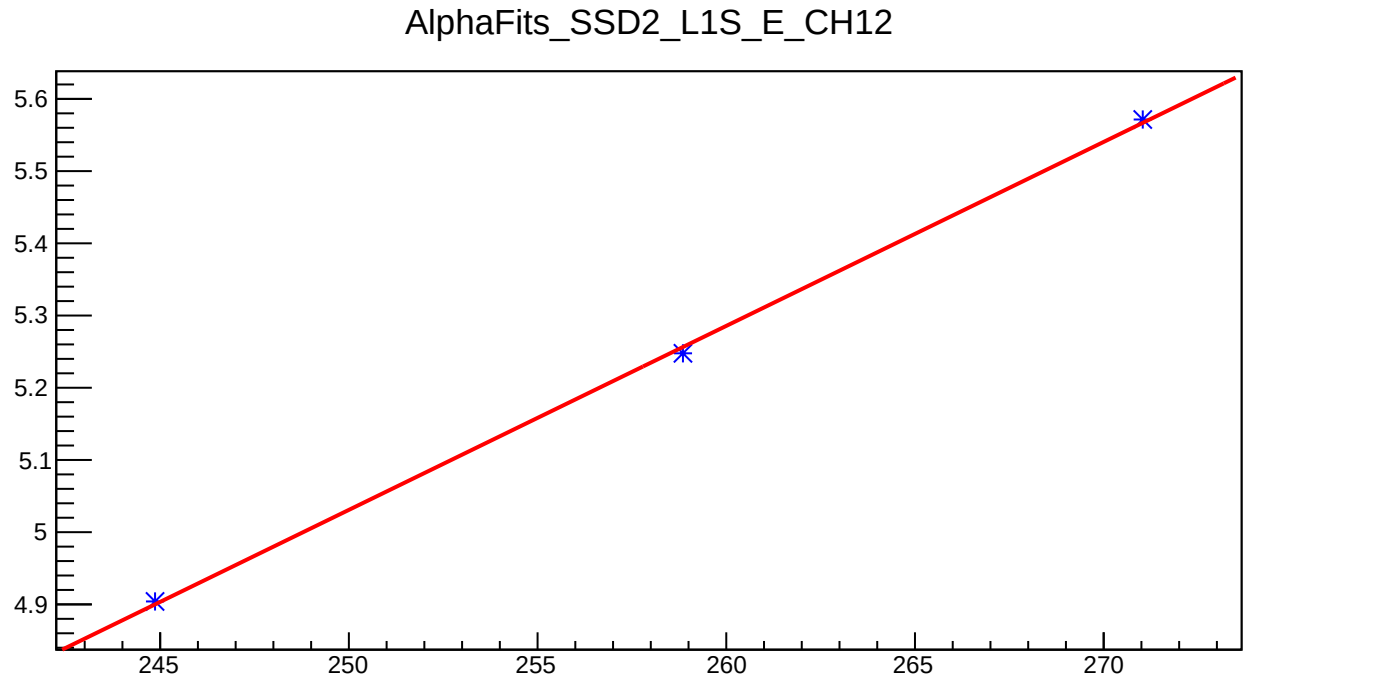
AlphaFits_SSD2_L1S_E_CH12
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4.9
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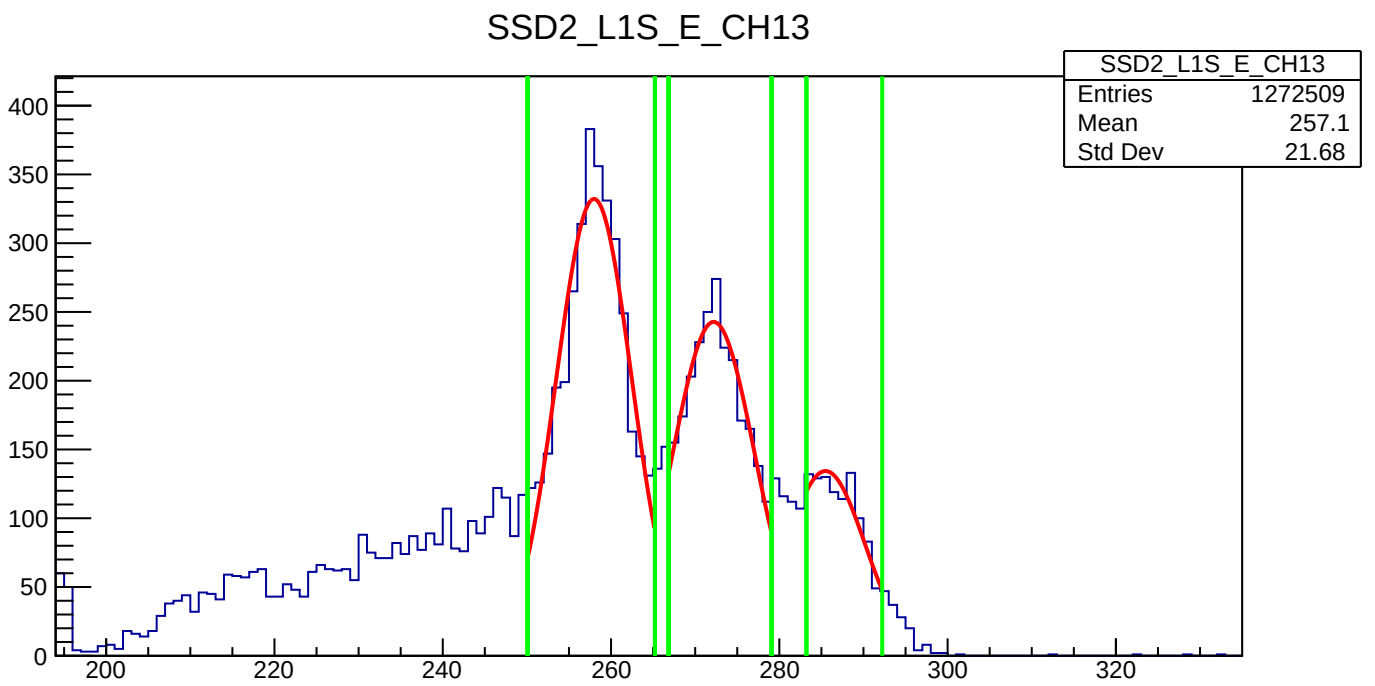
SSD2_L1S_E_CH13
SSD2_L1S_E_CH13
Entries 1272509
Mean 257.1
Std Dev 21.68
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300
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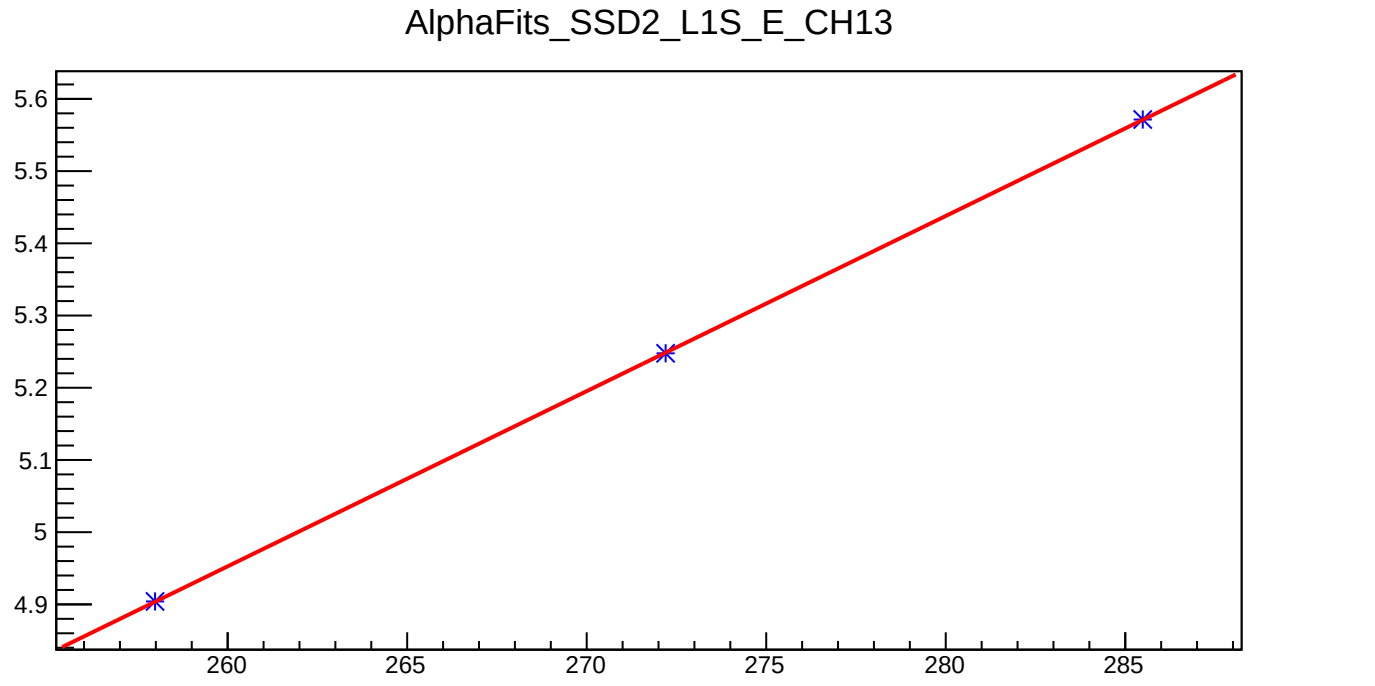
AlphaFits_SSD2_L1S_E_CH13
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5.1
5
4.9
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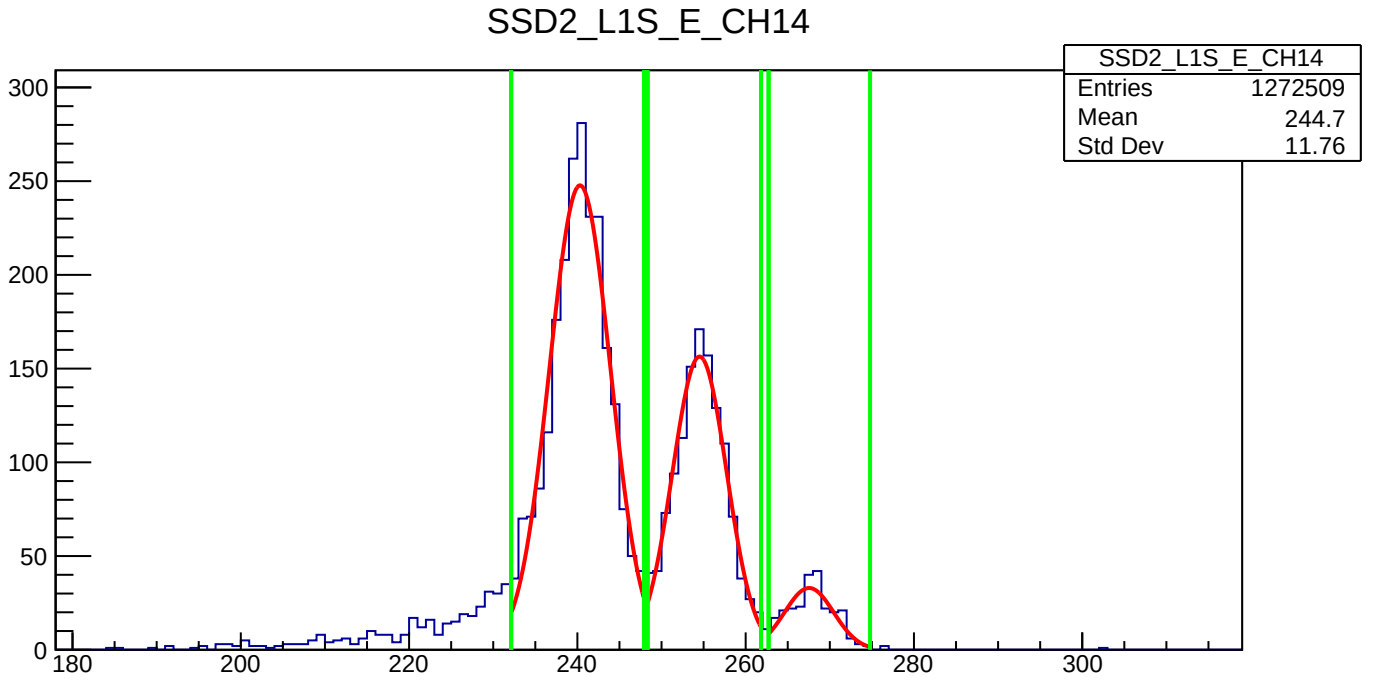
SSD2_L1S_E_CH14
SSD2_L1S_E_CH14
Entries 1272509
Mean 244.7
Std Dev 11.76
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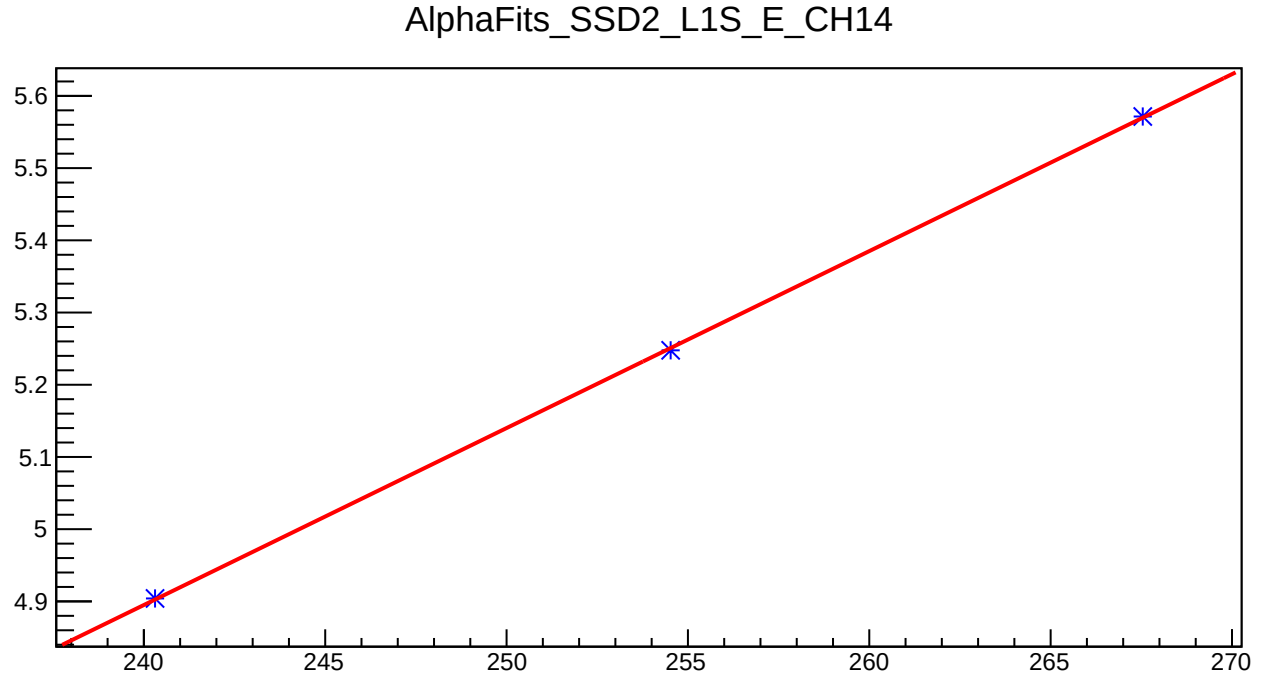
AlphaFits_SSD2_L1S_E_CH14
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4.9
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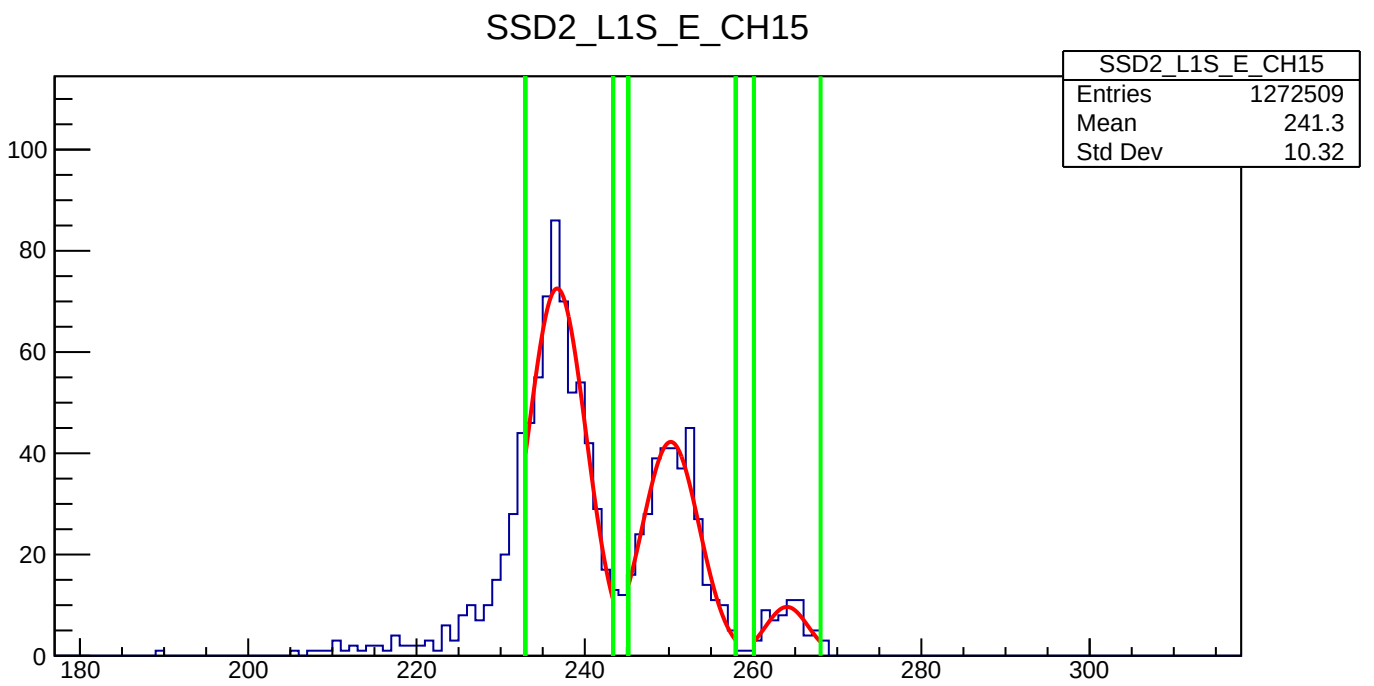
SSD2_L1S_E_CH15
SSD2_L1S_E_CH15
Entries 1272509
Mean 241.3
Std Dev 10.32
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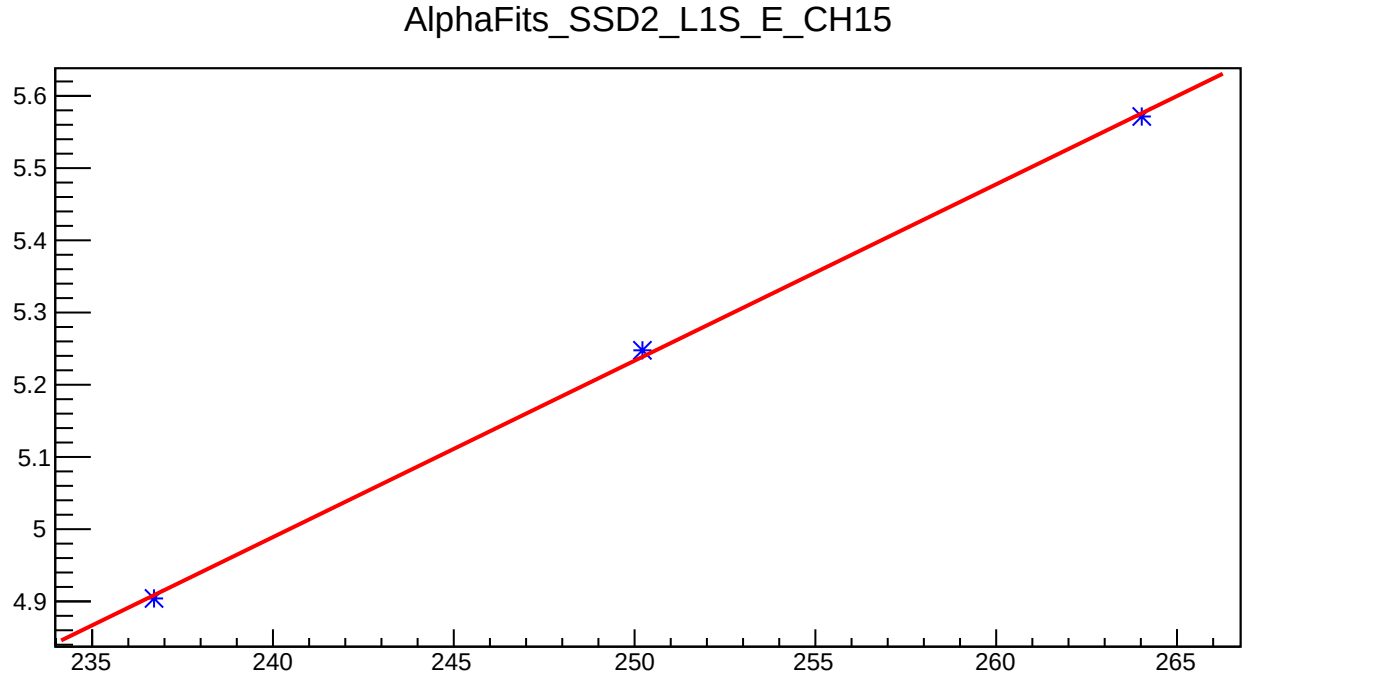
AlphaFits_SSD2_L1S_E_CH15
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5.4
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5.1
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4.9
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260
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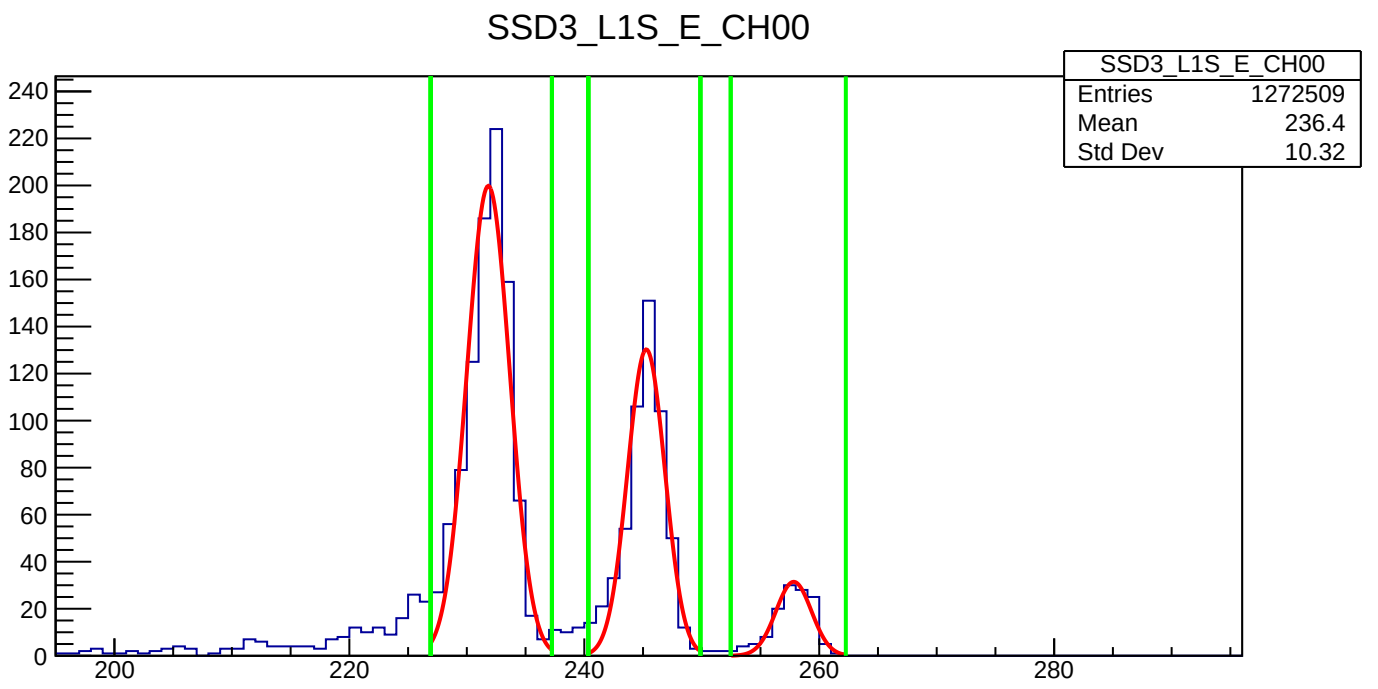
SSD3_L1S_E_CH00
SSD3_L1S_E_CH00
Entries 1272509
Mean 236.4
Std Dev 10.32
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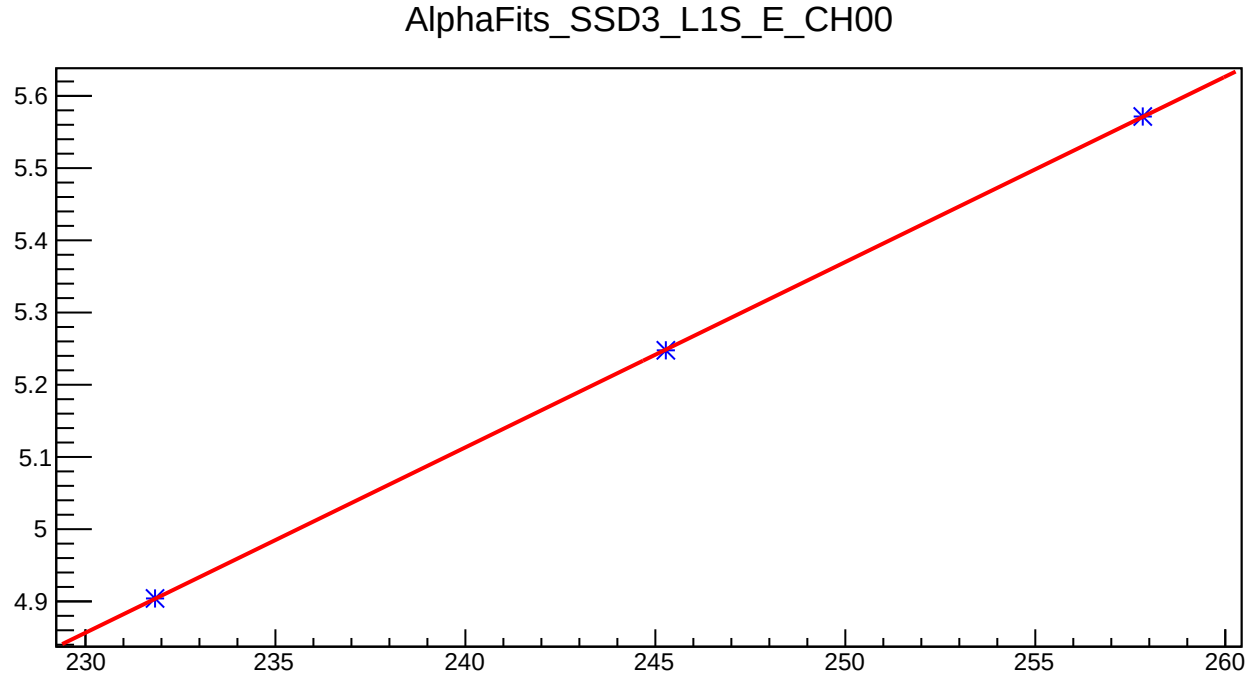
AlphaFits_SSD3_L1S_E_CH00
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5.3
5.2
5.1
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4.9
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255
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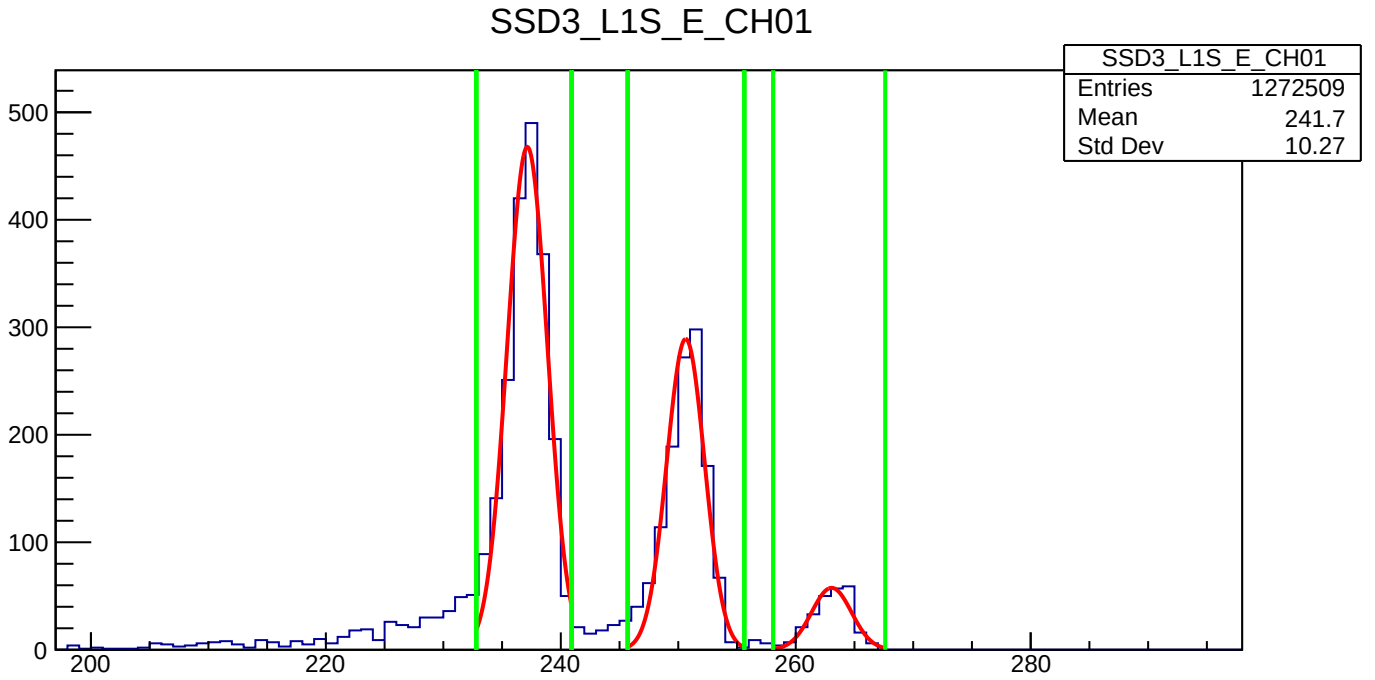
SSD3_L1S_E_CH01
SSD3_L1S_E_CH01
Entries 1272509
Mean 241.7
Std Dev 10.27
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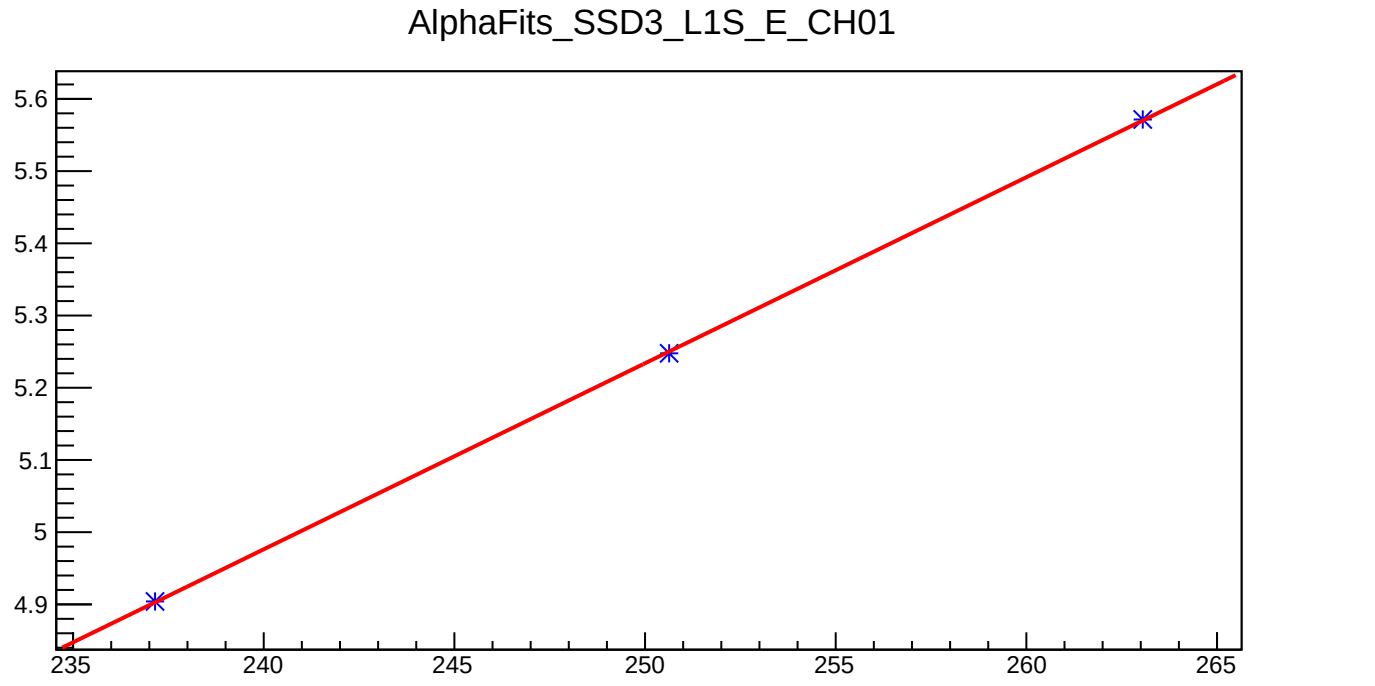
AlphaFits_SSD3_L1S_E_CH01
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5.2
5.1
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4.9
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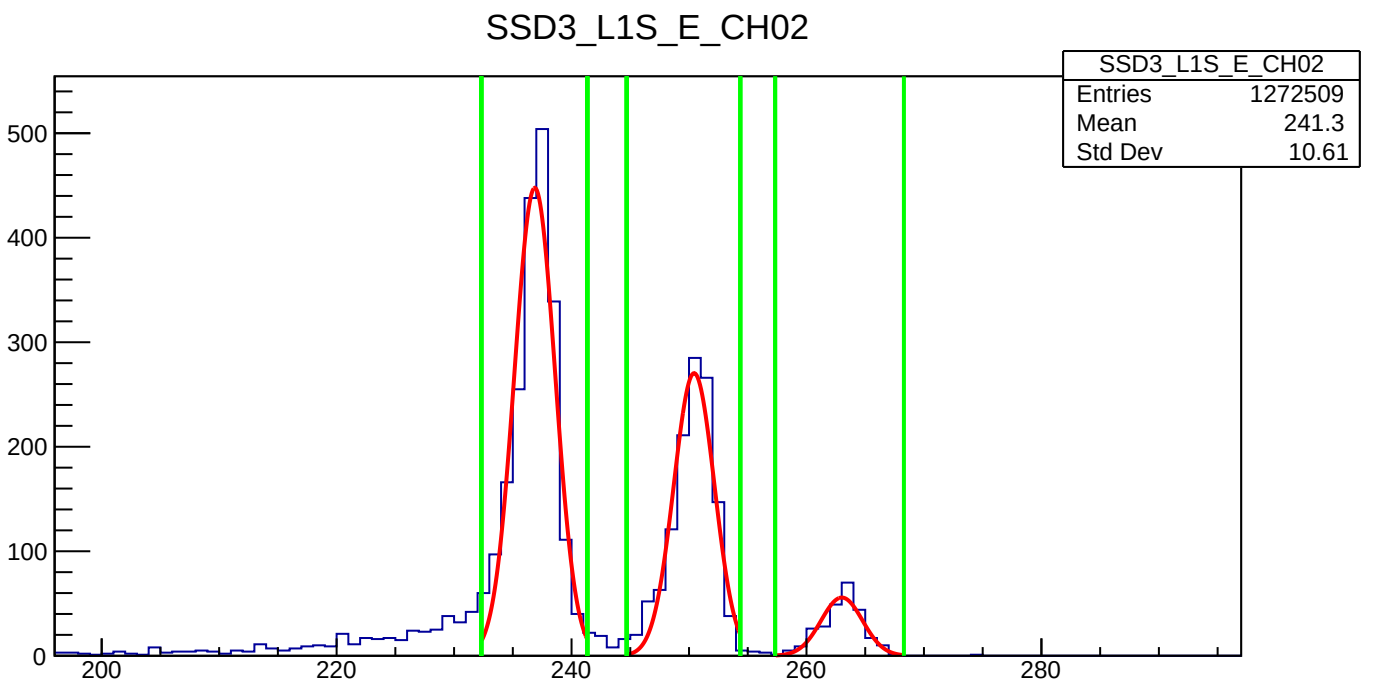
SSD3_L1S_E_CH02
SSD3_L1S_E_CH02
Entries 1272509
Mean 241.3
Std Dev 10.61
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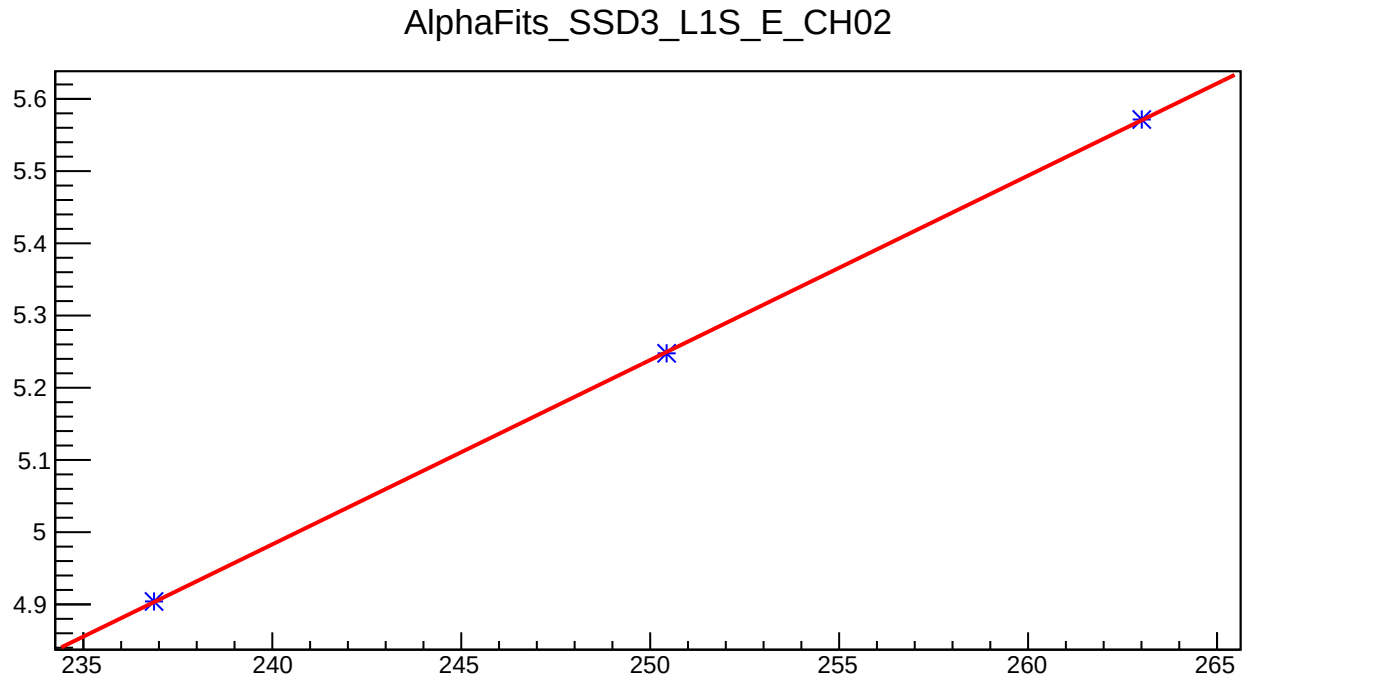
AlphaFits_SSD3_L1S_E_CH02
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5.4
5.3
5.2
5.1
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4.9
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260
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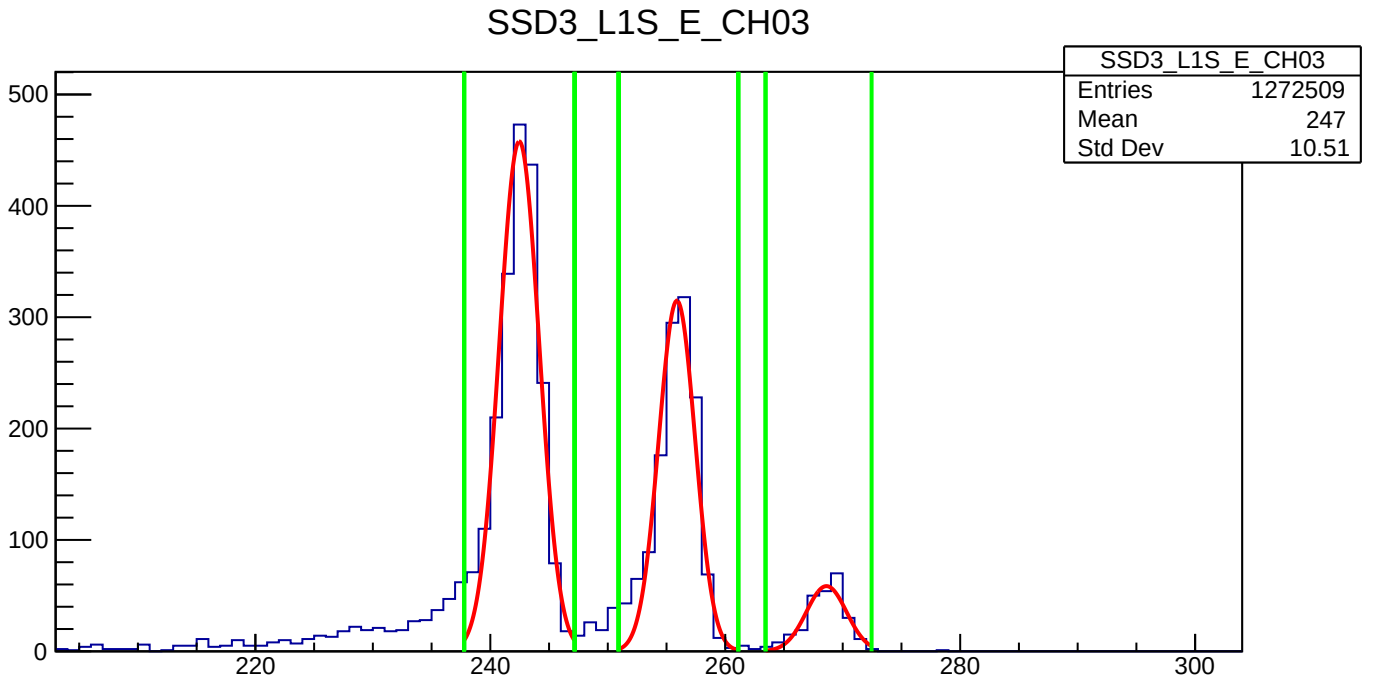
SSD3_L1S_E_CH03
SSD3_L1S_E_CH03
Entries 1272509
Mean 247
Std Dev 10.51
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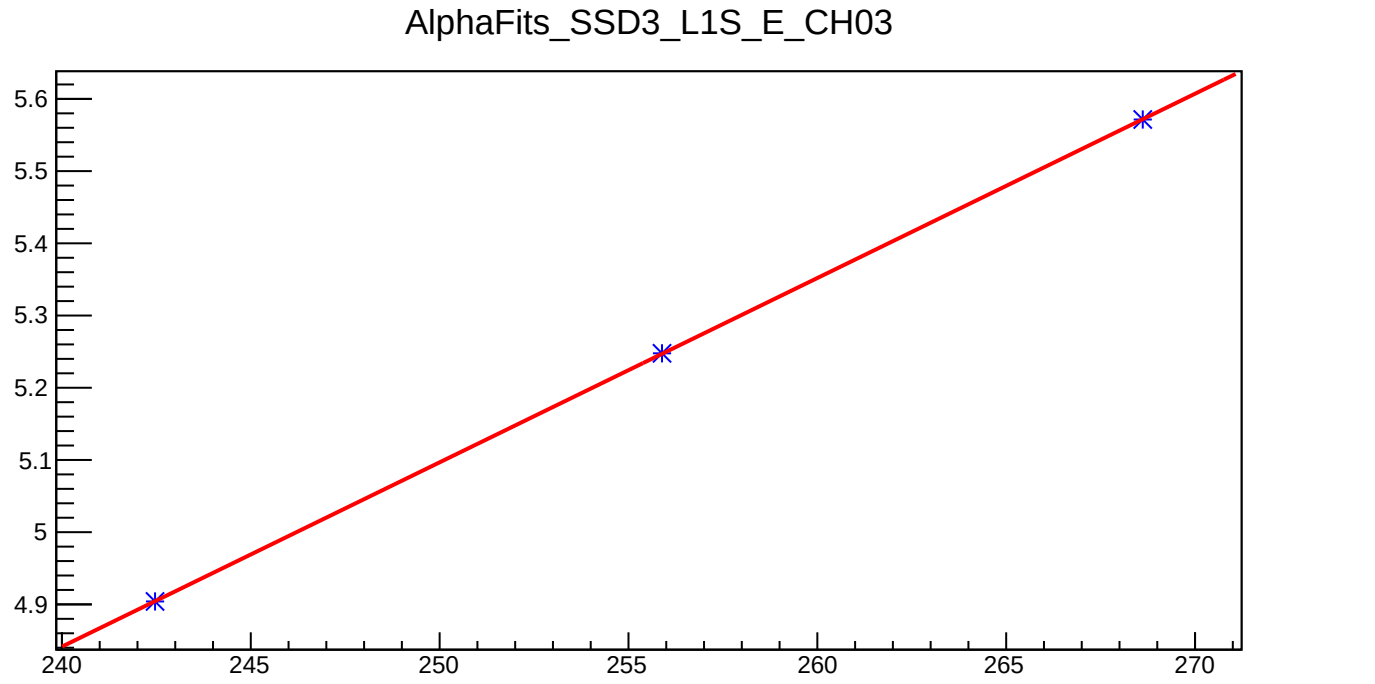
280
300


AlphaFits_SSD3_L1S_E_CH03
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5.4
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5.2
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4.9
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265
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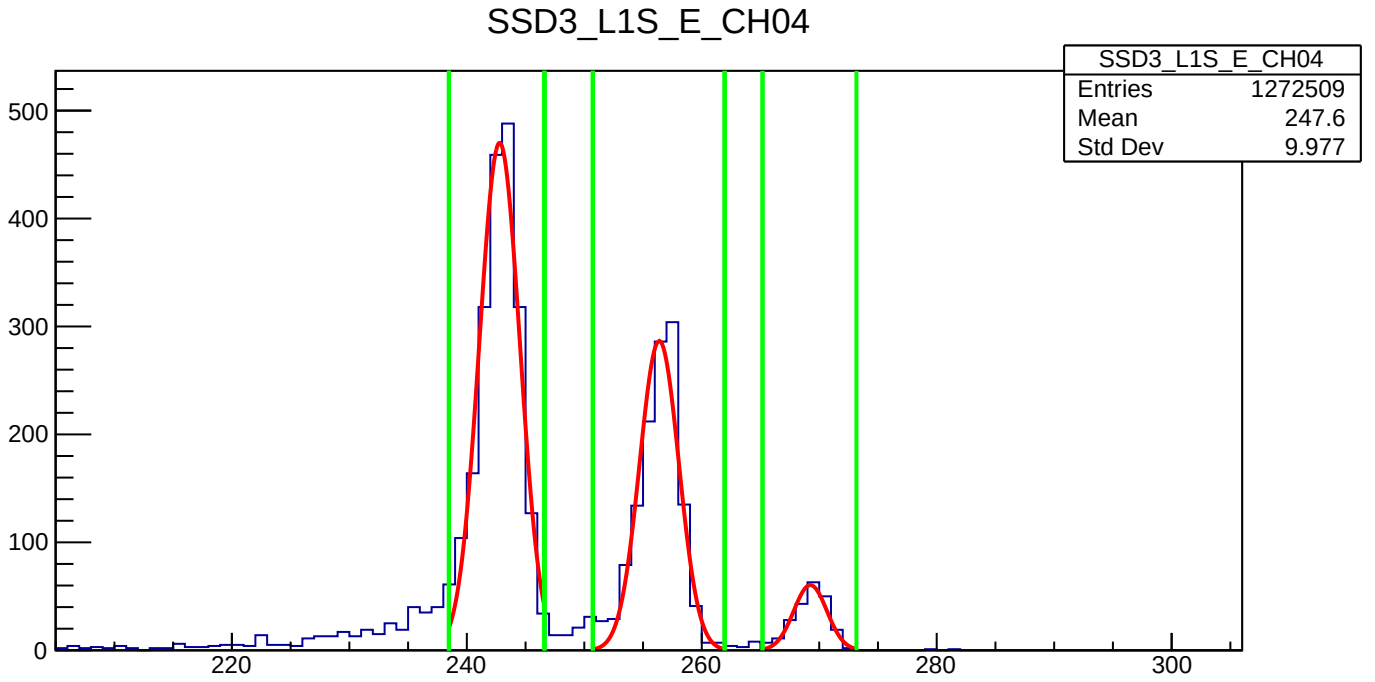
SSD3_L1S_E_CH04
SSD3_L1S_E_CH04
Entries 1272509
Mean 247.6
Std Dev 9.977
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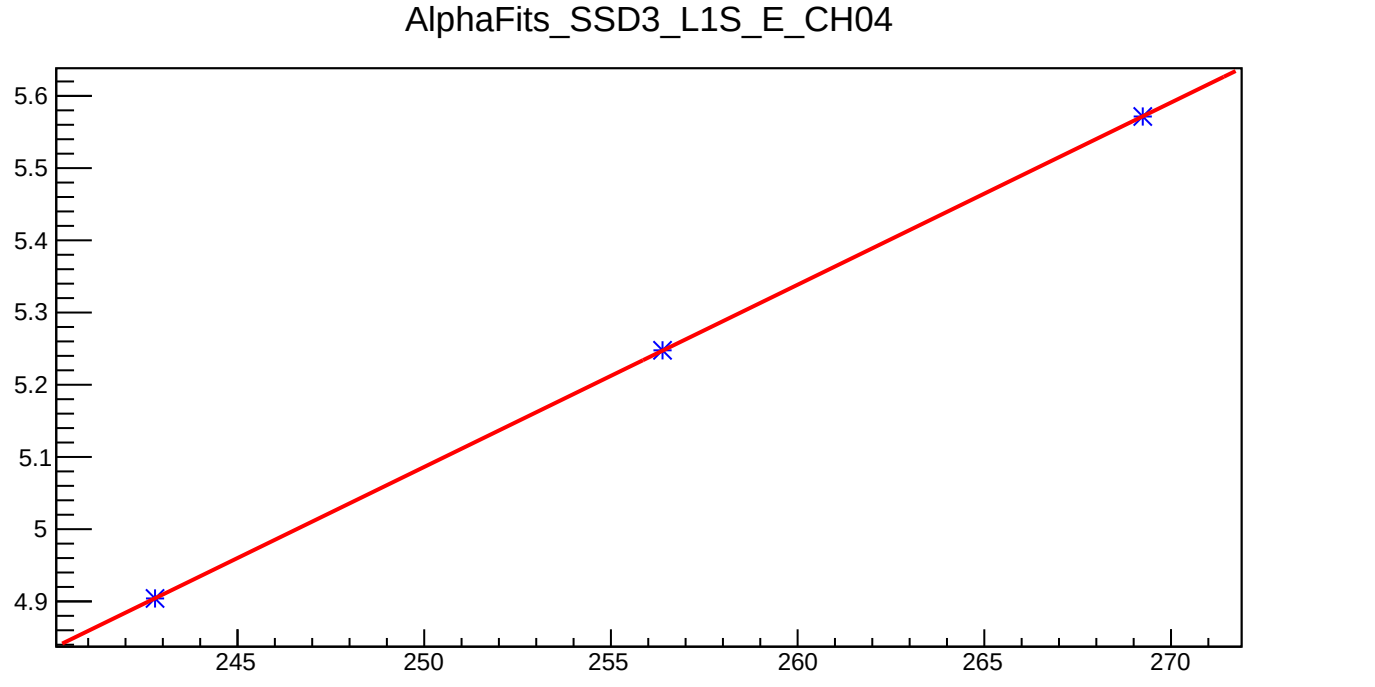
AlphaFits_SSD3_L1S_E_CH04
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5.3
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5.1
5
4.9
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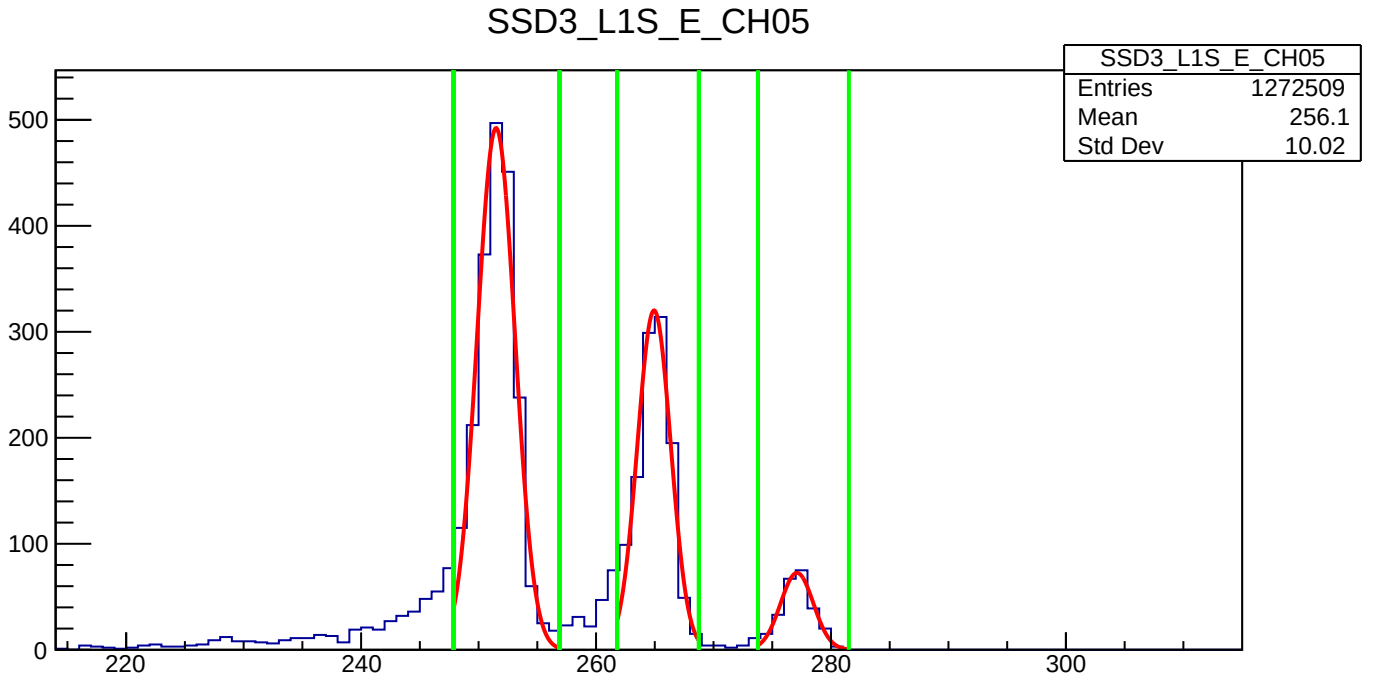
SSD3_L1S_E_CH05
SSD3_L1S_E_CH05
Entries 1272509
Mean 256.1
Std Dev 10.02
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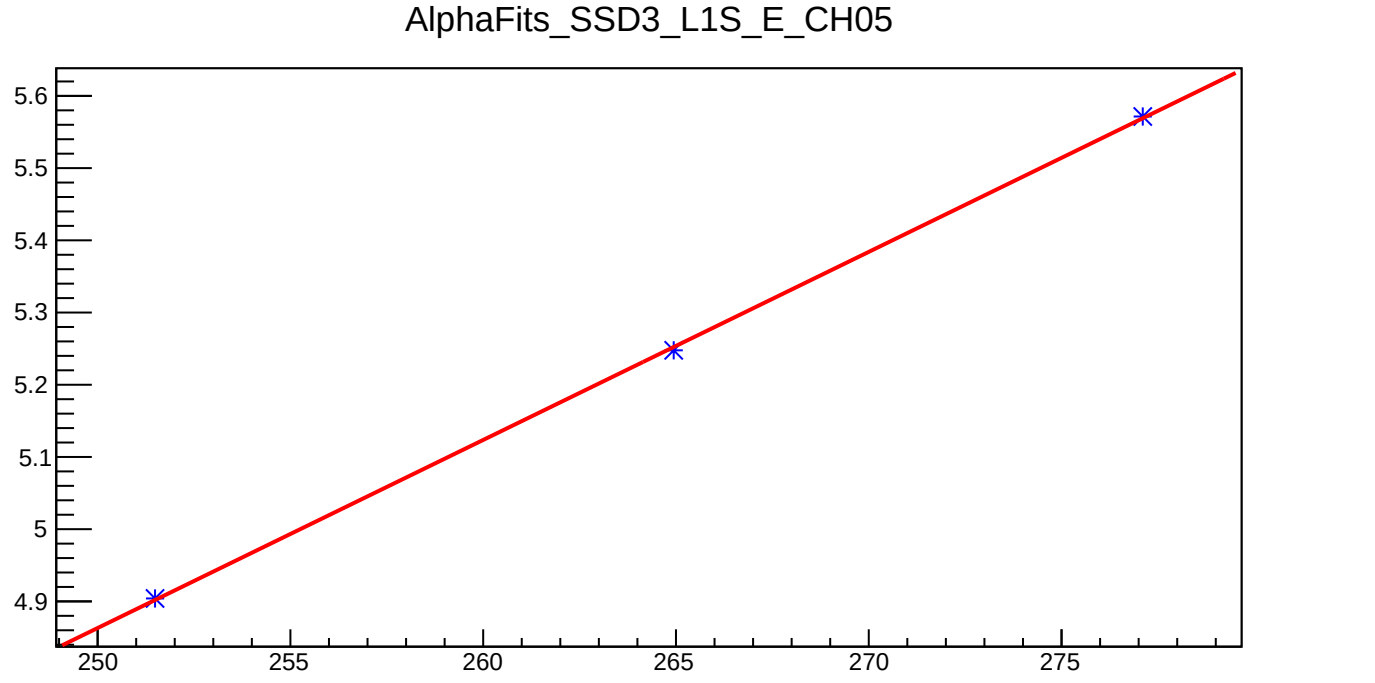
AlphaFits_SSD3_L1S_E_CH05
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5.3
5.2
5.1
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4.9
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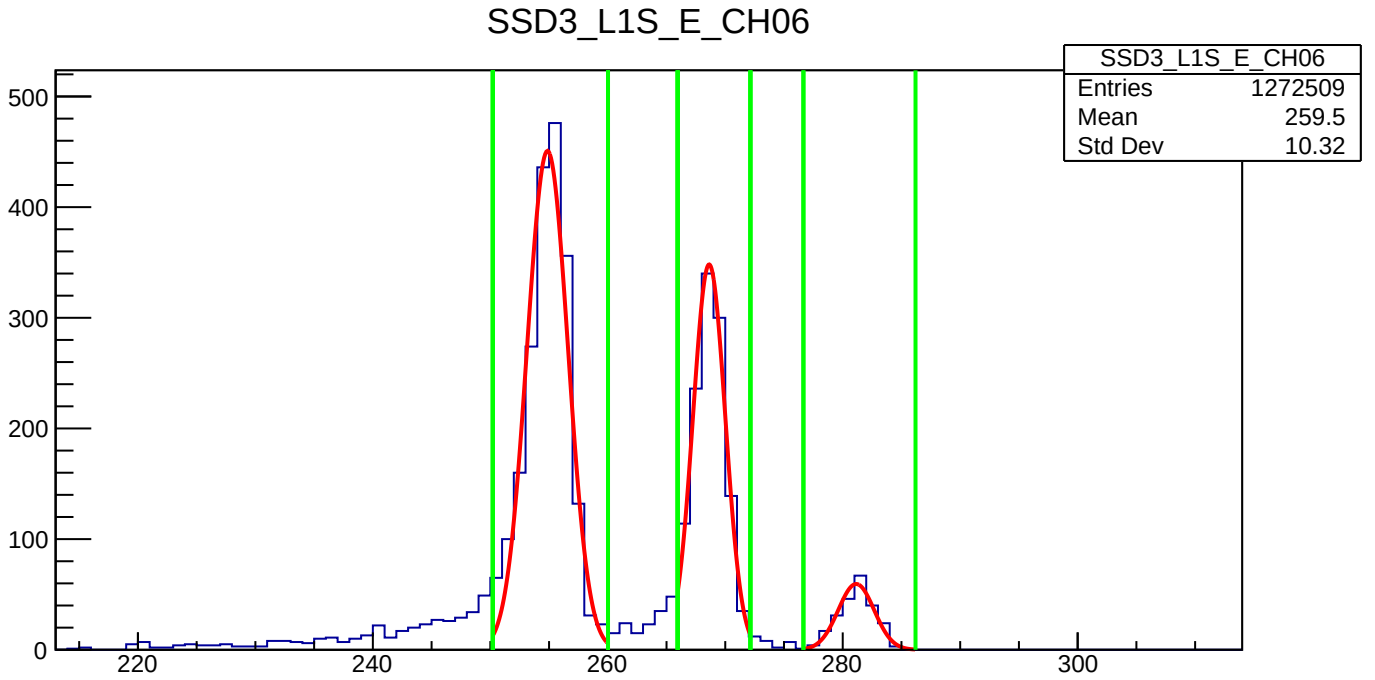
SSD3_L1S_E_CH06
SSD3_L1S_E_CH06
Entries 1272509
Mean 259.5
Std Dev 10.32
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280
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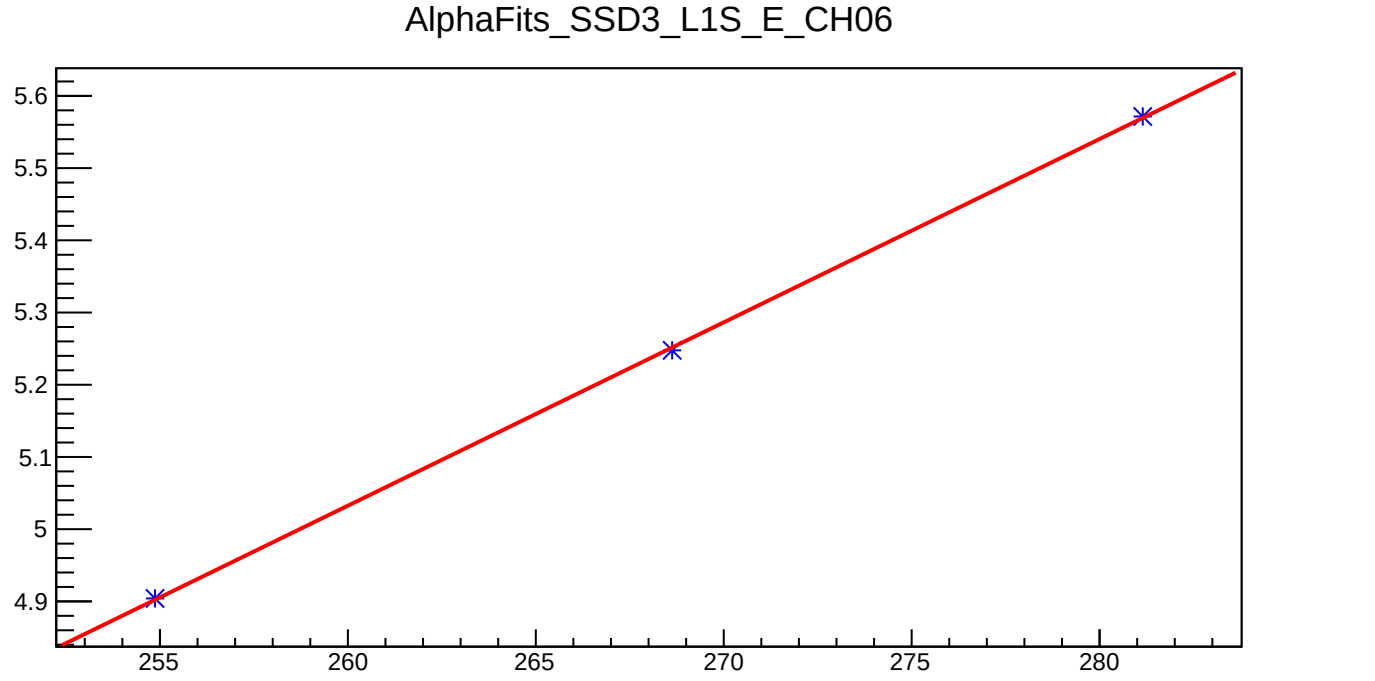
AlphaFits_SSD3_L1S_E_CH06
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5.4
5.3
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5.1
5
4.9
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275
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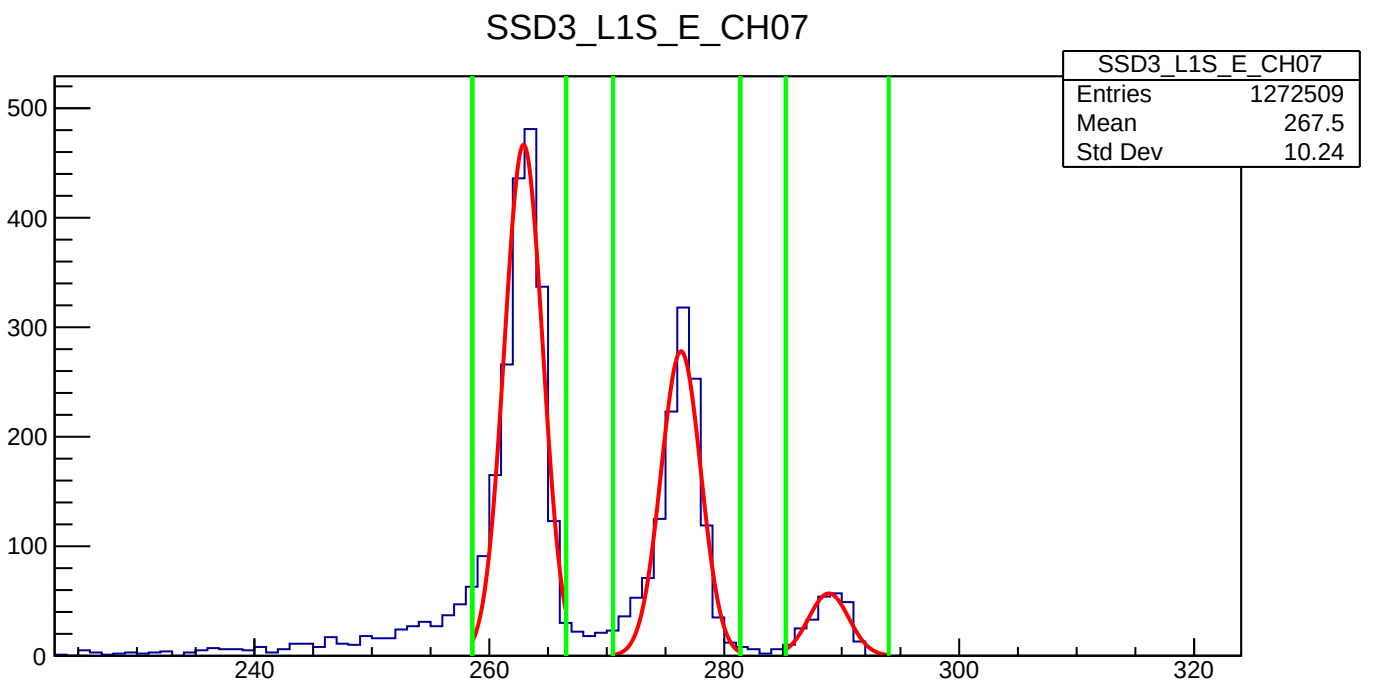
SSD3_L1S_E_CH07
SSD3_L1S_E_CH07
Entries 1272509
Mean 267.5
Std Dev 10.24
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200
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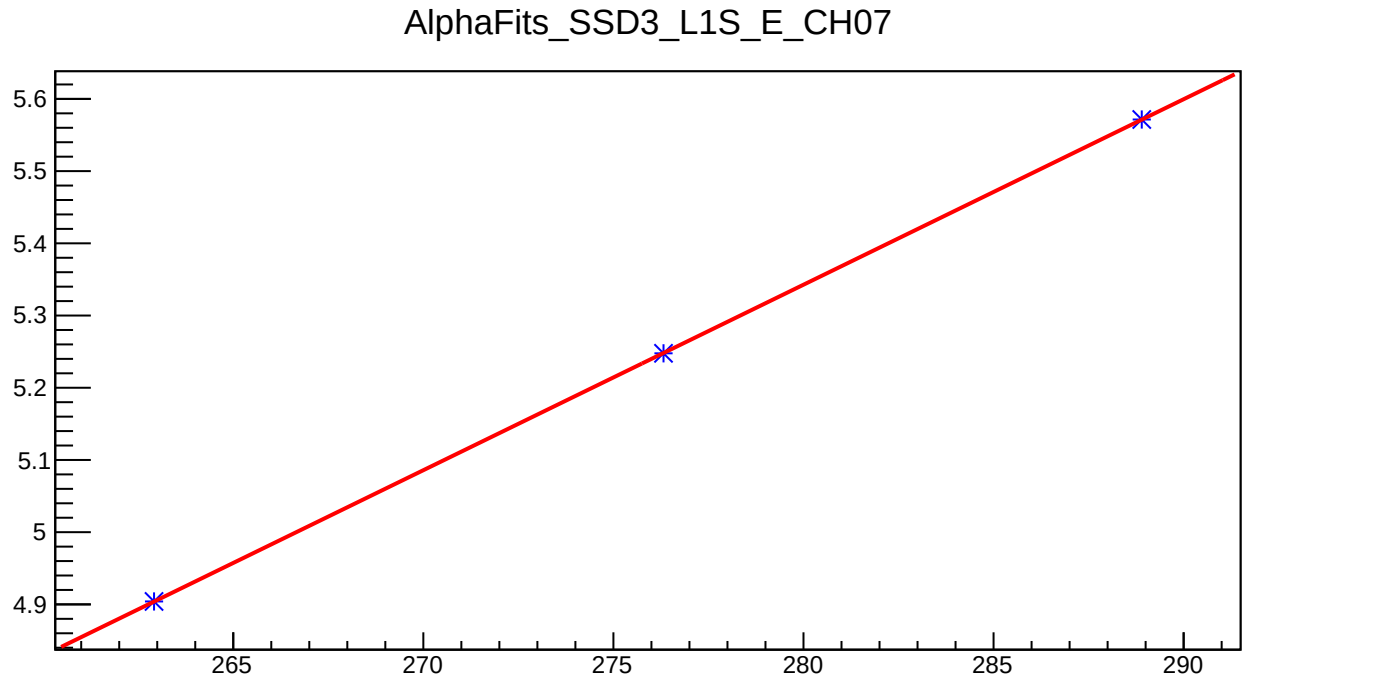
AlphaFits_SSD3_L1S_E_CH07
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5.4
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5.1
5
4.9
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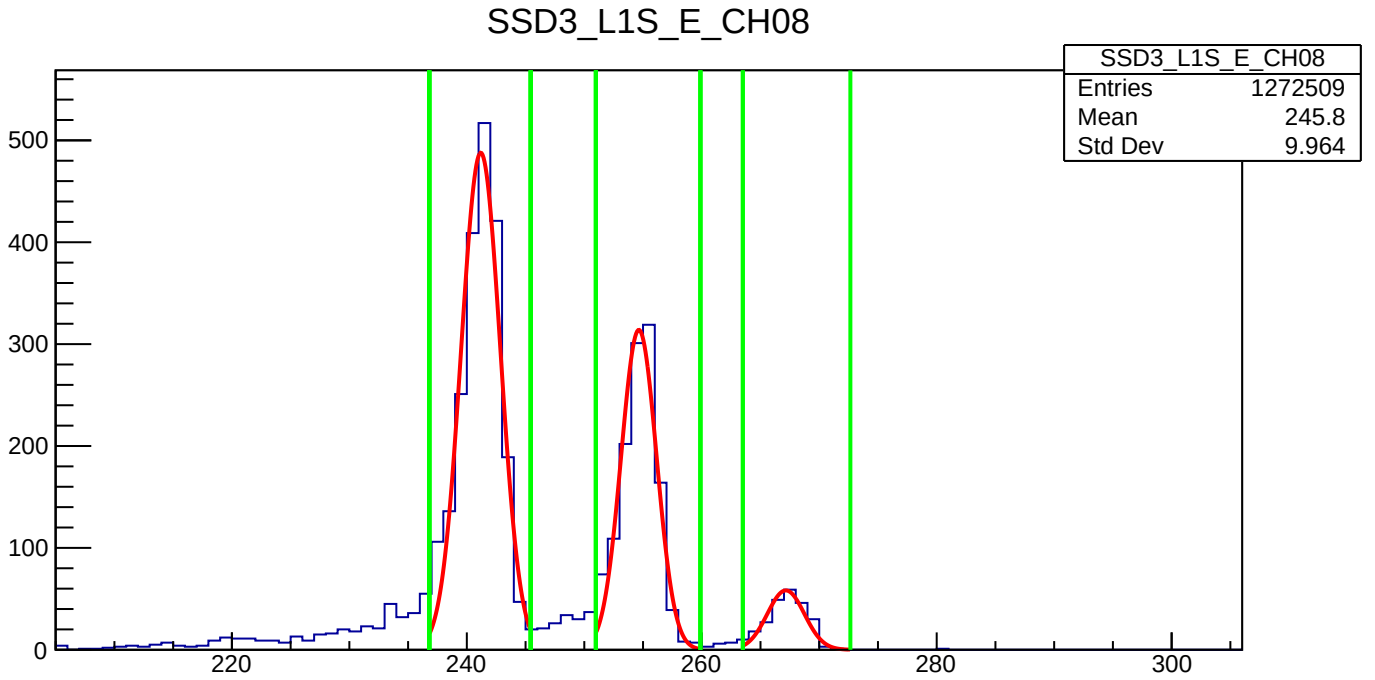
285
290


SSD3_L1S_E_CH08
SSD3_L1S_E_CH08
Entries 1272509
Mean 245.8
Std Dev 9.964
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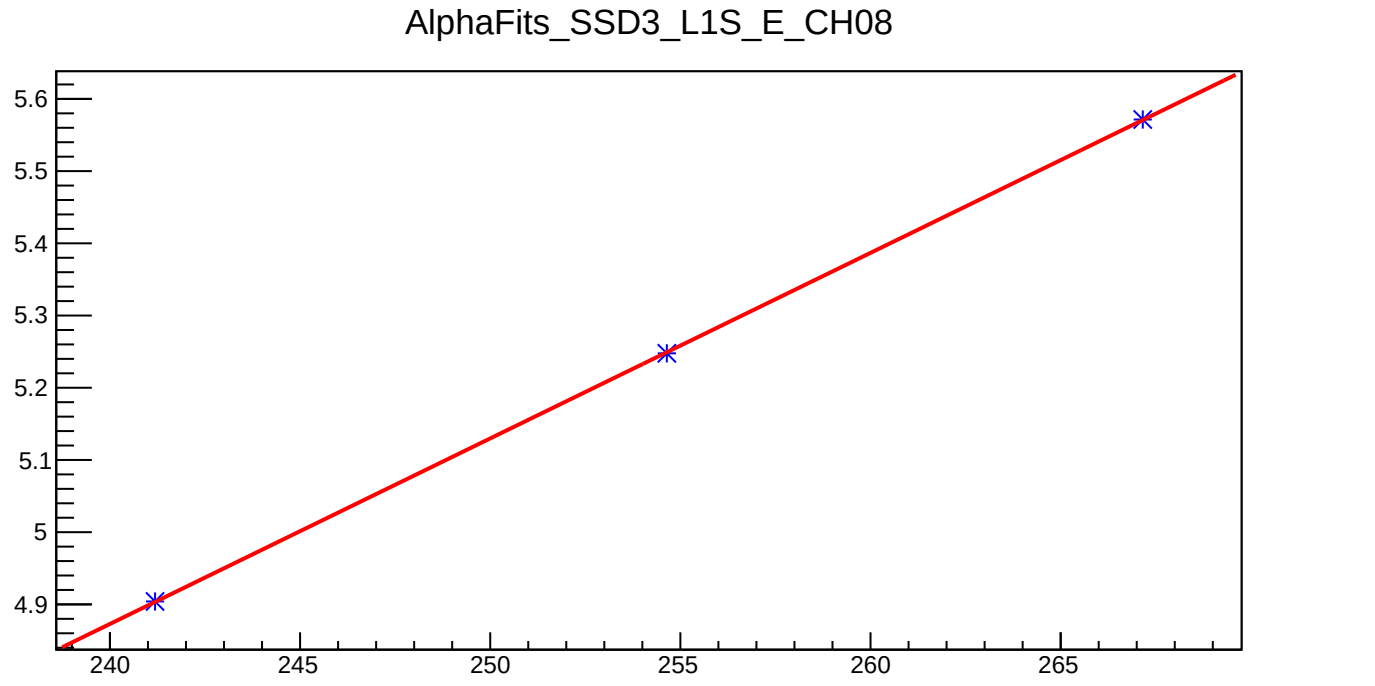
AlphaFits_SSD3_L1S_E_CH08
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5.4
5.3
5.2
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5
4.9
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255
260
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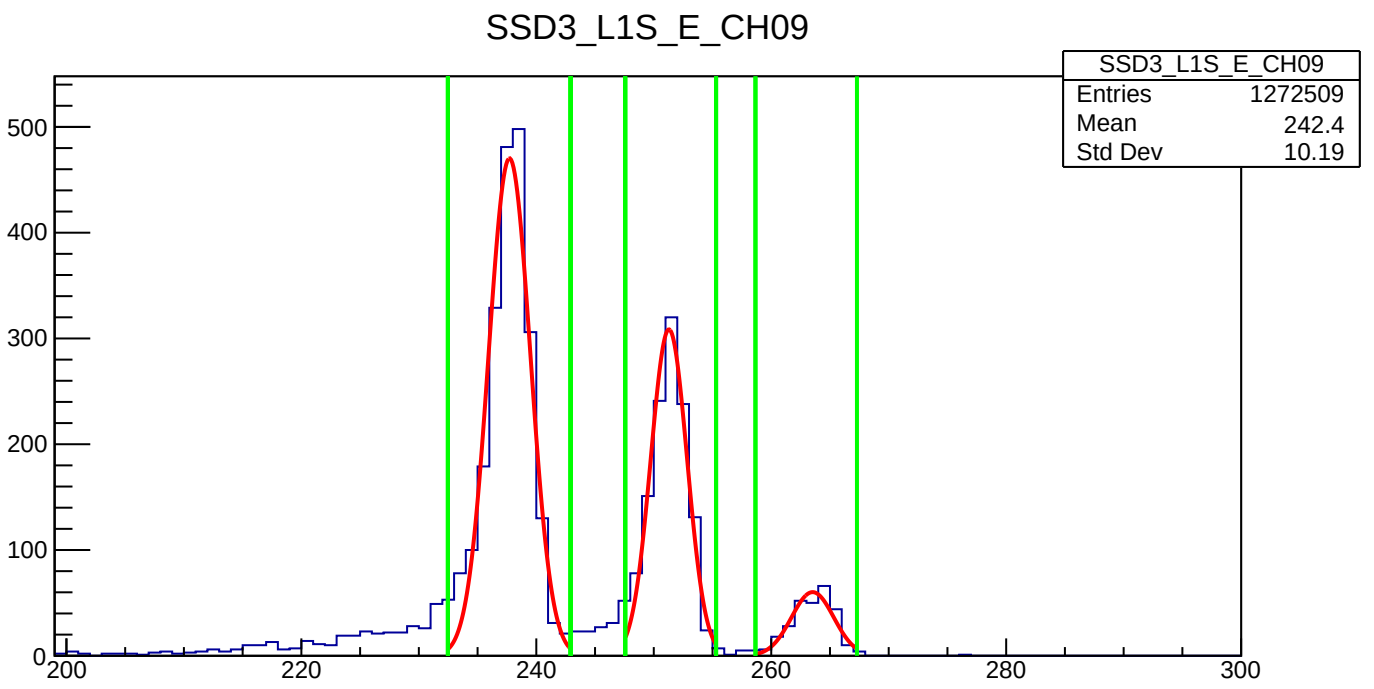
SSD3_L1S_E_CH09
SSD3_L1S_E_CH09
Entries 1272509
Mean 242.4
Std Dev 10.19
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240
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280
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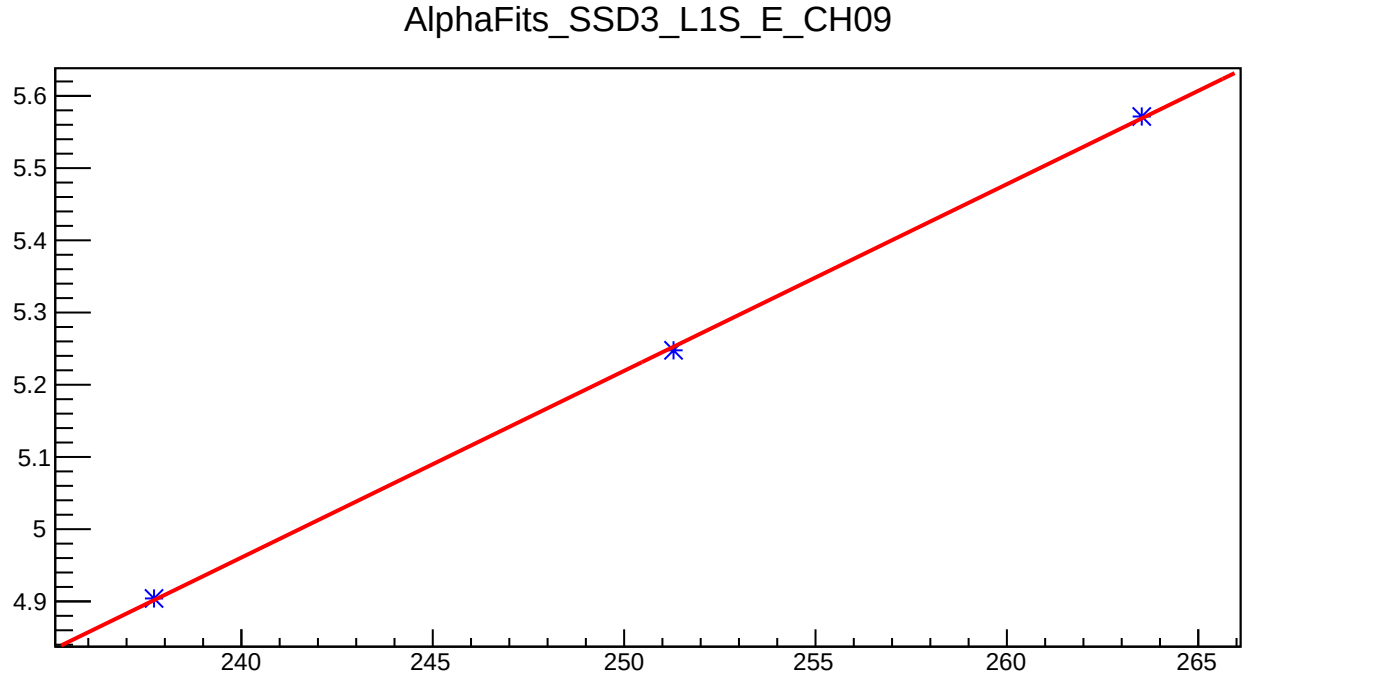
AlphaFits_SSD3_L1S_E_CH09
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5.4
5.3
5.2
5.1
5
4.9
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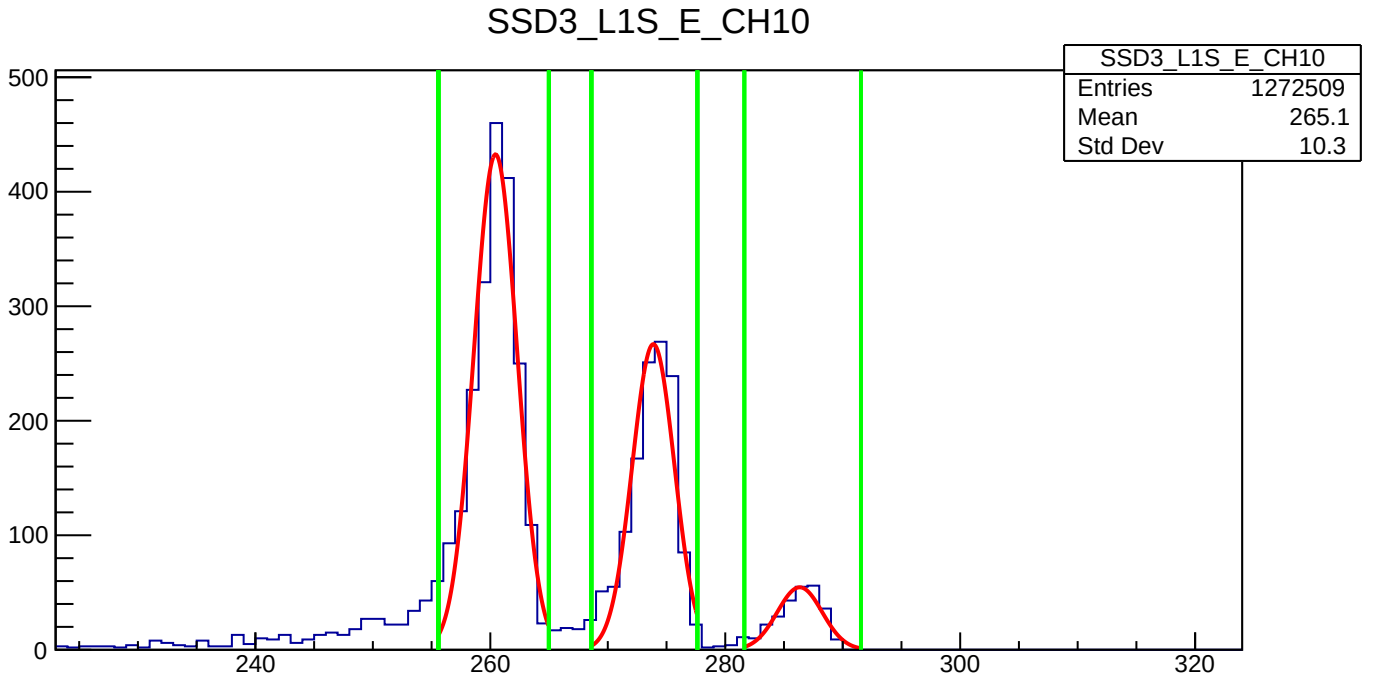
245
250
255
260
265


SSD3_L1S_E_CH10
SSD3_L1S_E_CH10
Entries 1272509
Mean 265.1
Std Dev 10.3
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300
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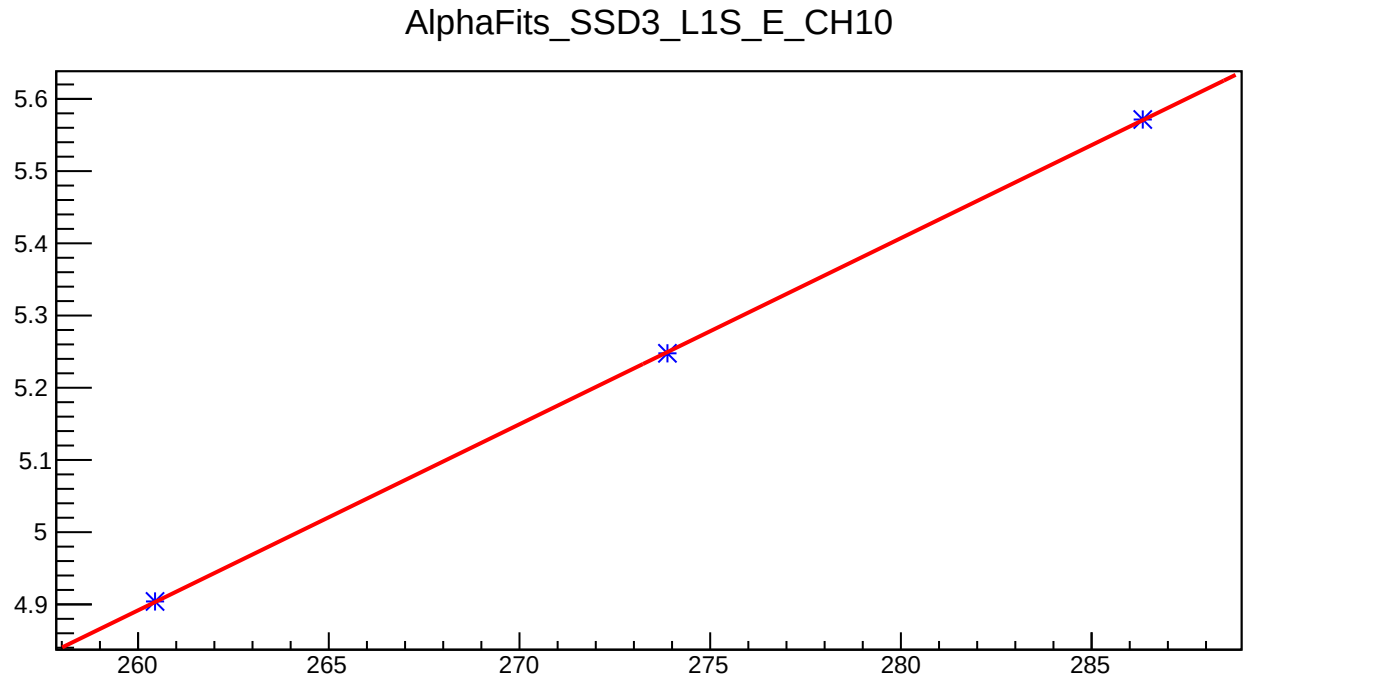
AlphaFits_SSD3_L1S_E_CH10
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5.2
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5
4.9
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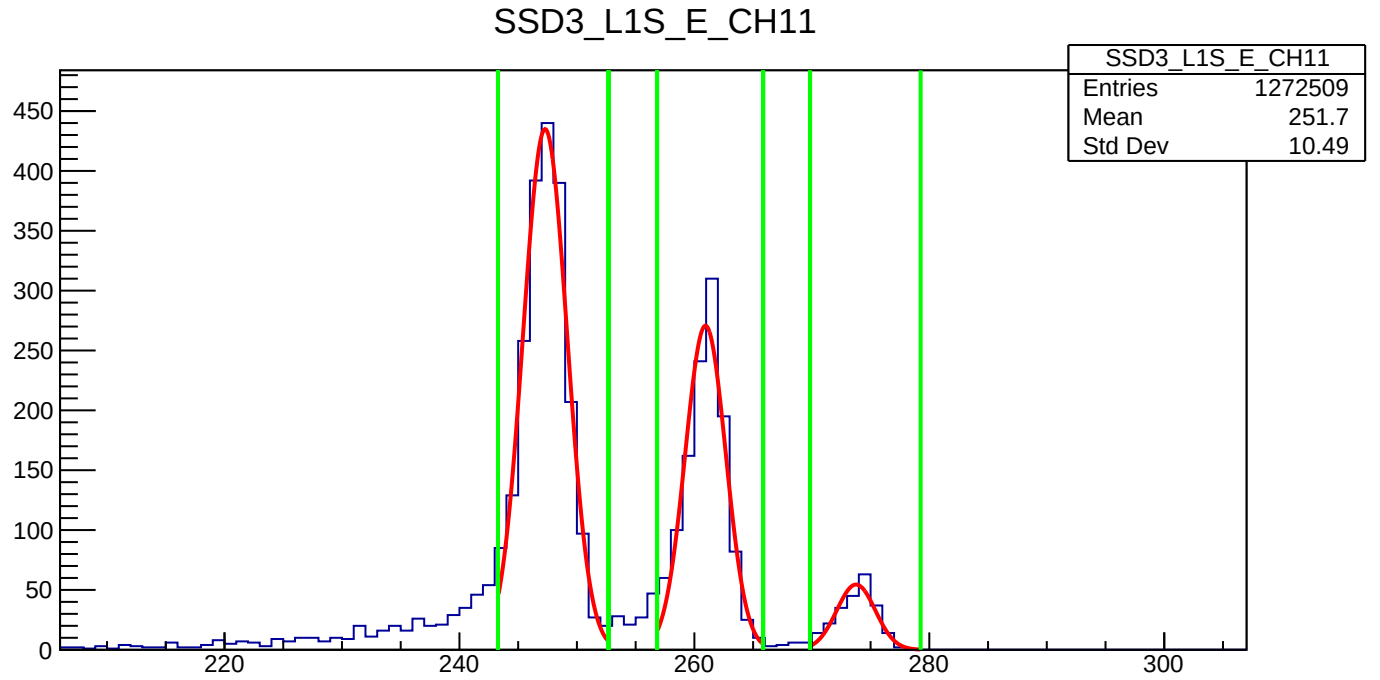
280
285


SSD3_L1S_E_CH11
SSD3_L1S_E_CH11
Entries 1272509
Mean 251.7
Std Dev 10.49
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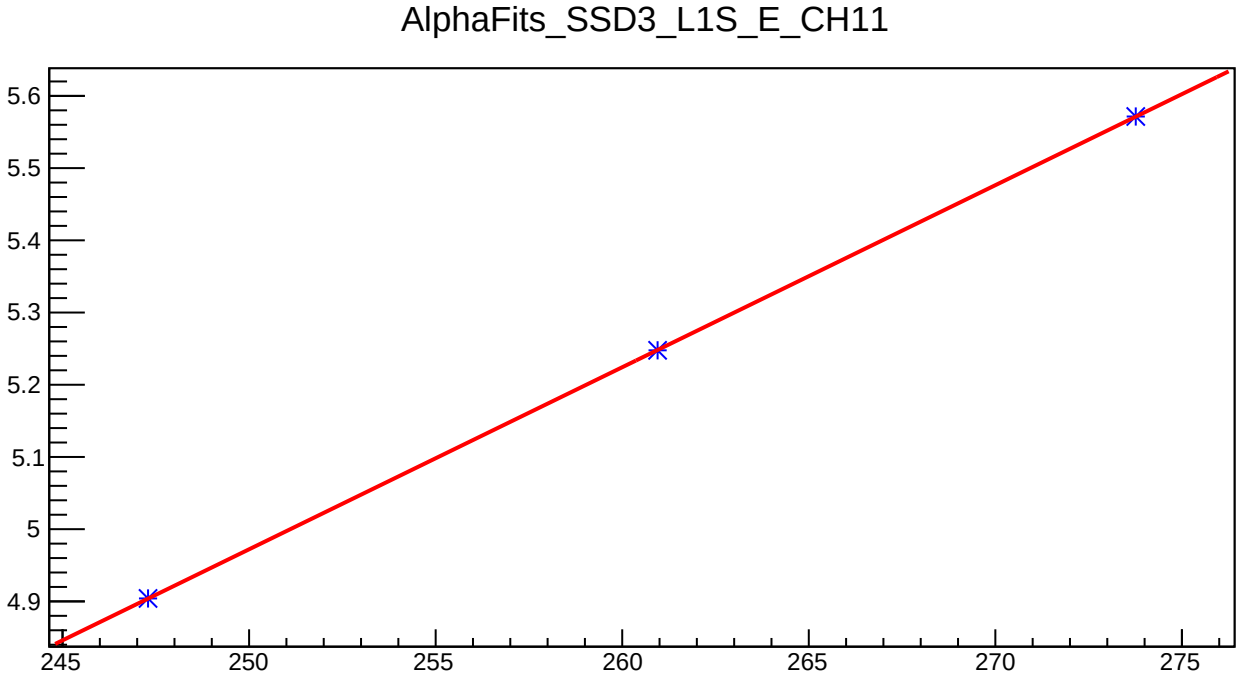
AlphaFits_SSD3_L1S_E_CH11
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5.4
5.3
5.2
5.1
5
4.9
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270
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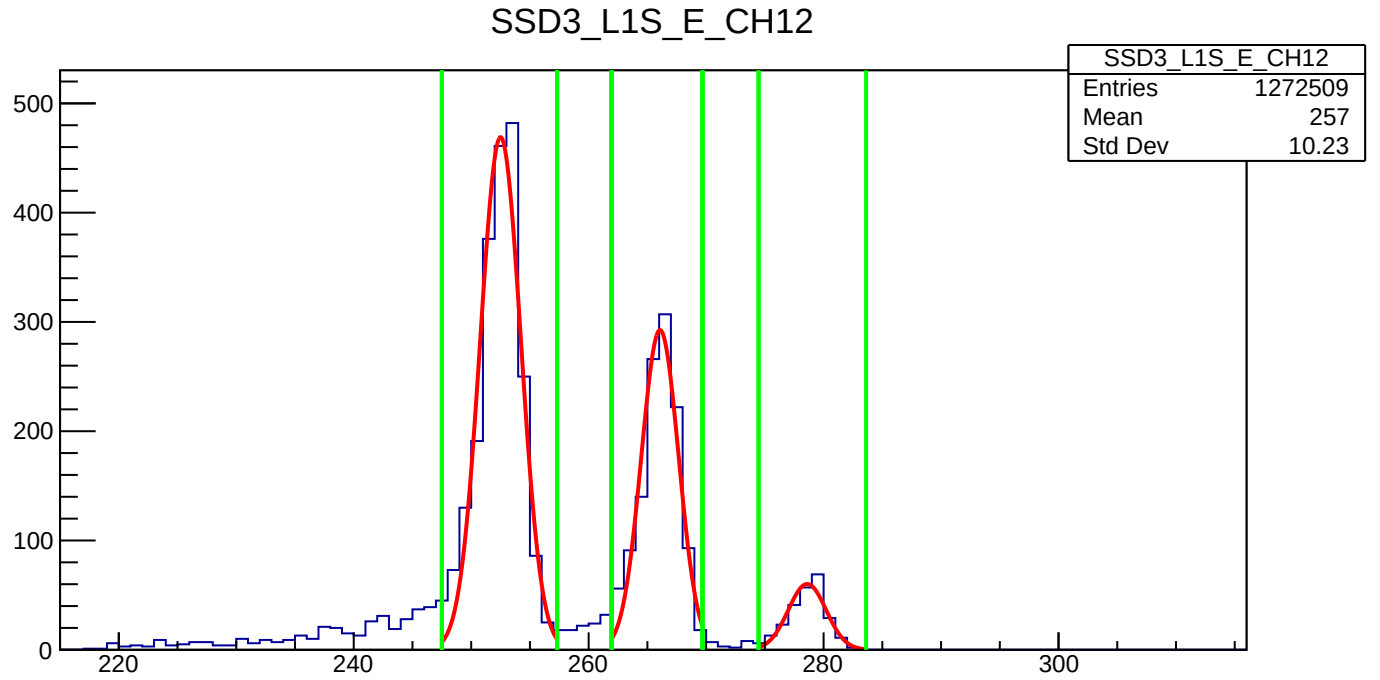
SSD3_L1S_E_CH12
SSD3_L1S_E_CH12
Entries 1272509
Mean 257
Std Dev 10.23
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240
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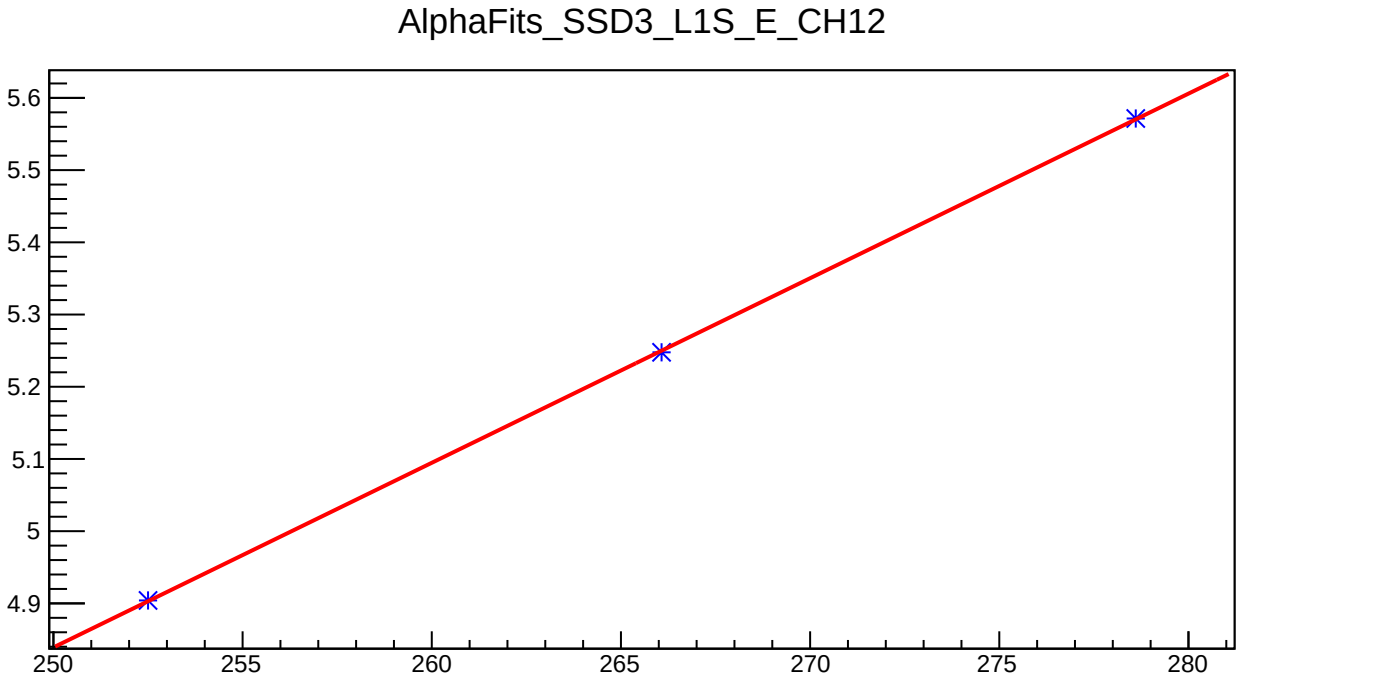
280
300


AlphaFits_SSD3_L1S_E_CH12
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5.3
5.2
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4.9
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275
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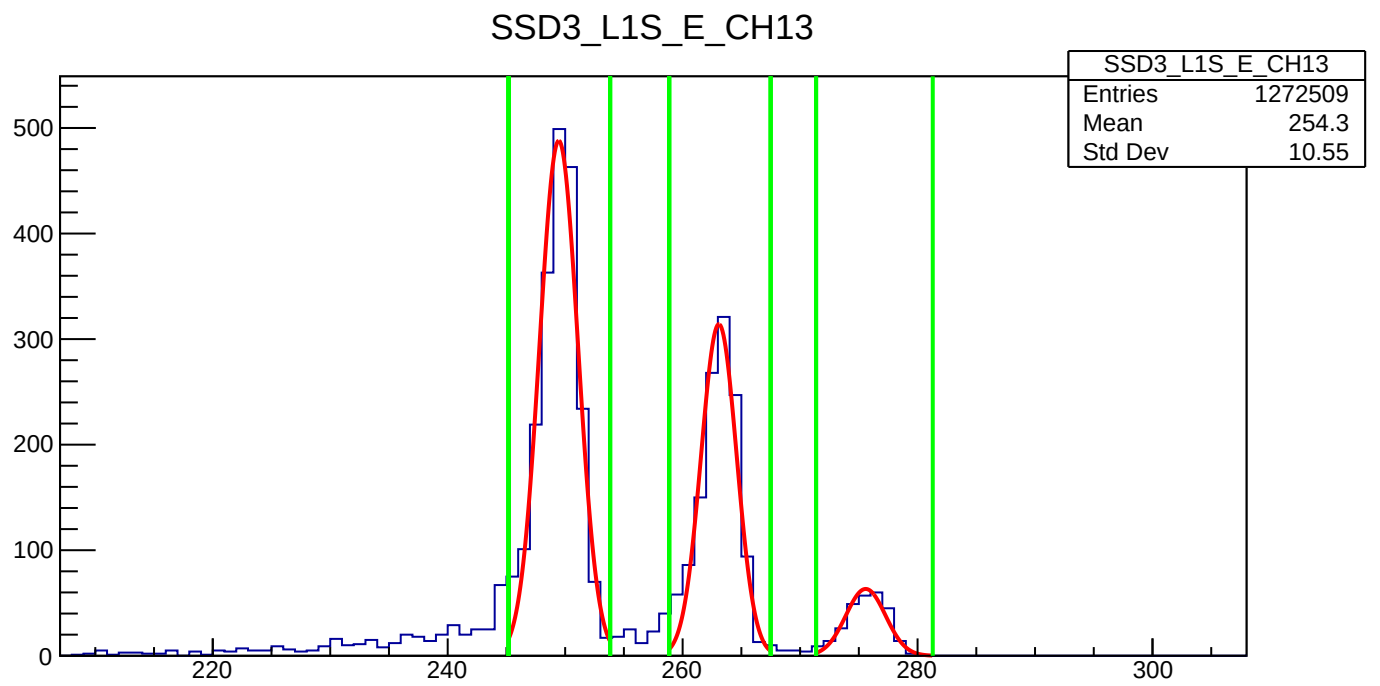
SSD3_L1S_E_CH13
SSD3_L1S_E_CH13
Entries 1272509
Mean 254.3
Std Dev 10.55
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200
100
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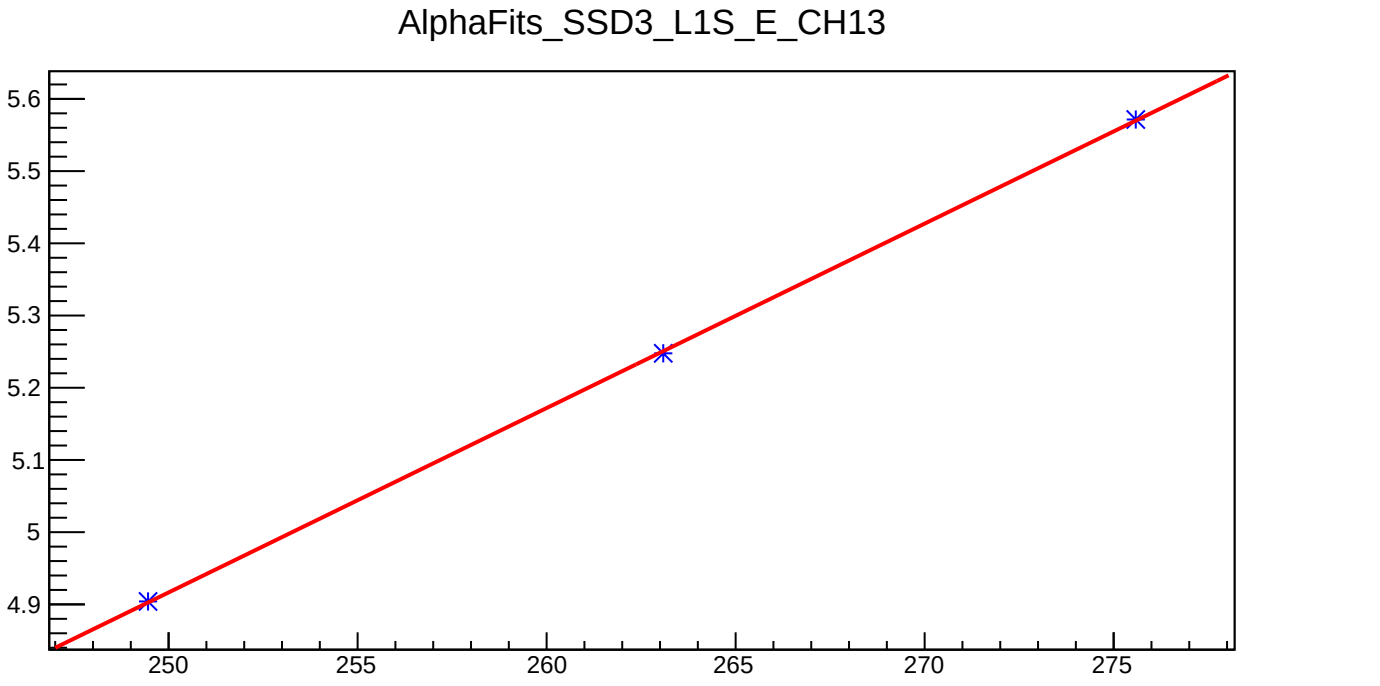
280
300


AlphaFits_SSD3_L1S_E_CH13
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5.4
5.3
5.2
5.1
5
4.9
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260
265
270
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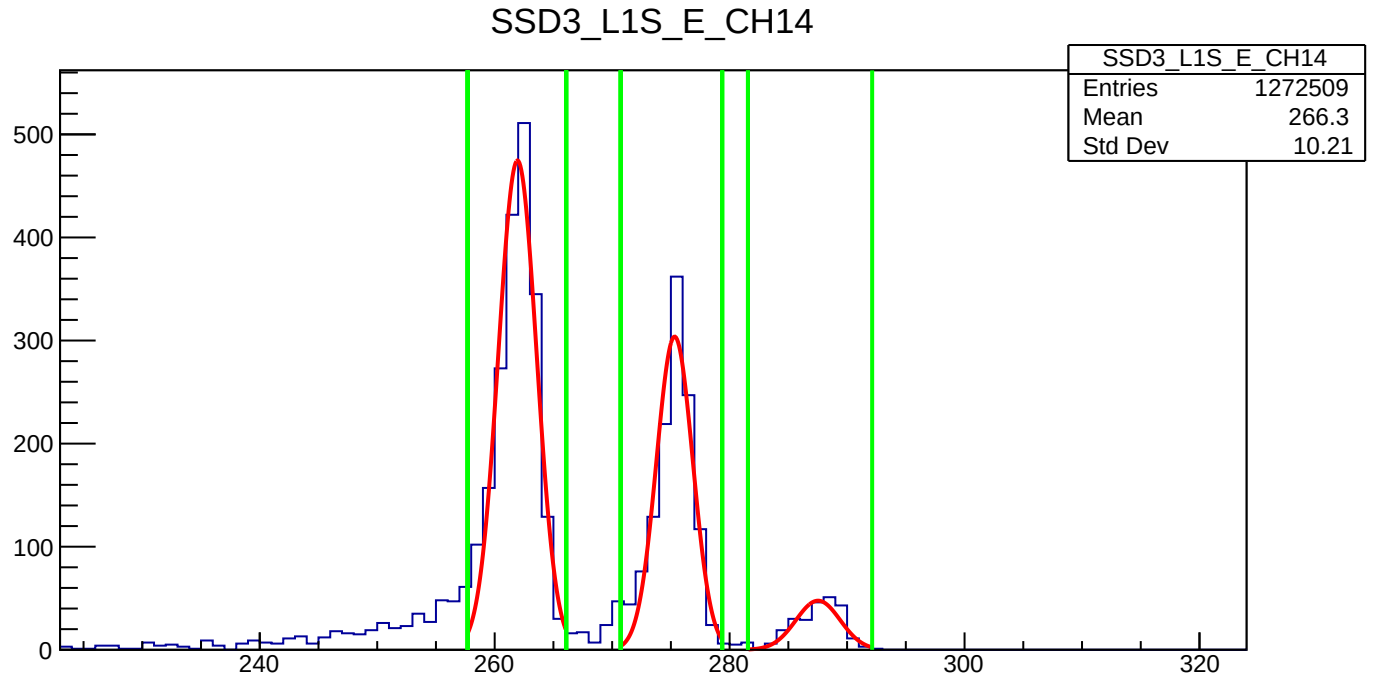
SSD3_L1S_E_CH14
SSD3_L1S_E_CH14
Entries 1272509
Mean 266.3
Std Dev 10.21
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300
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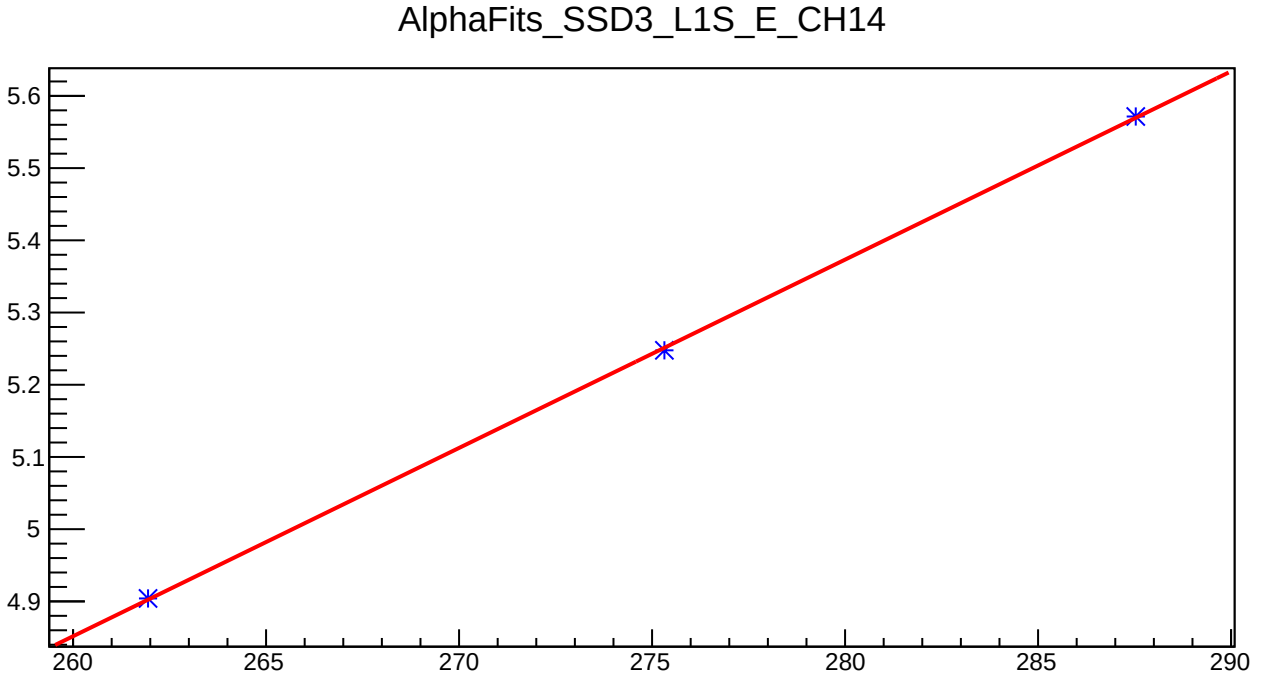
AlphaFits_SSD3_L1S_E_CH14
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5.4
5.3
5.2
5.1
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4.9
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285
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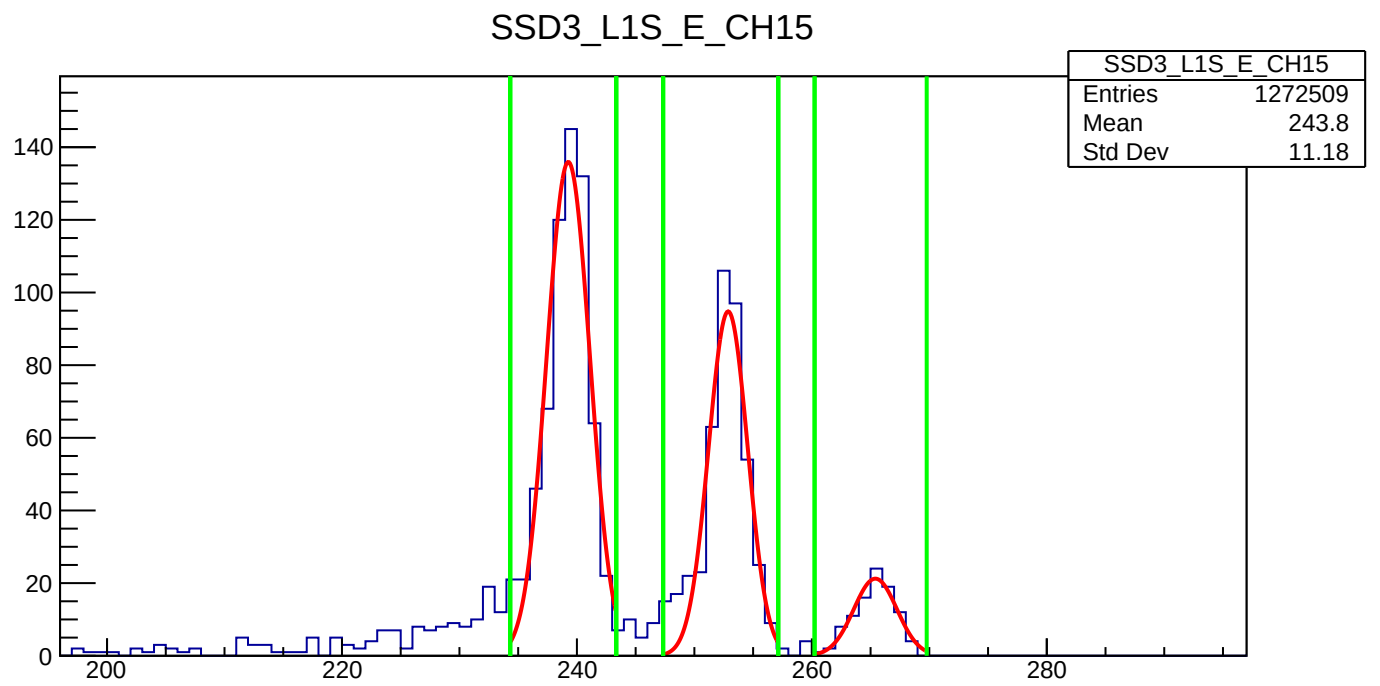
SSD3_L1S_E_CH15
SSD3_L1S_E_CH15
Entries 1272509
Mean 243.8
Std Dev 11.18
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120
100
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220
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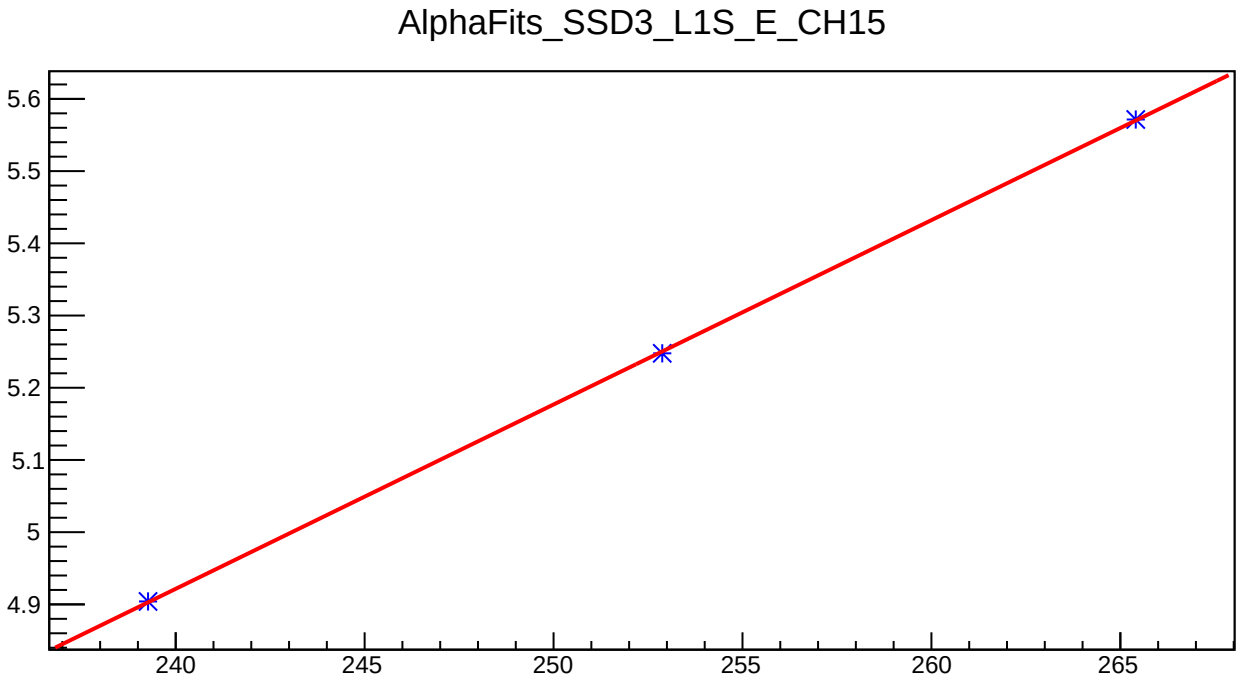
260
280


AlphaFits_SSD3_L1S_E_CH15
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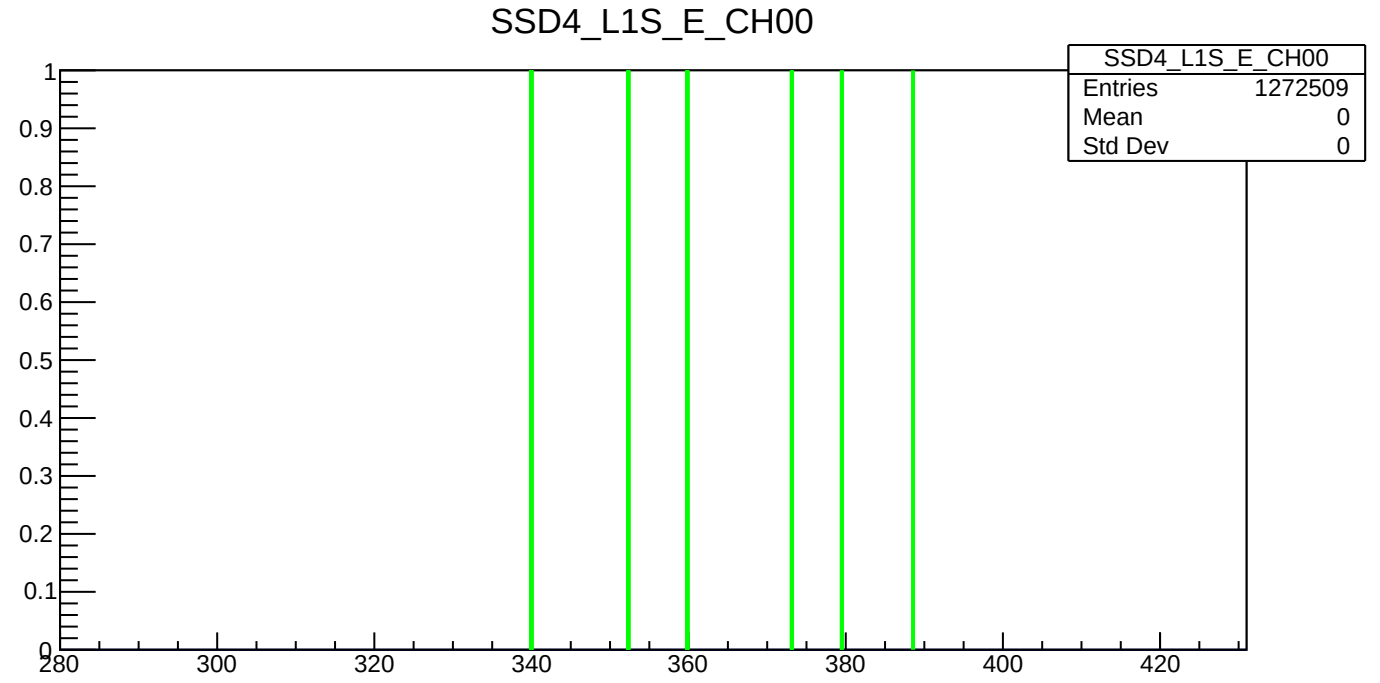
5.4
5.3
5.2
5.1
5
4.9
240
245
250
255
260
265


SSD4_L1S_E_CH00
SSD4_L1S_E_CH00
Entries 1272509
Mean 0
Std Dev 0
1
0.9
0.8
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0.4
0.3
0.2
0.1
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360
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400
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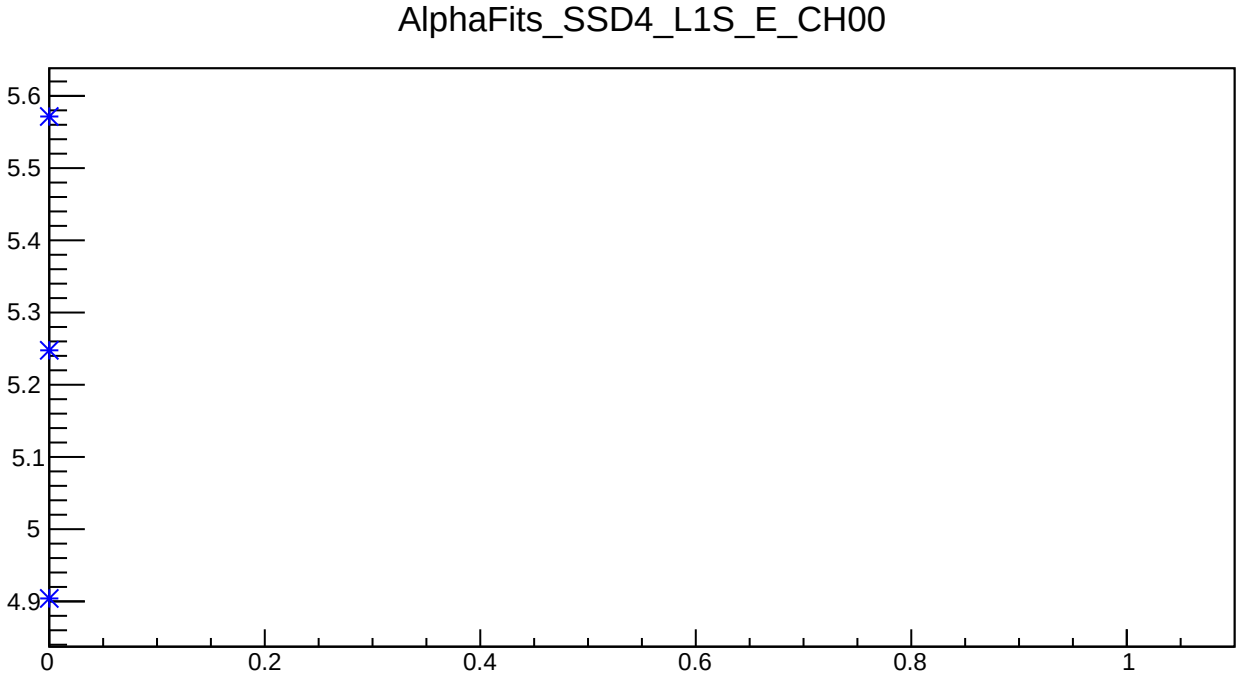
AlphaFits_SSD4_L1S_E_CH00
5.6
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5.4
5.3
5.2
5.1
5
4.9
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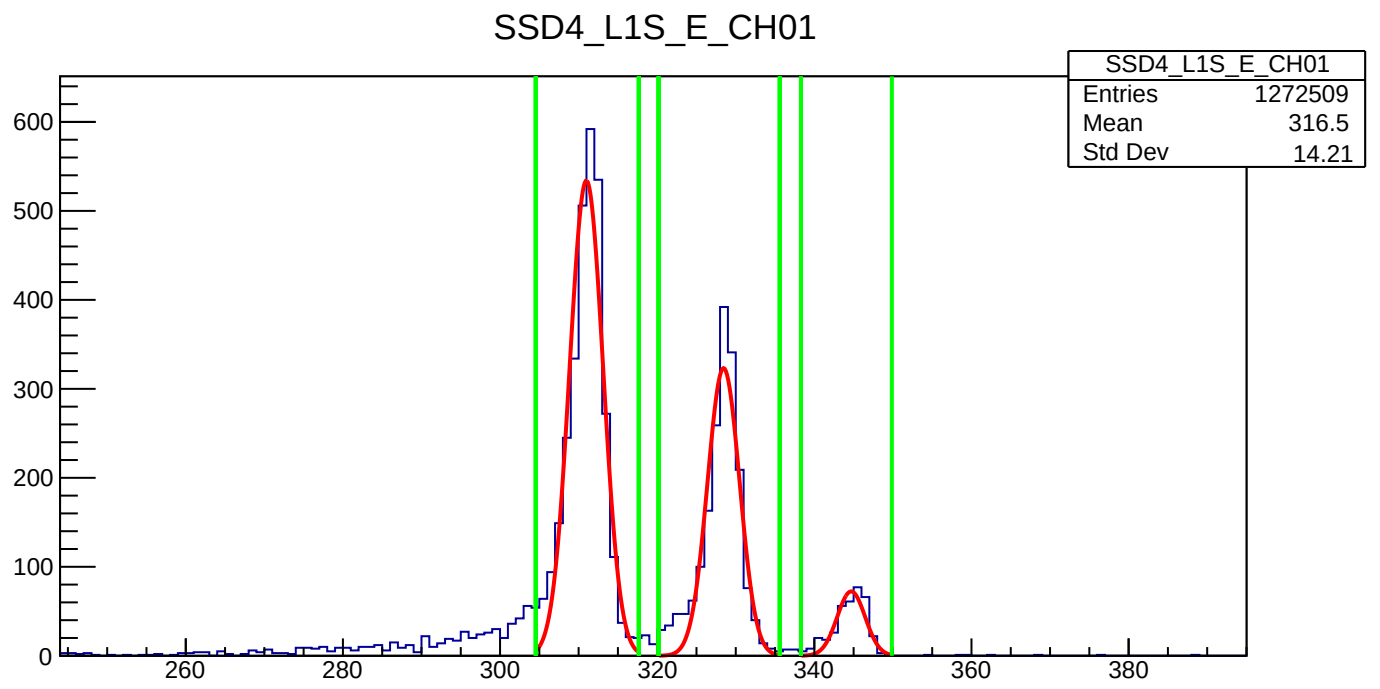
0.2
0.4
0.6
0.8
1


SSD4_L1S_E_CH01
SSD4_L1S_E_CH01
Entries 1272509
Mean 316.5
Std Dev 14.21
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0
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300
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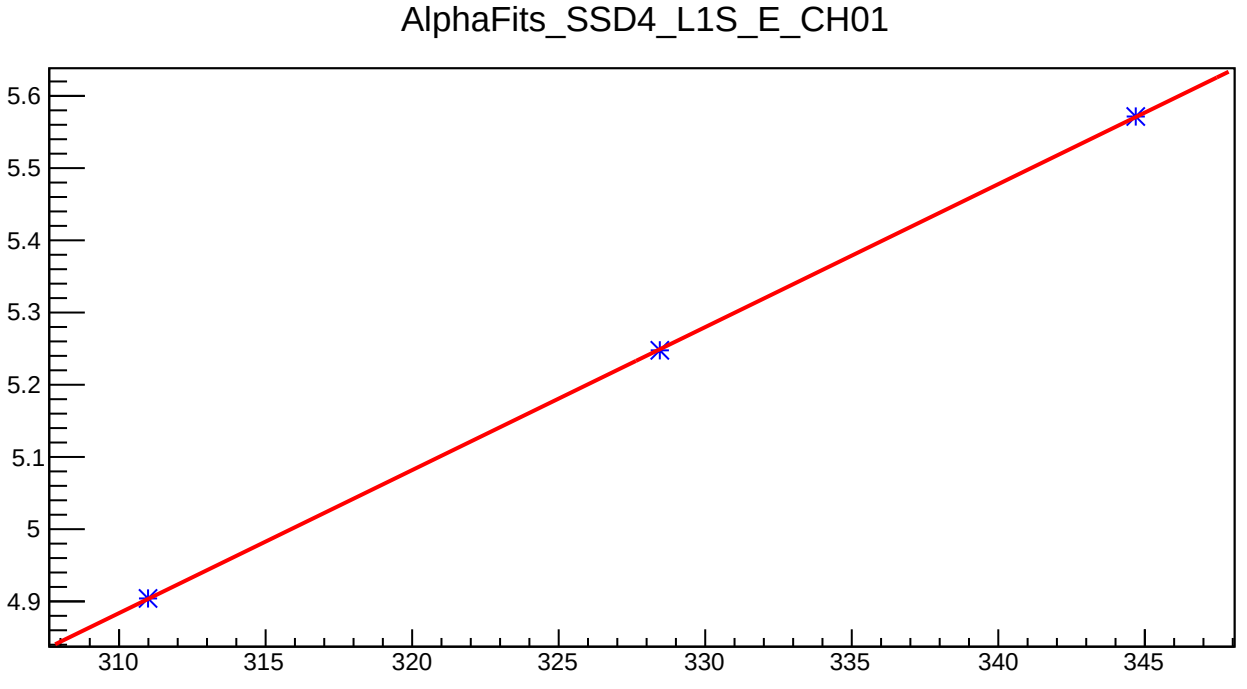
360
380


AlphaFits_SSD4_L1S_E_CH01
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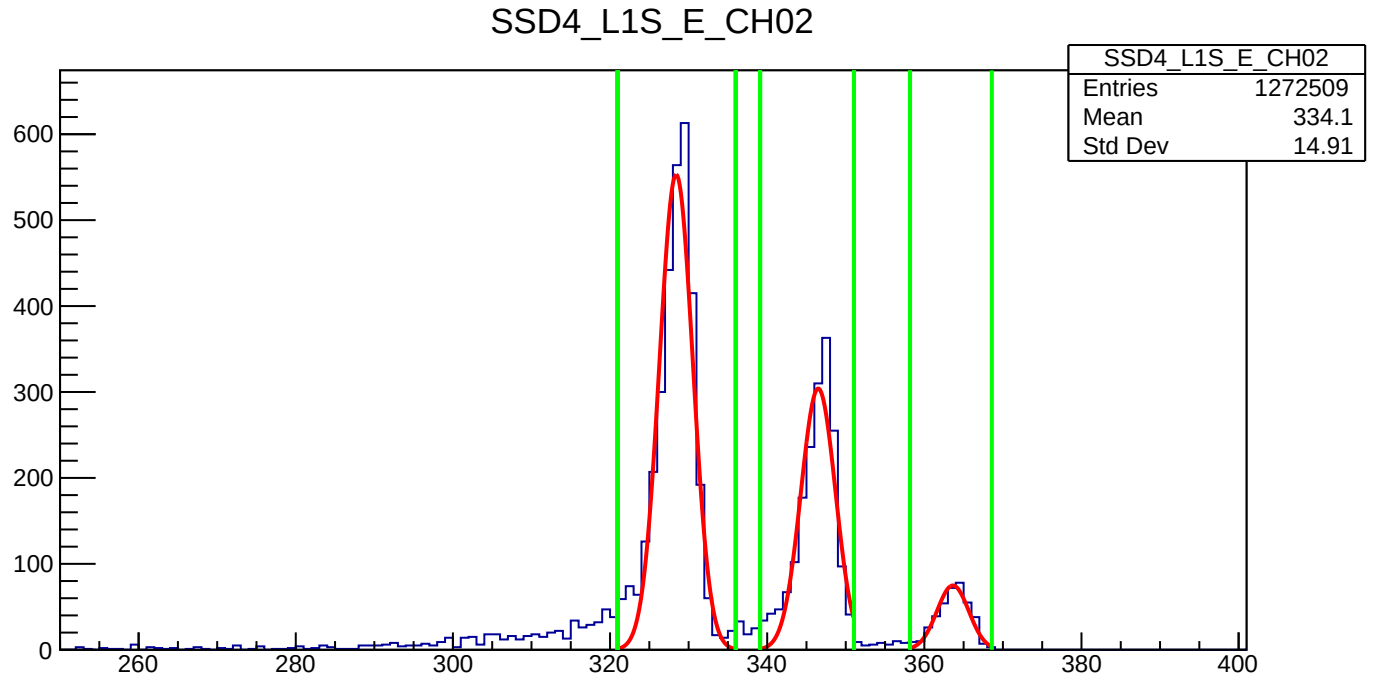
5.4
5.3
5.2
5.1
5
4.9
310
315
320
325
330
335
340
345


SSD4_L1S_E_CH02
SSD4_L1S_E_CH02
Entries 1272509
Mean 334.1
Std Dev 14.91
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340
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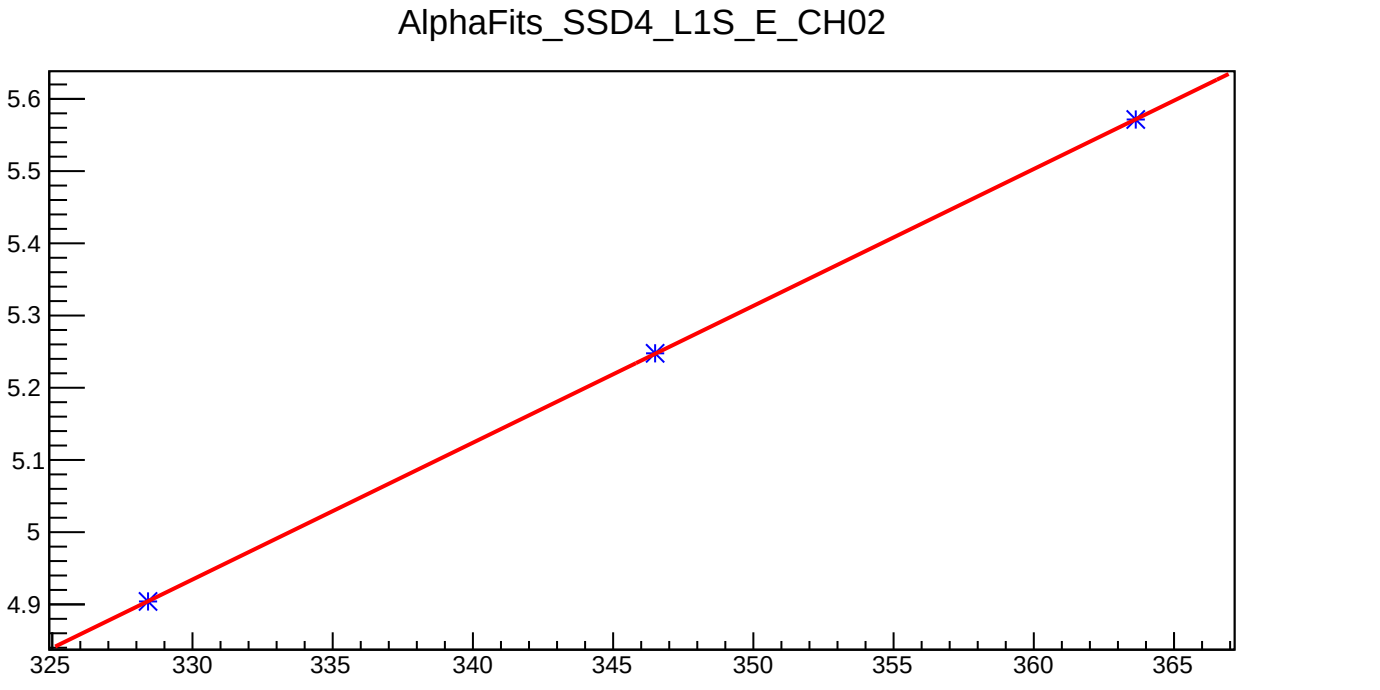
380
400


AlphaFits_SSD4_L1S_E_CH02
5.6
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5.3
5.2
5.1
5
4.9
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350
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360
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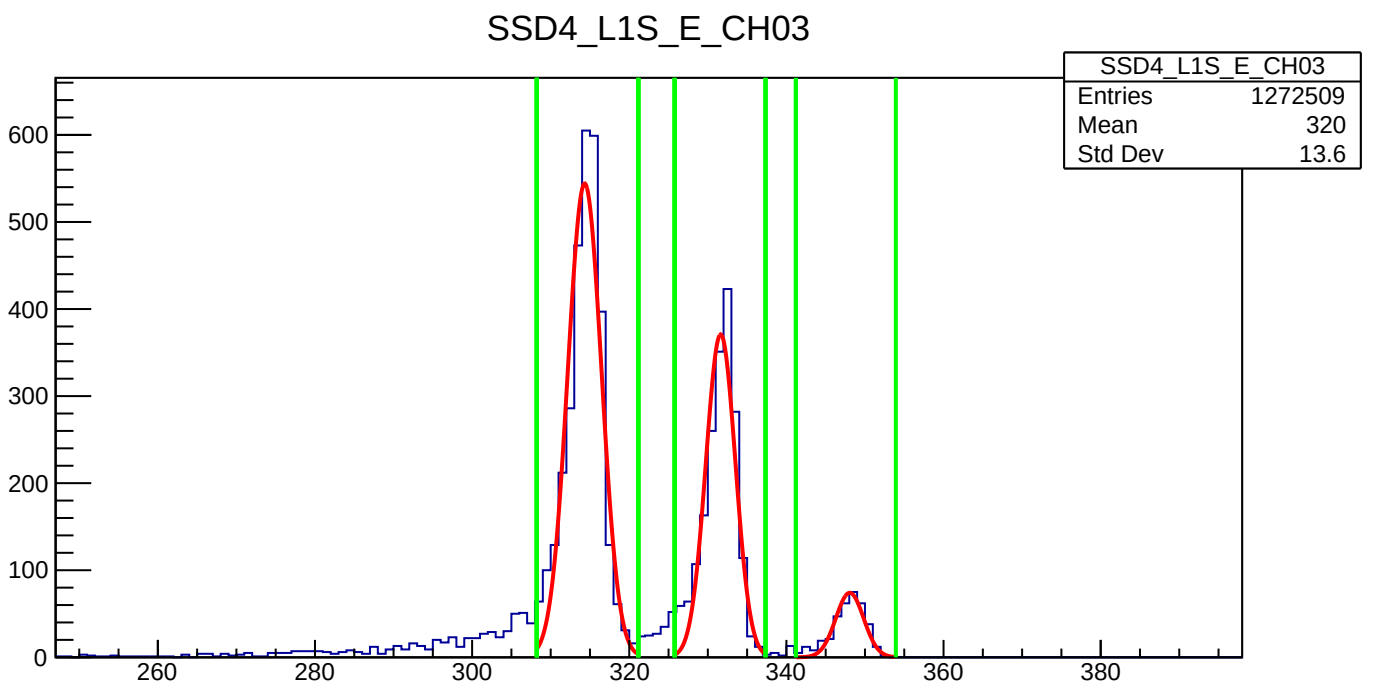
SSD4_L1S_E_CH03
SSD4_L1S_E_CH03
Entries 1272509
Mean 320
Std Dev 13.6
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0
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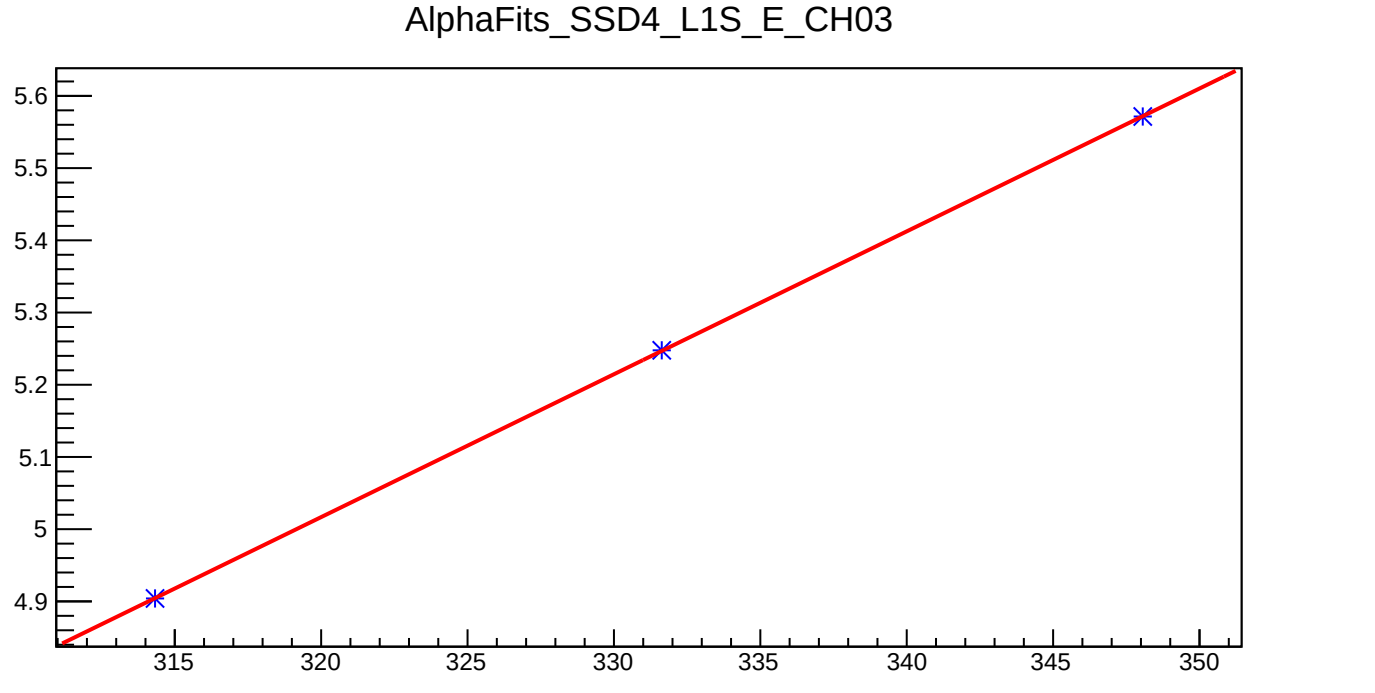
300
320
340
360
380


AlphaFits_SSD4_L1S_E_CH03
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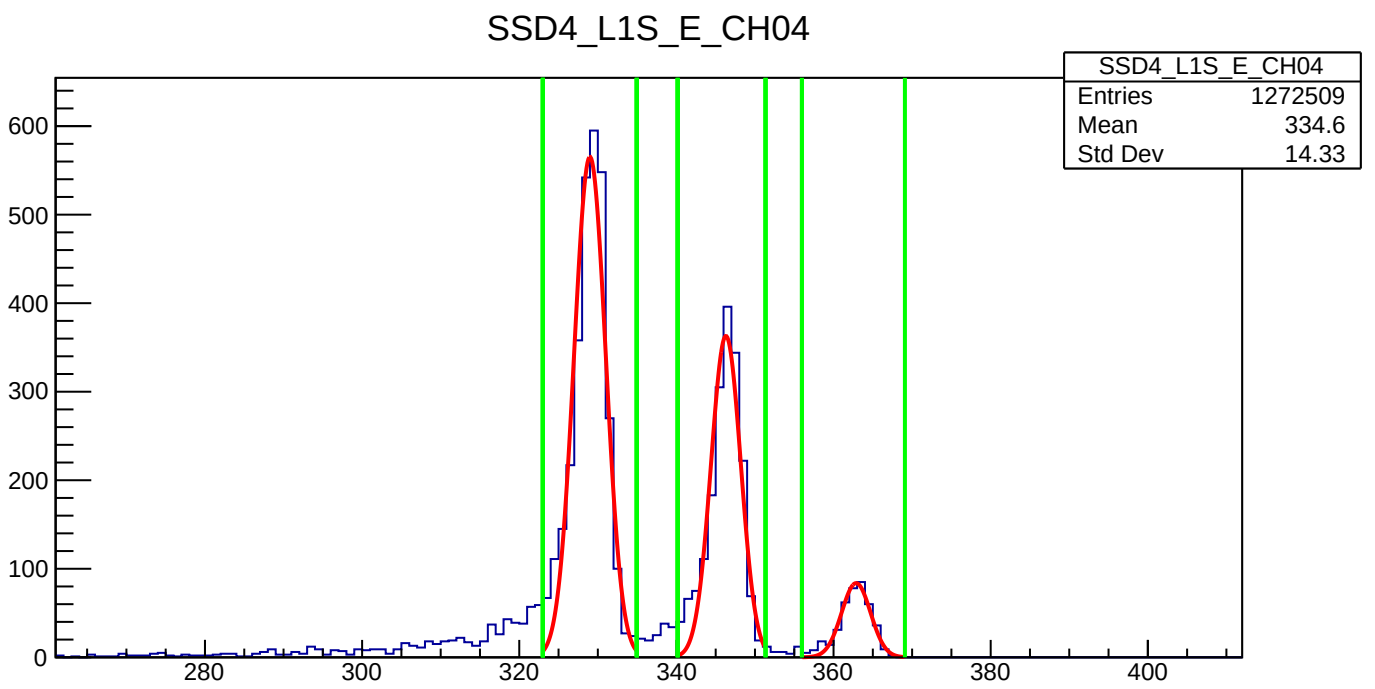
5.5
5.4
5.3
5.2
5.1
5
4.9
315
320
325
330
335
340
345
350


SSD4_L1S_E_CH04
SSD4_L1S_E_CH04
Entries 1272509
Mean 334.6
Std Dev 14.33
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0
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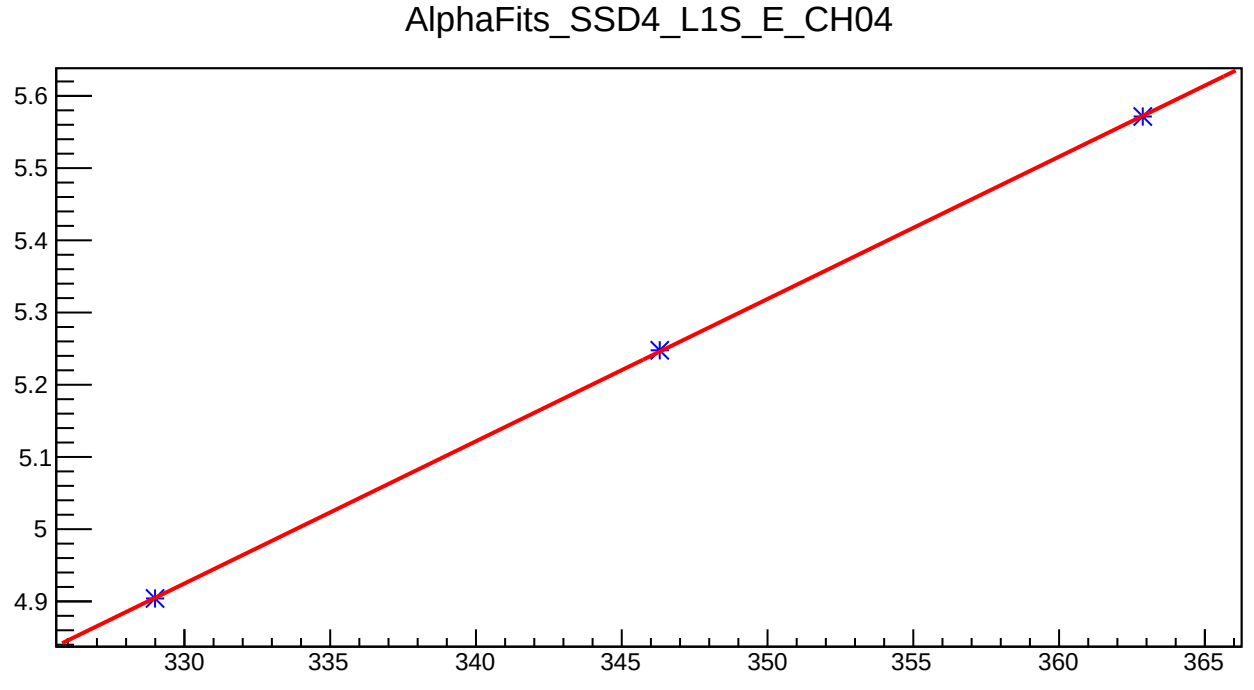
300
320
340
360
380
400


AlphaFits_SSD4_L1S_E_CH04
5.6
5.5
5.4
5.3
5.2
5.1
5
4.9
330
335
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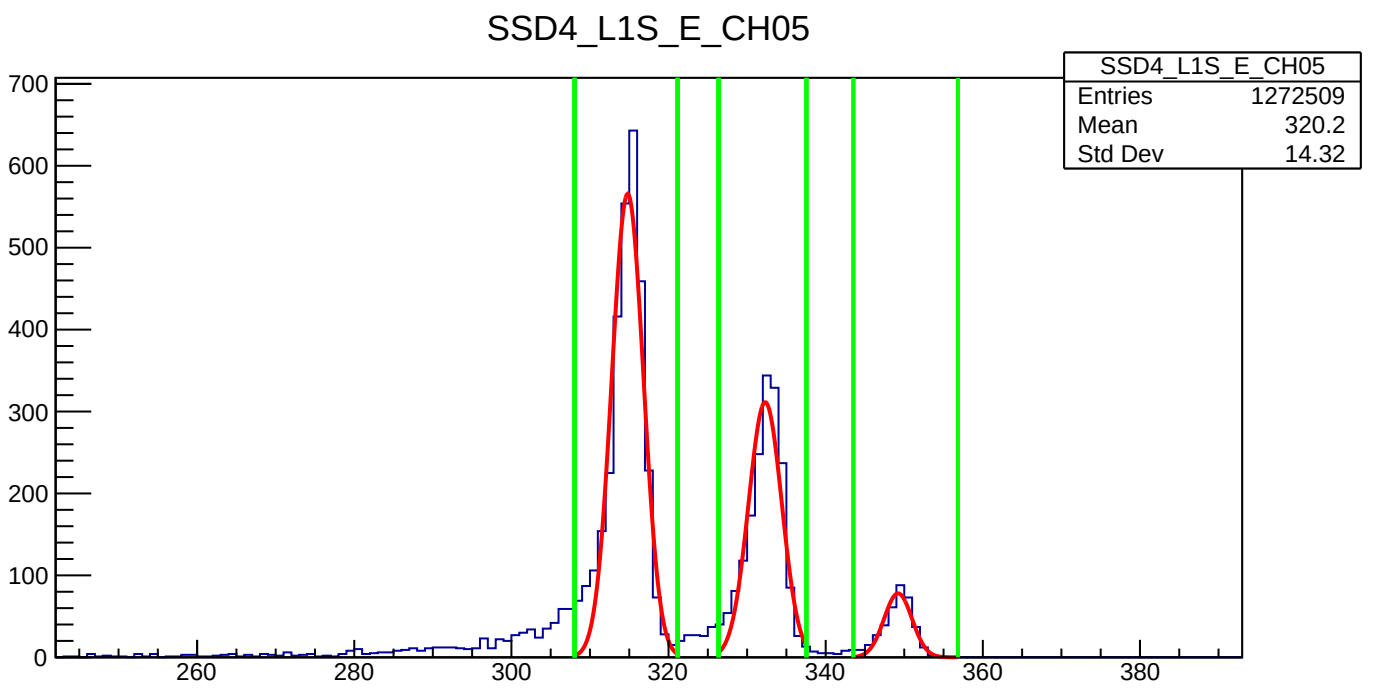
350
355
360
365


SSD4_L1S_E_CH05
SSD4_L1S_E_CH05
Entries 1272509
Mean 320.2
Std Dev 14.32
700
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100
0
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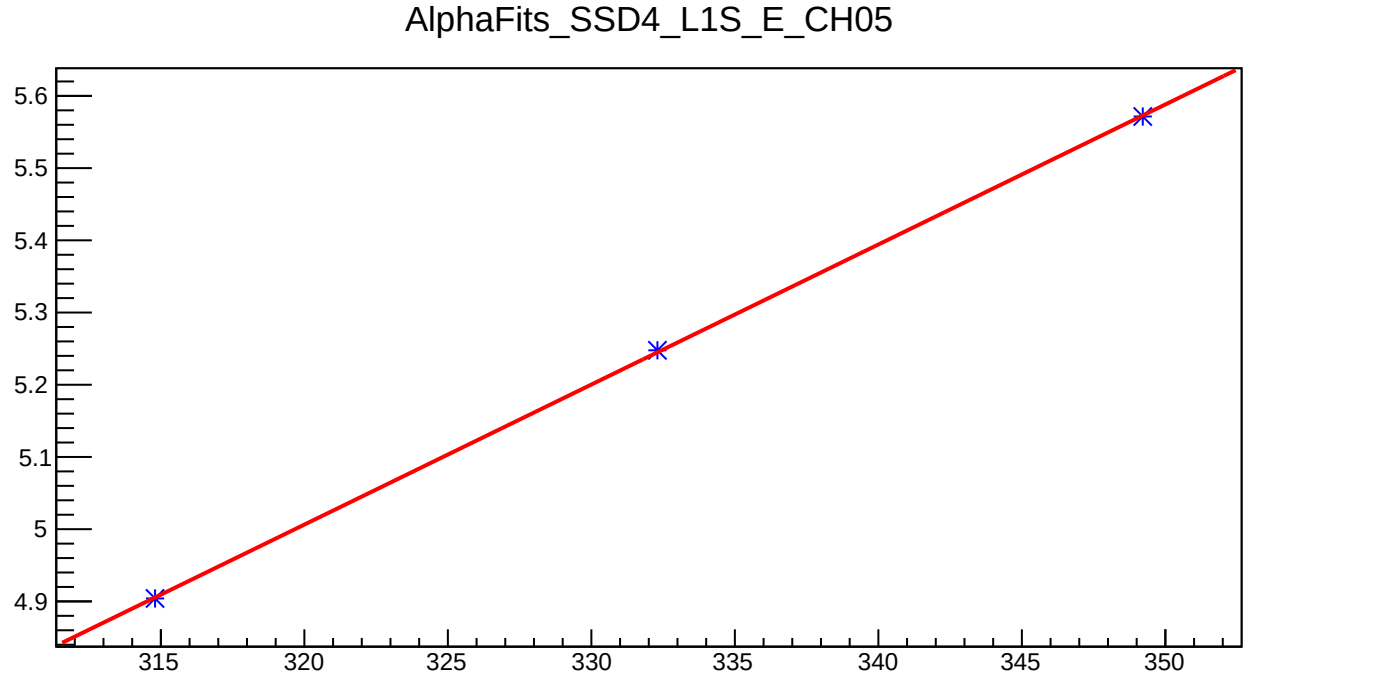
320
340
360
380


AlphaFits_SSD4_L1S_E_CH05
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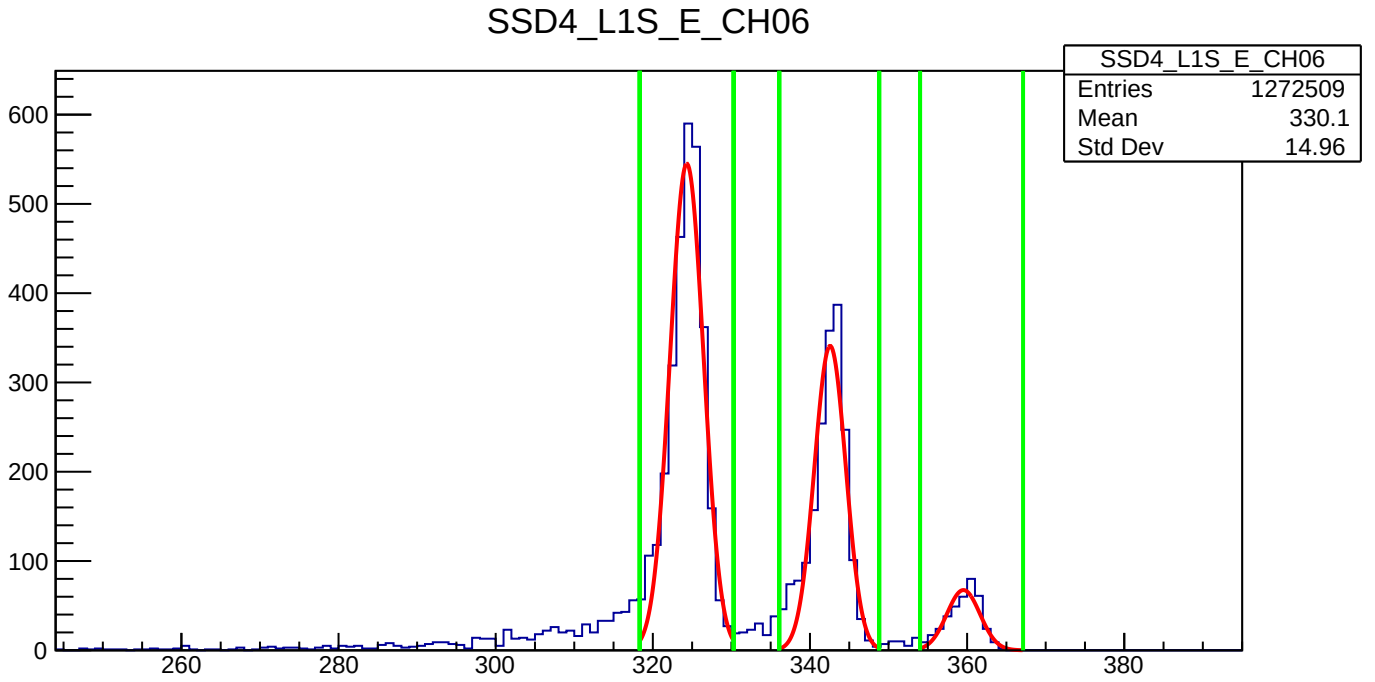
5.3
5.2
5.1
5
4.9
315
320
325
330
335
340
345
350


SSD4_L1S_E_CH06
SSD4_L1S_E_CH06
Entries 1272509
Mean 330.1
Std Dev 14.96
600
500
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200
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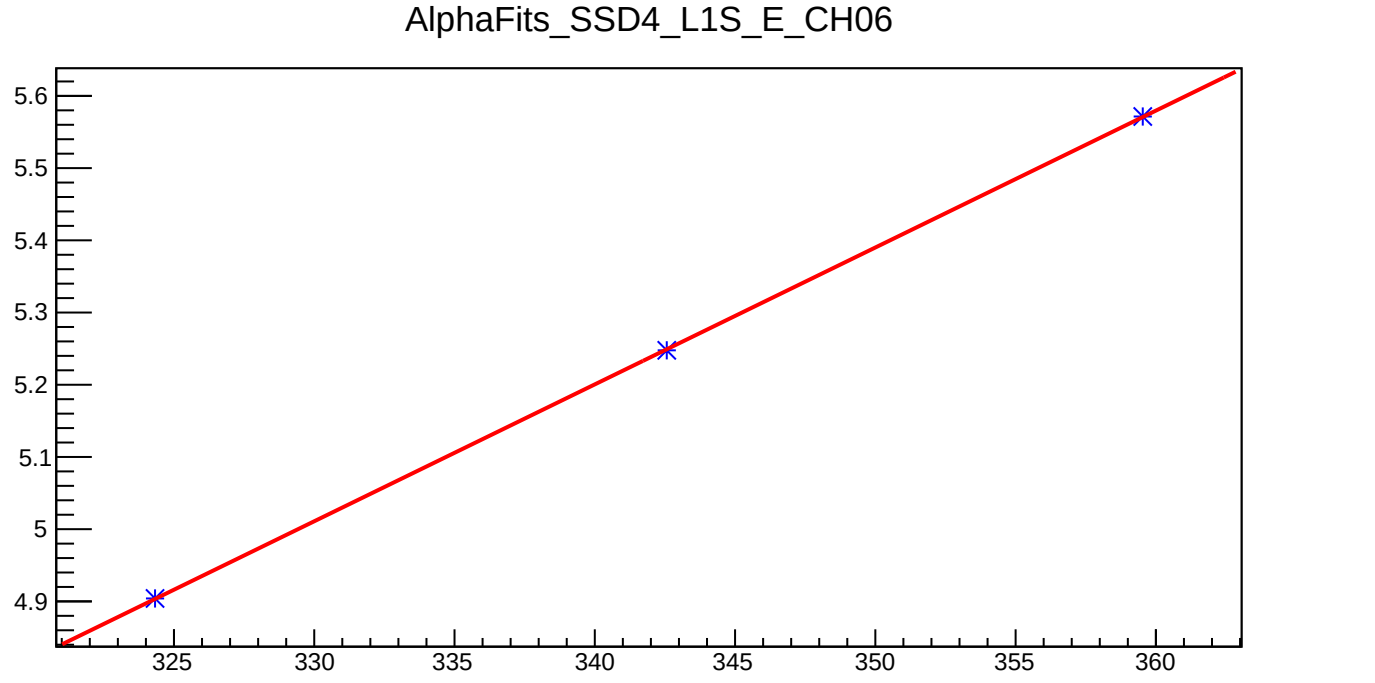
AlphaFits_SSD4_L1S_E_CH06
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5
4.9
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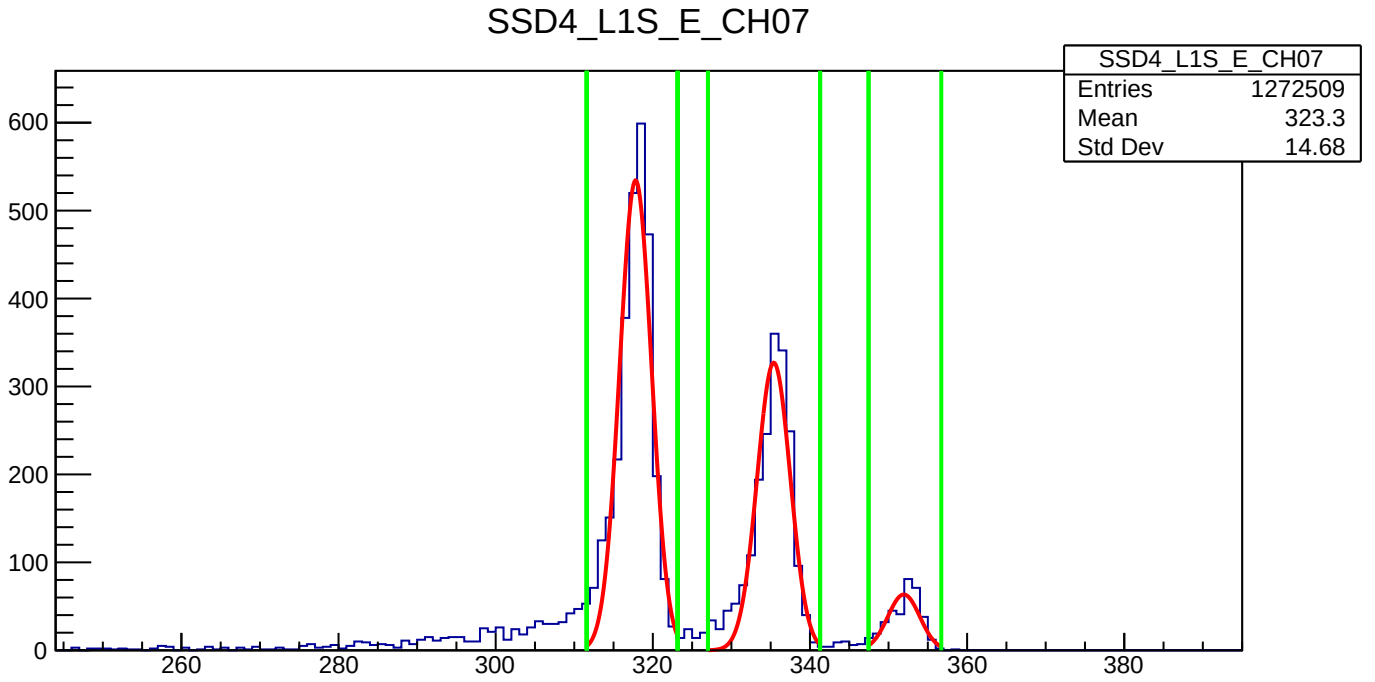
SSD4_L1S_E_CH07
SSD4_L1S_E_CH07
Entries 1272509
Mean 323.3
Std Dev 14.68
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0
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360
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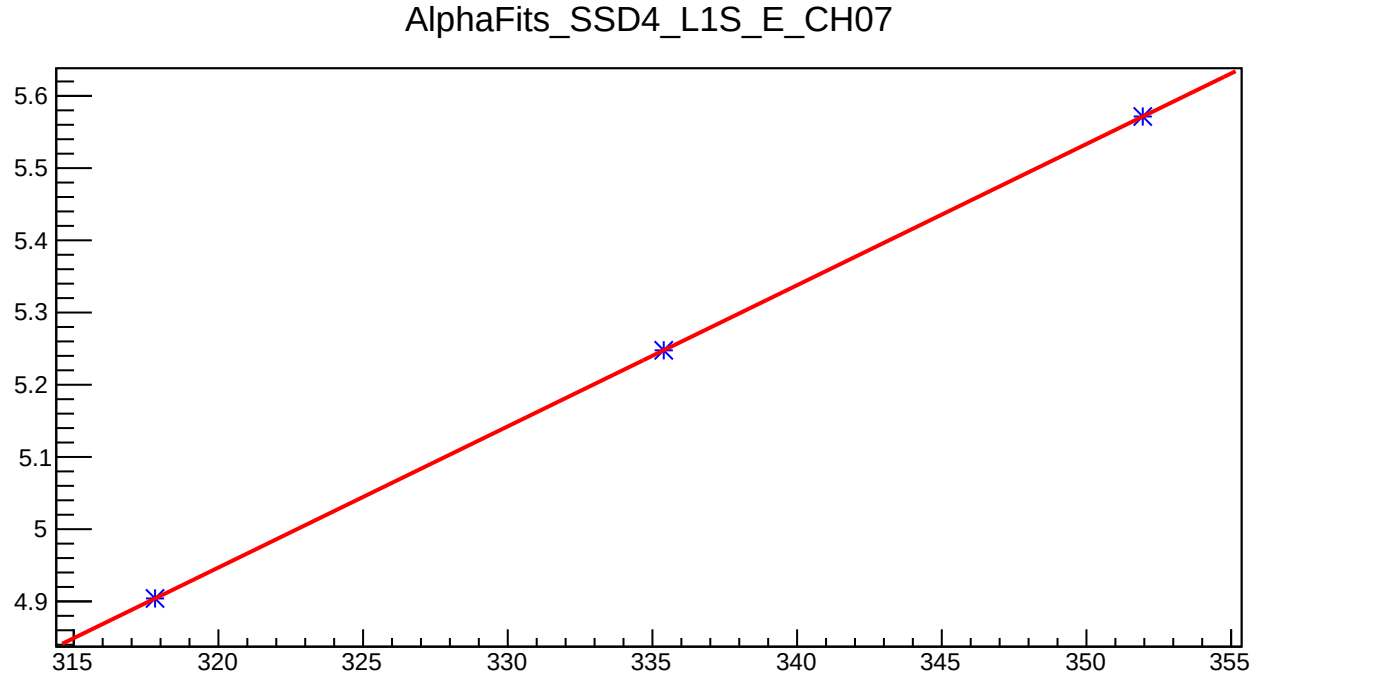
AlphaFits_SSD4_L1S_E_CH07
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5.3
5.2
5.1
5
4.9
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340
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350
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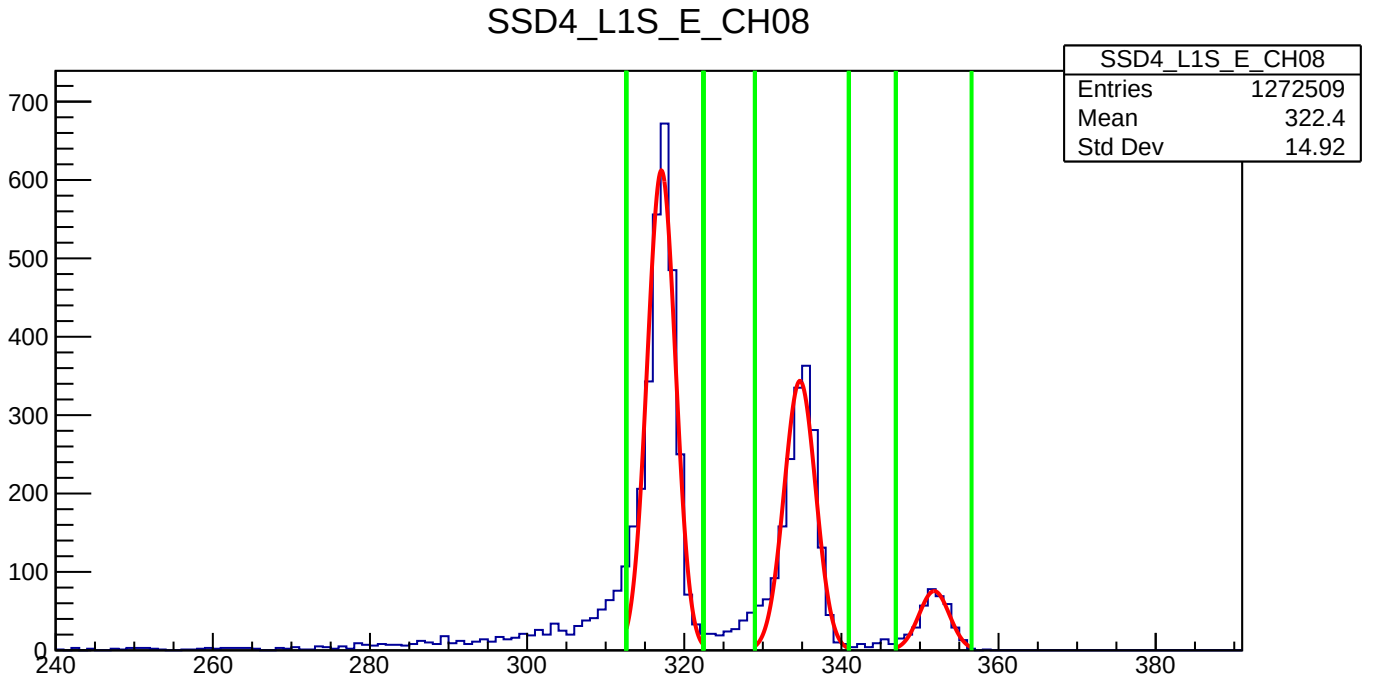
SSD4_L1S_E_CH08
SSD4_L1S_E_CH08
Entries 1272509
Mean 322.4
Std Dev 14.92
700
600
500
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300
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100
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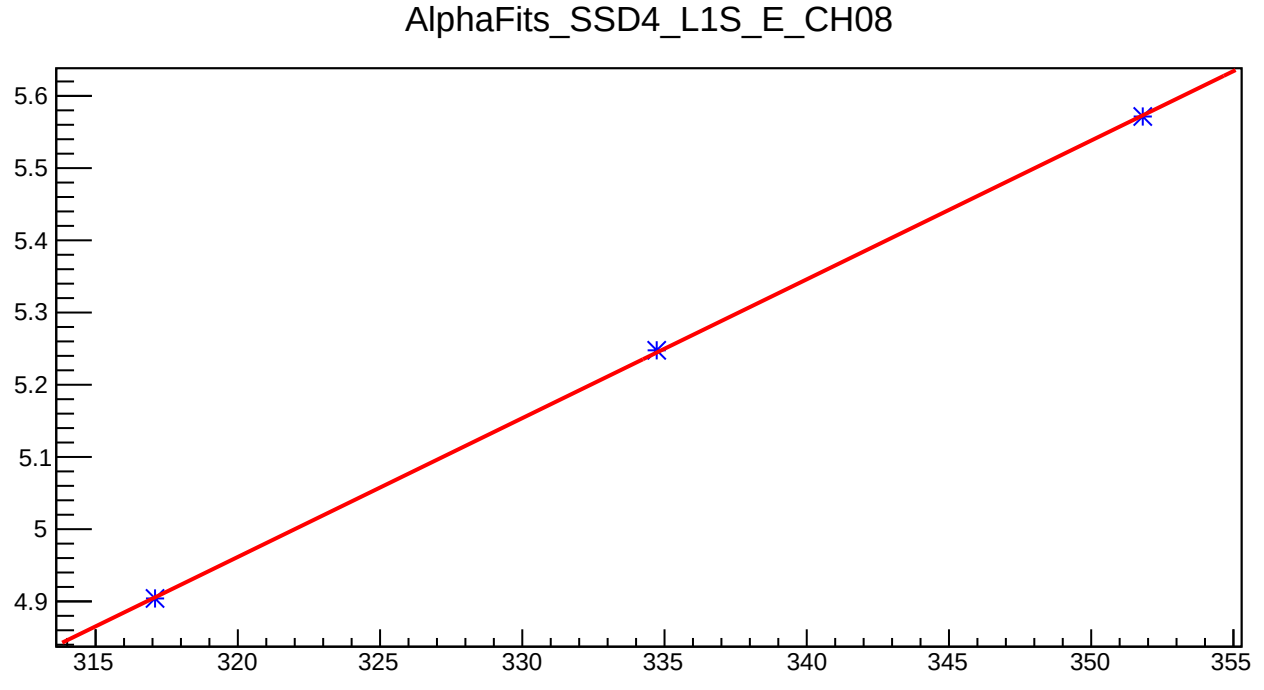
320
340
360
380


AlphaFits_SSD4_L1S_E_CH08
5.6
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5.4
5.3
5.2
5.1
5
4.9
315
320
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340
345
350
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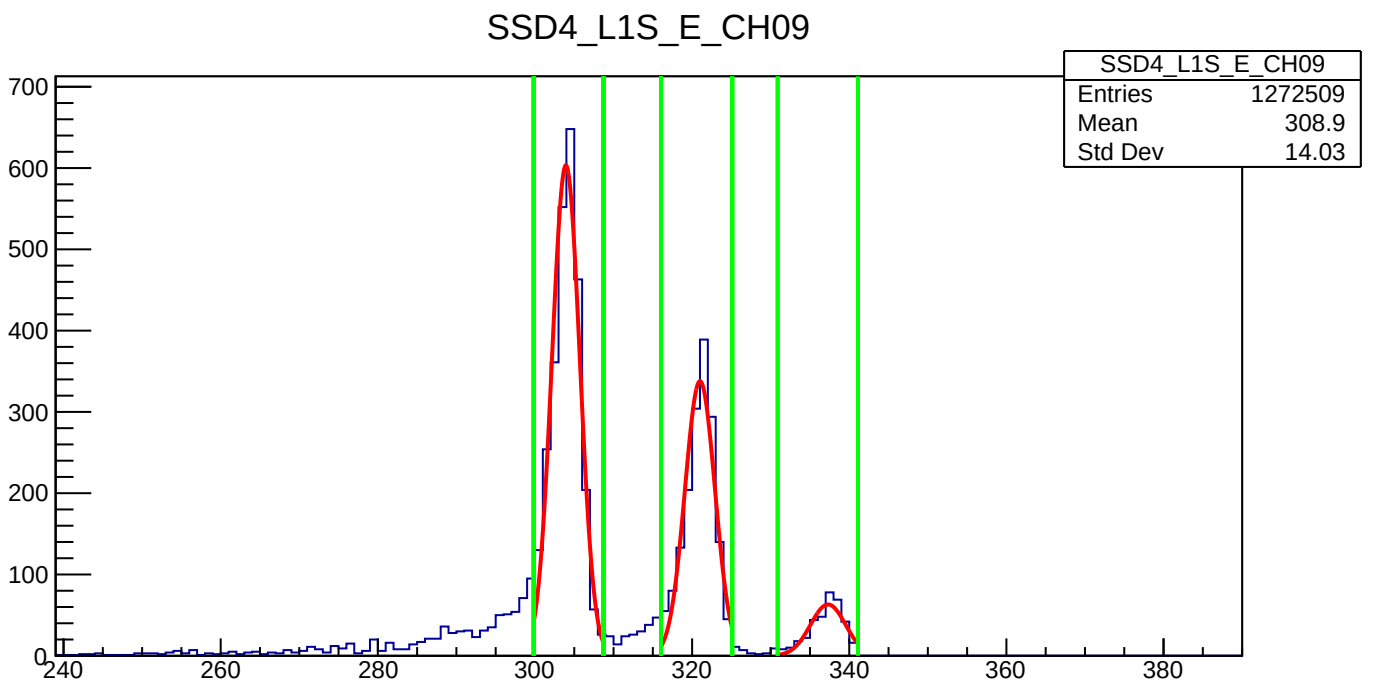
SSD4_L1S_E_CH09
SSD4_L1S_E_CH09
Entries 1272509
Mean 308.9
Std Dev 14.03
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600
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400
300
200
100
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240
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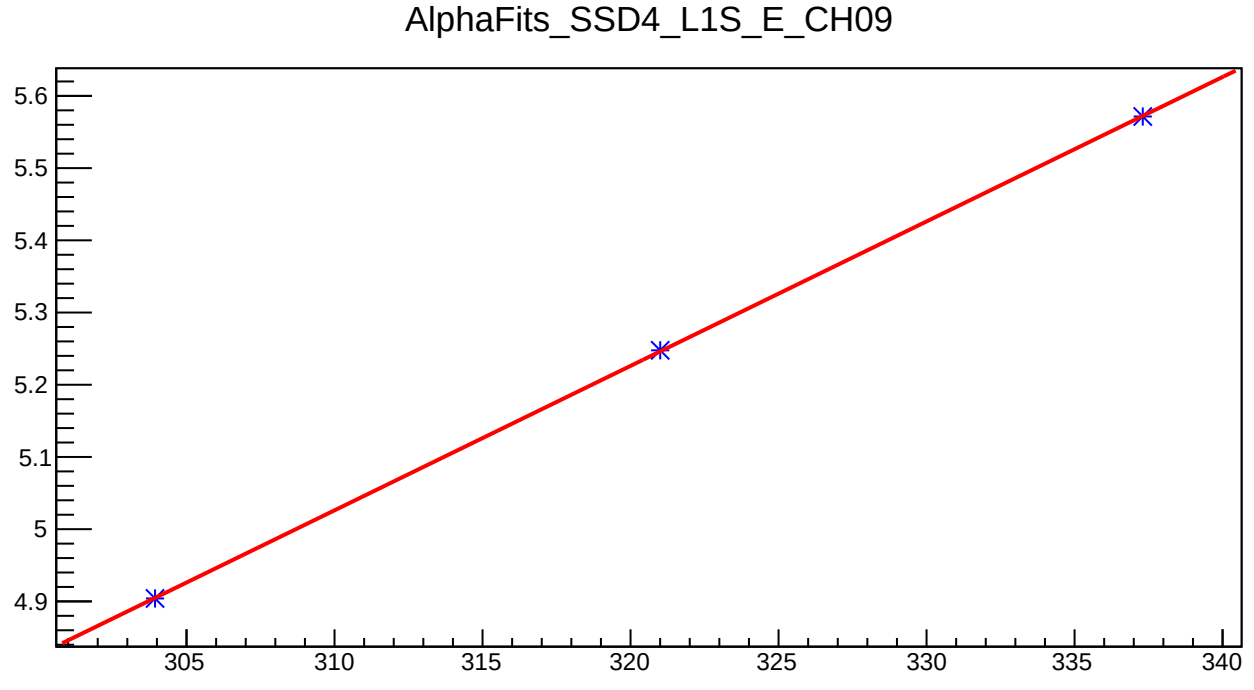
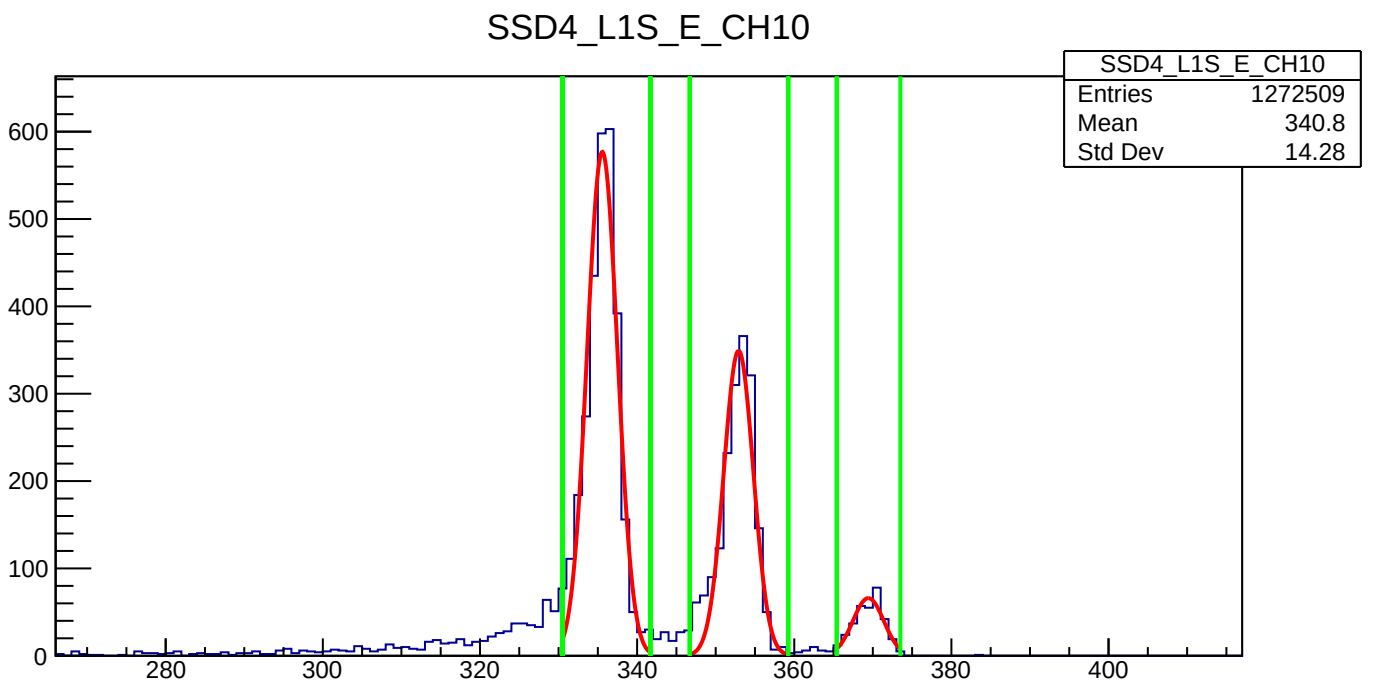
320
340
360
380


AlphaFits_SSD4_L1S_E_CH09
5.6
5.5
5.4
5.3
5.2
5.1
5
4.9
305
310
315
320
325
330
335
340


SSD4_L1S_E_CH10
SSD4_L1S_E_CH10
Entries 1272509
Mean 340.8
Std Dev 14.28
600
500
400
300
200
100
0
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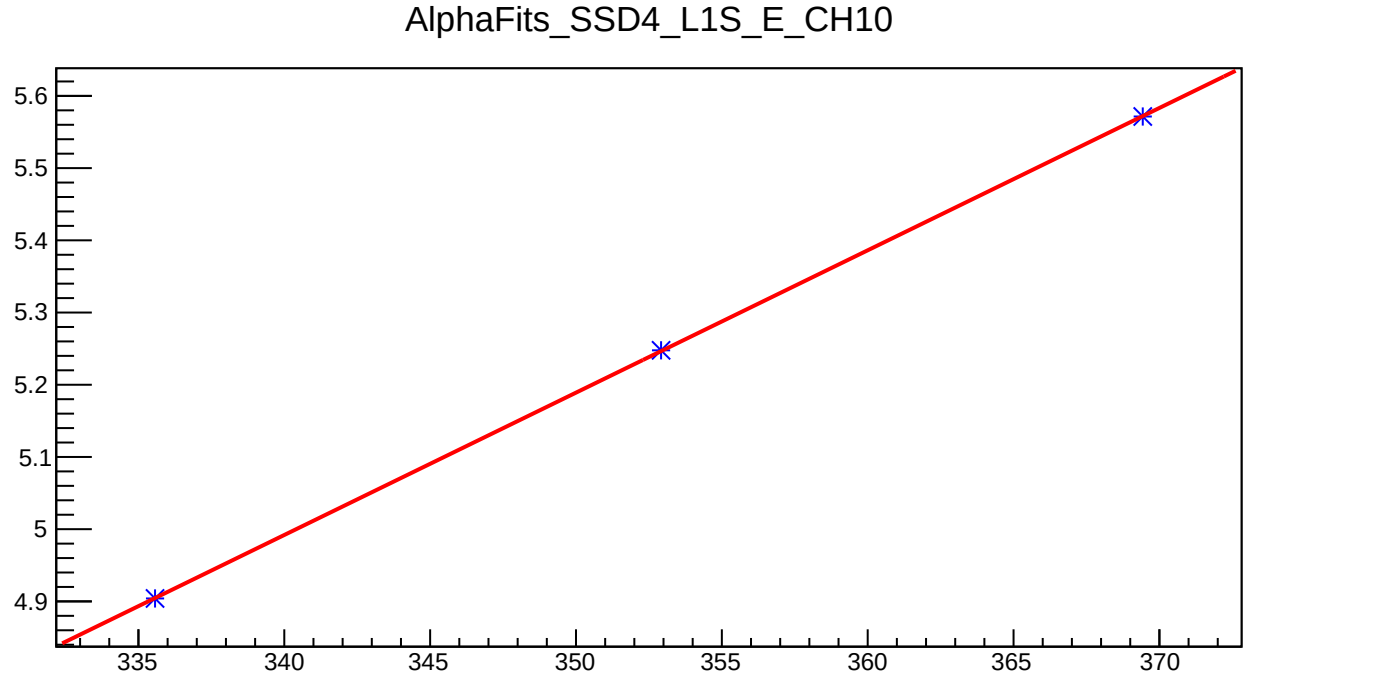
300
320
340
360
380
400


AlphaFits_SSD4_L1S_E_CH10
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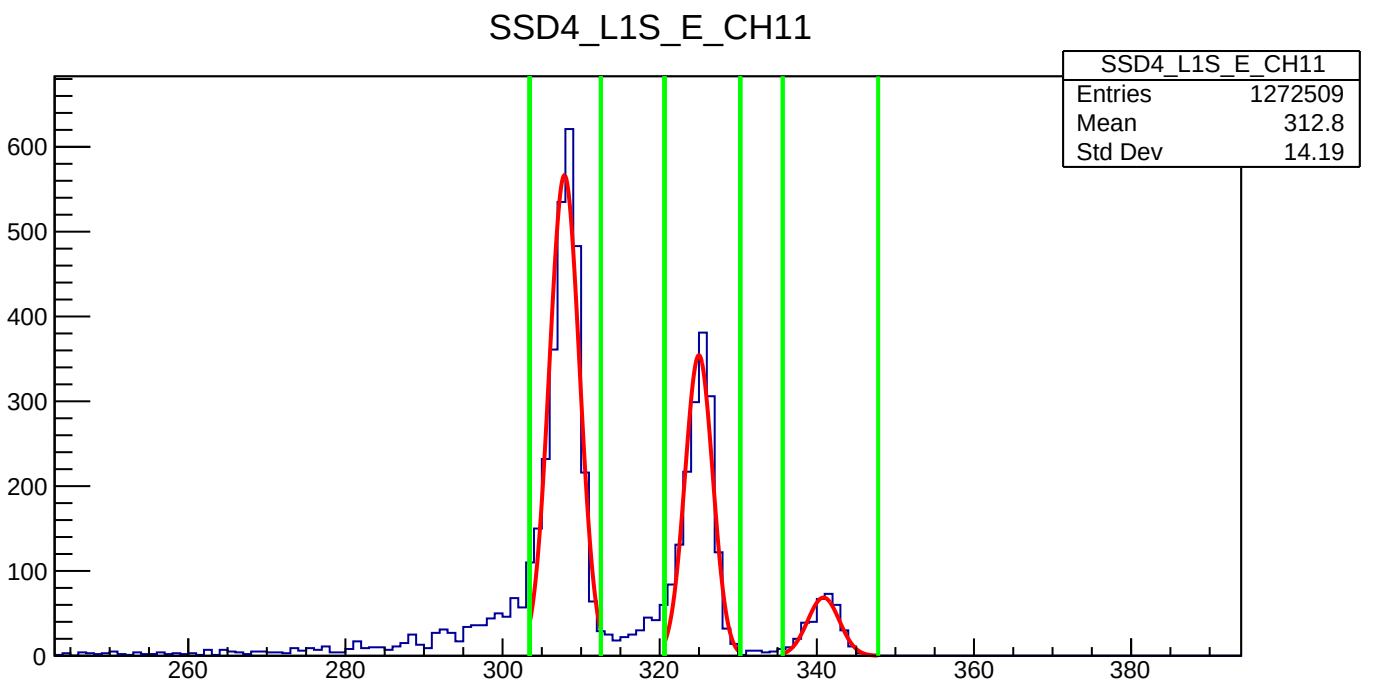
5.5
5.4
5.3
5.2
5.1
5
4.9
335
340
345
350
355
360
365
370


SSD4_L1S_E_CH11
SSD4_L1S_E_CH11
Entries 1272509
Mean 312.8
Std Dev 14.19
600
500
400
300
200
100
0
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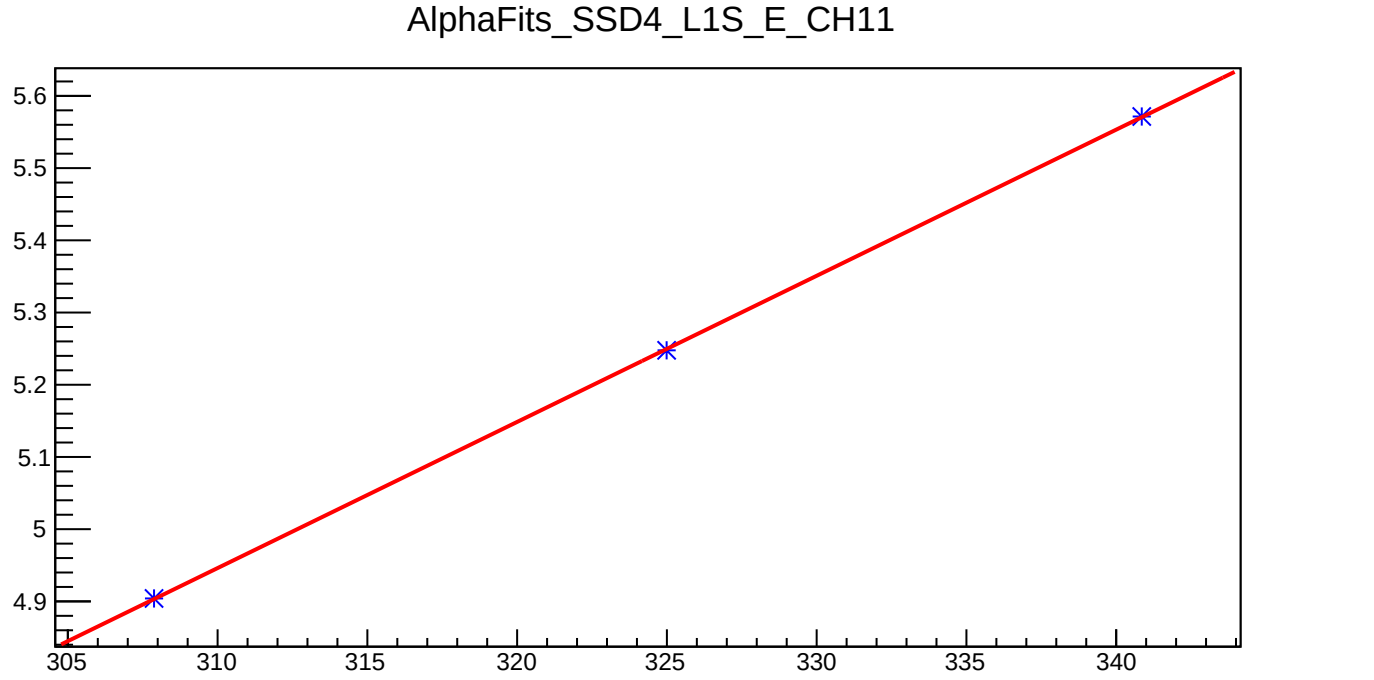
280
300
320
340
360
380


AlphaFits_SSD4_L1S_E_CH11
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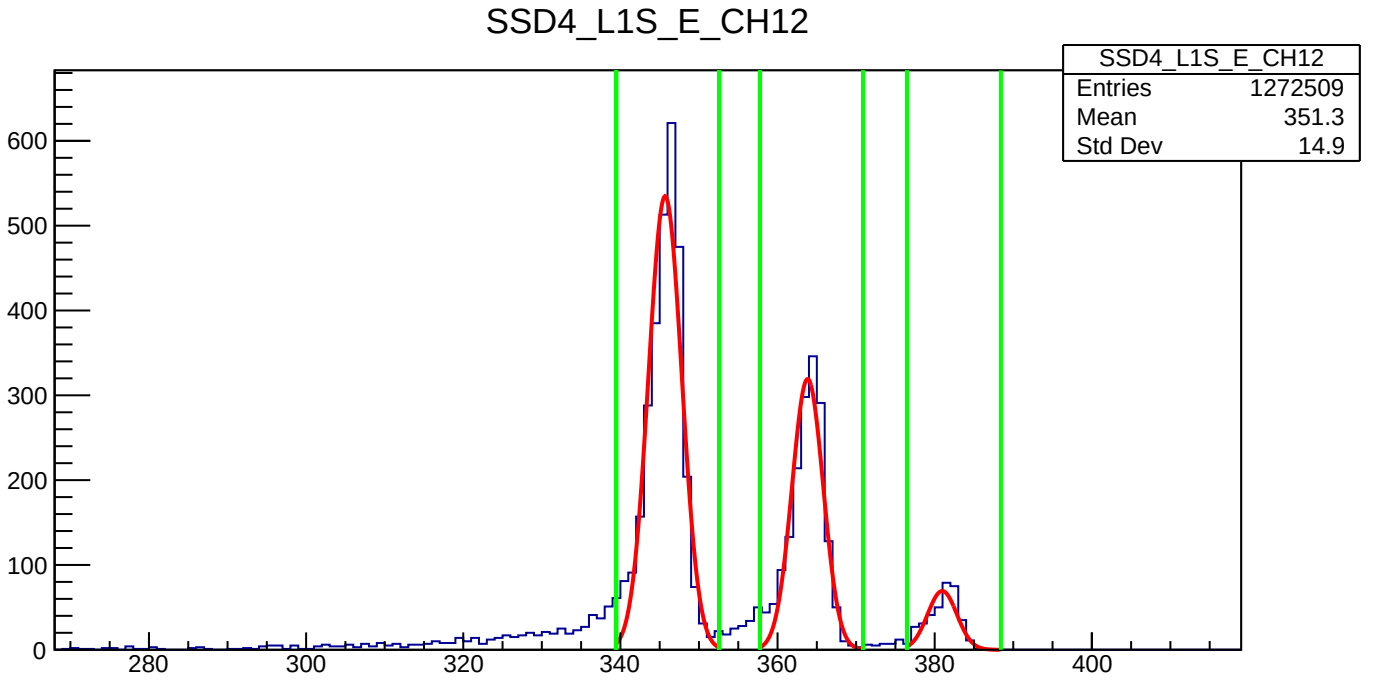
5.5
5.4
5.3
5.2
5.1
5
4.9
305
310
315
320
325
330
335
340


SSD4_L1S_E_CH12
SSD4_L1S_E_CH12
Entries 1272509
Mean 351.3
Std Dev 14.9
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0
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300
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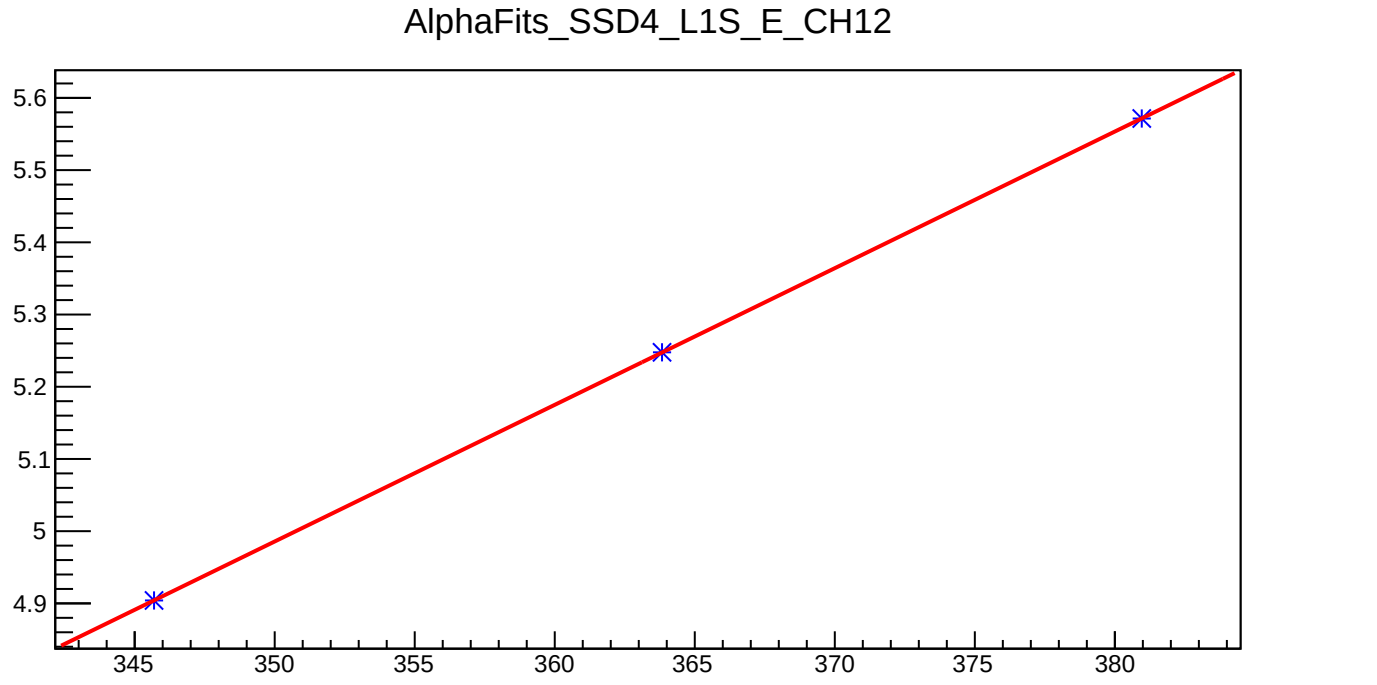
340
360
380
400


AlphaFits_SSD4_L1S_E_CH12
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5.4
5.3
5.2
5.1
5
4.9
345
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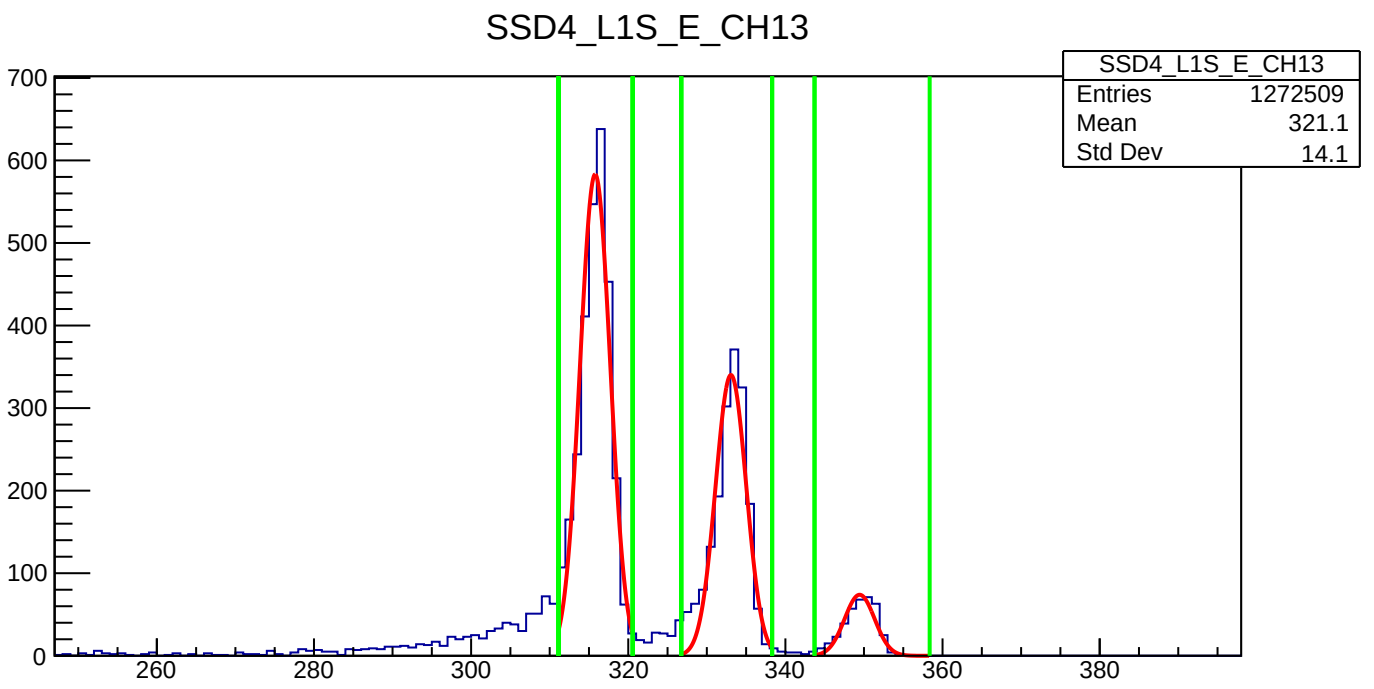
365
370
375
380


SSD4_L1S_E_CH13
SSD4_L1S_E_CH13
Entries 1272509
Mean 321.1
Std Dev 14.1
700
600
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400
300
200
100
0
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280
300
320
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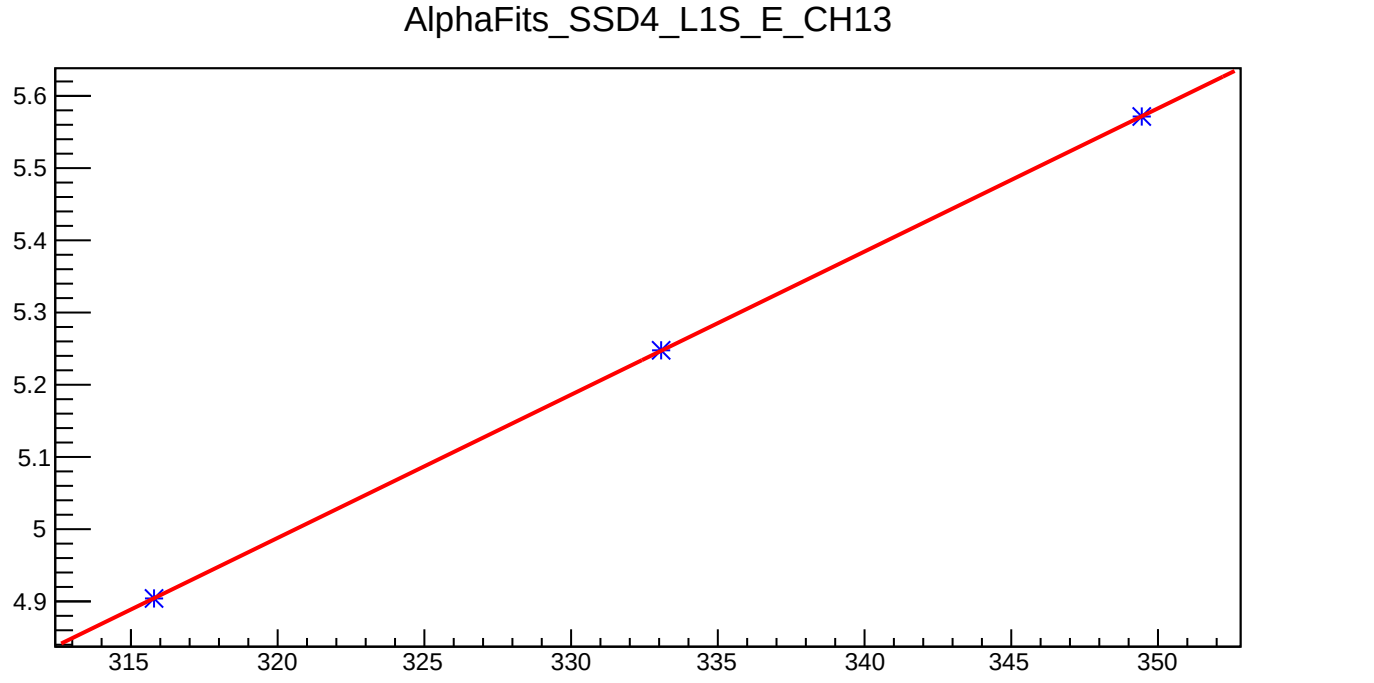
360
380


AlphaFits_SSD4_L1S_E_CH13
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5.4
5.3
5.2
5.1
5
4.9
315
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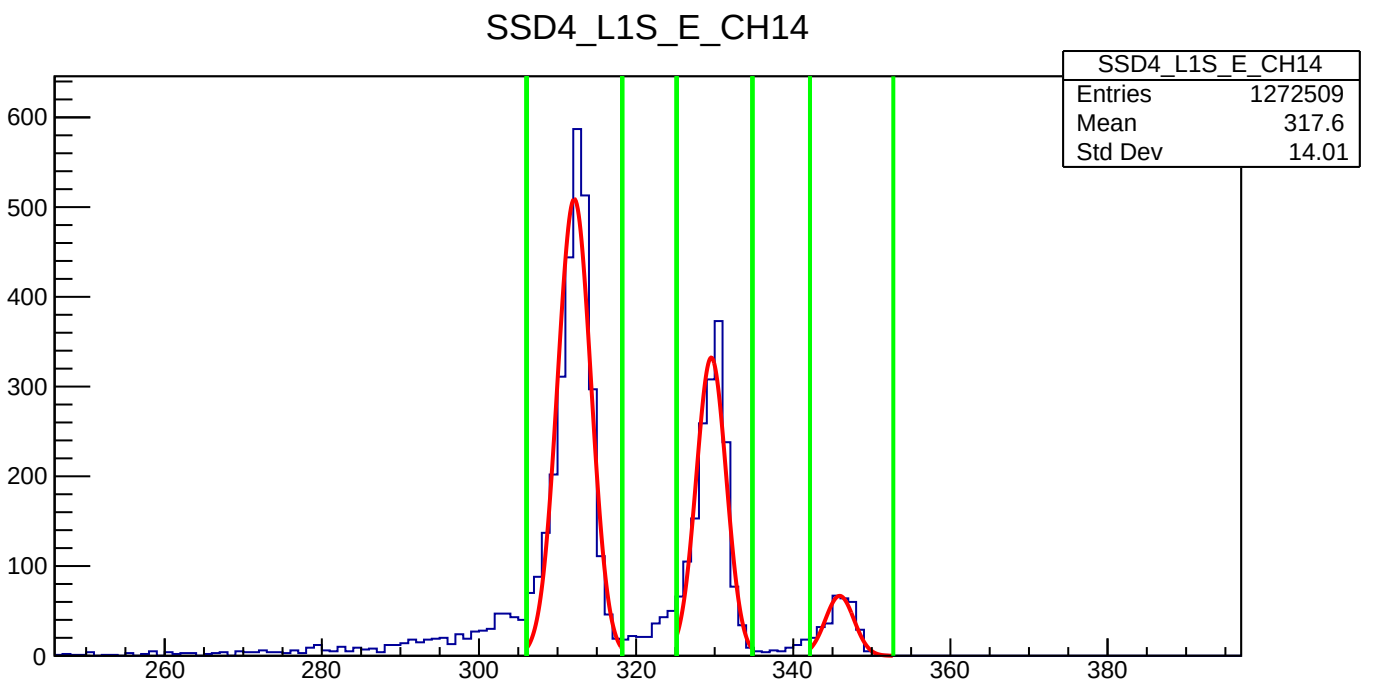
340
345
350


SSD4_L1S_E_CH14
SSD4_L1S_E_CH14
Entries 1272509
Mean 317.6
Std Dev 14.01
600
500
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200
100
0
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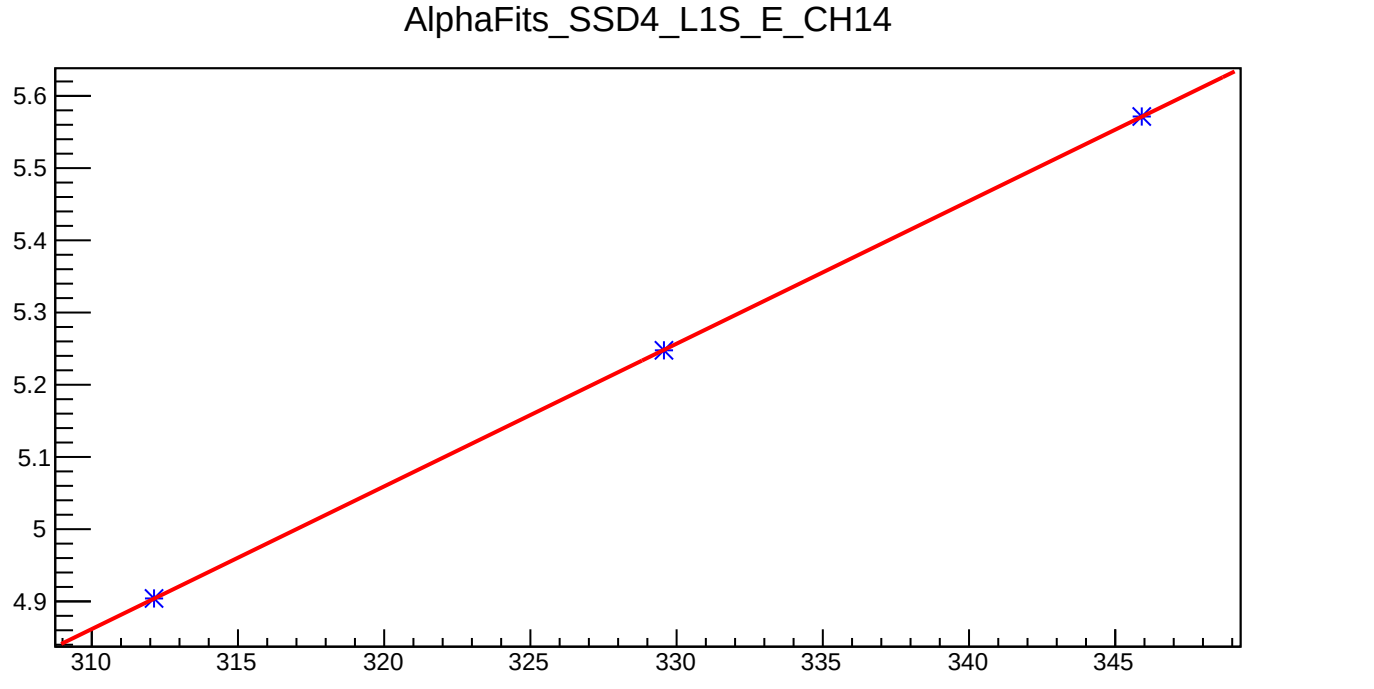
280
300
320
340
360
380


AlphaFits_SSD4_L1S_E_CH14
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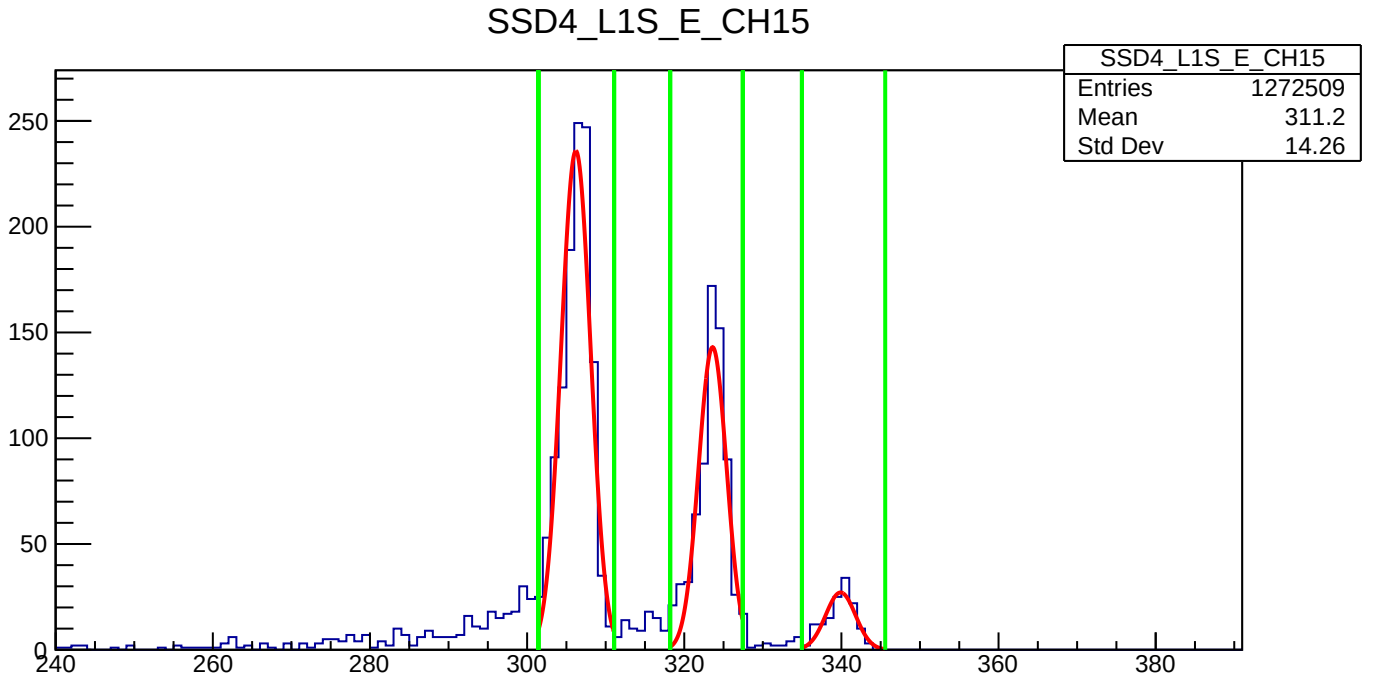
5.5
5.4
5.3
5.2
5.1
5
4.9
310
315
320
325
330
335
340
345


SSD4_L1S_E_CH15
SSD4_L1S_E_CH15
Entries 1272509
Mean 311.2
Std Dev 14.26
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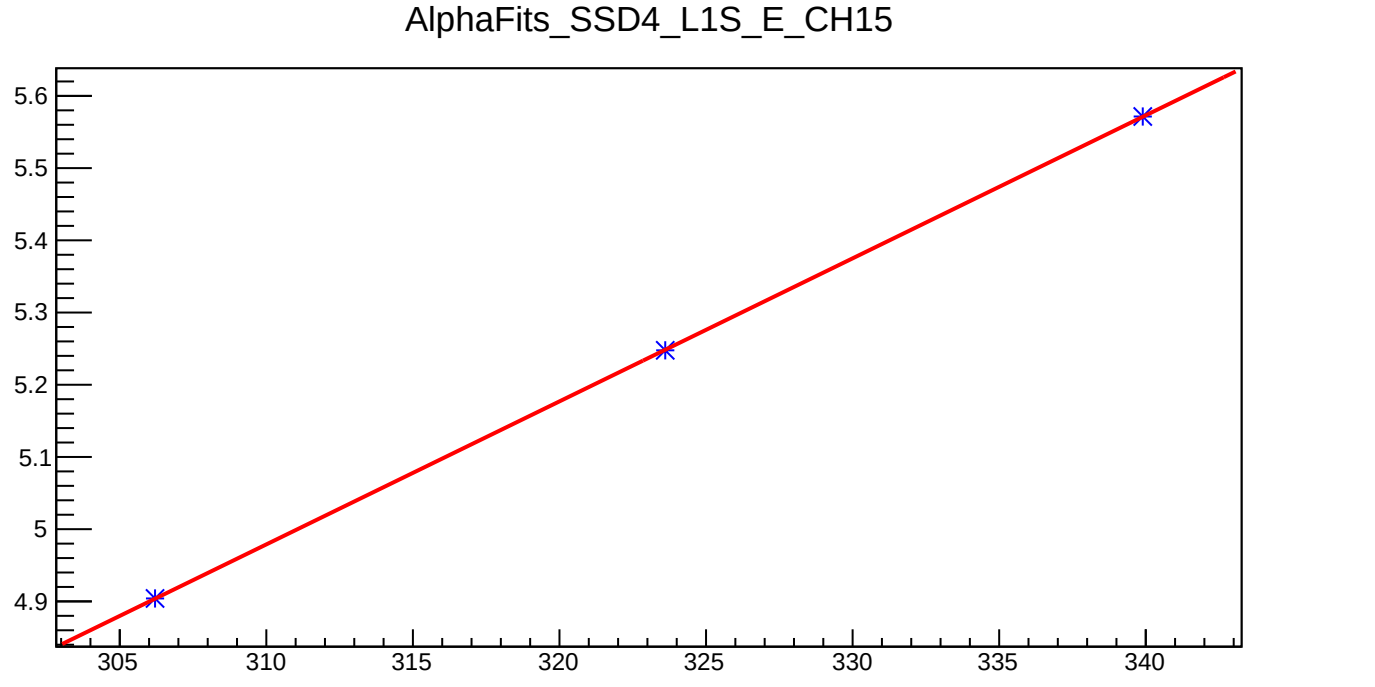
200
150
100
50
0
240
260
280
300
320
340
360
380


AlphaFits_SSD4_L1S_E_CH15
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5.5
5.4
5.3
5.2
5.1
5
4.9
305
310
315
320
325
330
335
340