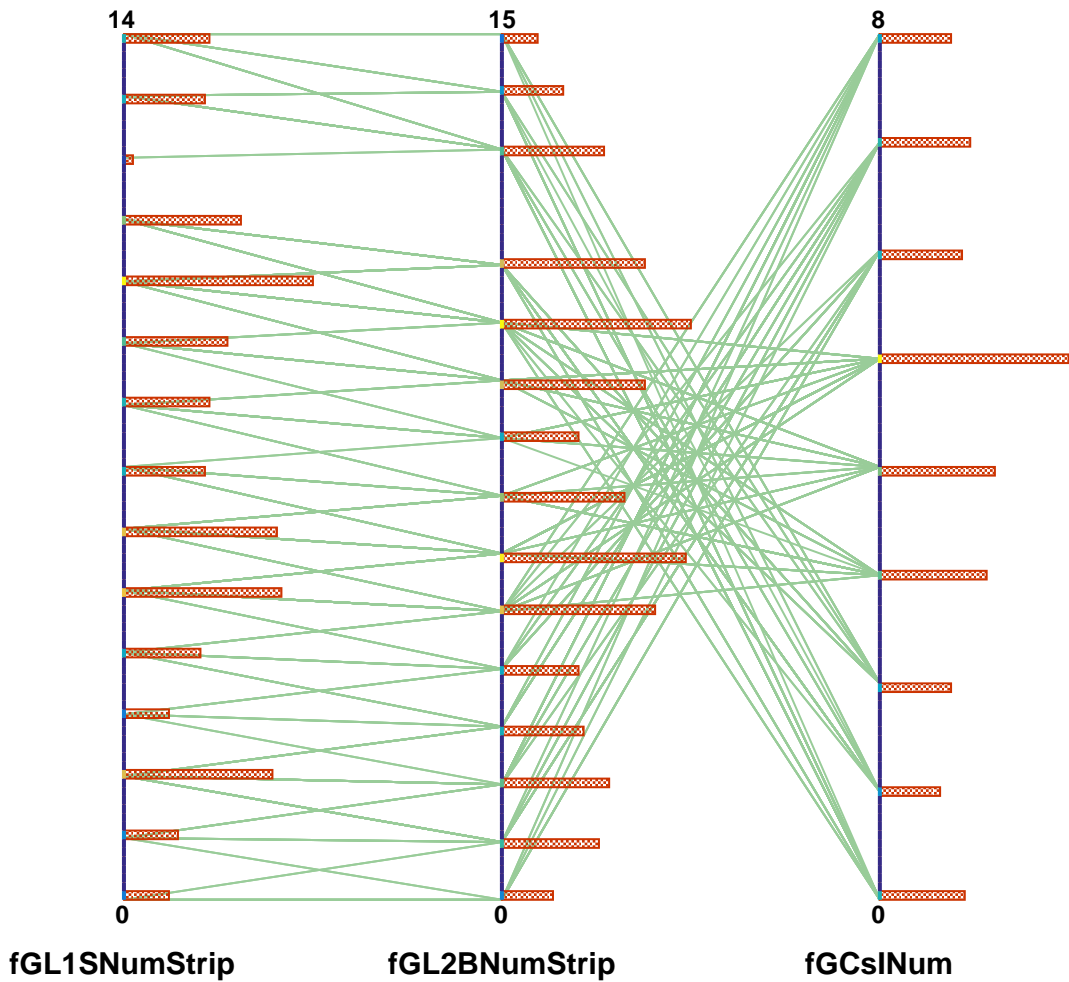
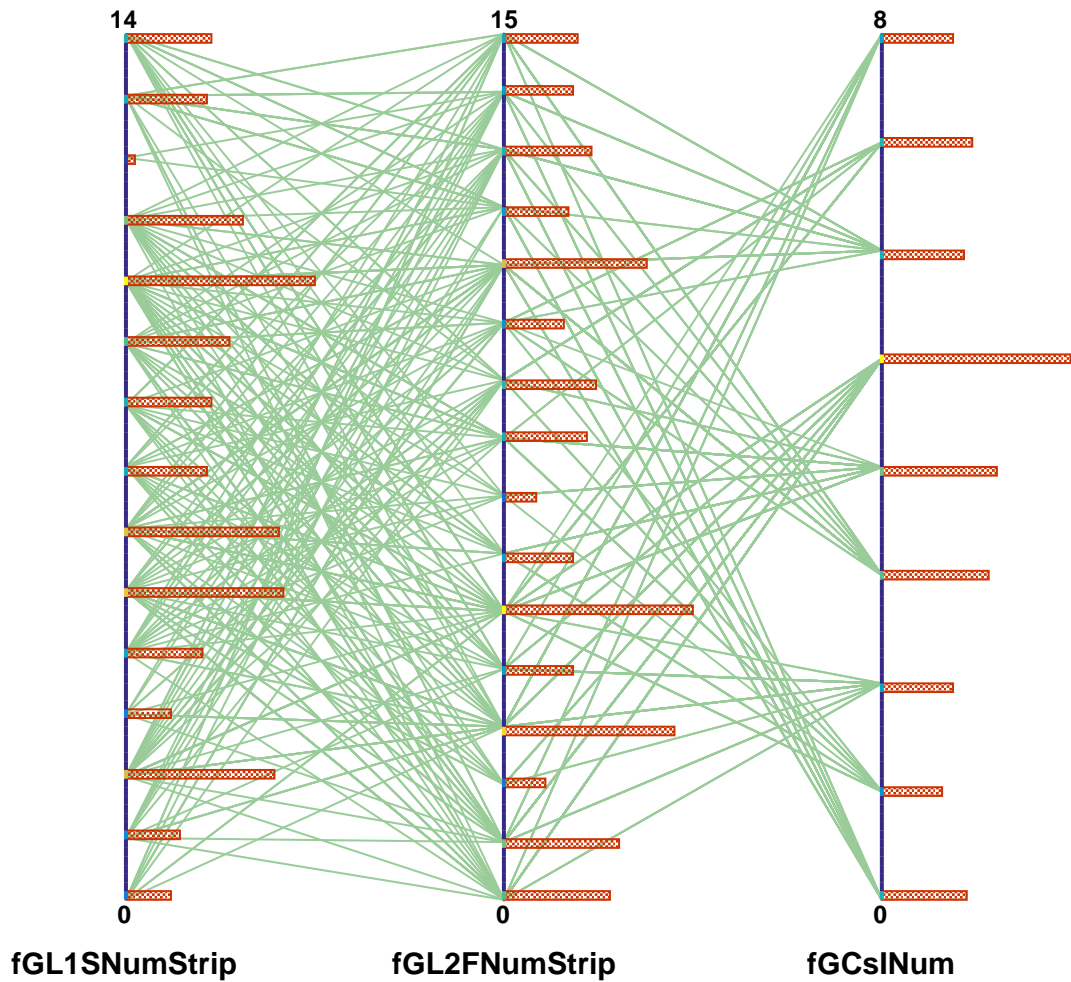


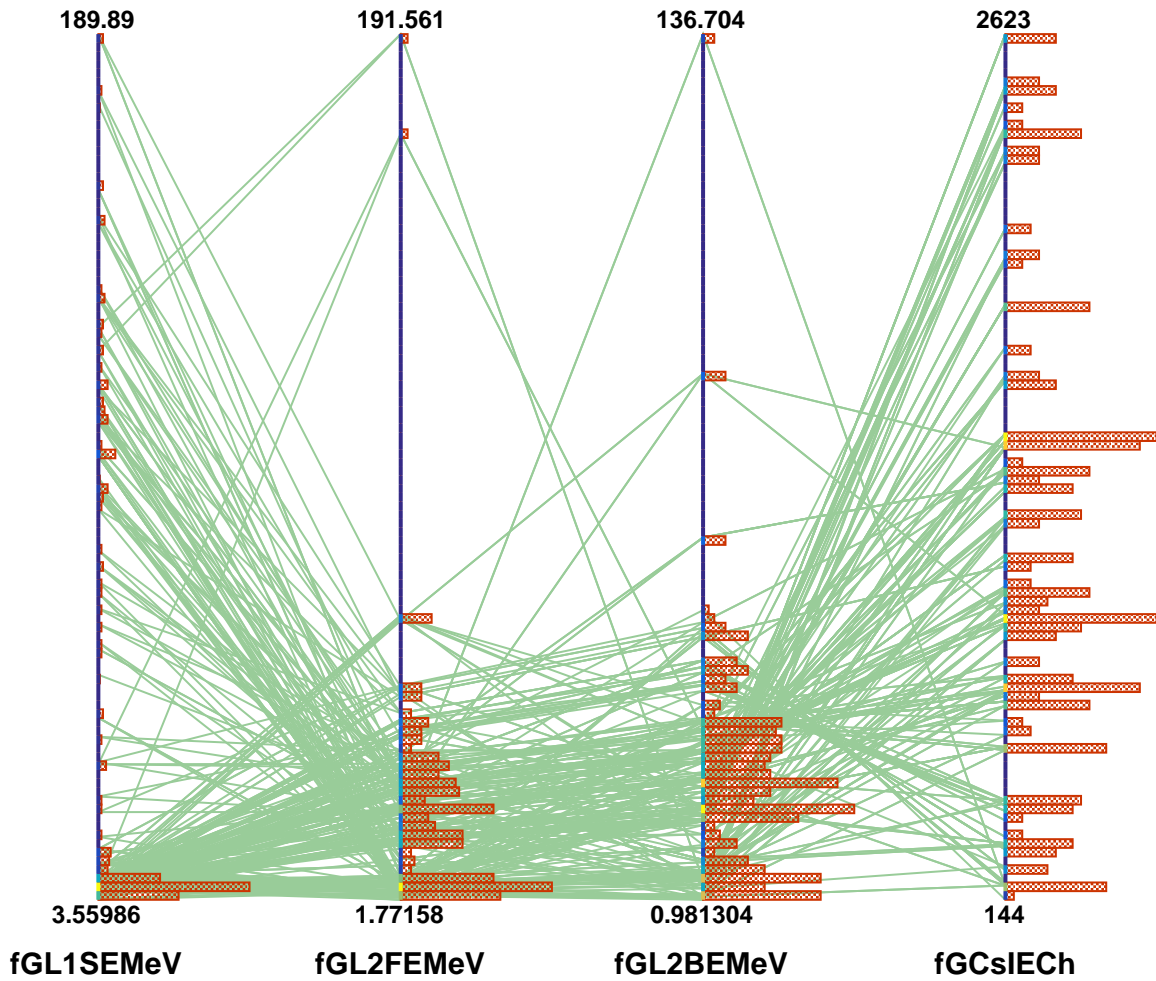
fGL1SNumStrip:fGL2BNumStrip:fGCsINum(fGSSDNum==0 && fSSDGlobalMulti[0]==6)

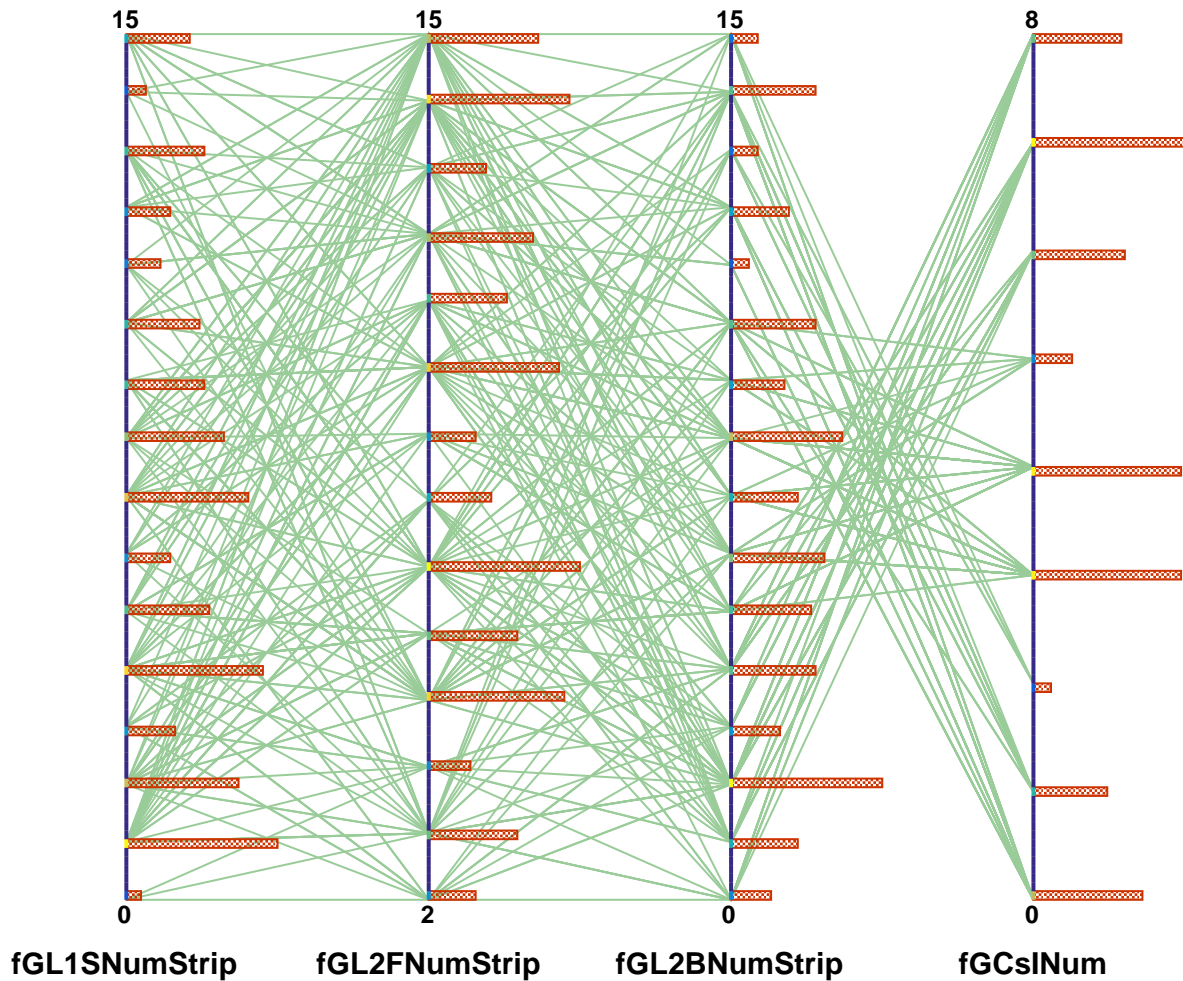


fGL1SNumStrip:fGL2FNumStrip:fGCsINum(fGSSDNum=0 && fSSDGlobalMult[0]==6)

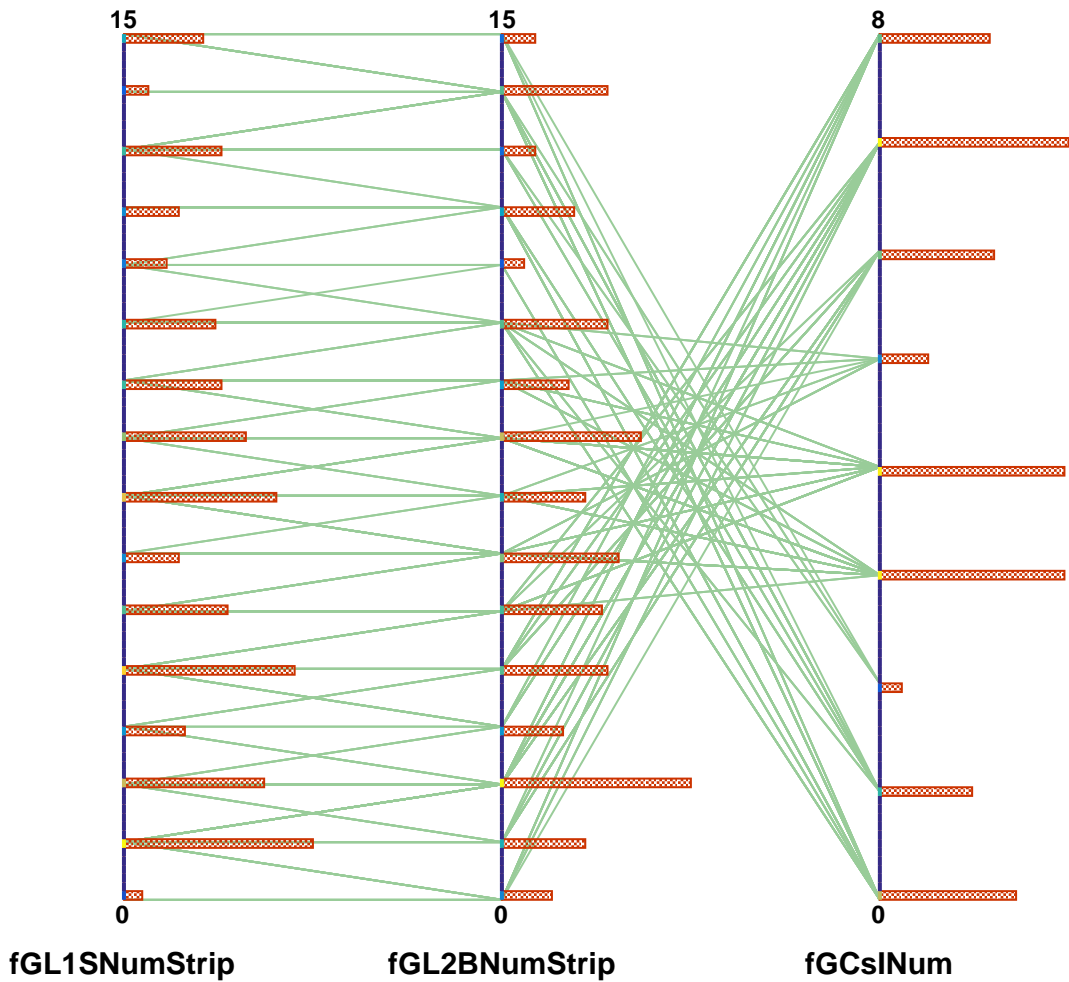


fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum==0 && fSSDGlobalMulti[0]==6)



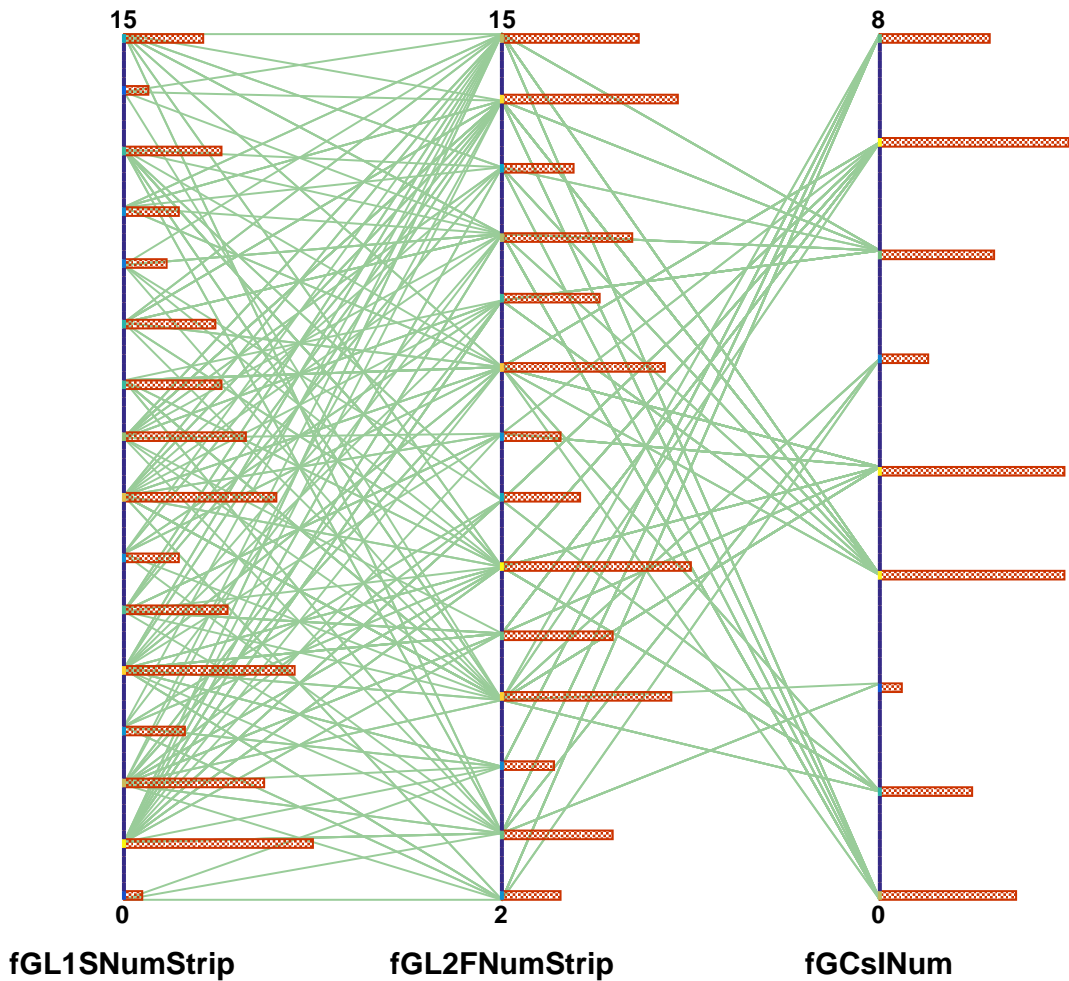


fGL1SNumStrip:fGL2BNumStrip:fGCsINum(fGSSDNum==1 && fSSDGlobalMult[1]==6)

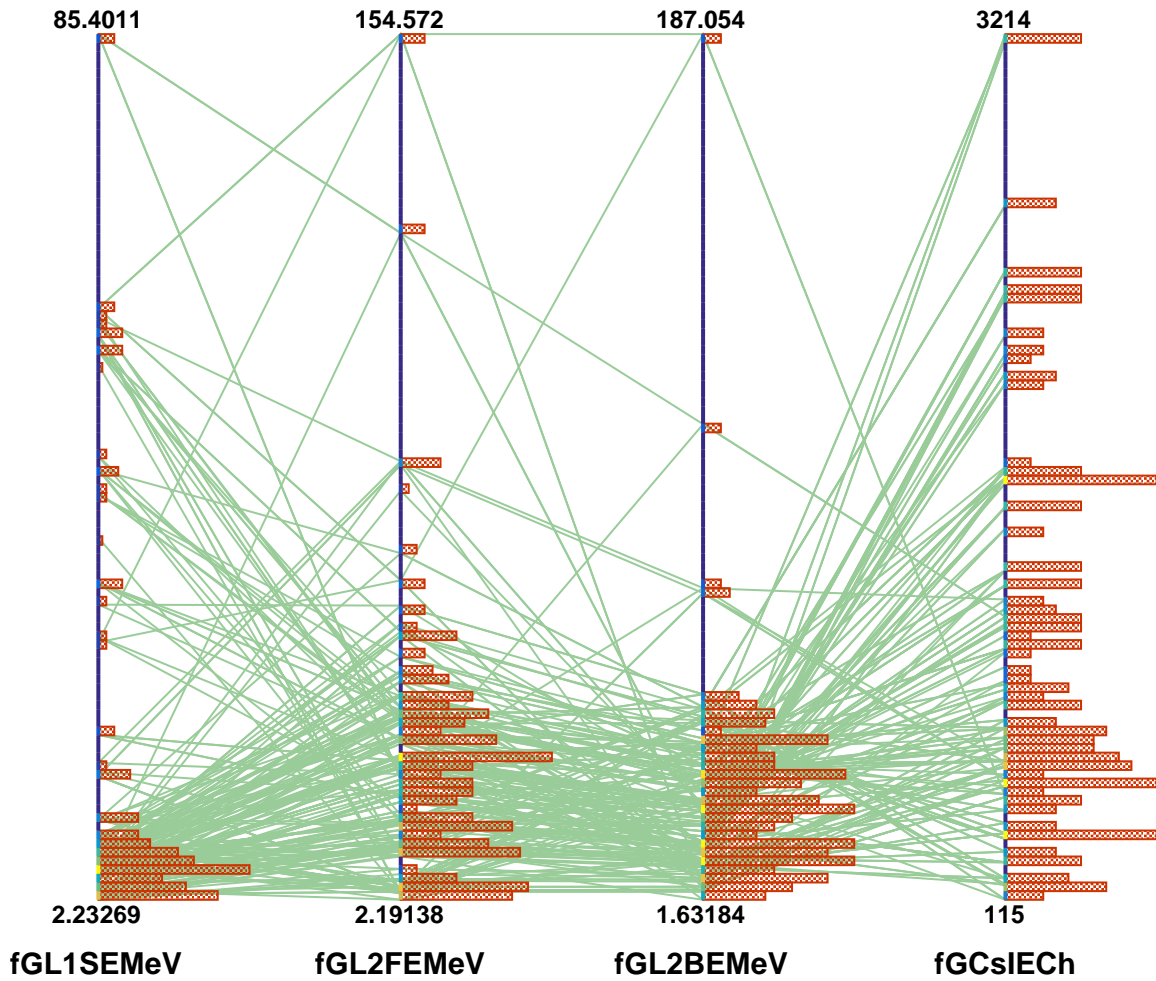




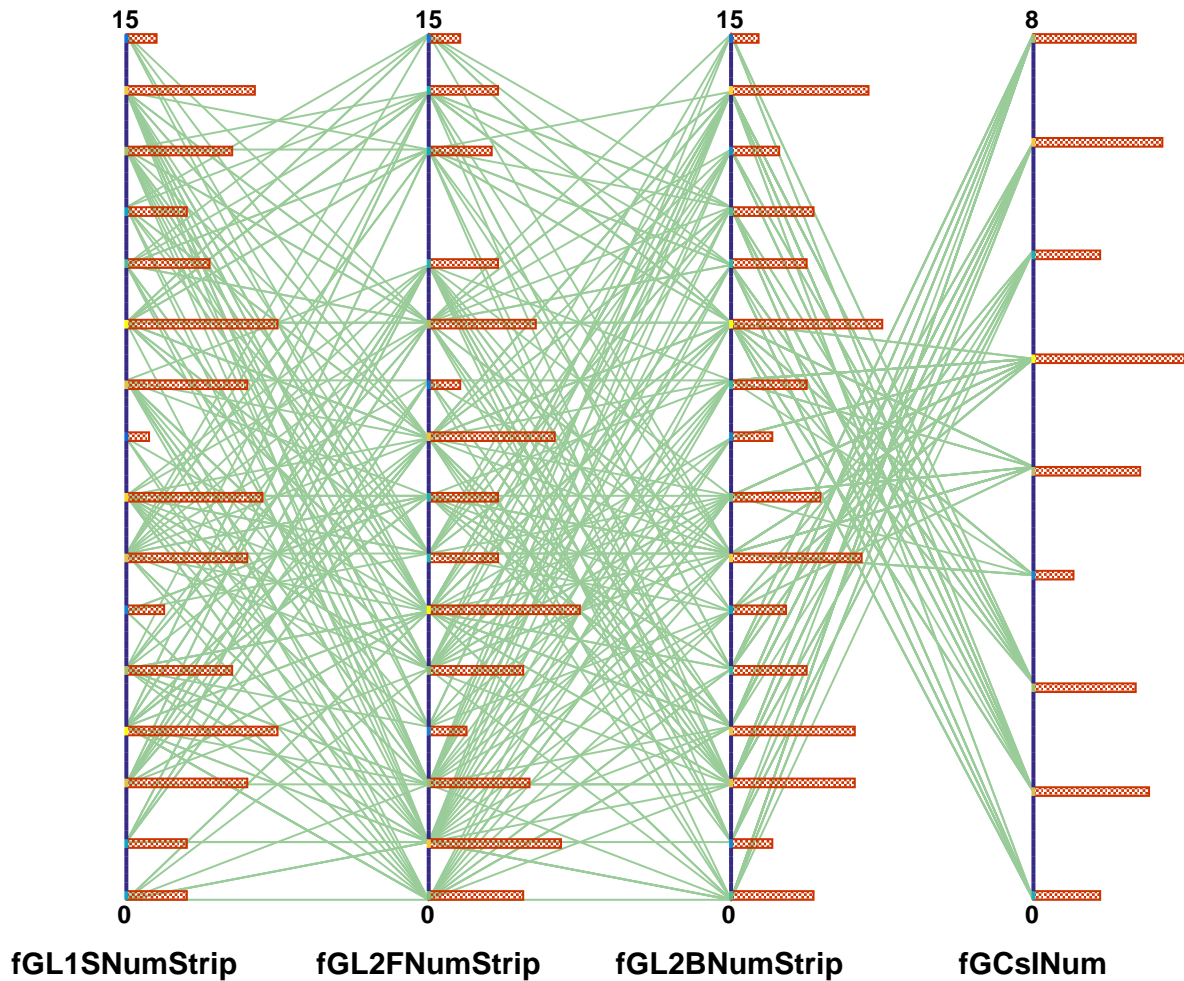
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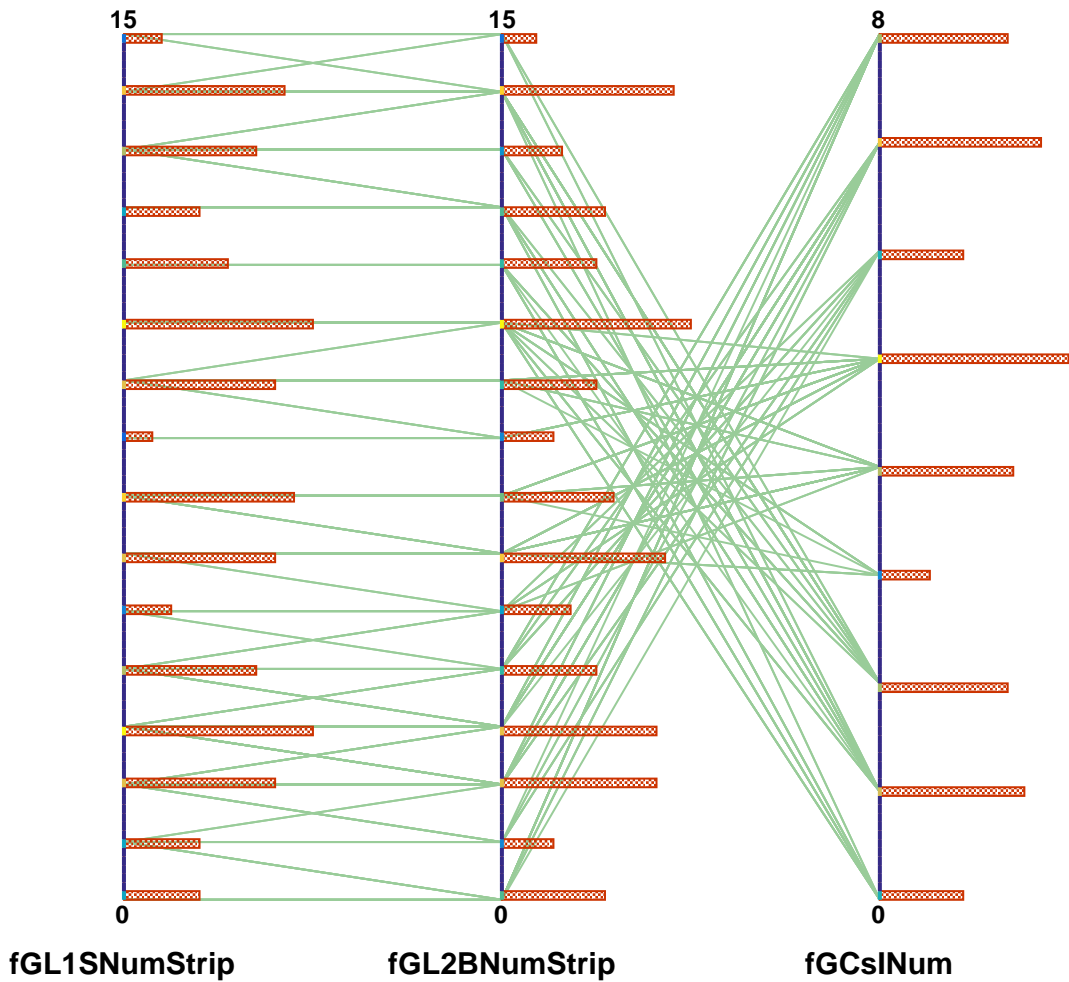
fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum==1 && fSSDGlobalMulti{1}=6)



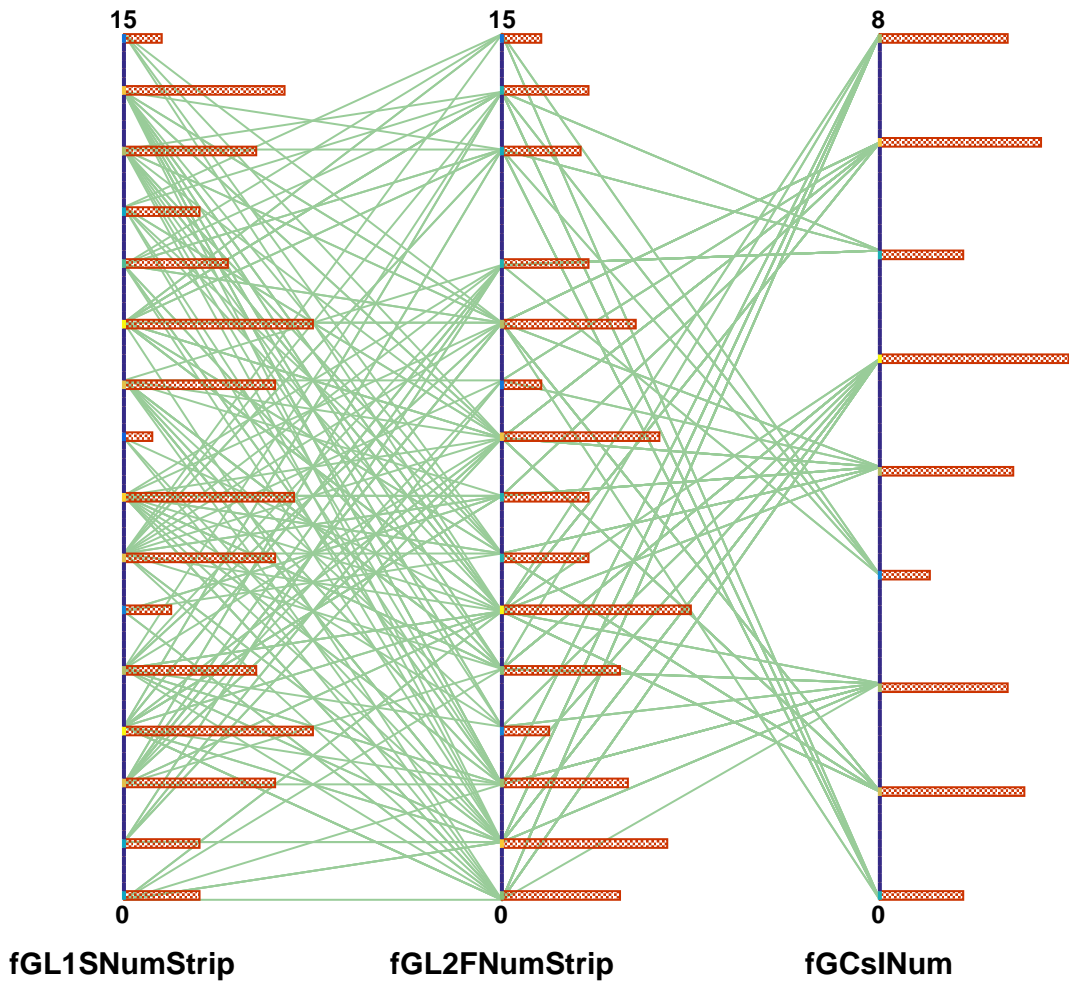




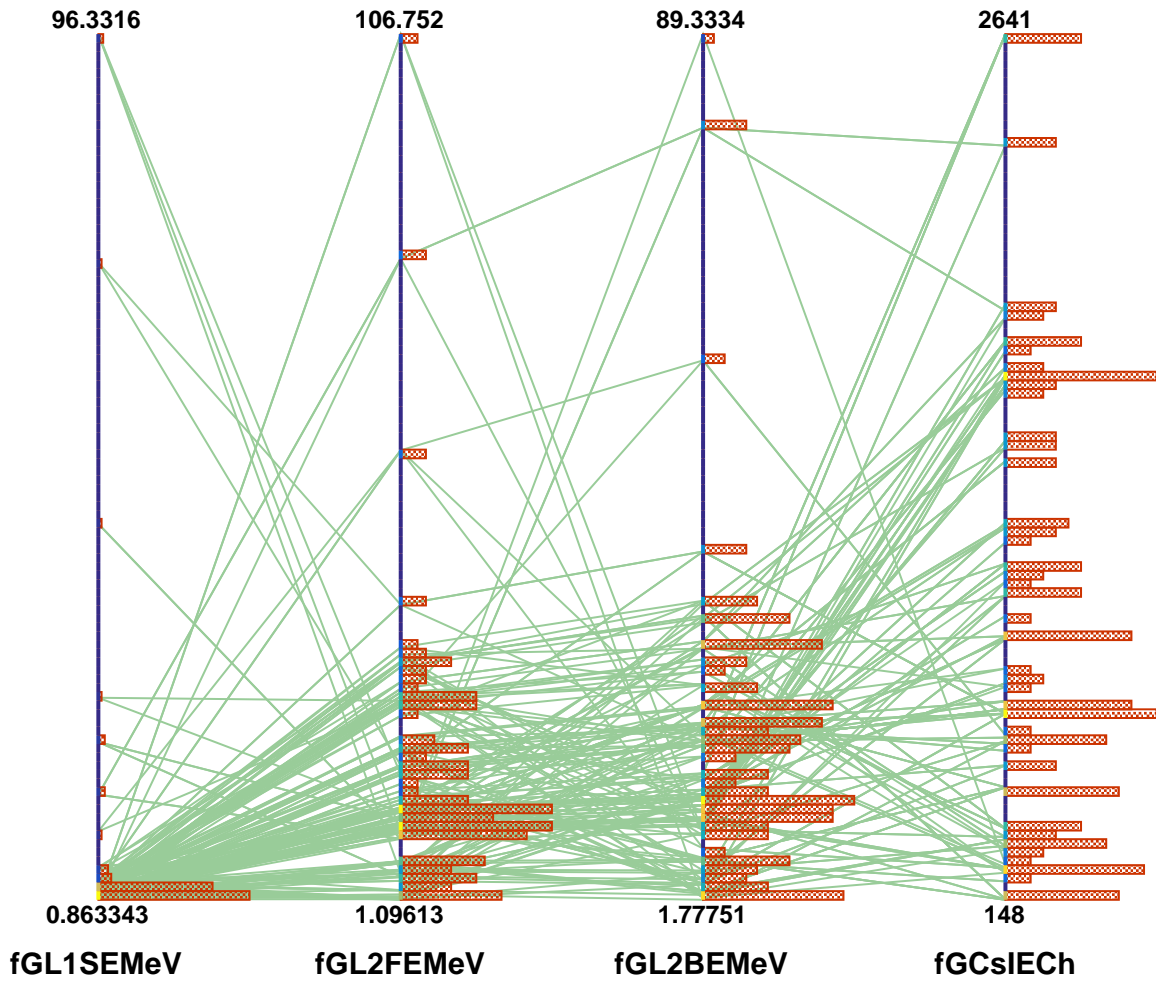
fGL1SNumStrip:fGL2BNumStrip:fGCsINum{fGSSDNum==2 && fSSDGlobalMulti[2]==6}

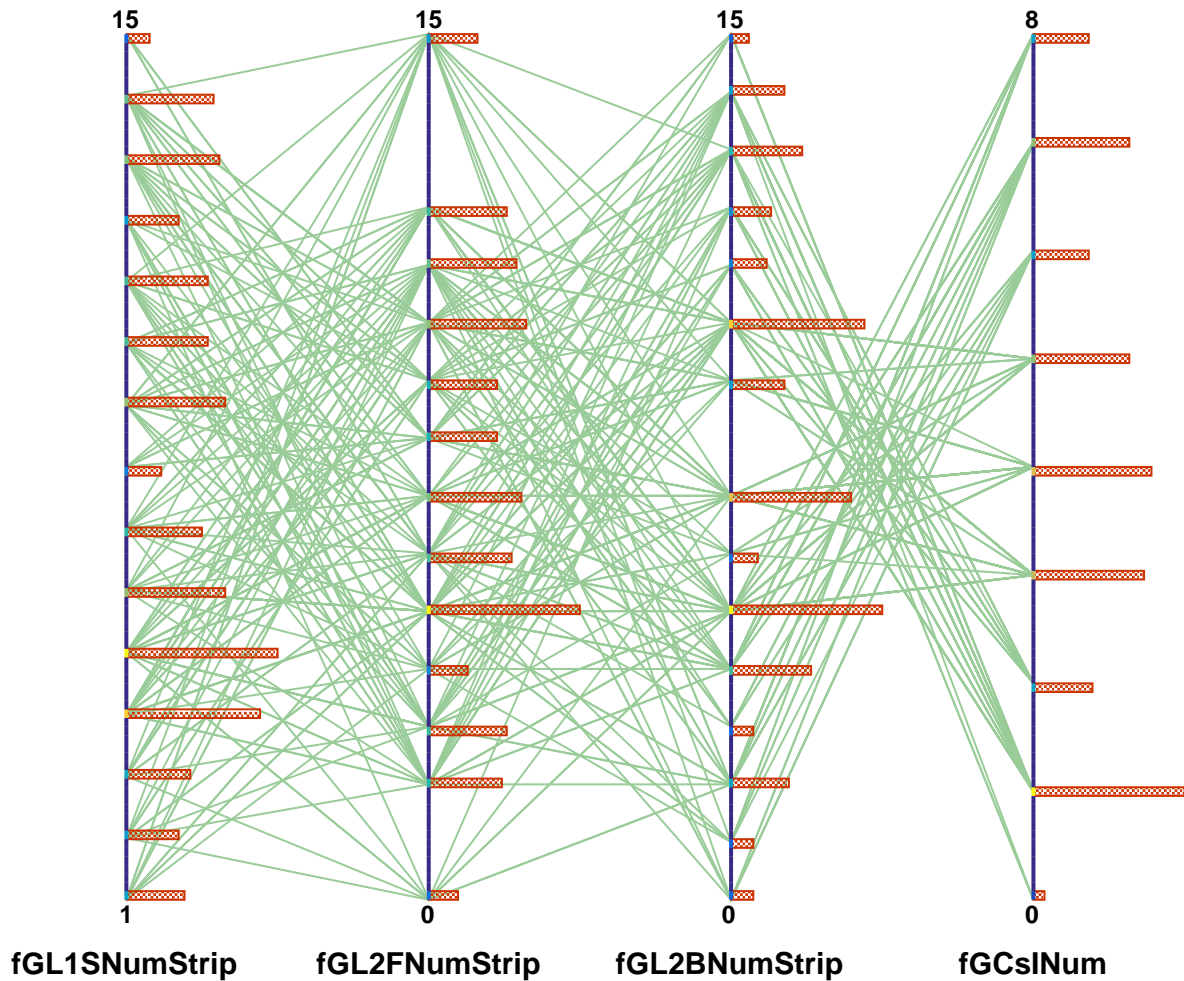


fGL1SNumStrip:fGL2FNumStrip:fGCsINum(fGSSDNum=2 && fSSDGlobalMulti[2]==6)

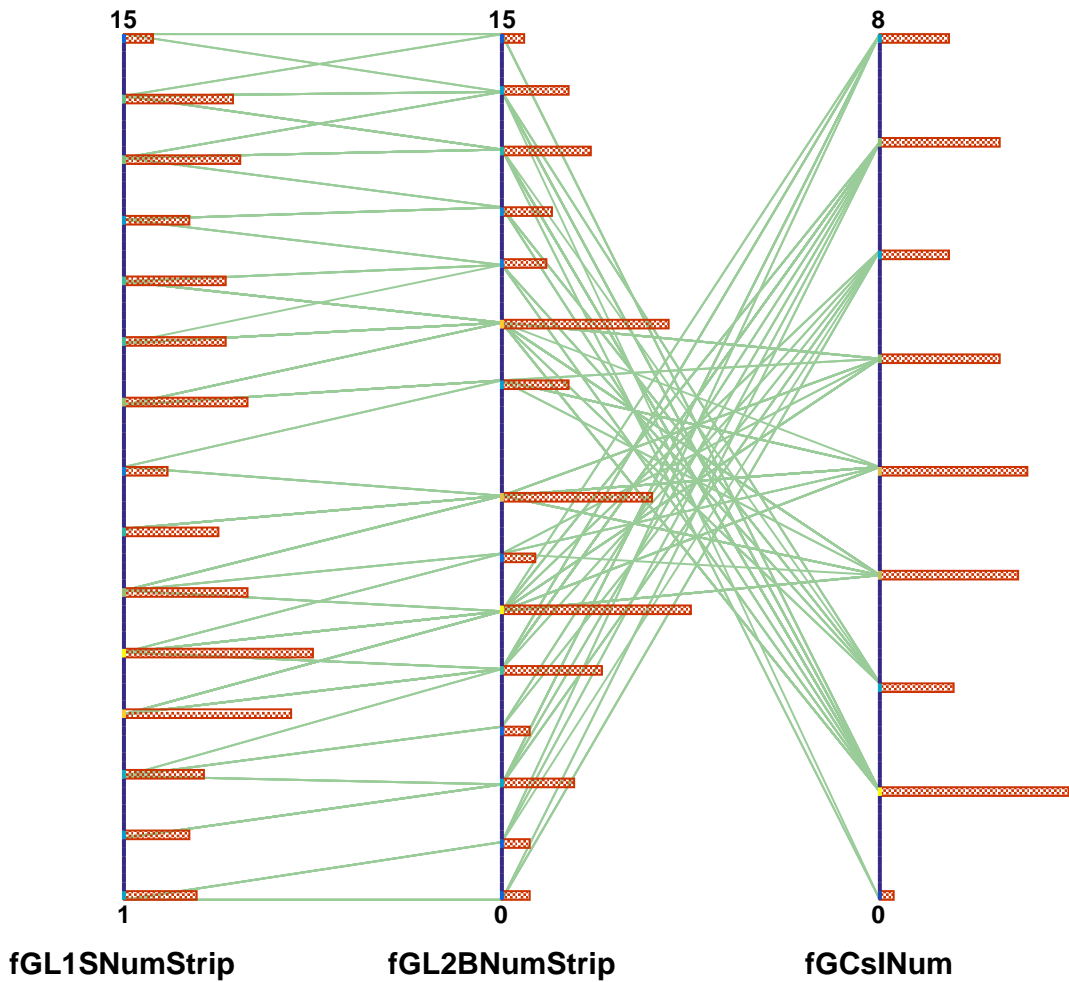


fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum==2 && fSSDGlobalMult[2]==6)



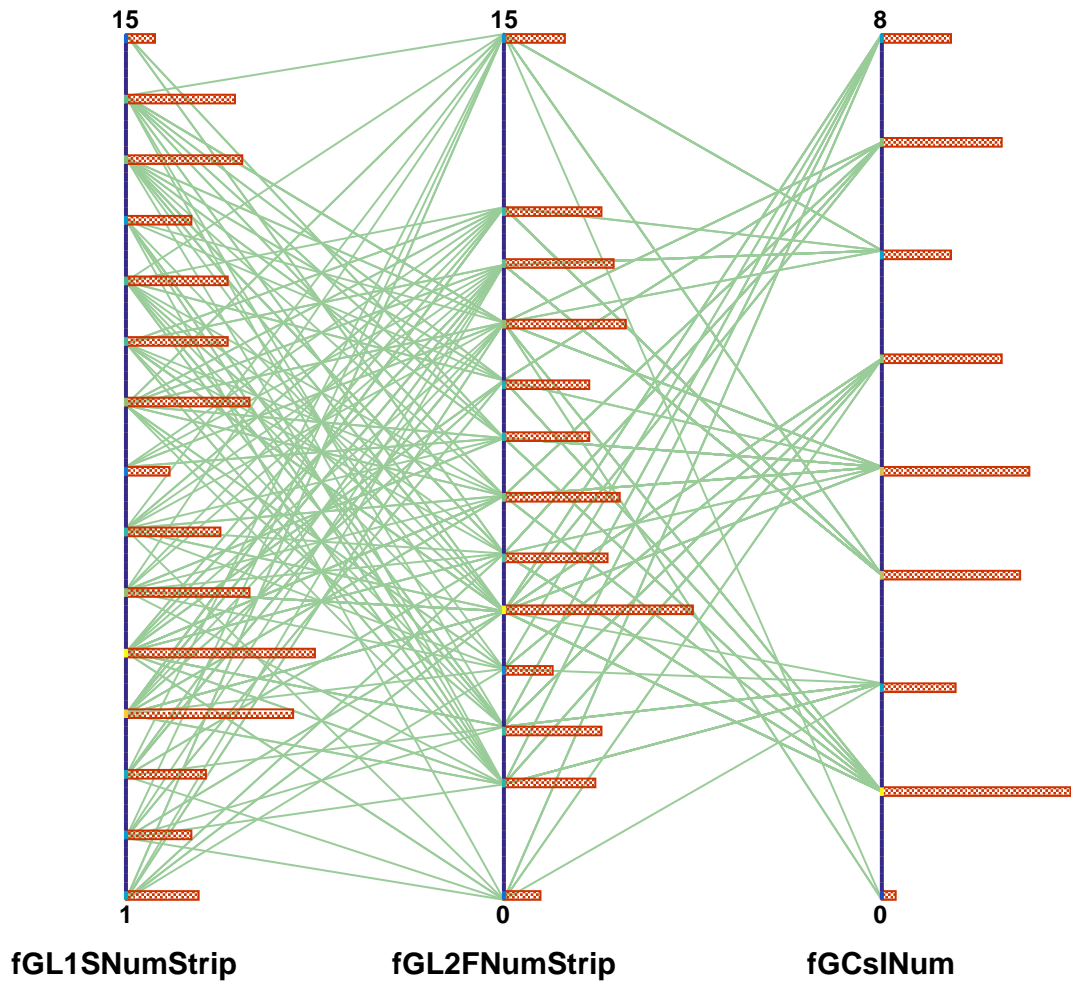


fGL1SNumStrip:fGL2BNumStrip:fGCsINum{fGSSDNum==3 && fSSDGlobalMulti[3]==6}





fGL1SNumStrip:fGL2FNumStrip:fGCsINum(fGSSDNum=3 && fSSDGlobalMulti[3]==6)



fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum==3 && fSSDGlobalMulti[3]==6)

