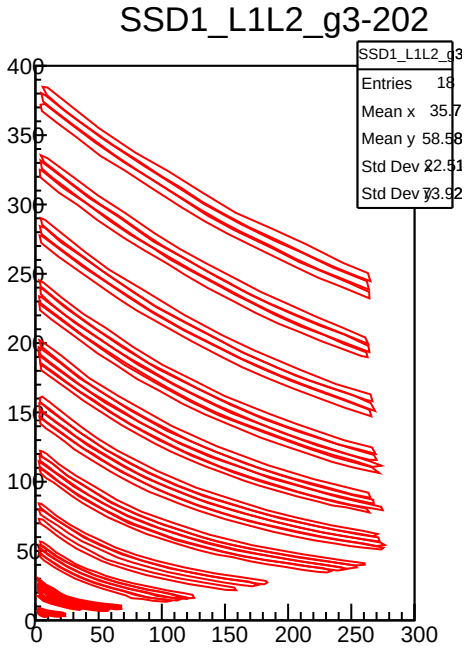
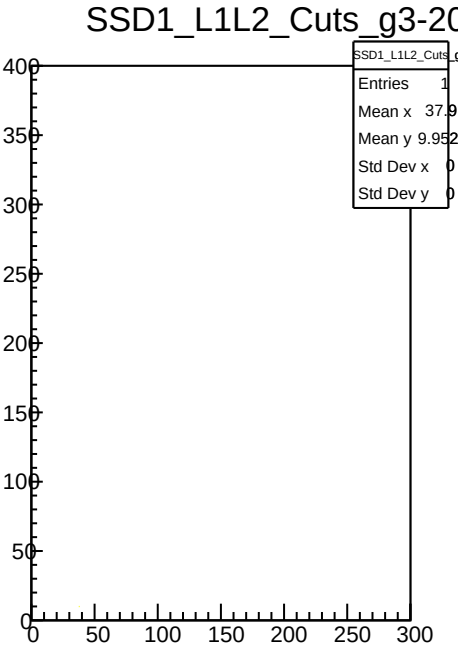
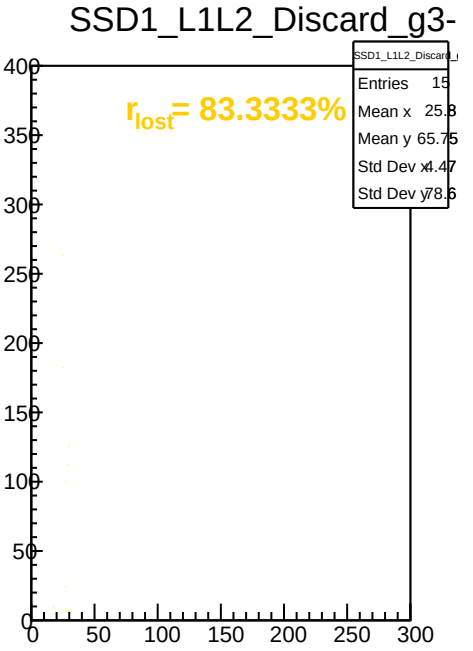
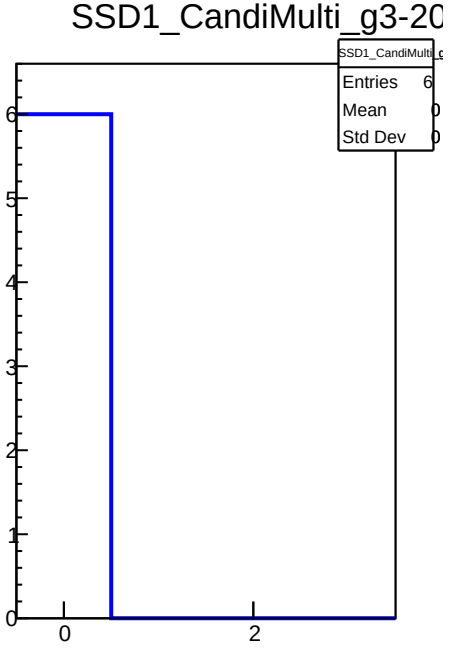
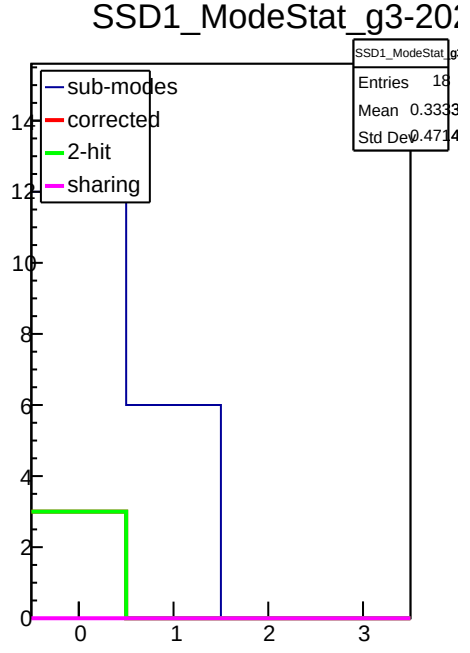

SSD1_L1L2_g3-202
SSD1_L1L2_g3
Entries 18
Mean x 35.7
Mean y 58.58
Std Dev x 22.51
Std Dev y 73.92

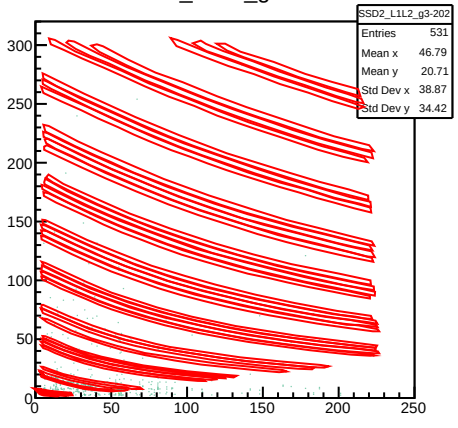

SSD1_L1L2_Cuts_g3-20
SSD1_L1L2_Cuts_g
Entries 1
Mean x 37.9
Mean y 9.952
Std Dev x 0
Std Dev y 0


SSD1_L1L2_Discard_g3-
SSD1_L1L2_Discard_g
r_lost = 83.3333%
Entries 15
Mean x 25.8
Mean y 65.75
Std Dev x 4.47
Std Dev y 78.6

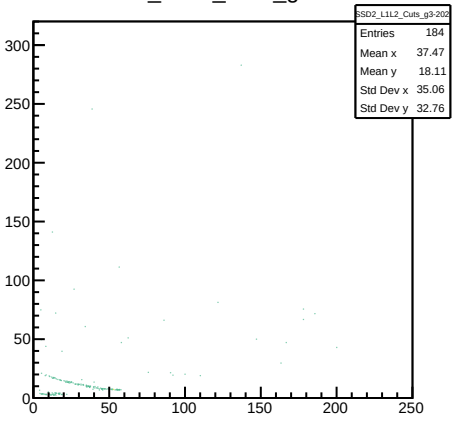

SSD1_CandiMulti_g3-20
SSD1_CandiMulti_g
Entries 6
Mean 0
Std Dev 0


SSD1_ModeStat_g3-20
SSD1_ModeStat_g
Entries 18
Mean 0.3333
Std Dev 0.4714
sub-modes
corrected
2-hit
sharing

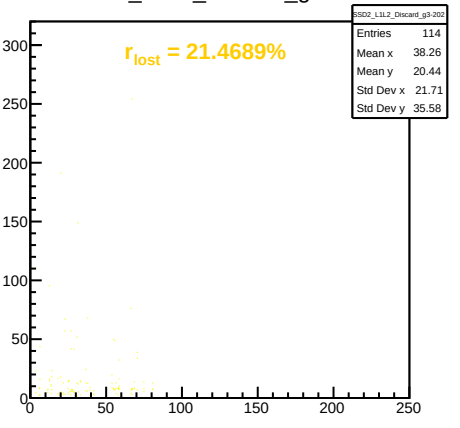
SSD2_L1L2_g3-202



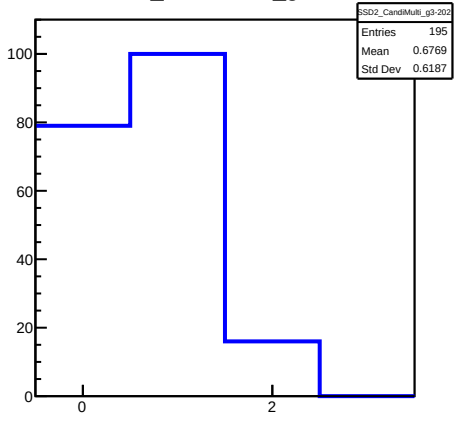
SSD2_L1L2_Cuts_g3-202



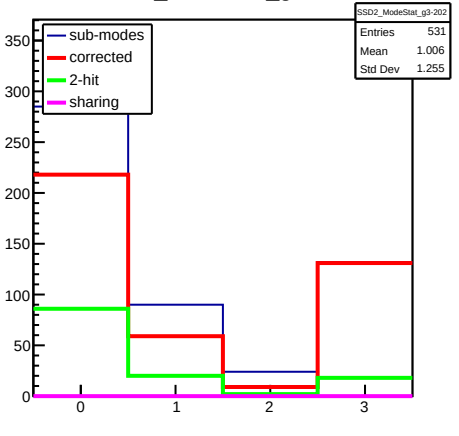
SSD2_L1L2_Discard_g3-202



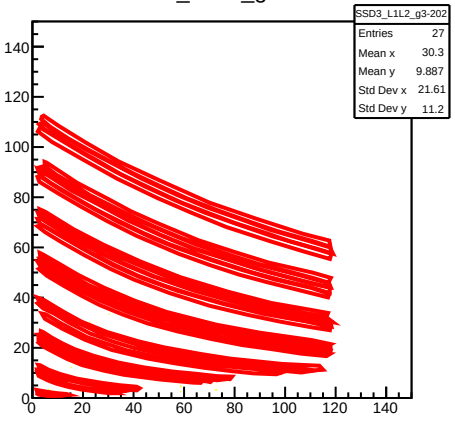
SSD2_CandiMulti_g3-202



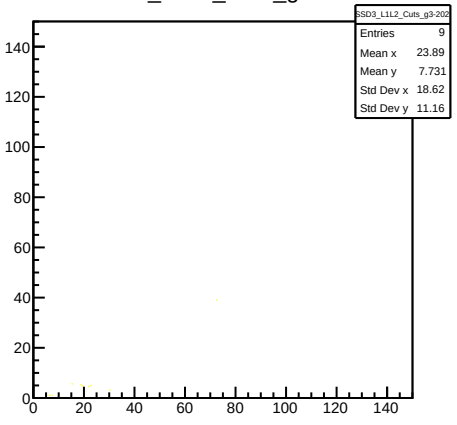
SSD2_ModeStat_g3-202



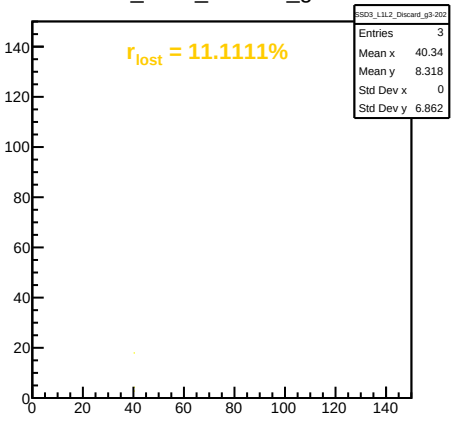
SSD3_L1L2_g3-202



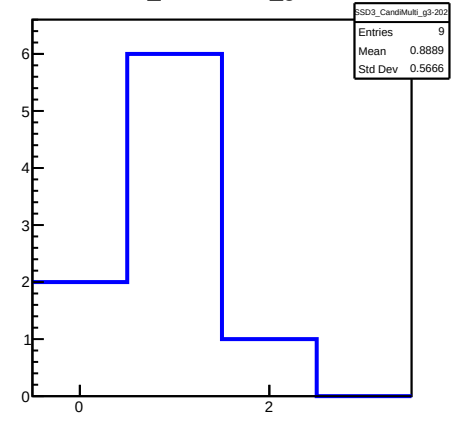
SSD3_L1L2_Cuts_g3-202



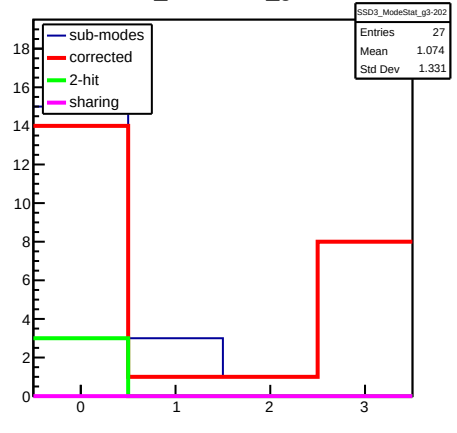
SSD3_L1L2_Discard_g3-202



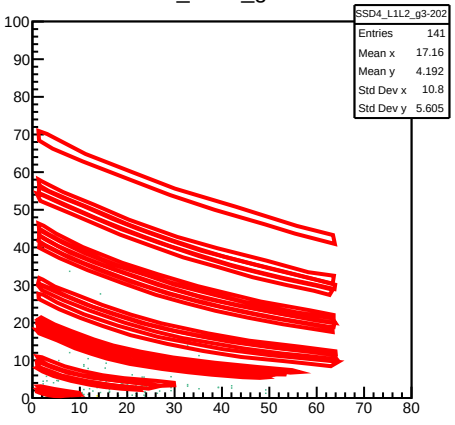
SSD3_CandiMulti_g3-202



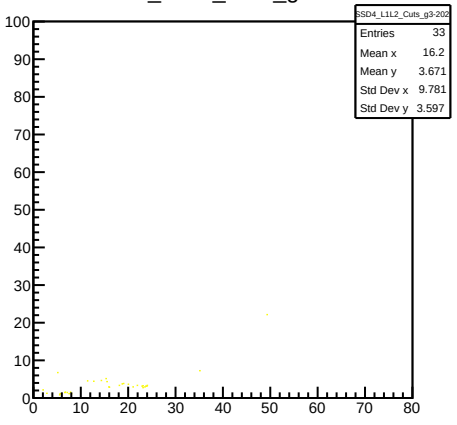
SSD3_ModeStat_g3-202



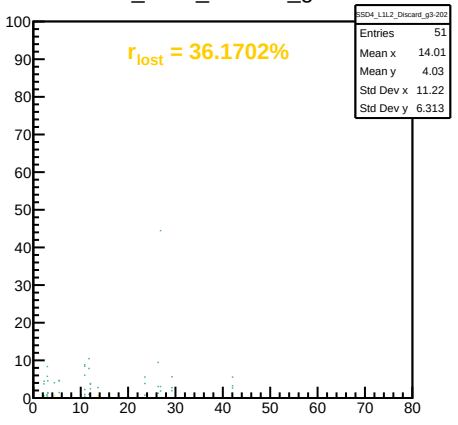
SSD4_L1L2_g3-202



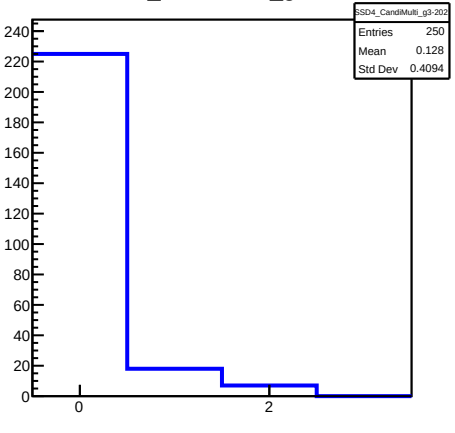
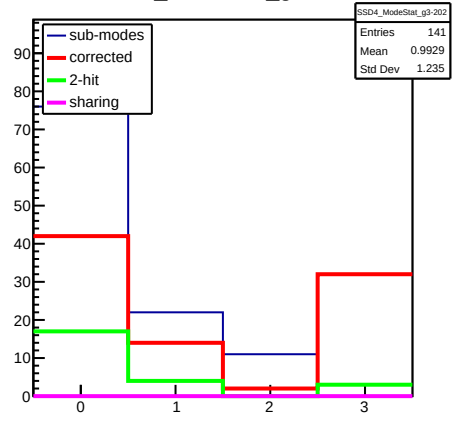
SSD4_L1L2_Cuts_g3-202



SSD4_L1L2_Discard_g3-202



SSD4_CandiMulti_g3-202



SSD4_ModeStat_g3-202