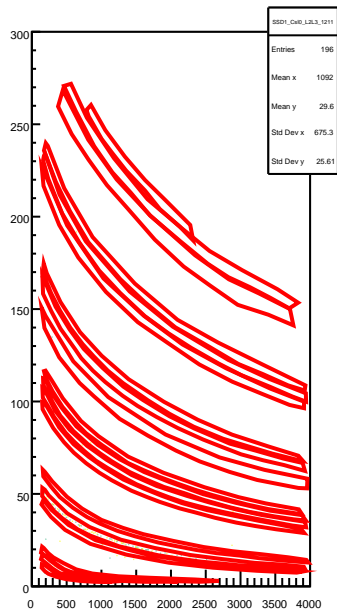
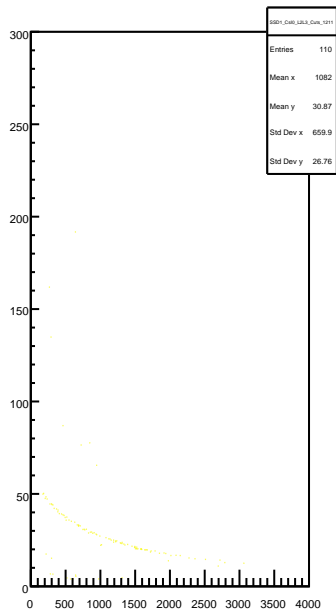


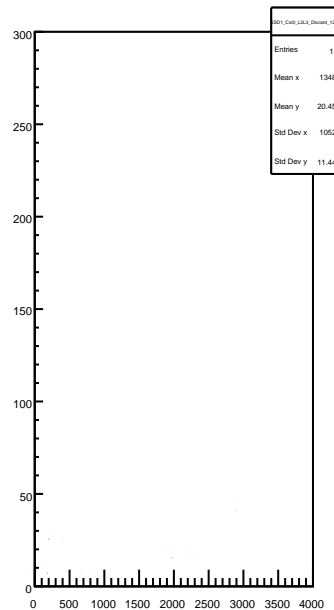
SSD1\_Csl0\_L2L3\_1211



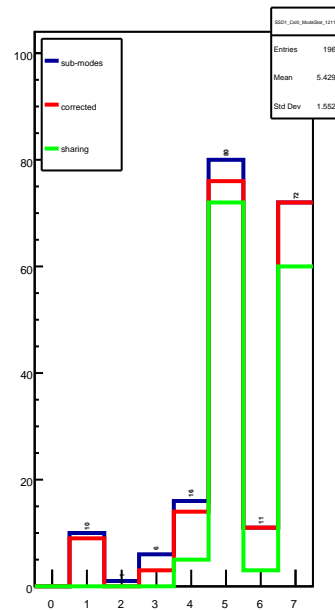
SSD1\_Csl0\_L2L3\_Cuts\_1211



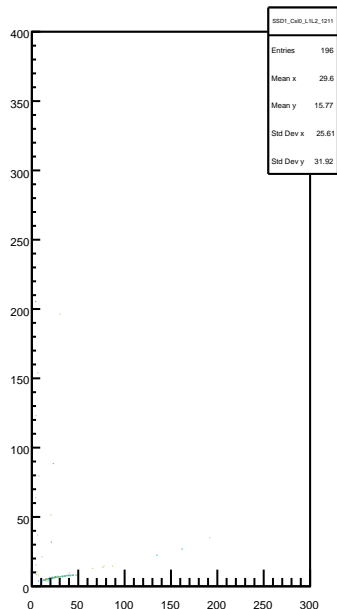
SSD1\_Csl0\_L2L3\_Discard\_1211



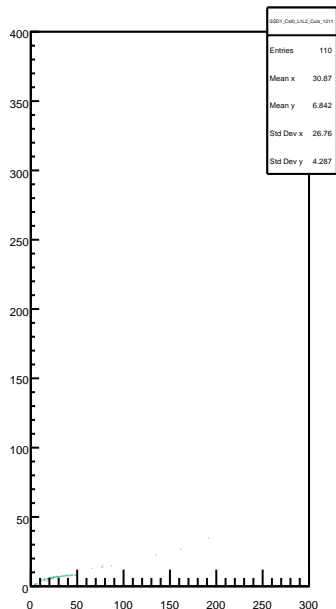
SSD1\_Csl0\_ModeStat\_1211



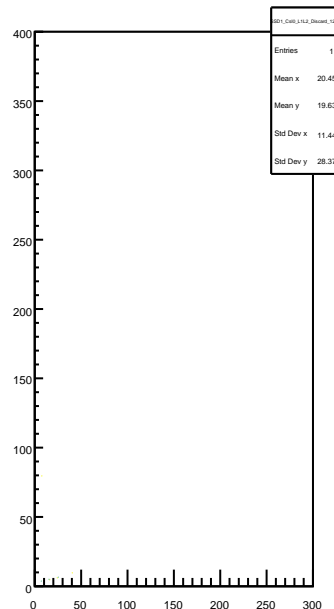
SSD1\_Csl0\_L1L2\_1211



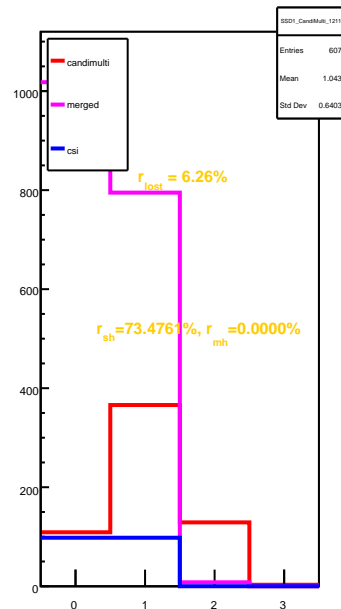
SSD1\_Csl0\_L1L2\_Cuts\_1211



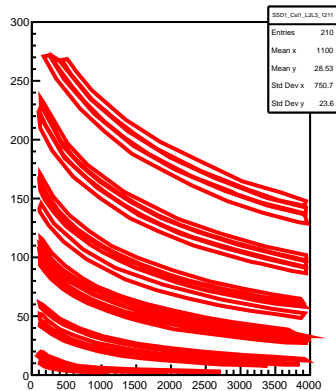
SSD1\_Csl0\_L1L2\_Discard\_1211



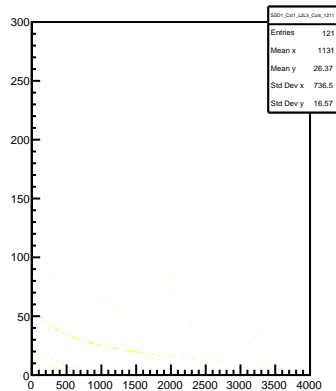
SSD1\_CandiMulti\_1211



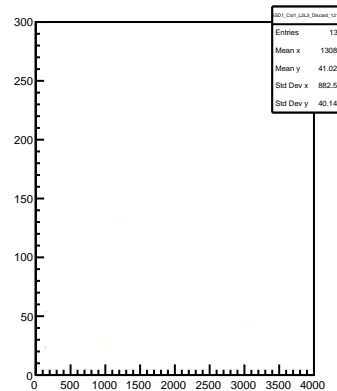
SSD1\_Cs1\_L2L3\_1211



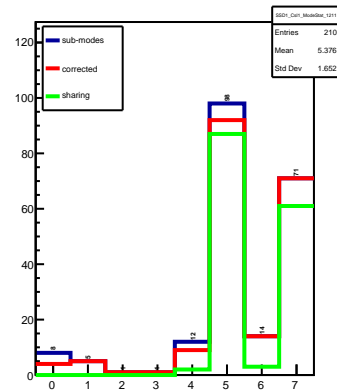
SSD1\_Cs1\_L2L3\_Cuts\_1211



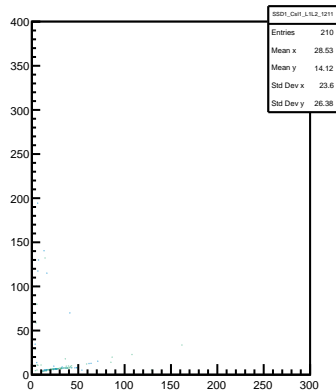
SSD1\_Cs1\_L2L3\_Discard\_1211



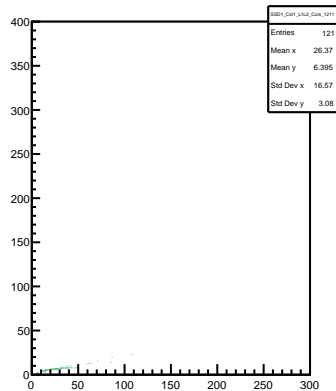
SSD1\_Cs1\_ModeStat\_1211



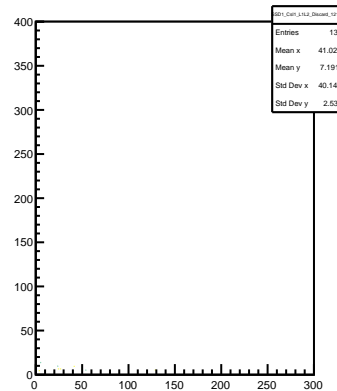
SSD1\_Cs1\_L1L2\_1211



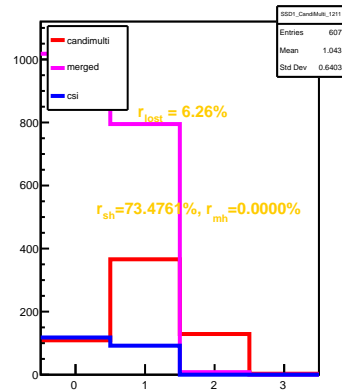
SSD1\_Cs1\_L1L2\_Cuts\_1211



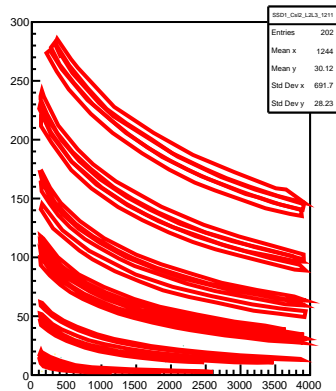
SSD1\_Cs1\_L1L2\_Discard\_1211



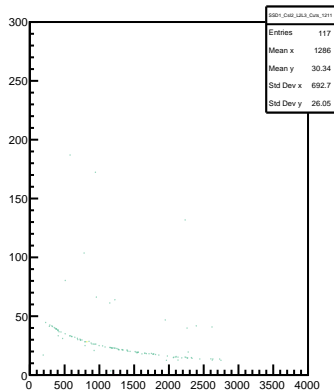
SSD1\_CandiMulti\_1211



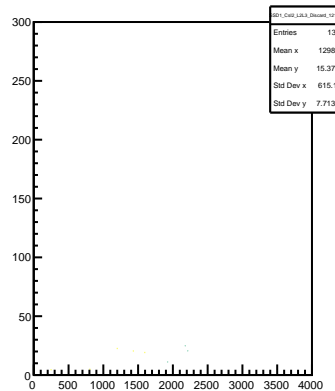
SSD1\_Csl2\_L2L3\_1211



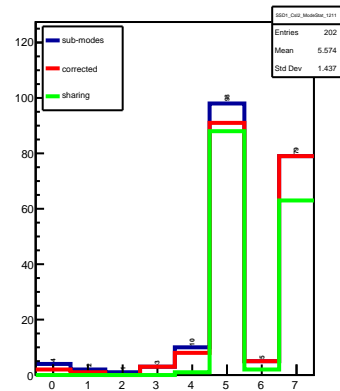
SSD1\_Csl2\_L2L3\_Cuts\_1211



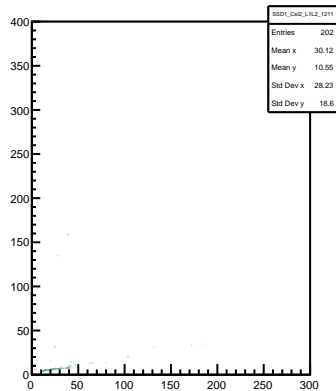
SSD1\_Csl2\_L2L3\_Discard\_1211



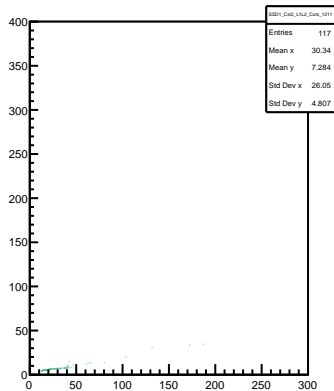
SSD1\_Csl2\_ModeStat\_1211



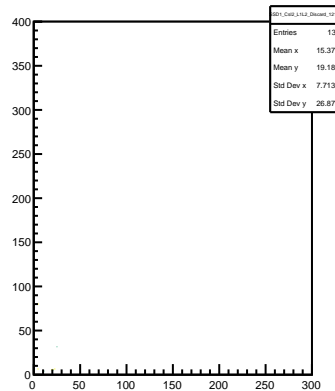
SSD1\_Csl2\_L1L2\_1211



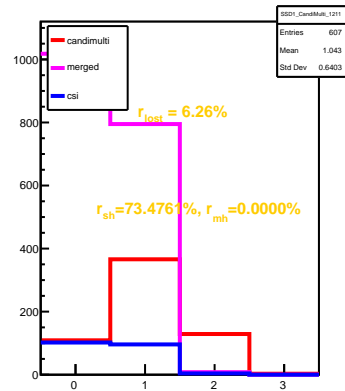
SSD1\_Csl2\_L1L2\_Cuts\_1211



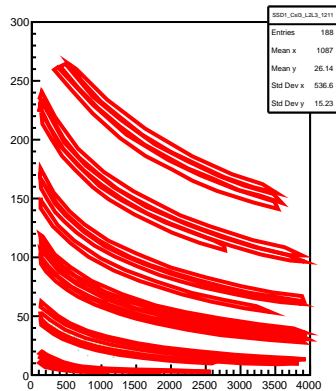
SSD1\_Csl2\_L1L2\_Discard\_1211



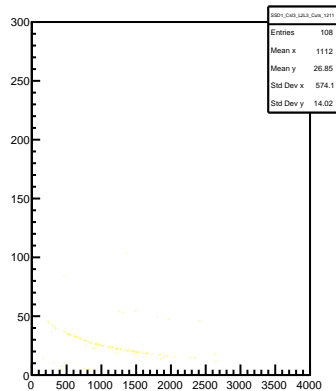
SSD1\_CandiMulti\_1211



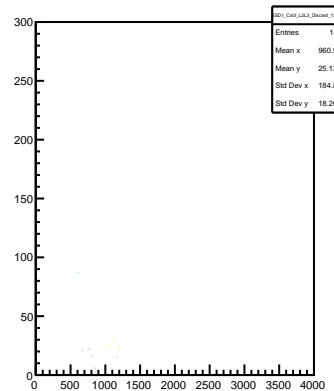
SSD1\_Csl3\_L2L3\_1211



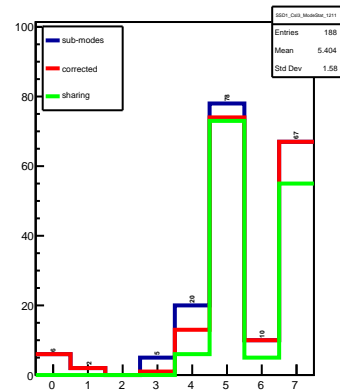
SSD1\_Csl3\_L2L3\_Cuts\_1211



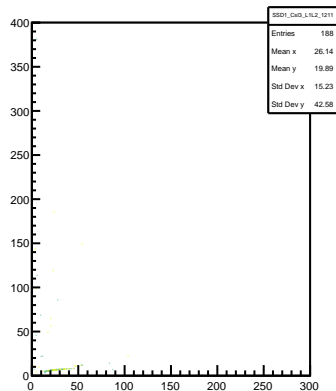
SSD1\_Csl3\_L2L3\_Discard\_1211



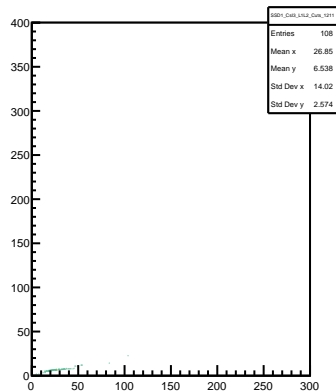
SSD1\_Csl3\_ModeStat\_1211



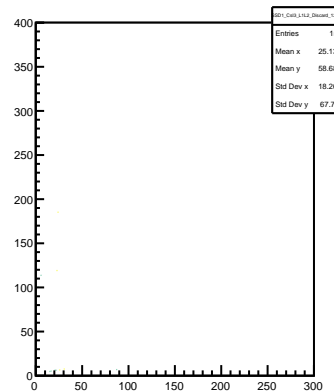
SSD1\_Csl3\_L1L2\_1211



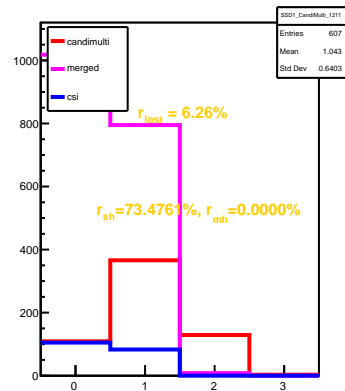
SSD1\_Csl3\_L1L2\_Cuts\_1211



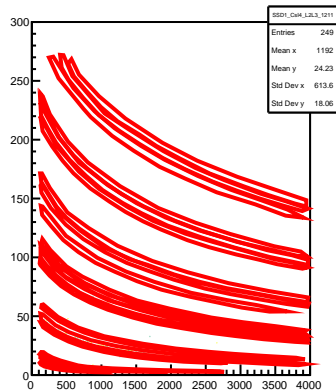
SSD1\_Csl3\_L1L2\_Discard\_1211



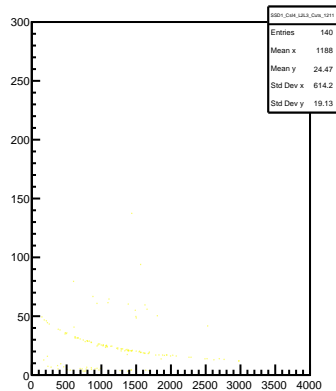
SSD1\_CandiMulti\_1211



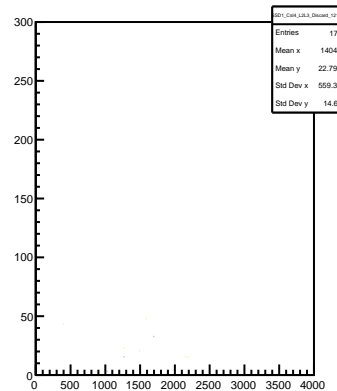
SSD1\_Csl4\_L2L3\_1211



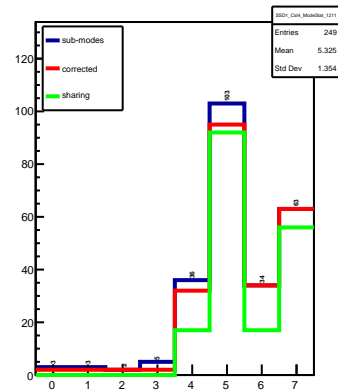
SSD1\_Csl4\_L2L3\_Cuts\_1211



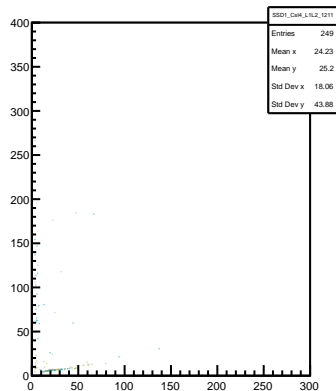
SSD1\_Csl4\_L2L3\_Discard\_1211



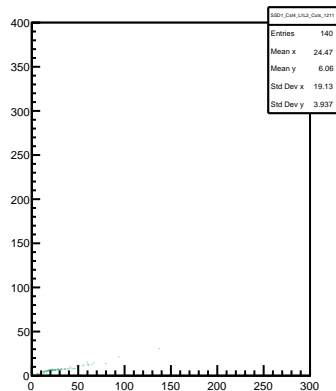
SSD1\_Csl4\_ModeStat\_1211



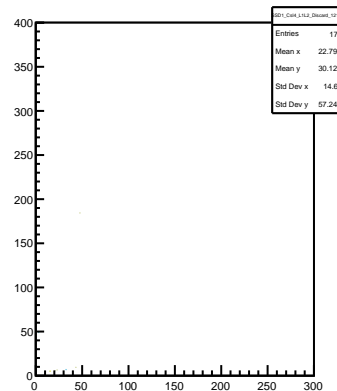
SSD1\_Csl4\_L1L2\_1211



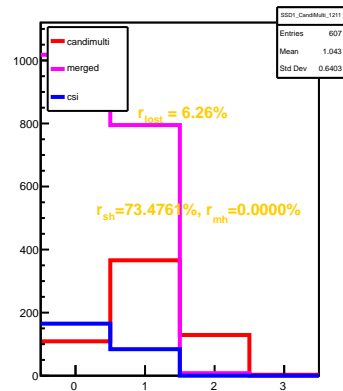
SSD1\_Csl4\_L1L2\_Cuts\_1211



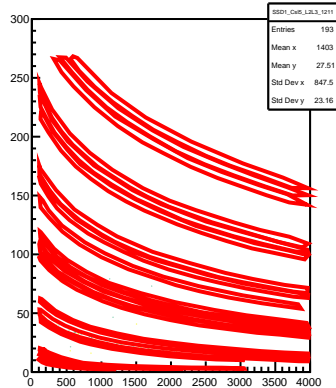
SSD1\_Csl4\_L1L2\_Discard\_1211



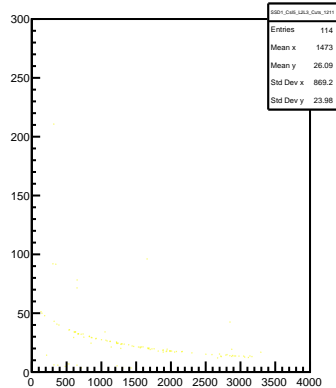
SSD1\_CandiMulti\_1211



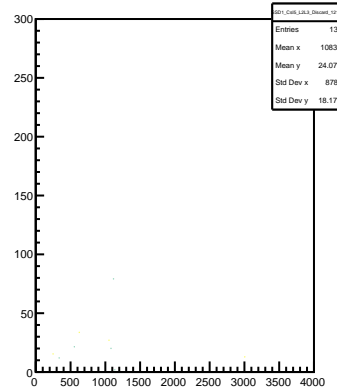
SSD1\_Csl5\_L2L3\_1211



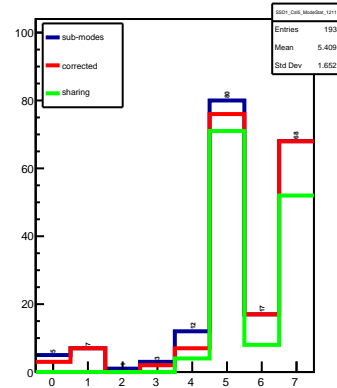
SSD1\_Csl5\_L2L3\_Cuts\_1211



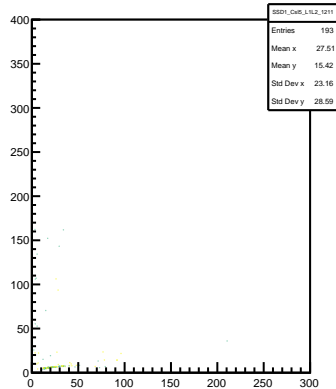
SSD1\_Csl5\_L2L3\_Discard\_1211



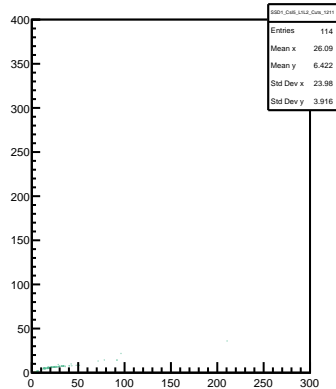
SSD1\_Csl5\_ModeStat\_1211



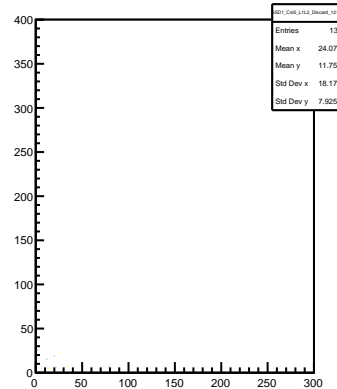
SSD1\_Csl5\_L1L2\_1211



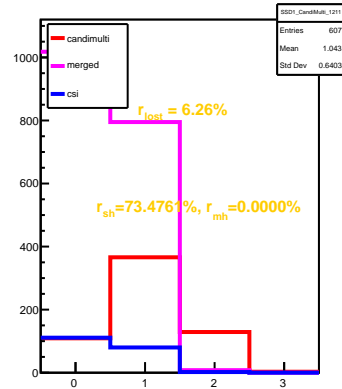
SSD1\_Csl5\_L1L2\_Cuts\_1211



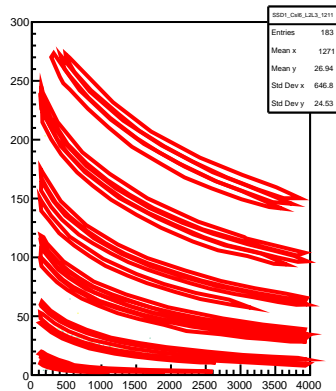
SSD1\_Csl5\_L1L2\_Discard\_1211



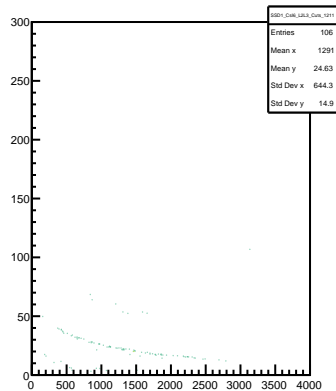
SSD1\_CandiMulti\_1211



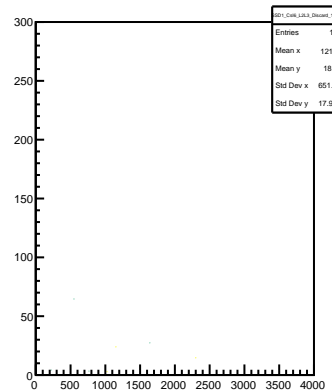
SSD1\_Csl6\_L2L3\_1211



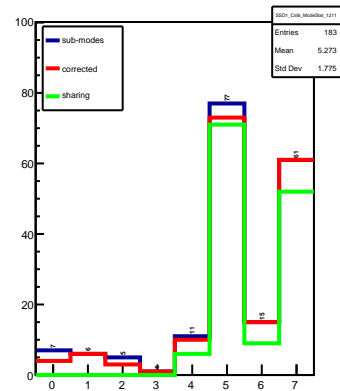
SSD1\_Csl6\_L2L3\_Cuts\_1211



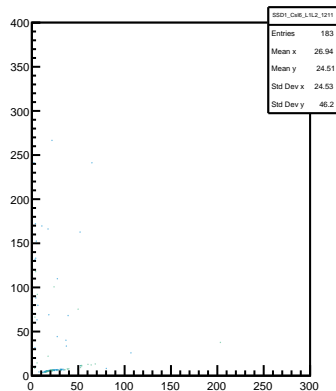
SSD1\_Csl6\_L2L3\_Discard\_1211



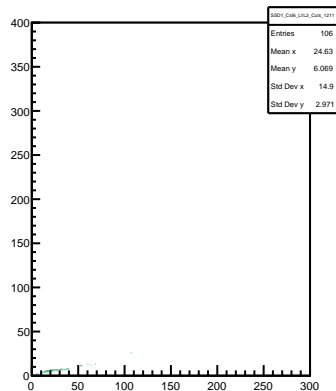
SSD1\_Csl6\_ModeStat\_1211



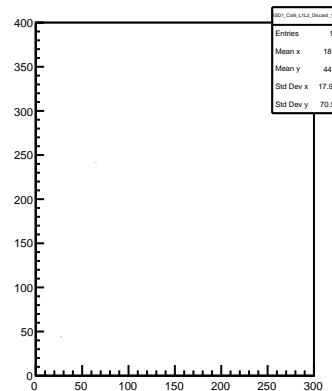
SSD1\_Csl6\_L1L2\_1211



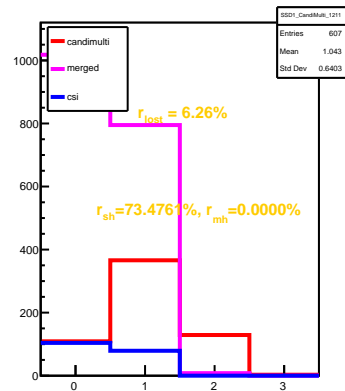
SSD1\_Csl6\_L1L2\_Cuts\_1211



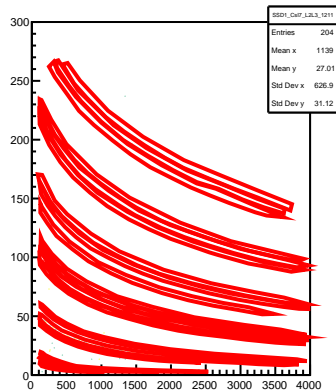
SSD1\_Csl6\_L1L2\_Discard\_1211



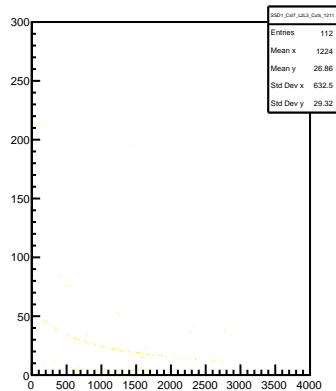
SSD1\_CandiMulti\_1211



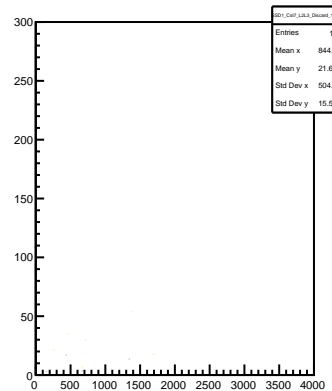
SSD1\_CsI7\_L2L3\_1211



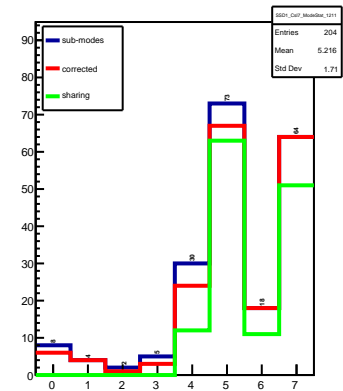
SSD1\_CsI7\_L2L3\_Cuts\_1211



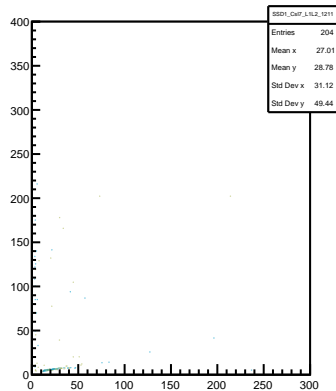
SSD1\_CsI7\_L2L3\_Discard\_1211



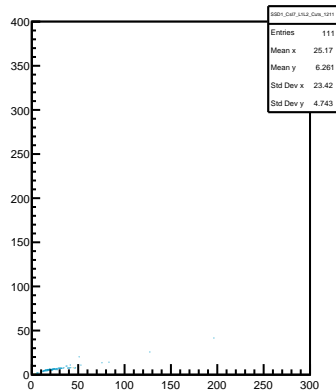
SSD1\_CsI7\_ModeStat\_1211



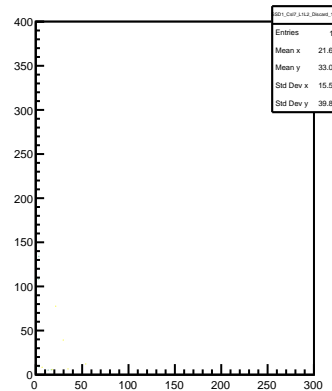
SSD1\_CsI7\_L1L2\_1211



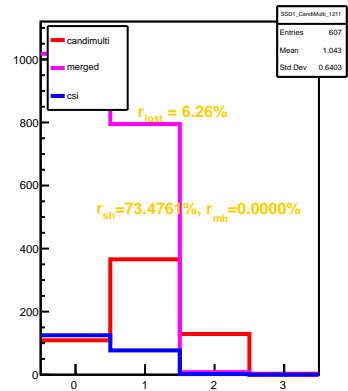
SSD1\_CsI7\_L1L2\_Cuts\_1211



SSD1\_CsI7\_L1L2\_Discard\_1211

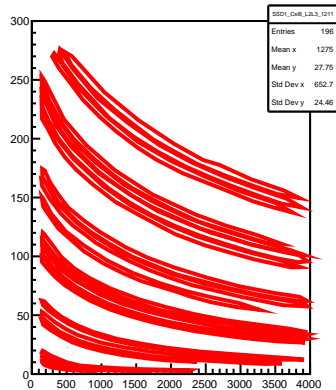


SSD1\_CandiMulti\_1211

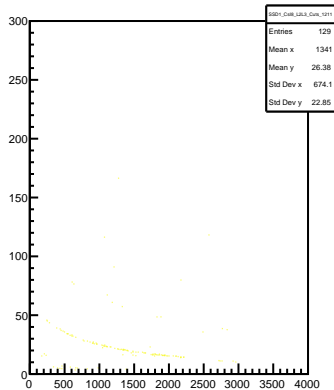




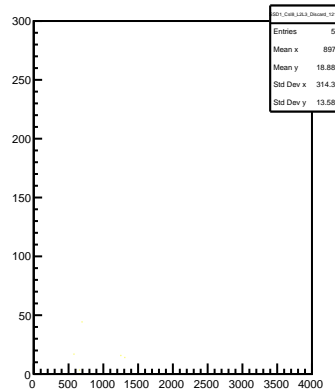
SSD1\_Csl8\_L2L3\_1211



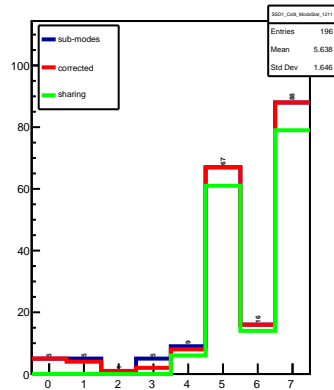
SSD1\_Csl8\_L2L3\_Cuts\_1211



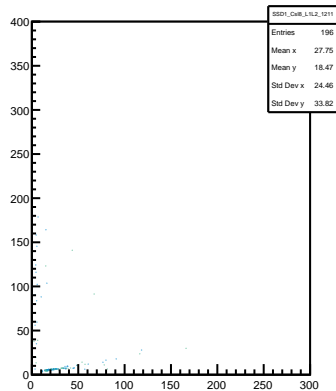
SSD1\_Csl8\_L2L3\_Discard\_1211



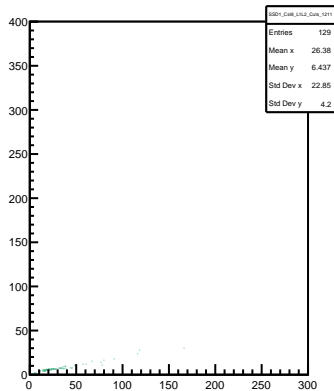
SSD1\_Csl8\_ModeStat\_1211



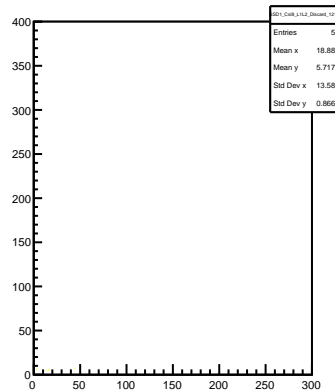
SSD1\_Csl8\_L1L2\_1211



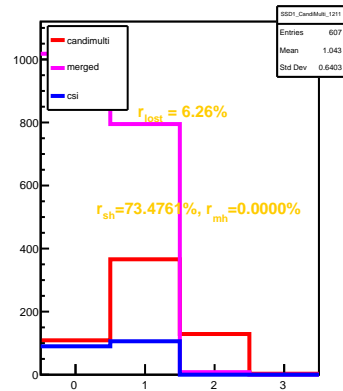
SSD1\_Csl8\_L1L2\_Cuts\_1211



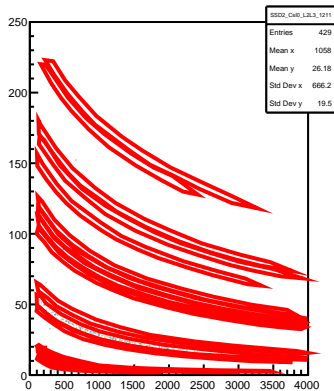
SSD1\_Csl8\_L1L2\_Discard\_1211



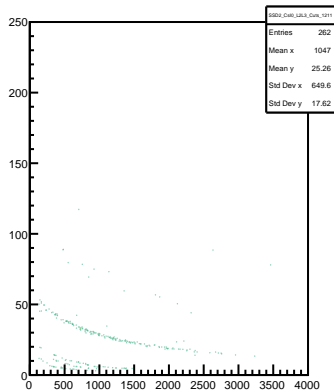
SSD1\_CandiMulti\_1211



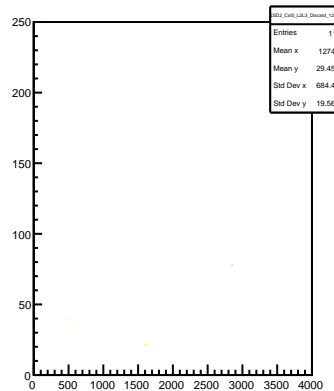
SSD2\_Csl0\_L2L3\_1211



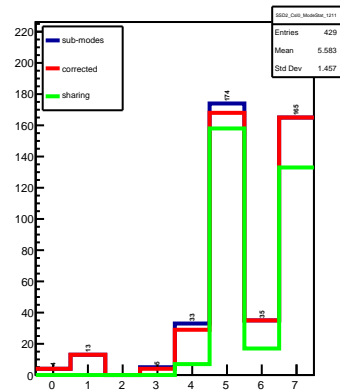
SSD2\_Csl0\_L2L3\_Cuts\_1211



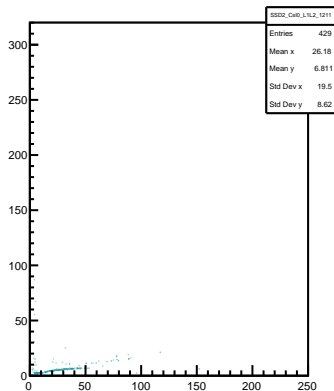
SSD2\_Csl0\_L2L3\_Discard\_1211



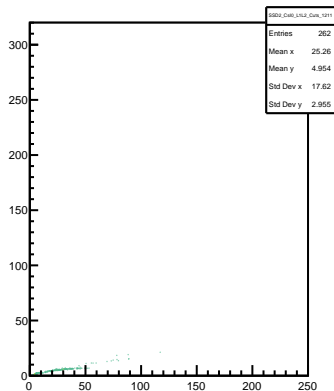
SSD2\_Csl0\_ModeStat\_1211



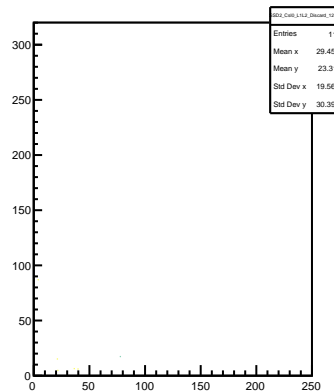
SSD2\_Csl0\_L1L2\_1211



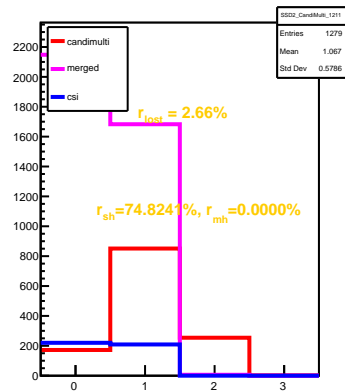
SSD2\_Csl0\_L1L2\_Cuts\_1211



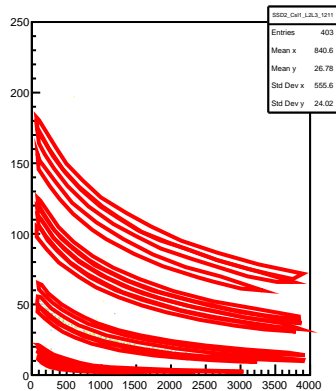
SSD2\_Csl0\_L1L2\_Discard\_1211



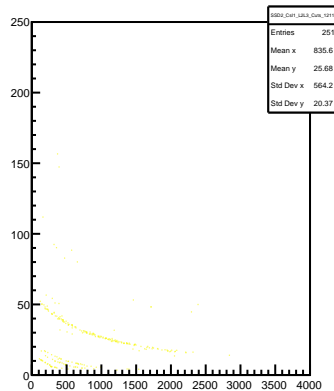
SSD2\_CandiMulti\_1211



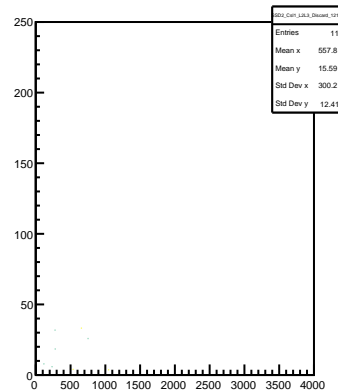
SSD2\_Csl1\_L2L3\_1211



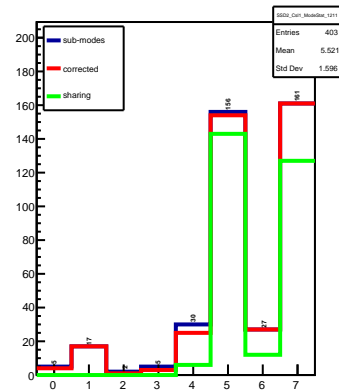
SSD2\_Csl1\_L2L3\_Cuts\_1211



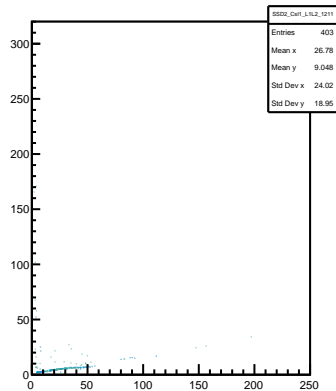
SSD2\_Csl1\_L2L3\_Discard\_1211



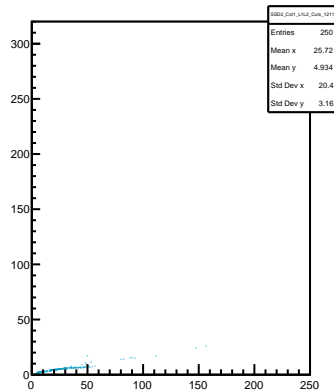
SSD2\_Csl1\_ModeStat\_1211



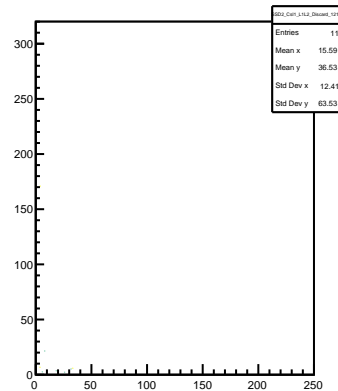
SSD2\_Csl1\_L1L2\_1211



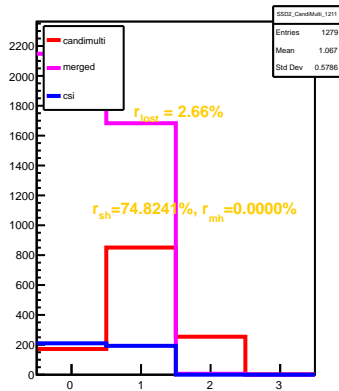
SSD2\_Csl1\_L1L2\_Cuts\_1211



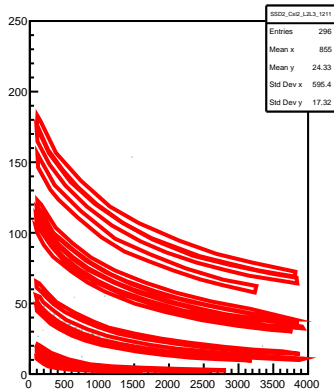
SSD2\_Csl1\_L1L2\_Discard\_1211



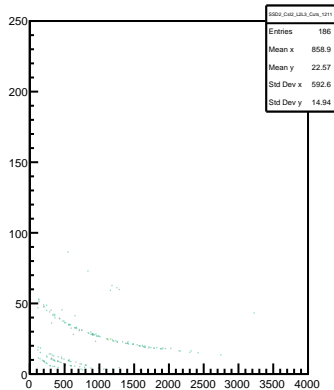
SSD2\_CandiMulti\_1211



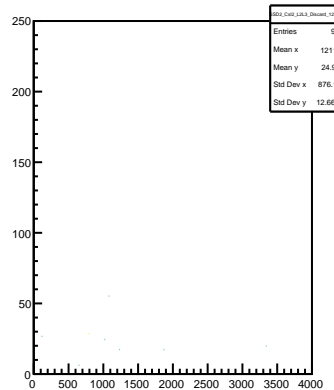
SSD2\_Csl2\_L2L3\_1211



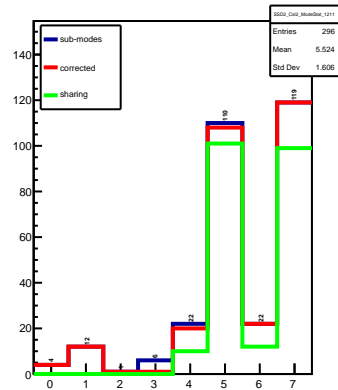
SSD2\_Csl2\_L2L3\_Cuts\_1211



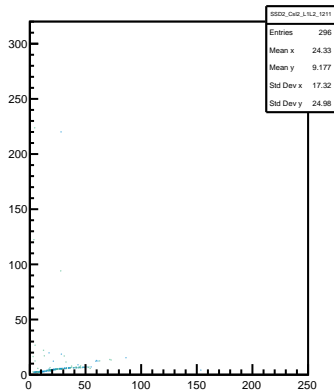
SSD2\_Csl2\_L2L3\_Discard\_1211



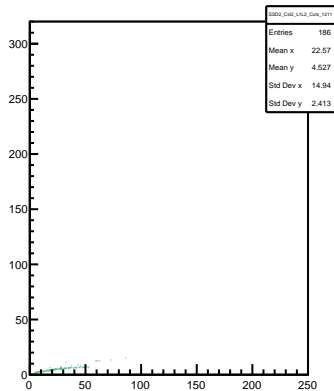
SSD2\_Csl2\_ModeStat\_1211



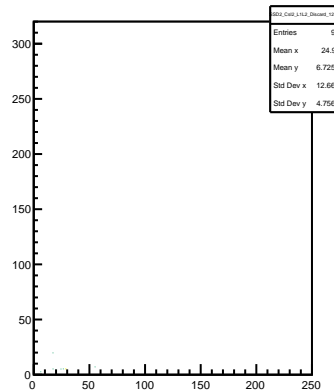
SSD2\_Csl2\_L1L2\_1211



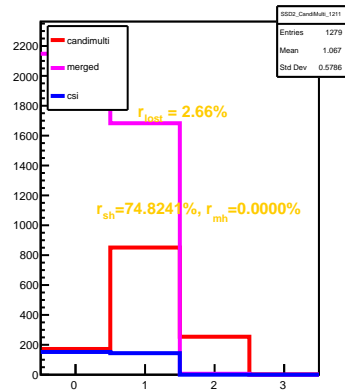
SSD2\_Csl2\_L1L2\_Cuts\_1211



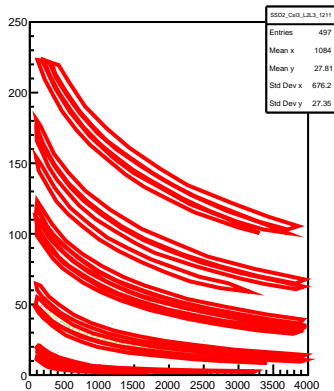
SSD2\_Csl2\_L1L2\_Discard\_1211



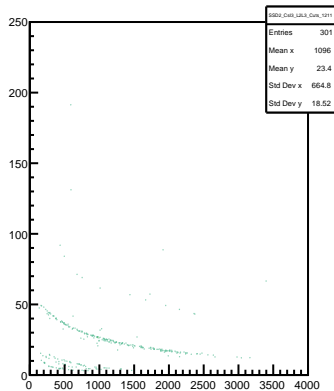
SSD2\_CandiMulti\_1211



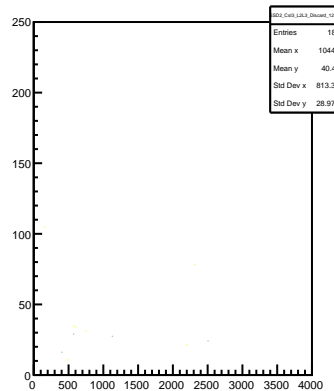
SSD2\_Csl3\_L2L3\_1211



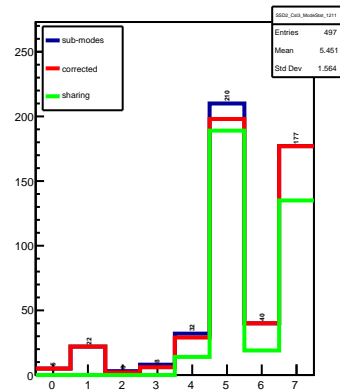
SSD2\_Csl3\_L2L3\_Cuts\_1211



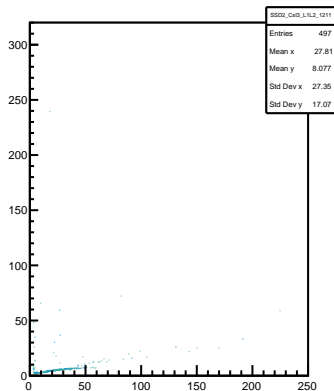
SSD2\_Csl3\_L2L3\_Discard\_1211



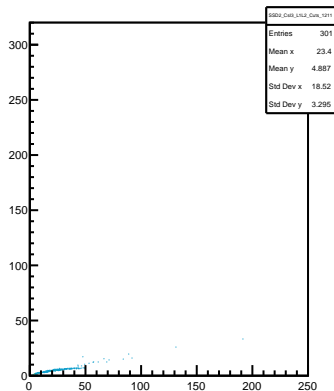
SSD2\_Csl3\_ModeStat\_1211



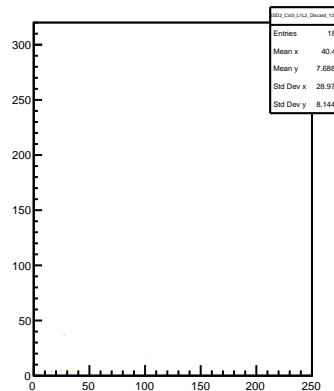
SSD2\_Csl3\_L1L2\_1211



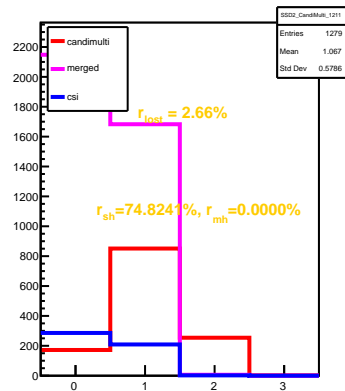
SSD2\_Csl3\_L1L2\_Cuts\_1211



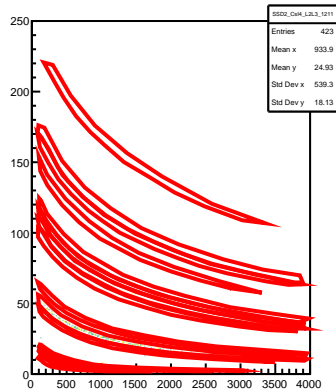
SSD2\_Csl3\_L1L2\_Discard\_1211



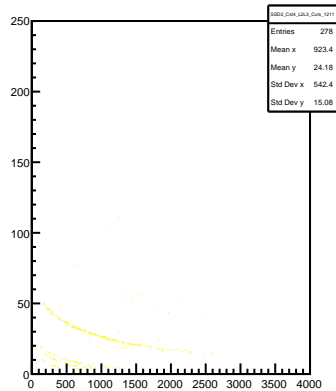
SSD2\_CandiMulti\_1211



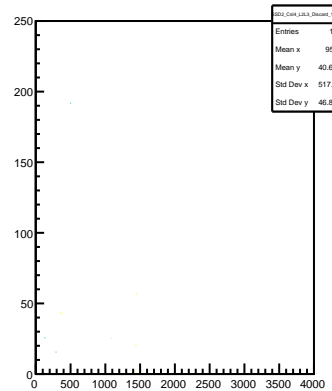
SSD2\_Csl4\_L2L3\_1211



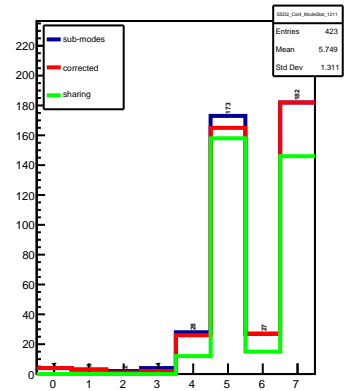
SSD2\_Csl4\_L2L3\_Cuts\_1211



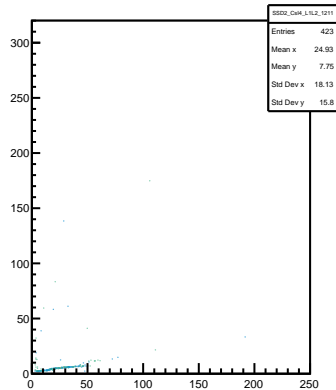
SSD2\_Csl4\_L2L3\_Discard\_1211



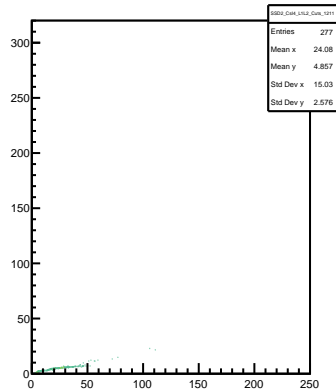
SSD2\_Csl4\_ModeStat\_1211



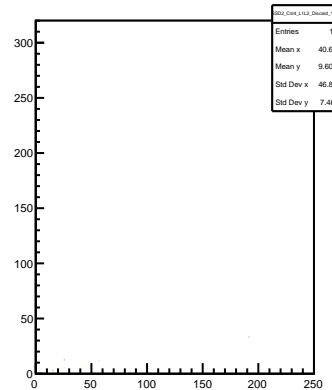
SSD2\_Csl4\_L1L2\_1211



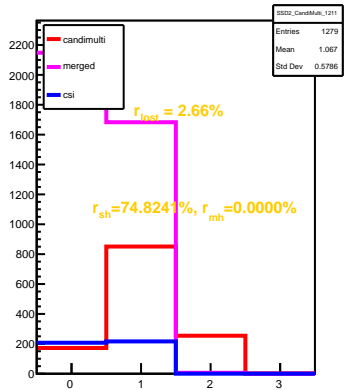
SSD2\_Csl4\_L1L2\_Cuts\_1211



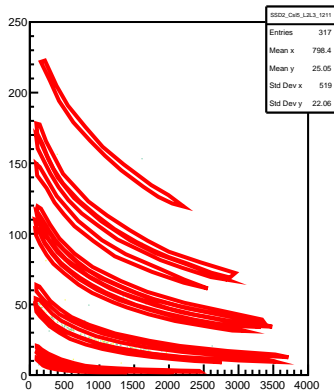
SSD2\_Csl4\_L1L2\_Discard\_1211



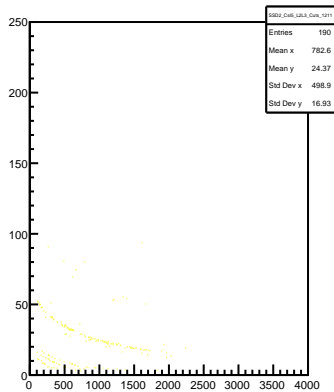
SSD2\_CandiMulti\_1211



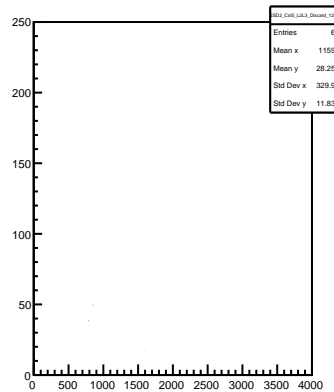
SSD2\_Csl5\_L2L3\_1211



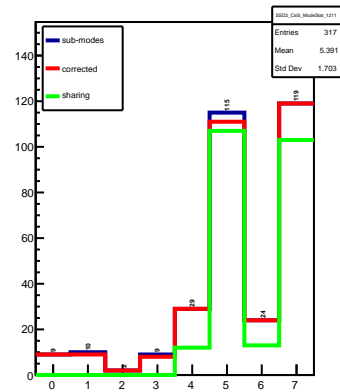
SSD2\_Csl5\_L2L3\_Cuts\_1211



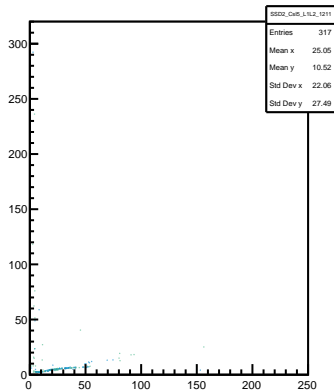
SSD2\_Csl5\_L2L3\_Discard\_1211



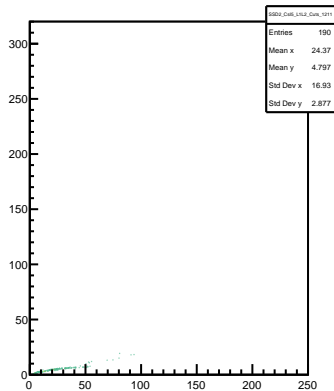
SSD2\_Csl5\_ModeStat\_1211



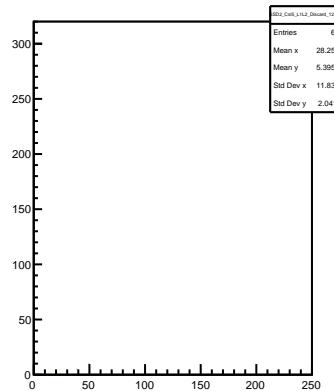
SSD2\_Csl5\_L1L2\_1211



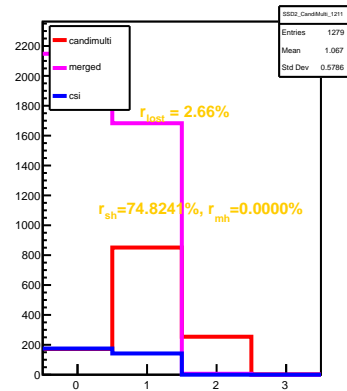
SSD2\_Csl5\_L1L2\_Cuts\_1211



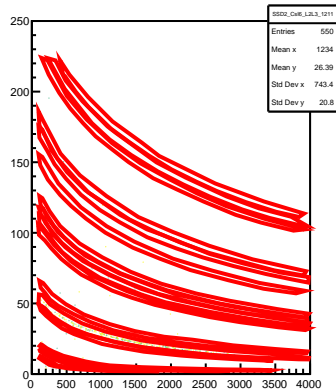
SSD2\_Csl5\_L1L2\_Discard\_1211



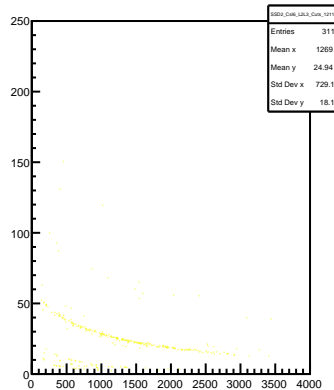
SSD2\_CandiMulti\_1211



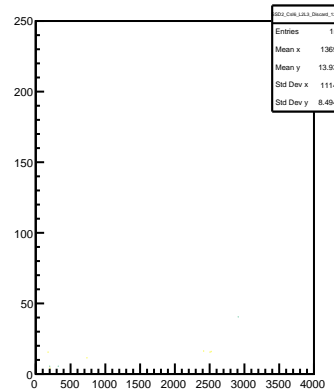
SSD2\_Csl6\_L2L3\_1211



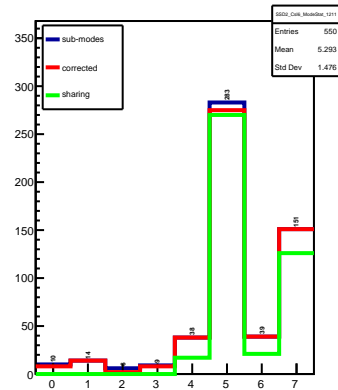
SSD2\_Csl6\_L2L3\_Cuts\_1211



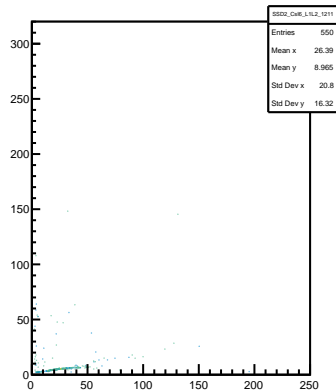
SSD2\_Csl6\_L2L3\_Discard\_1211



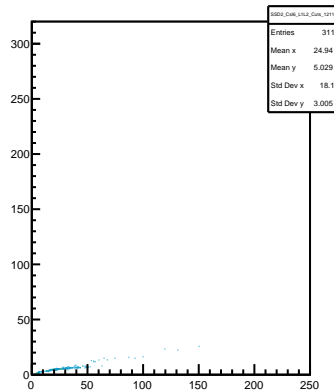
SSD2\_Csl6\_ModeStat\_1211



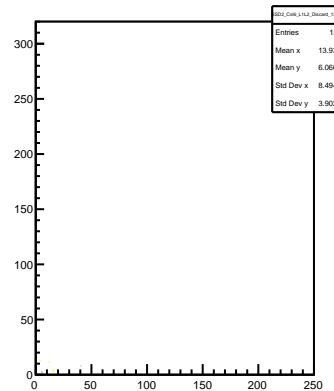
SSD2\_Csl6\_L1L2\_1211



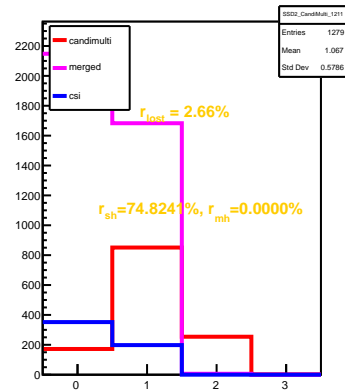
SSD2\_Csl6\_L1L2\_Cuts\_1211



SSD2\_Csl6\_L1L2\_Discard\_1211

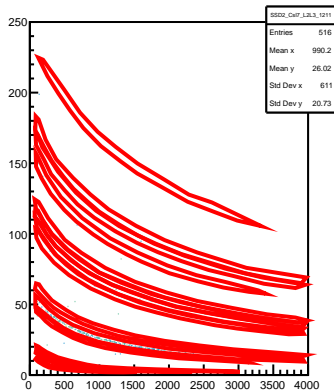


SSD2\_CandiMulti\_1211

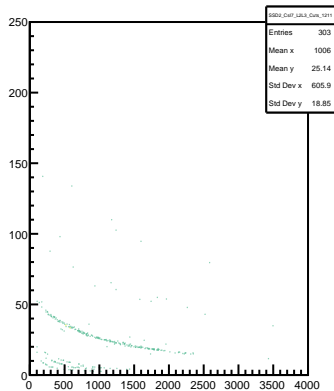




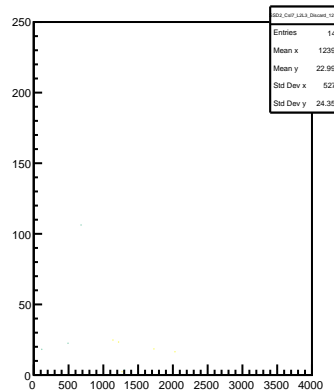
SSD2\_CsI7\_L2L3\_1211



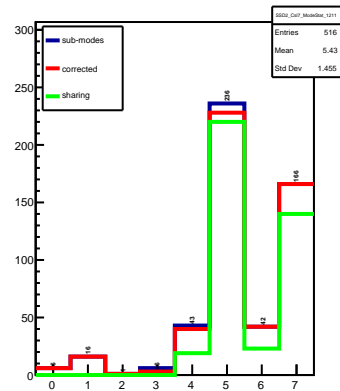
SSD2\_CsI7\_L2L3\_Cuts\_1211



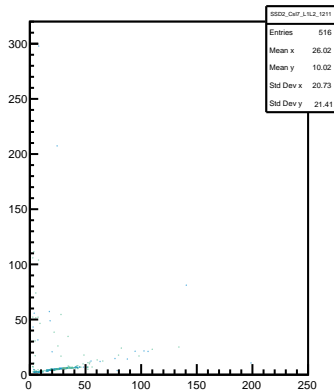
SSD2\_CsI7\_L2L3\_Discard\_1211



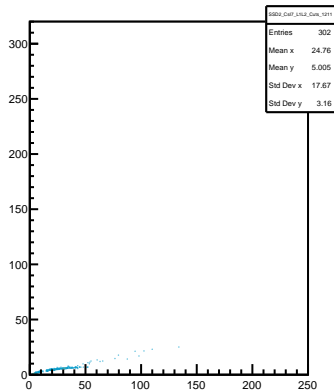
SSD2\_CsI7\_ModeStat\_1211



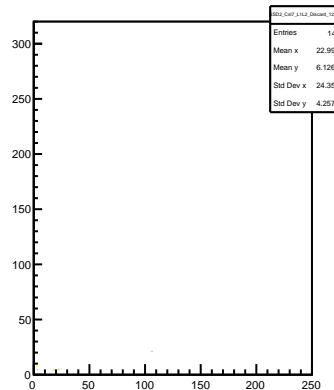
SSD2\_CsI7\_L1L2\_1211



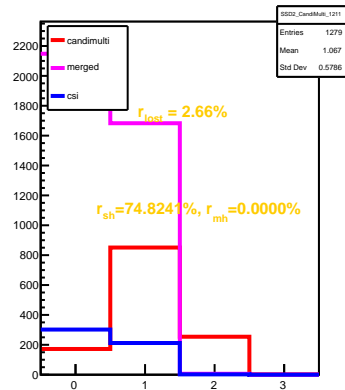
SSD2\_CsI7\_L1L2\_Cuts\_1211



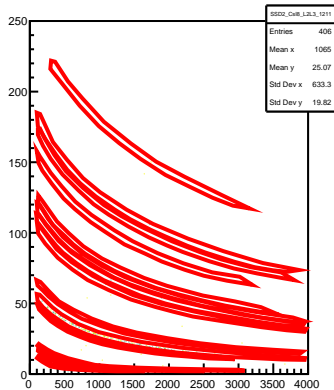
SSD2\_CsI7\_L1L2\_Discard\_1211



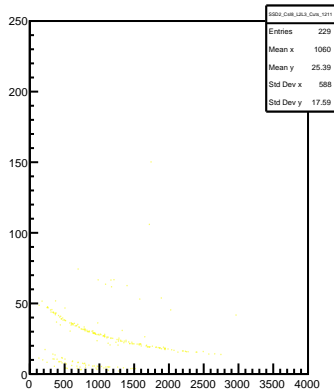
SSD2\_CandiMulti\_1211



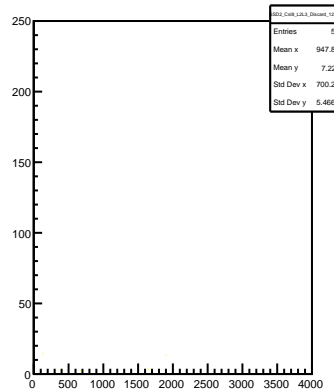
SSD2\_Csl8\_L2L3\_1211



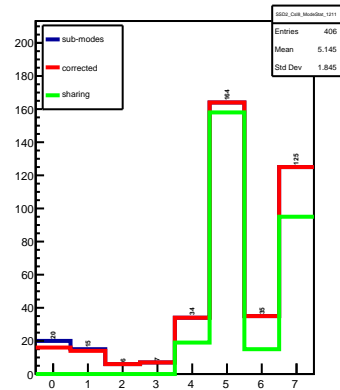
SSD2\_Csl8\_L2L3\_Cuts\_1211



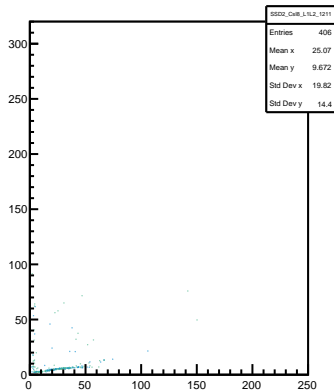
SSD2\_Csl8\_L2L3\_Discard\_1211



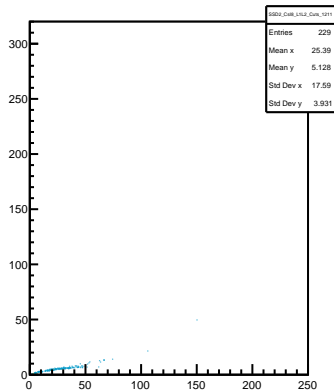
SSD2\_Csl8\_ModeStat\_1211



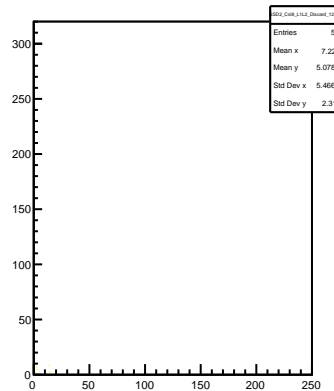
SSD2\_Csl8\_L1L2\_1211



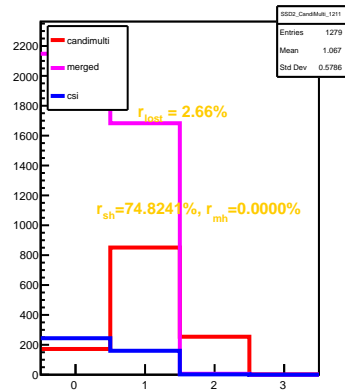
SSD2\_Csl8\_L1L2\_Cuts\_1211



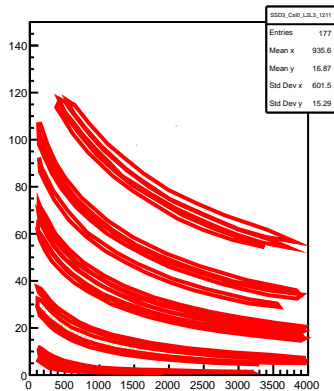
SSD2\_Csl8\_L1L2\_Discard\_1211



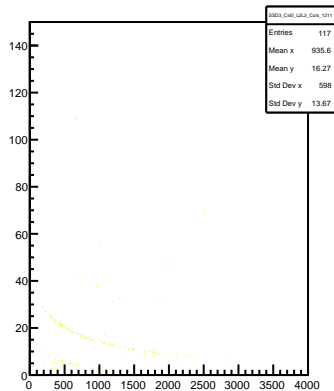
SSD2\_CandiMulti\_1211



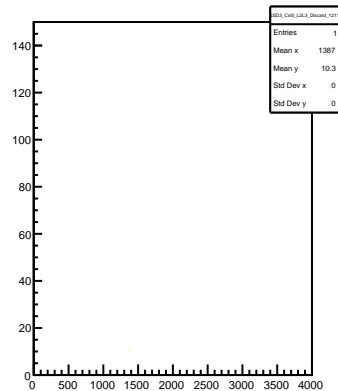
SSD3\_Csl0\_L2L3\_1211



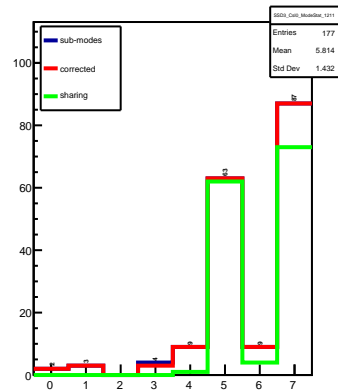
SSD3\_Csl0\_L2L3\_Cuts\_1211



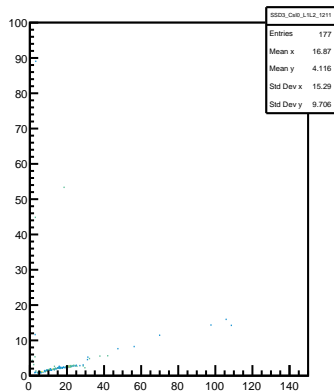
SSD3\_Csl0\_L2L3\_Discard\_1211



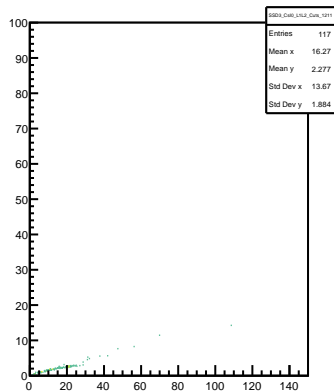
SSD3\_Csl0\_ModeStat\_1211



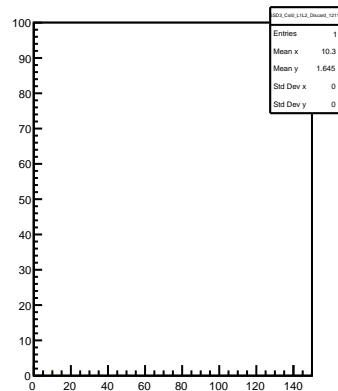
SSD3\_Csl0\_L1L2\_1211



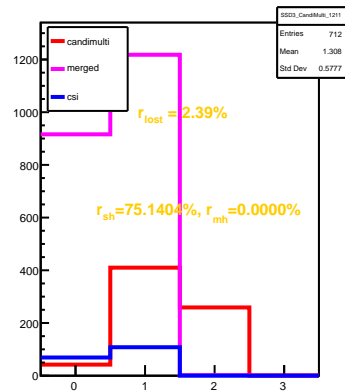
SSD3\_Csl0\_L1L2\_Cuts\_1211



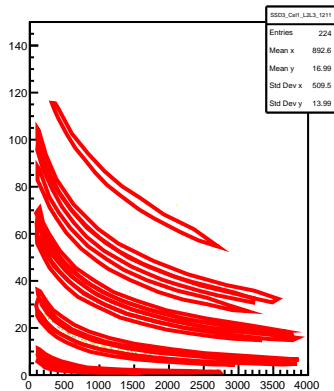
SSD3\_Csl0\_L1L2\_Discard\_1211



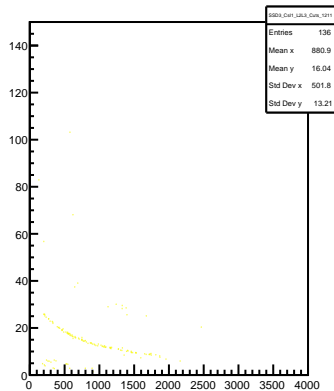
SSD3\_CandiMulti\_1211



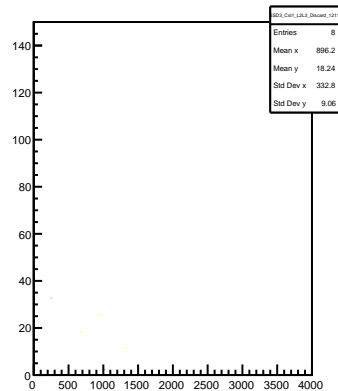
SSD3\_Csl1\_L2L3\_1211



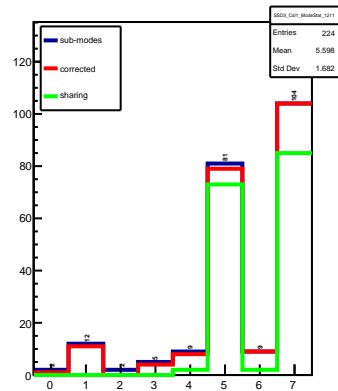
SSD3\_Csl1\_L2L3\_Cuts\_1211



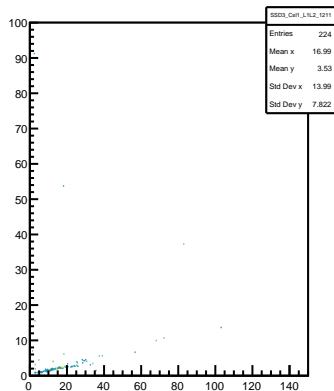
SSD3\_Csl1\_L2L3\_Discard\_1211



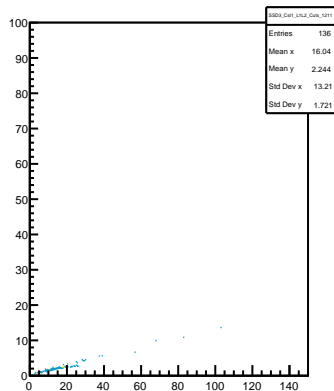
SSD3\_Csl1\_ModeStat\_1211



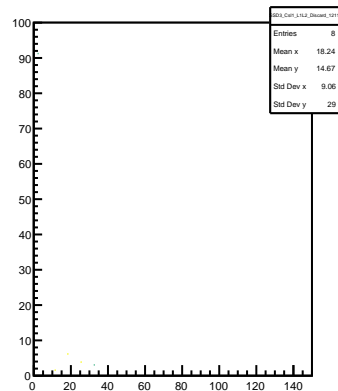
SSD3\_Csl1\_L1L2\_1211



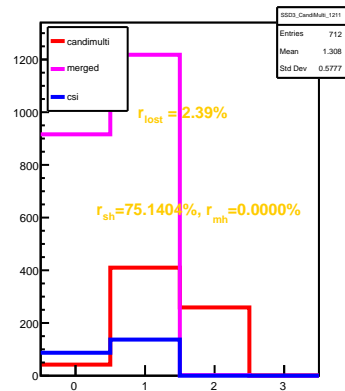
SSD3\_Csl1\_L1L2\_Cuts\_1211



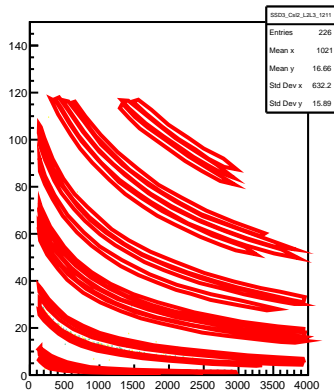
SSD3\_Csl1\_L1L2\_Discard\_1211



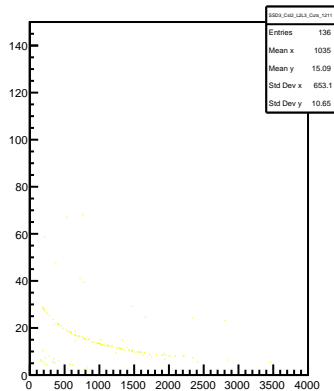
SSD3\_CandiMulti\_1211



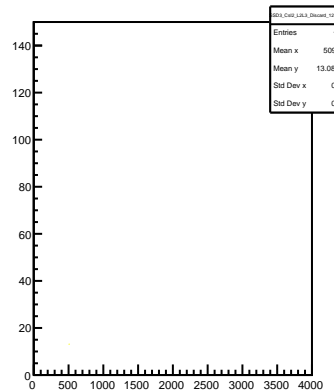
SSD3\_Csl2\_L2L3\_1211



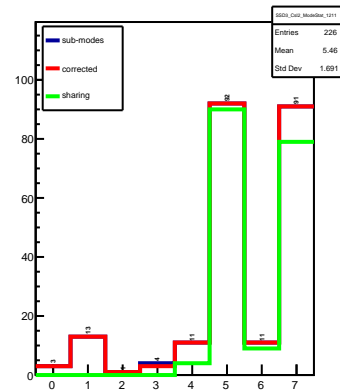
SSD3\_Csl2\_L2L3\_Cuts\_1211



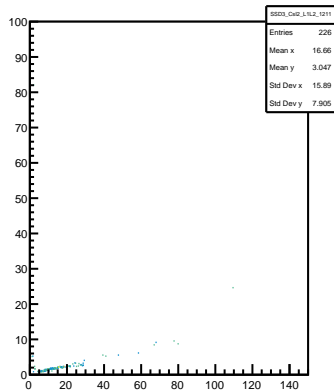
SSD3\_Csl2\_L2L3\_Discard\_1211



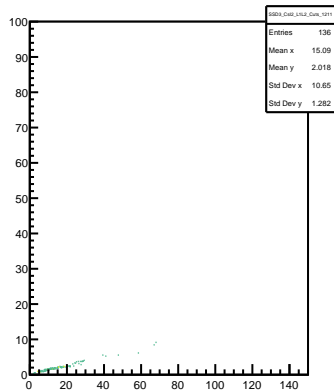
SSD3\_Csl2\_ModeStat\_1211



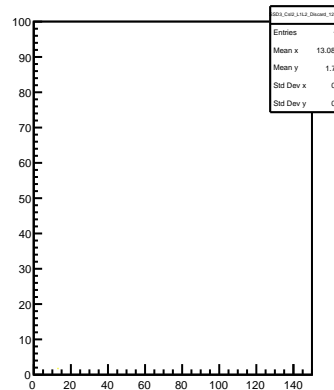
SSD3\_Csl2\_L1L2\_1211



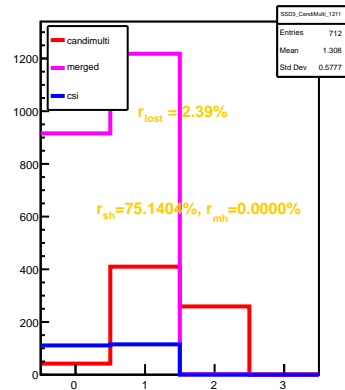
SSD3\_Csl2\_L1L2\_Cuts\_1211



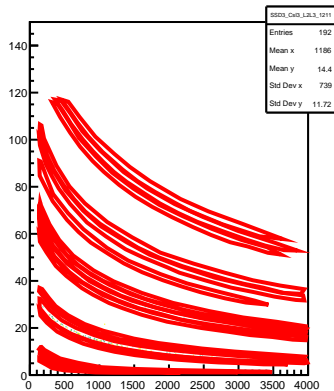
SSD3\_Csl2\_L1L2\_Discard\_1211



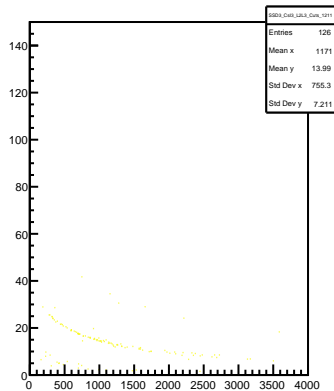
SSD3\_CandiMulti\_1211



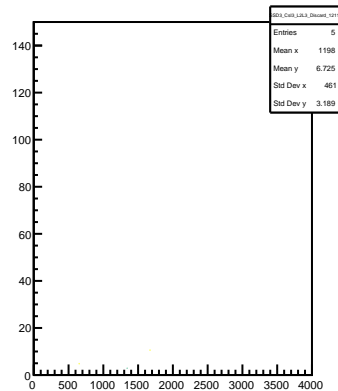
SSD3\_Csl3\_L2L3\_1211



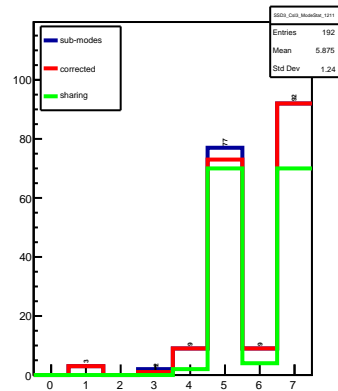
SSD3\_Csl3\_L2L3\_Cuts\_1211



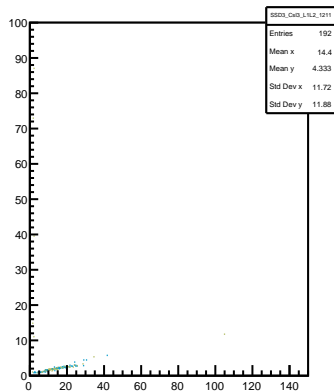
SSD3\_Csl3\_L2L3\_Discard\_1211



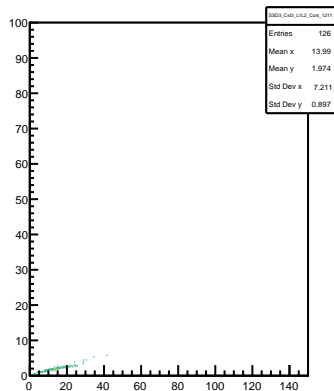
SSD3\_Csl3\_ModeStat\_1211



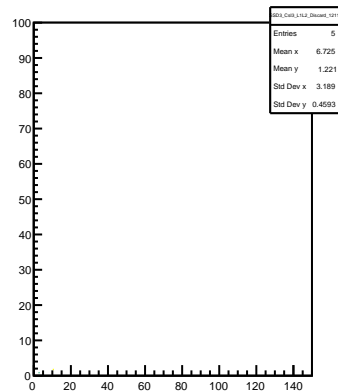
SSD3\_Csl3\_L1L2\_1211



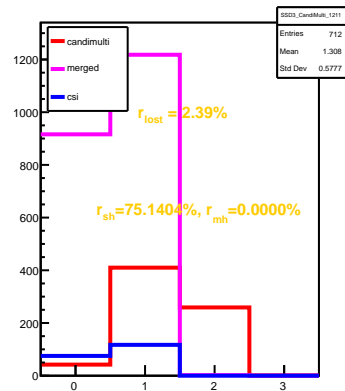
SSD3\_Csl3\_L1L2\_Cuts\_1211



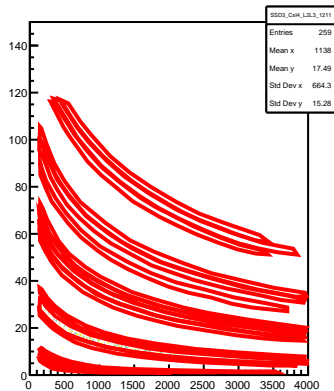
SSD3\_Csl3\_L1L2\_Discard\_1211



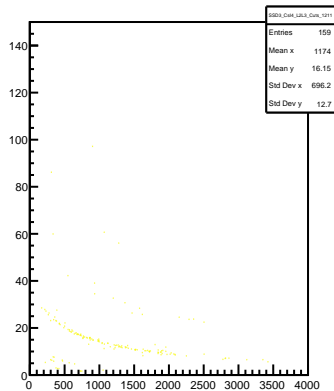
SSD3\_CandiMulti\_1211



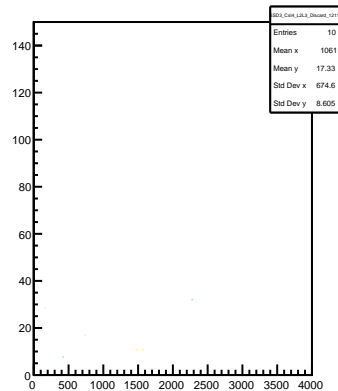
SSD3\_Csl4\_L2L3\_1211



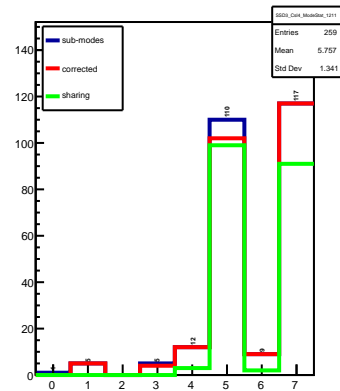
SSD3\_Csl4\_L2L3\_Cuts\_1211



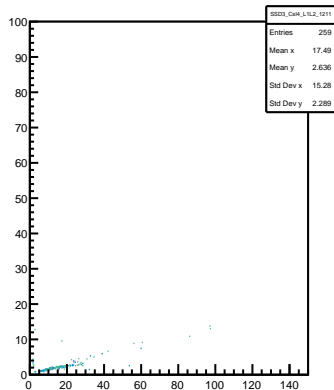
SSD3\_Csl4\_L2L3\_Discard\_1211



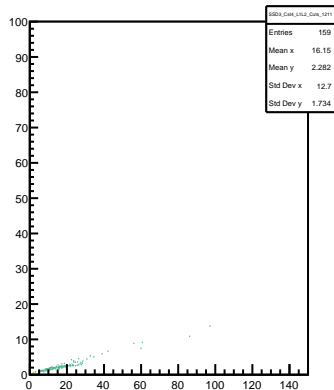
SSD3\_Csl4\_ModeStat\_1211



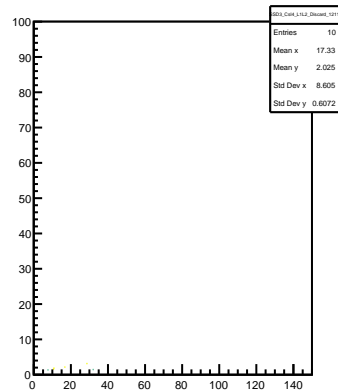
SSD3\_Csl4\_L1L2\_1211



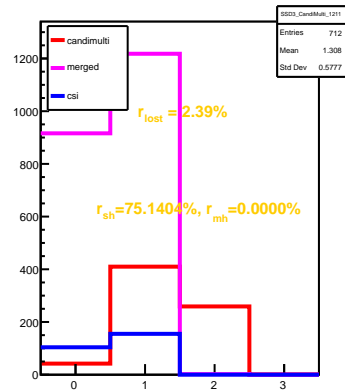
SSD3\_Csl4\_L1L2\_Cuts\_1211



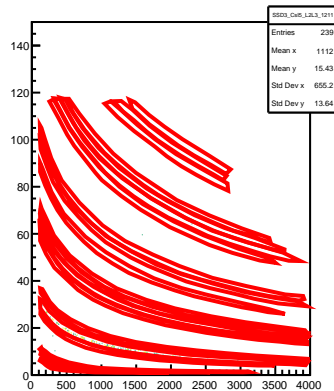
SSD3\_Csl4\_L1L2\_Discard\_1211



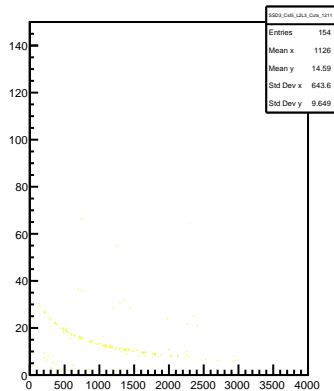
SSD3\_CandiMulti\_1211



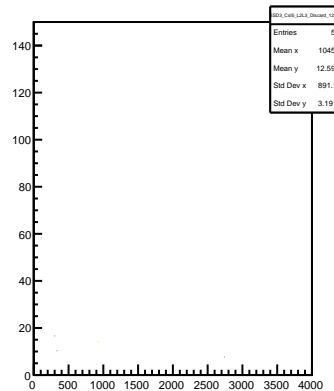
SSD3\_Csl5\_L2L3\_1211



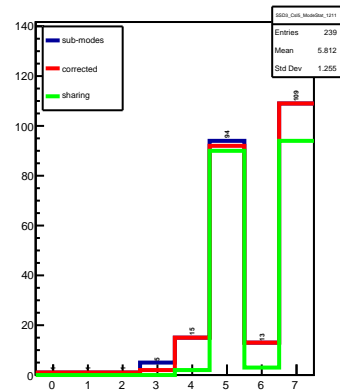
SSD3\_Csl5\_L2L3\_Cuts\_1211



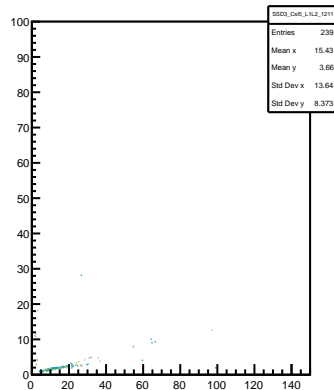
SSD3\_Csl5\_L2L3\_Discard\_1211



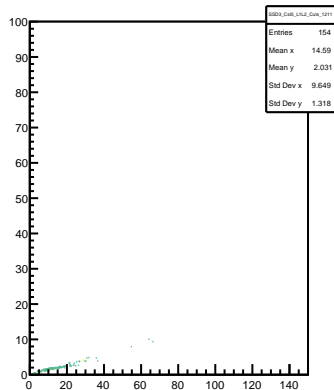
SSD3\_Csl5\_ModeStat\_1211



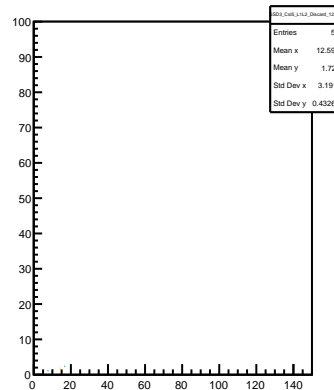
SSD3\_Csl5\_L1L2\_1211



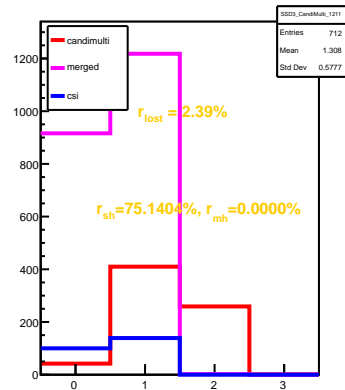
SSD3\_Csl5\_L1L2\_Cuts\_1211



SSD3\_Csl5\_L1L2\_Discard\_1211

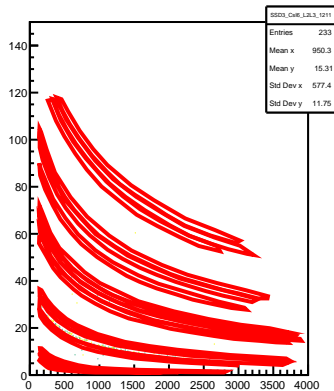


SSD3\_CandiMulti\_1211

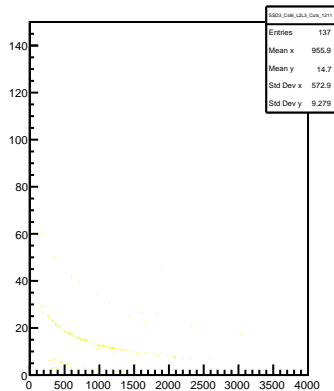




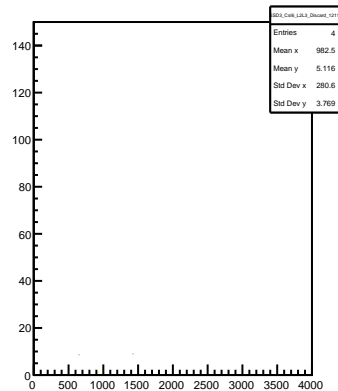
SSD3\_Csl6\_L2L3\_1211



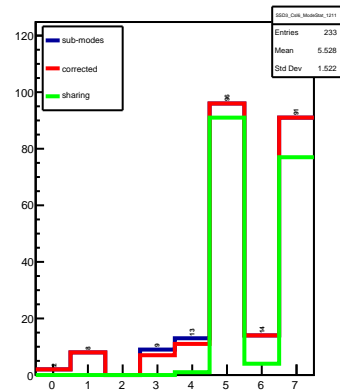
SSD3\_Csl6\_L2L3\_Cuts\_1211



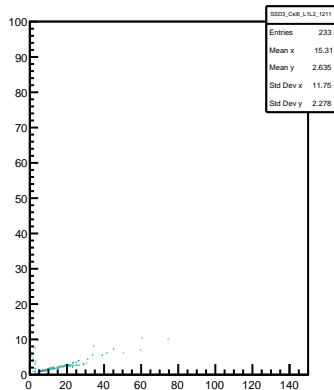
SSD3\_Csl6\_L2L3\_Discard\_1211



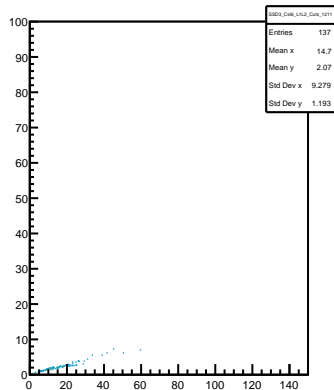
SSD3\_Csl6\_ModeStat\_1211



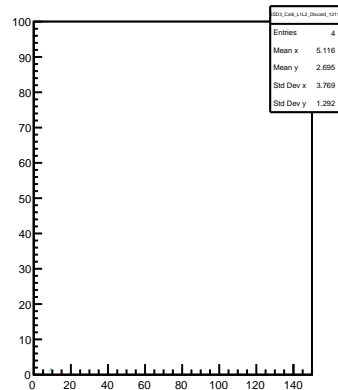
SSD3\_Csl6\_L1L2\_1211



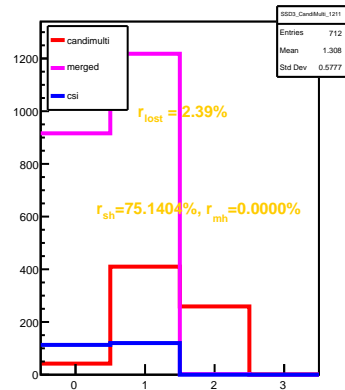
SSD3\_Csl6\_L1L2\_Cuts\_1211



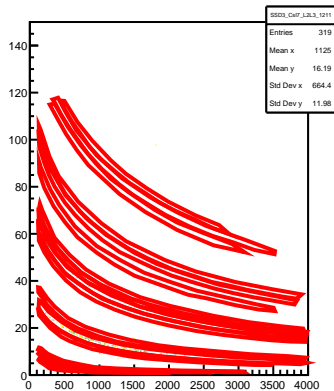
SSD3\_Csl6\_L1L2\_Discard\_1211



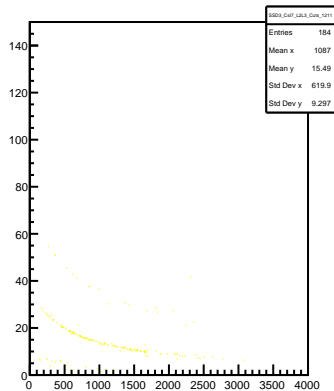
SSD3\_CandiMulti\_1211



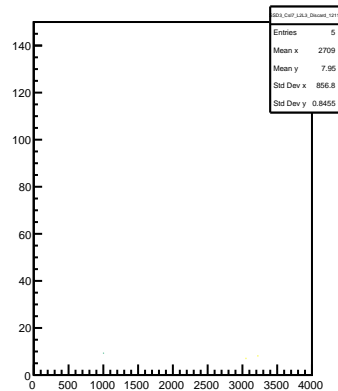
SSD3\_Csl7\_L2L3\_1211



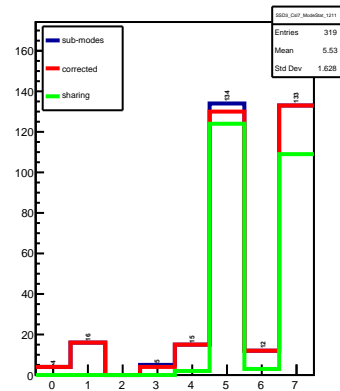
SSD3\_Csl7\_L2L3\_Cuts\_1211



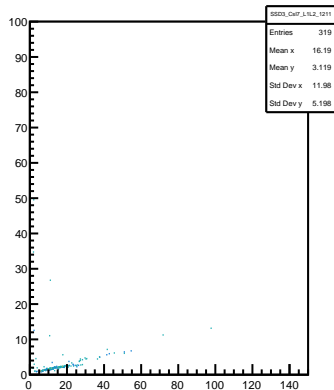
SSD3\_Csl7\_L2L3\_Discard\_1211



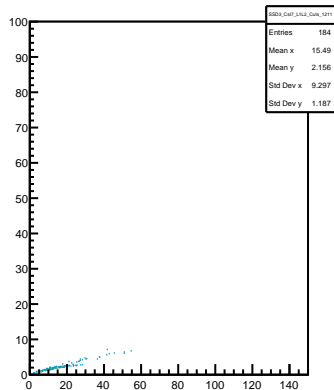
SSD3\_Csl7\_ModeStat\_1211



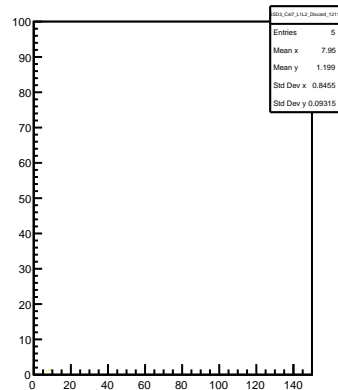
SSD3\_Csl7\_L1L2\_1211



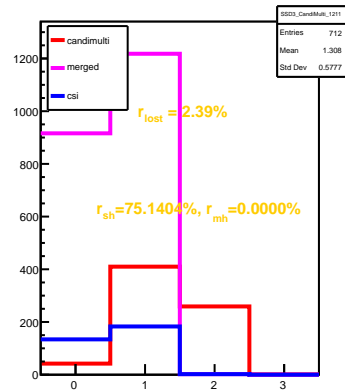
SSD3\_Csl7\_L1L2\_Cuts\_1211



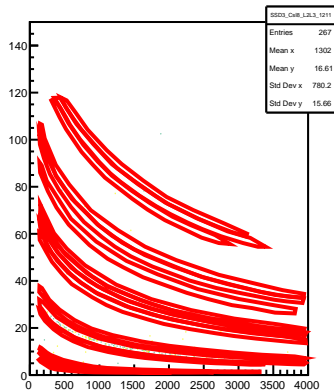
SSD3\_Csl7\_L1L2\_Discard\_1211



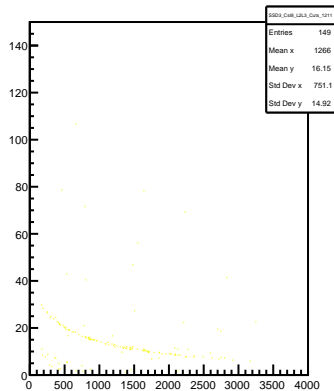
SSD3\_CandiMulti\_1211



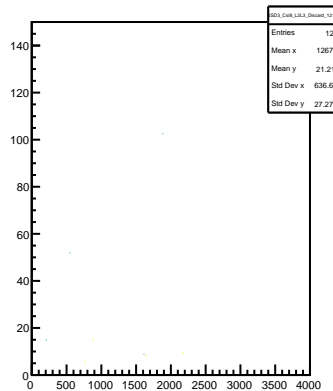
SSD3\_Csl8\_L2L3\_1211



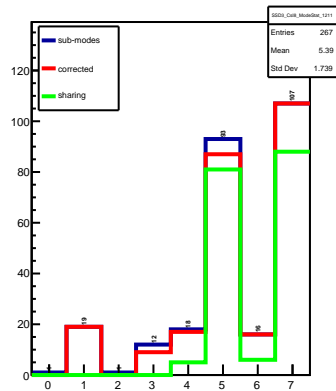
SSD3\_Csl8\_L2L3\_Cuts\_1211



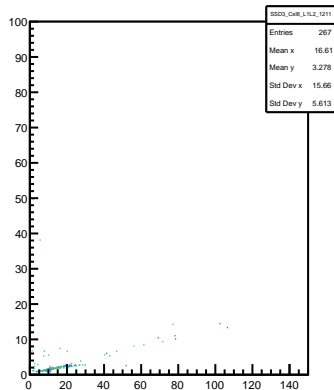
SSD3\_Csl8\_L2L3\_Discard\_1211



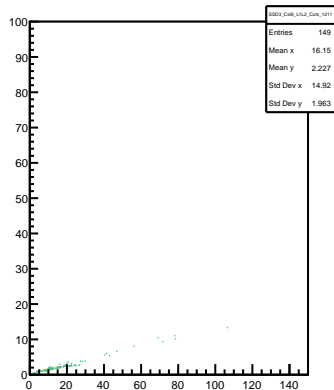
SSD3\_Csl8\_ModeStat\_1211



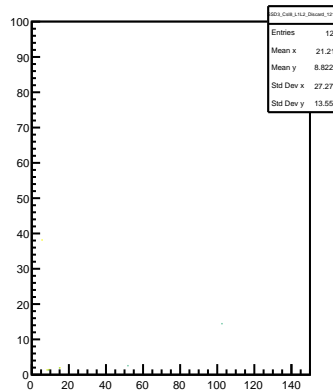
SSD3\_Csl8\_L1L2\_1211



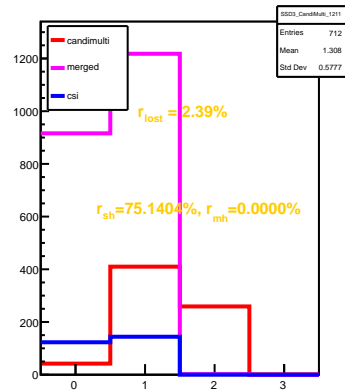
SSD3\_Csl8\_L1L2\_Cuts\_1211



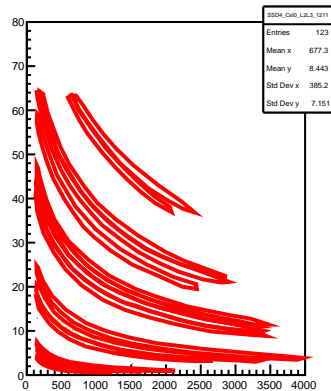
SSD3\_Csl8\_L1L2\_Discard\_1211



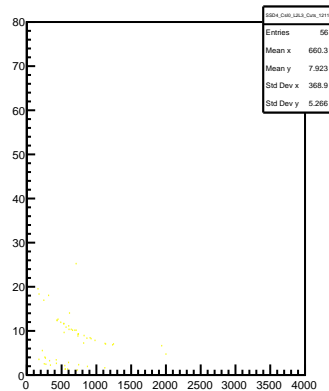
SSD3\_CandiMulti\_1211



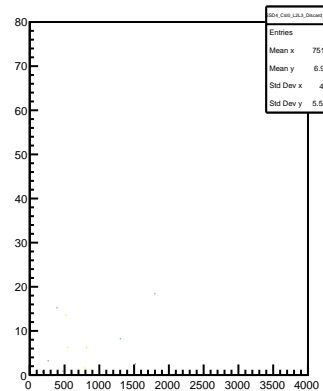
SSD4\_Csl0\_L2L3\_1211



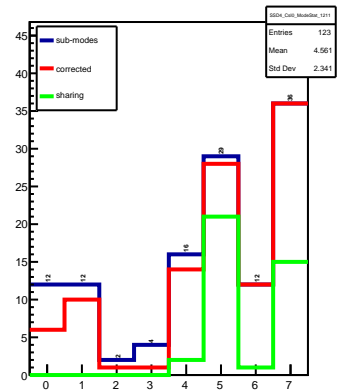
SSD4\_Csl0\_L2L3\_Cuts\_1211



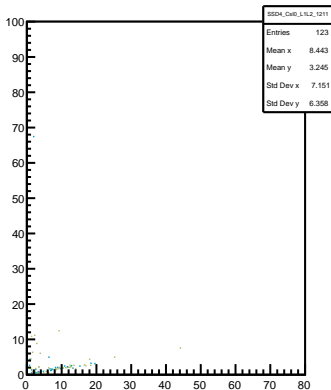
SSD4\_Csl0\_L2L3\_Discard\_1211



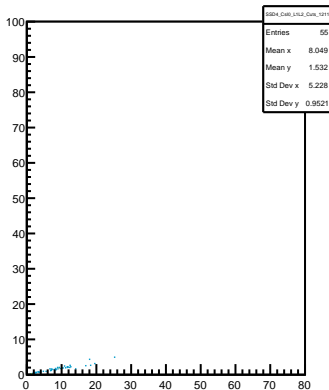
SSD4\_Csl0\_ModeStat\_1211



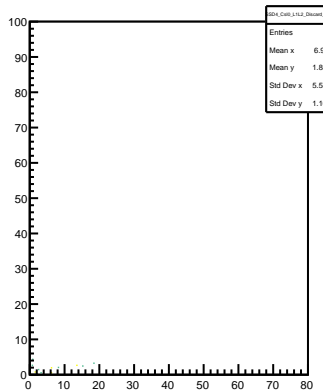
SSD4\_Csl0\_L1L2\_1211



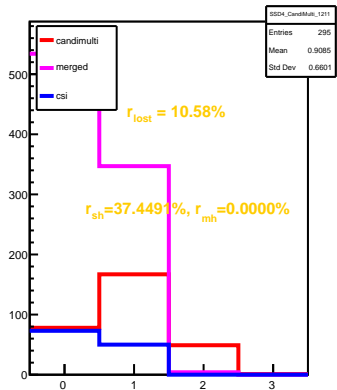
SSD4\_Csl0\_L1L2\_Cuts\_1211



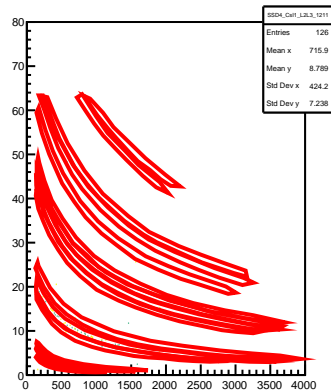
SSD4\_Csl0\_L1L2\_Discard\_1211



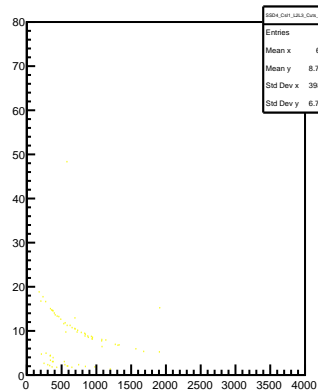
SSD4\_CandiMulti\_1211



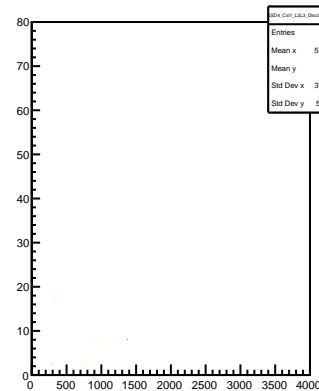
SSD4\_Cs1\_L2L3\_1211



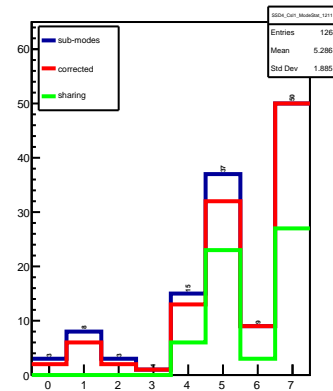
SSD4\_Cs1\_L2L3\_Cuts\_1211



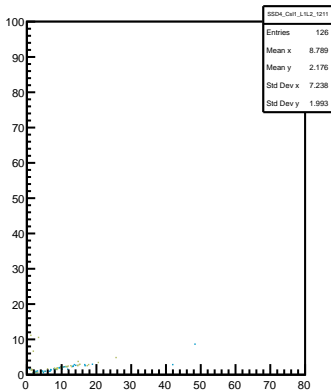
SSD4\_Cs1\_L2L3\_Discard\_1211



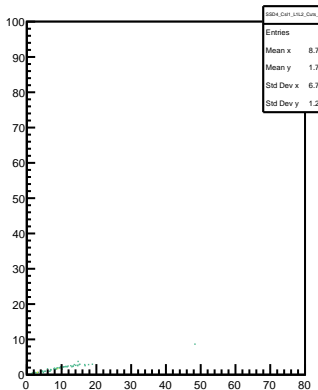
SSD4\_Cs1\_ModeStat\_1211



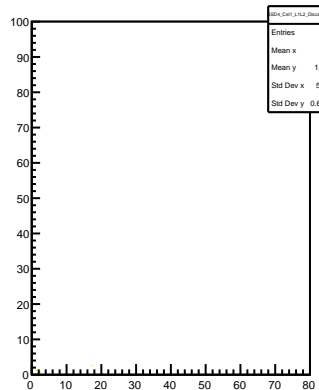
SSD4\_Cs1\_L1L2\_1211



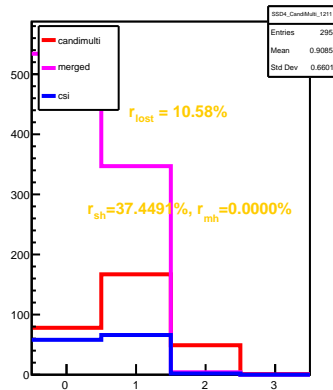
SSD4\_Cs1\_L1L2\_Cuts\_1211



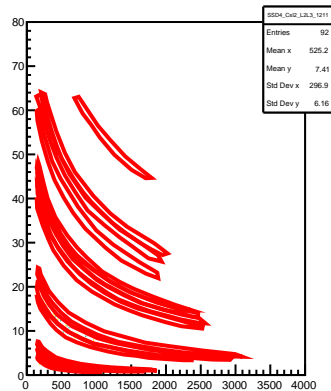
SSD4\_Cs1\_L1L2\_Discard\_1211



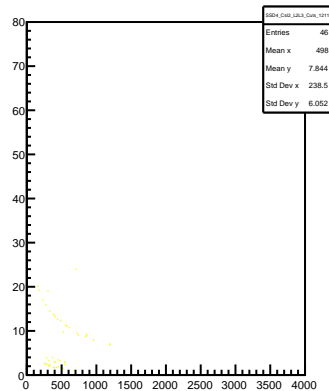
SSD4\_CandiMulti\_1211



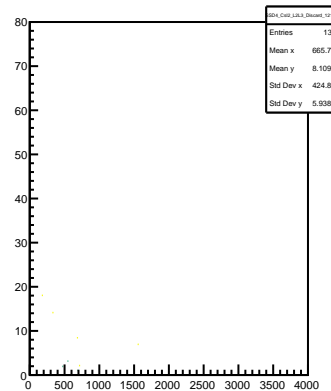
SSD4\_CsI2\_L2L3\_1211



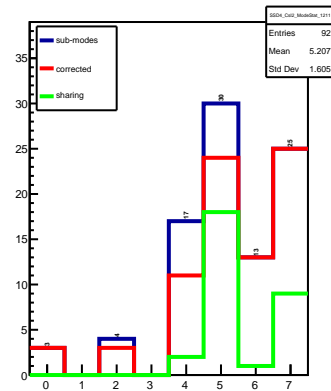
SSD4\_CsI2\_L2L3\_Cuts\_1211



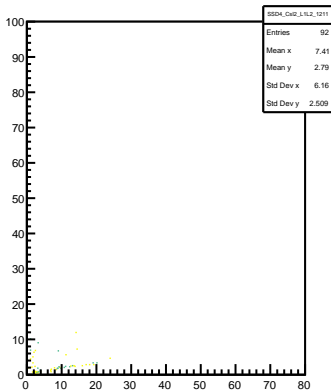
SSD4\_CsI2\_L2L3\_Discard\_1211



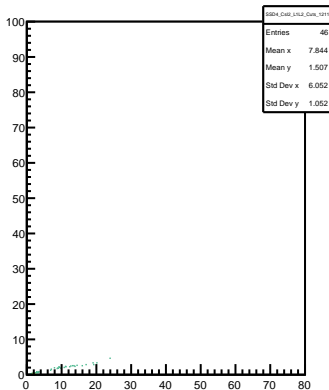
SSD4\_CsI2\_ModeStat\_1211



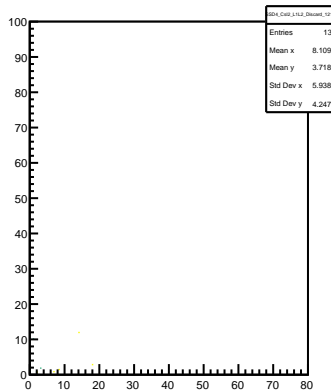
SSD4\_CsI2\_L1L2\_1211



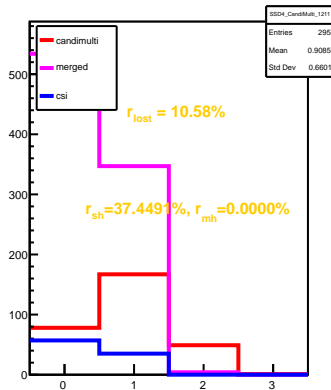
SSD4\_CsI2\_L1L2\_Cuts\_1211



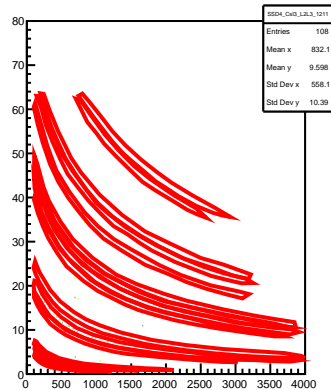
SSD4\_CsI2\_L1L2\_Discard\_1211



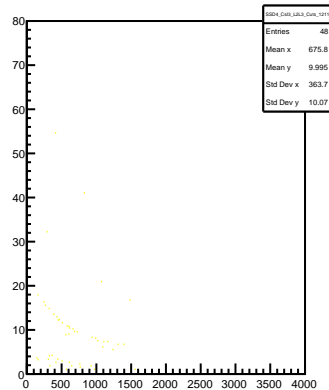
SSD4\_CandiMulti\_1211



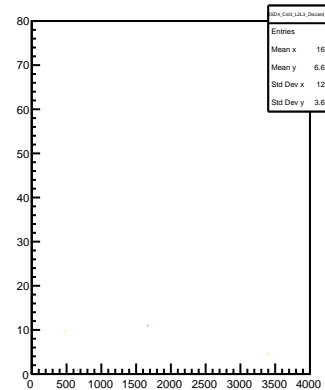
SSD4\_Csl3\_L2L3\_1211



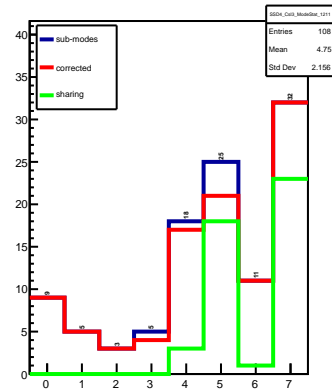
SSD4\_Csl3\_L2L3\_Cuts\_1211



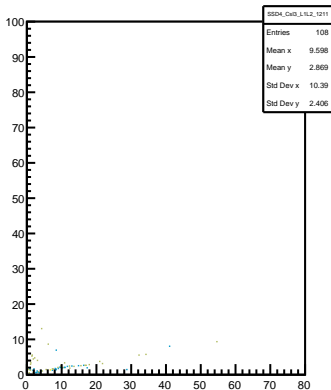
SSD4\_Csl3\_L2L3\_Discard\_1211



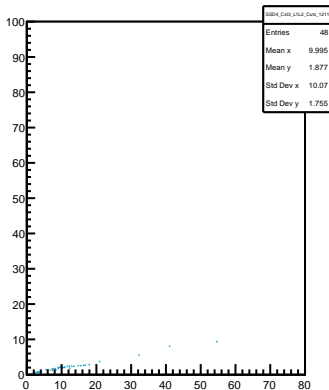
SSD4\_Csl3\_ModeStat\_1211



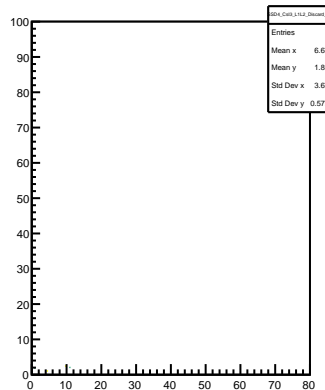
SSD4\_Csl3\_L1L2\_1211



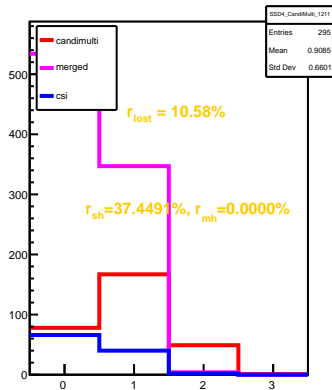
SSD4\_Csl3\_L1L2\_Cuts\_1211



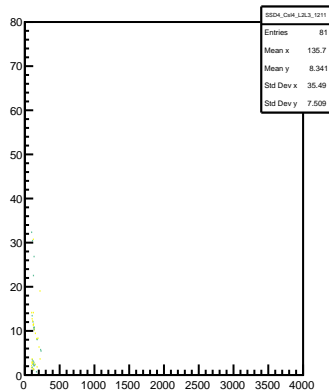
SSD4\_Csl3\_L1L2\_Discard\_1211



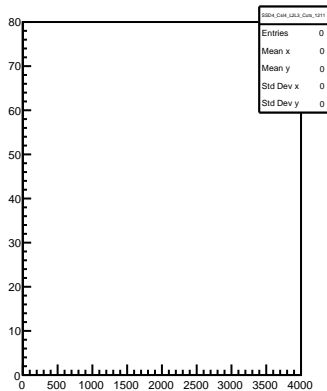
SSD4\_CandiMulti\_1211



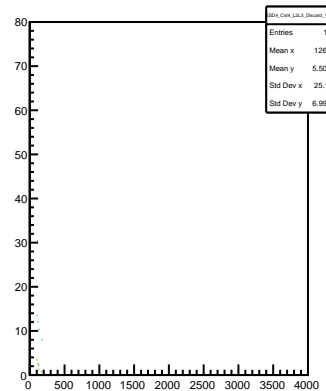
SSD4\_Csl4\_L2L3\_1211



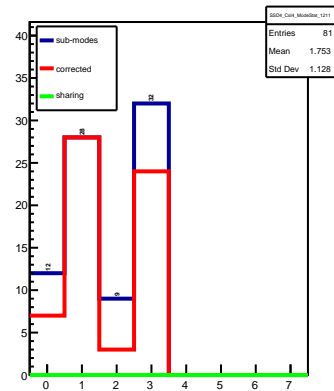
SSD4\_Csl4\_L2L3\_Cuts\_1211



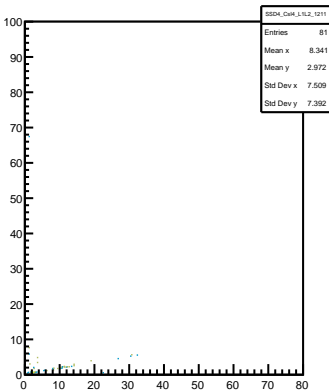
SSD4\_Csl4\_L2L3\_Discard\_1211



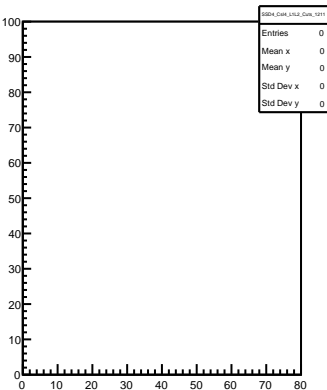
SSD4\_Csl4\_ModeStat\_1211



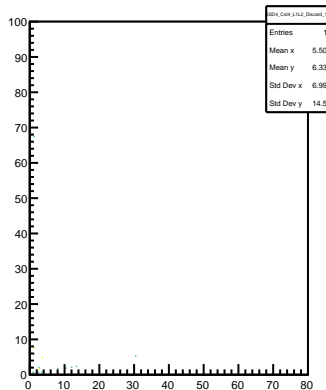
SSD4\_Csl4\_L1L2\_1211



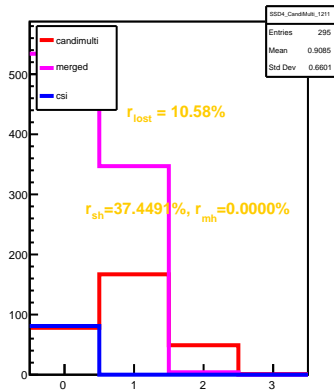
SSD4\_Csl4\_L1L2\_Cuts\_1211



SSD4\_Csl4\_L1L2\_Discard\_1211

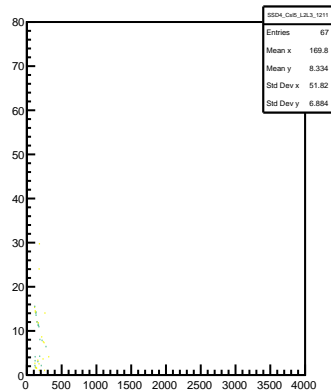


SSD4\_CandiMulti\_1211

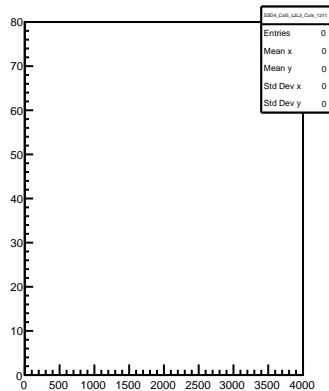




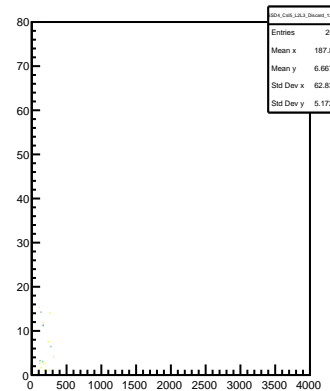
SSD4\_Csl5\_L2L3\_1211



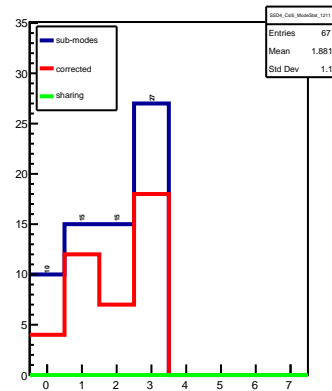
SSD4\_Csl5\_L2L3\_Cuts\_1211



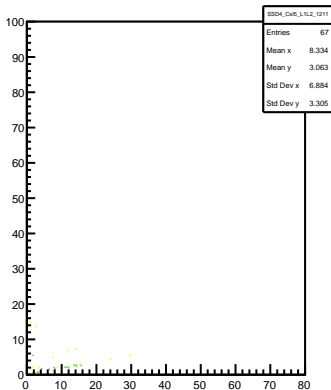
SSD4\_Csl5\_L2L3\_Discard\_1211



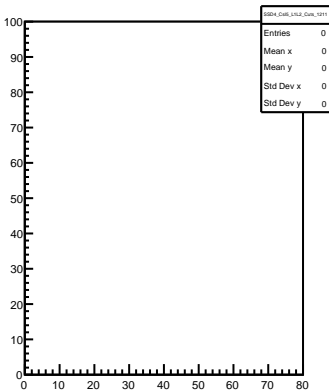
SSD4\_Csl5\_ModeStat\_1211



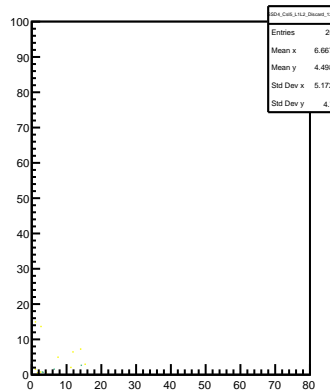
SSD4\_Csl5\_L1L2\_1211



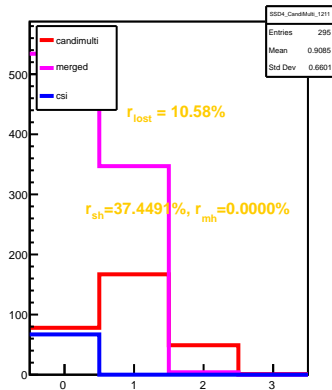
SSD4\_Csl5\_L1L2\_Cuts\_1211



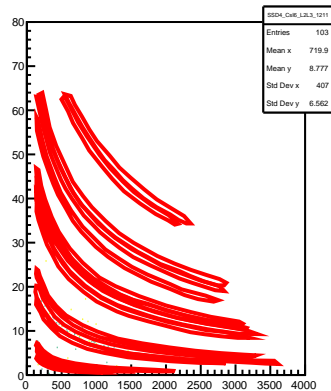
SSD4\_Csl5\_L1L2\_Discard\_1211



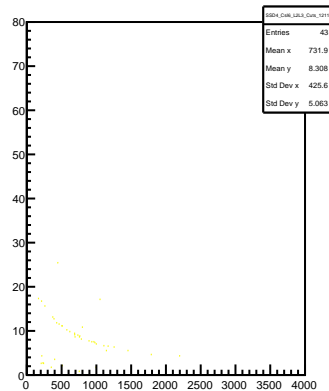
SSD4\_CandiMulti\_1211



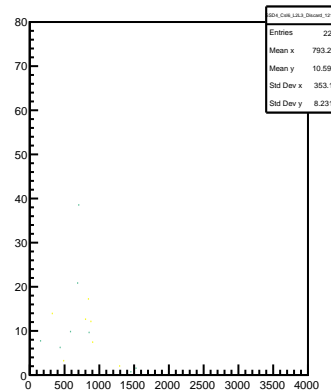
SSD4\_Csl6\_L2L3\_1211



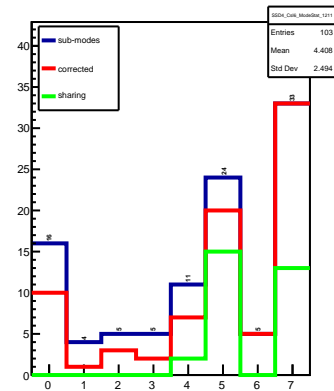
SSD4\_Csl6\_L2L3\_Cuts\_1211



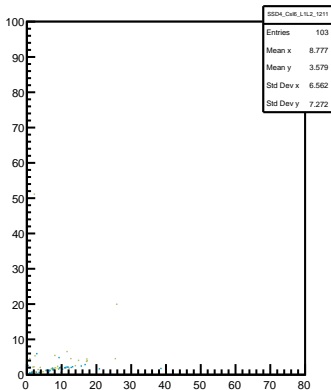
SSD4\_Csl6\_L2L3\_Discard\_1211



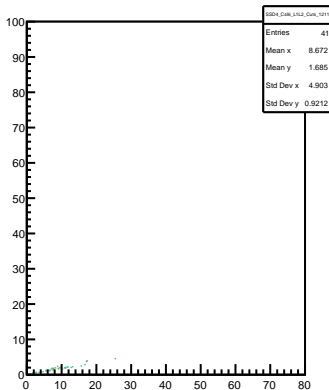
SSD4\_Csl6\_ModeStat\_1211



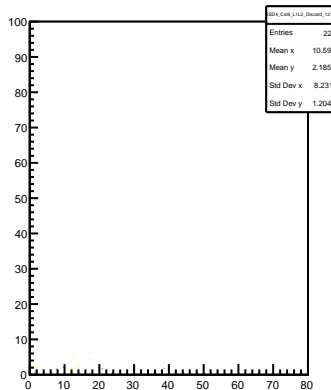
SSD4\_Csl6\_L1L2\_1211



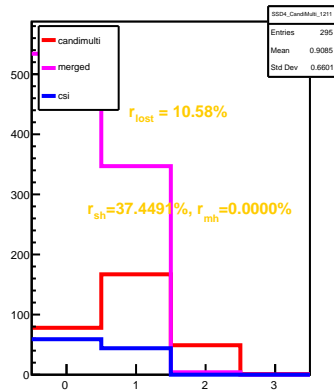
SSD4\_Csl6\_L1L2\_Cuts\_1211



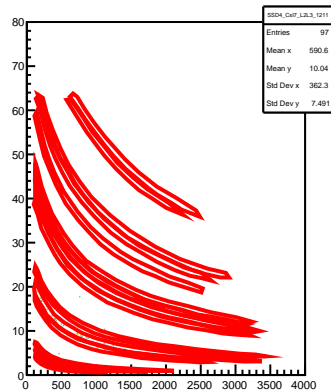
SSD4\_Csl6\_L1L2\_Discard\_1211



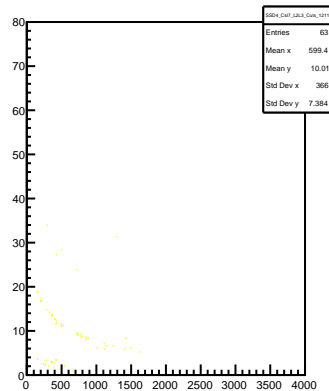
SSD4\_CandiMulti\_1211



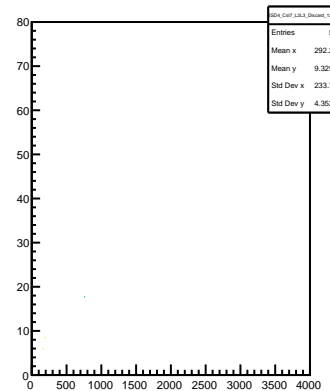
SSD4\_CsI7\_L2L3\_1211



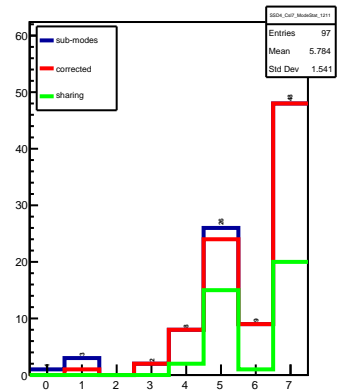
SSD4\_CsI7\_L2L3\_Cuts\_1211



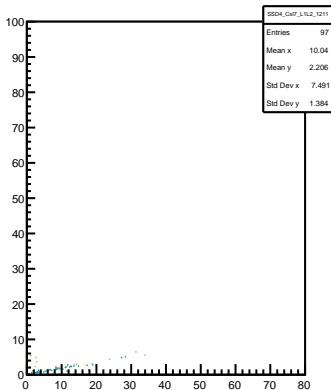
SSD4\_CsI7\_L2L3\_Discard\_1211



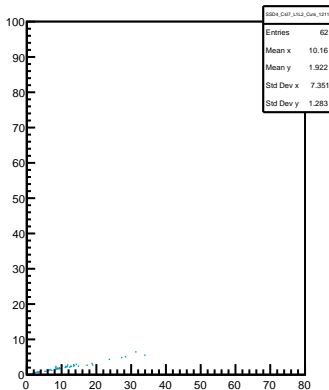
SSD4\_CsI7\_ModeStat\_1211



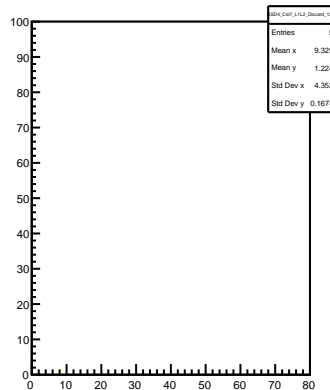
SSD4\_CsI7\_L1L2\_1211



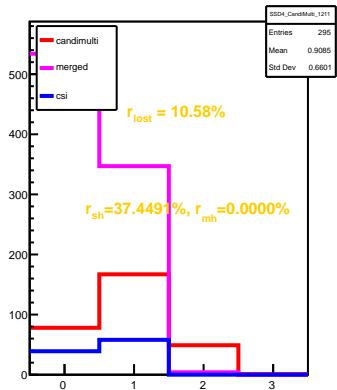
SSD4\_CsI7\_L1L2\_Cuts\_1211



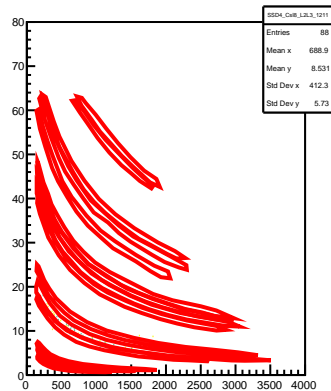
SSD4\_CsI7\_L1L2\_Discard\_1211



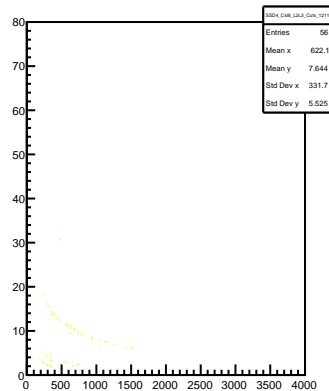
SSD4\_CandiMulti\_1211



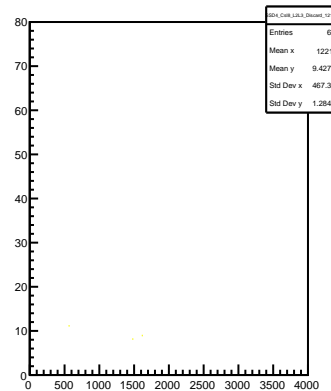
SSD4\_Csl8\_L2L3\_1211



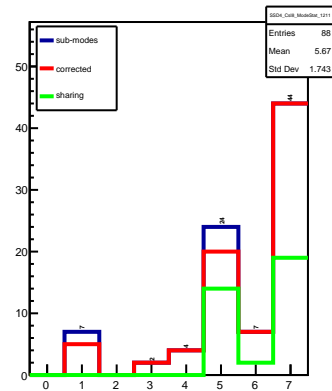
SSD4\_Csl8\_L2L3\_Cuts\_1211



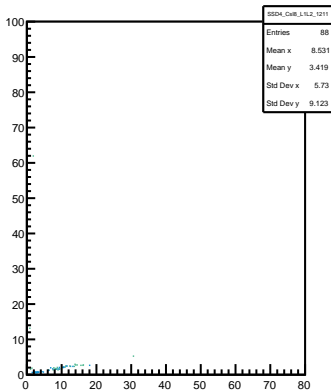
SSD4\_Csl8\_L2L3\_Discard\_1211



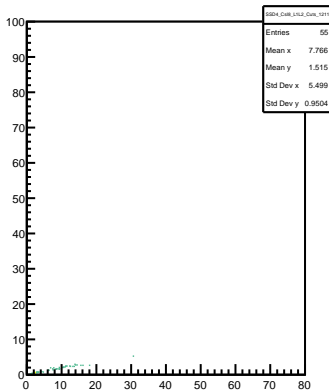
SSD4\_Csl8\_ModeStat\_1211



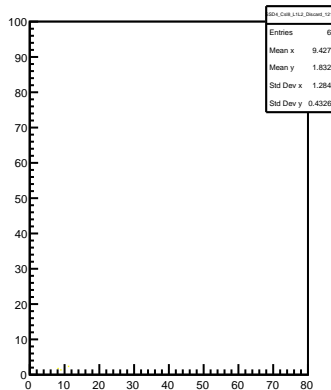
SSD4\_Csl8\_L1L2\_1211



SSD4\_Csl8\_L1L2\_Cuts\_1211



SSD4\_Csl8\_L1L2\_Discard\_1211



SSD4\_CandiMulti\_1211

