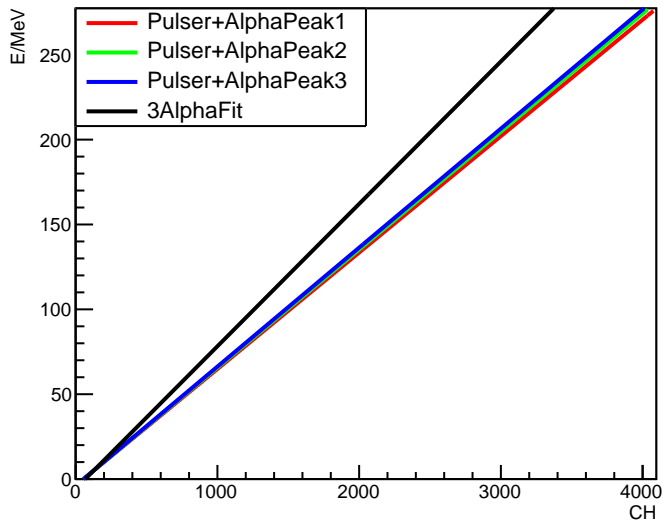
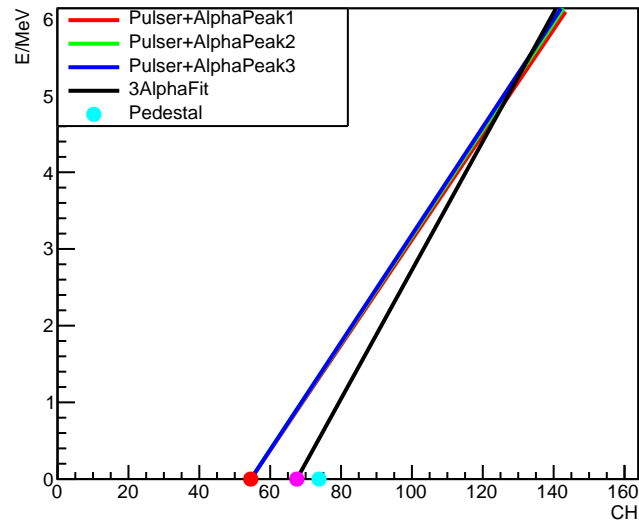


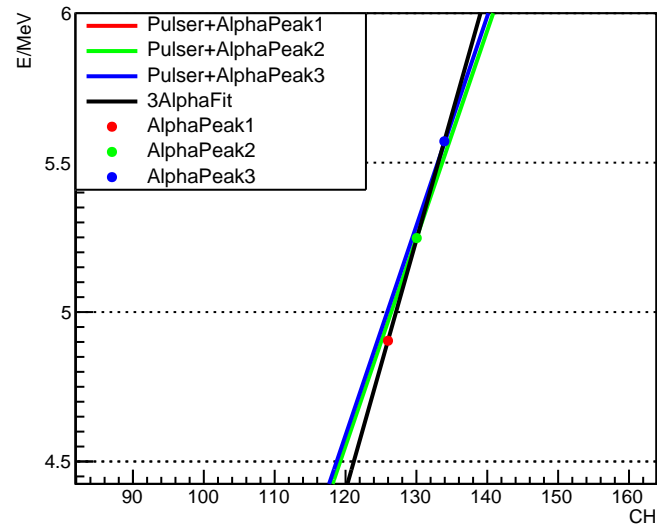
SSD1_L2F_Switch_AlphaCali00_32_CH00



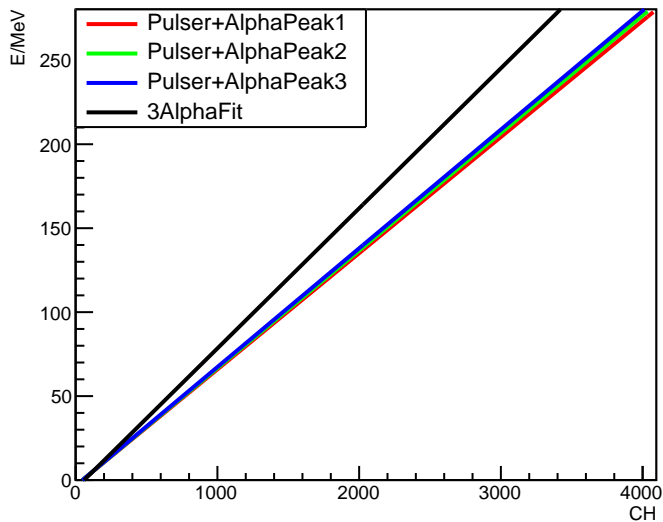
SSD1_L2F_Switch_AlphaCali00_32_CH00



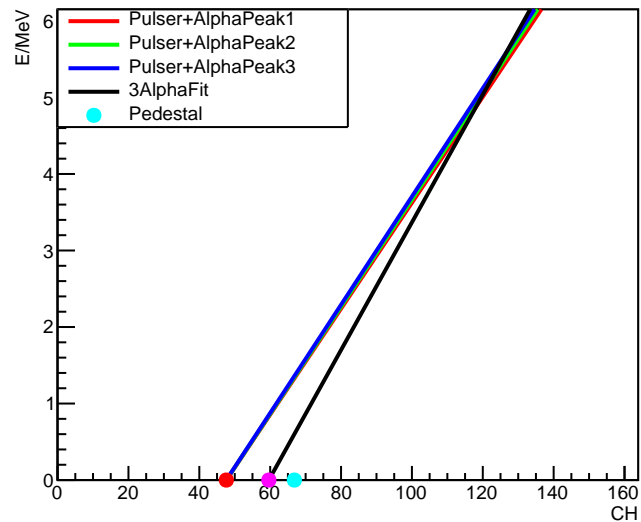
SSD1_L2F_Switch_AlphaCali00_32_CH00



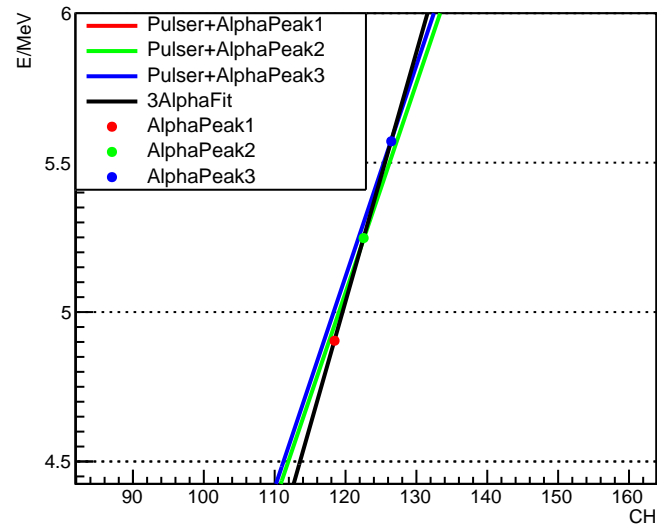
SSD1_L2F_Switch_AlphaCali00_32_CH01



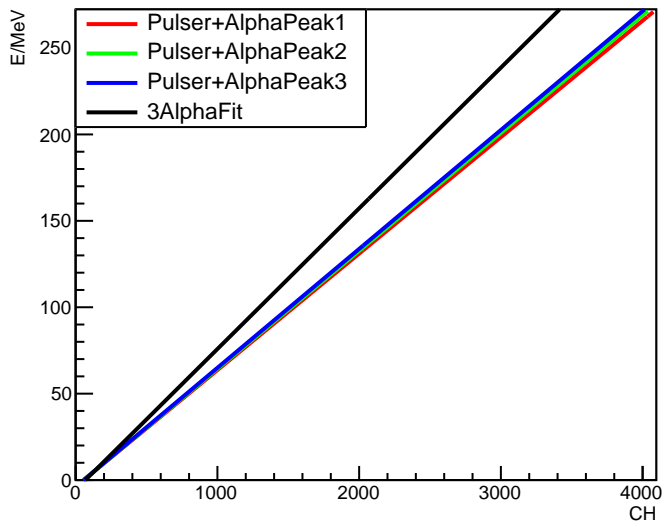
SSD1_L2F_Switch_AlphaCali00_32_CH01



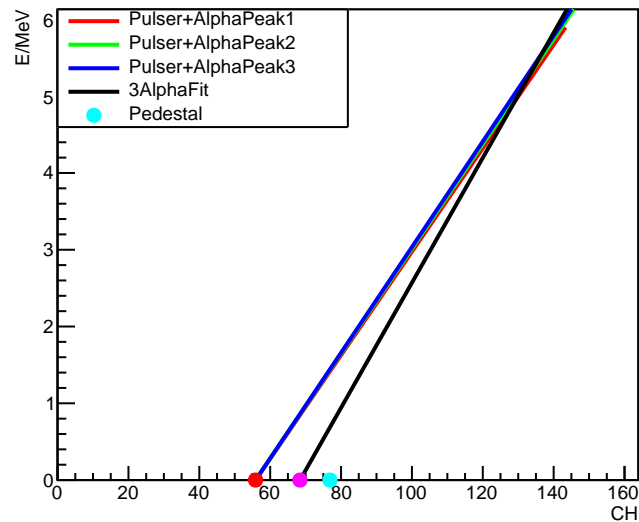
SSD1_L2F_Switch_AlphaCali00_32_CH01



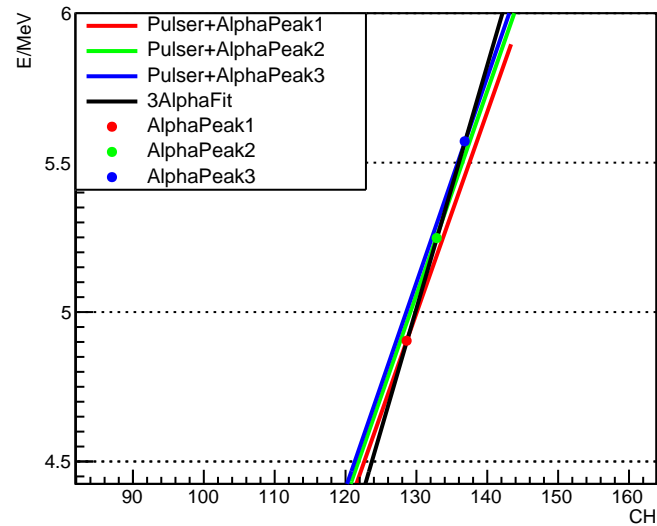
SSD1_L2F_Switch_AlphaCali00_32_CH02



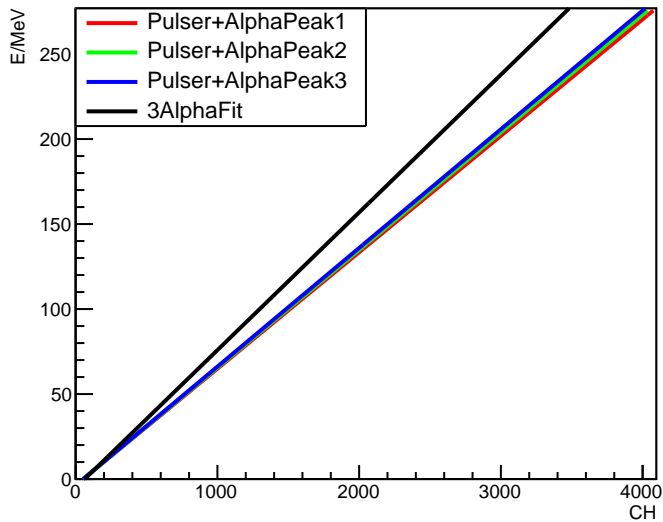
SSD1_L2F_Switch_AlphaCali00_32_CH02



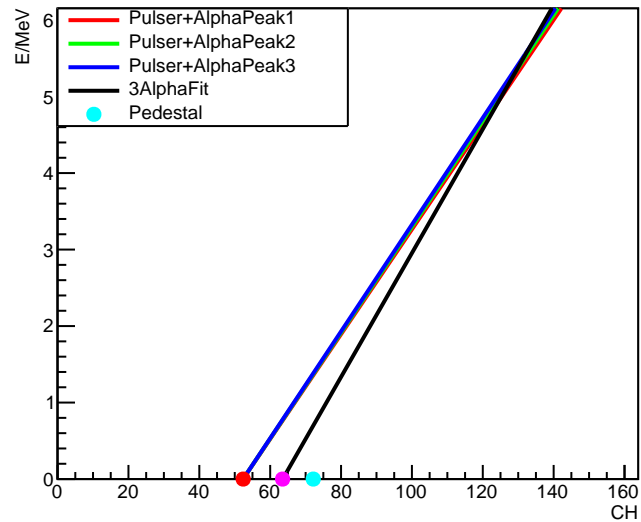
SSD1_L2F_Switch_AlphaCali00_32_CH02



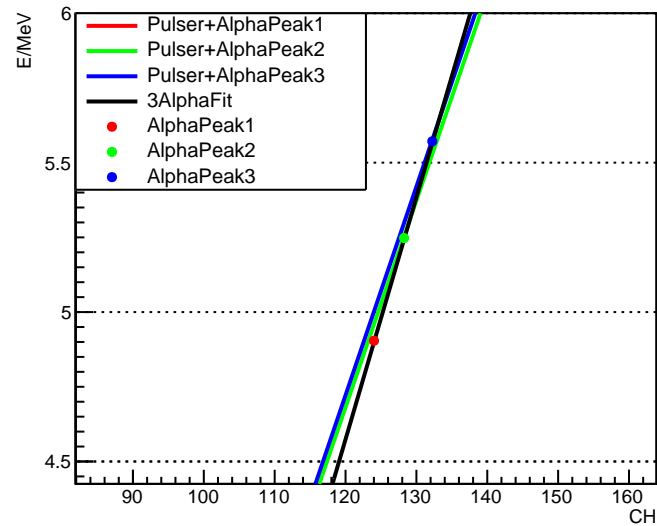
SSD1_L2F_Switch_AlphaCali00_32_CH03



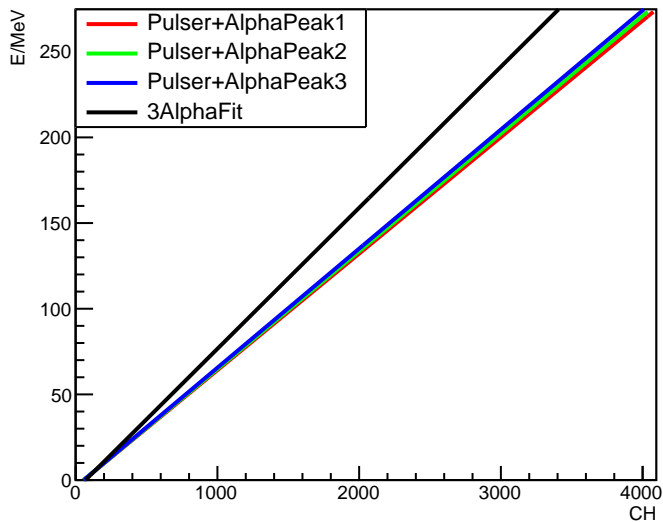
SSD1_L2F_Switch_AlphaCali00_32_CH03



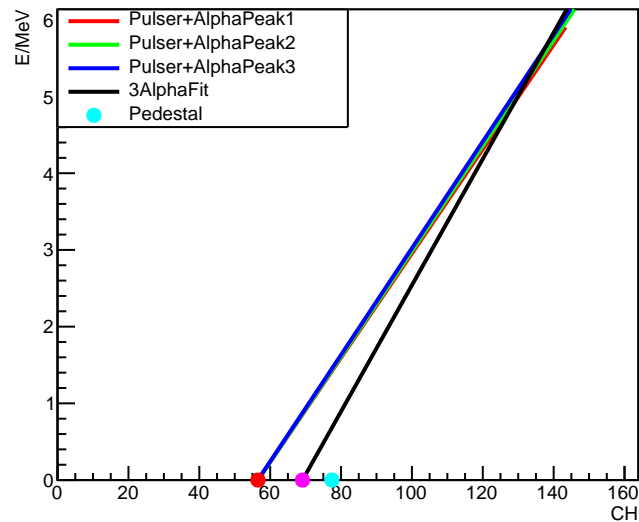
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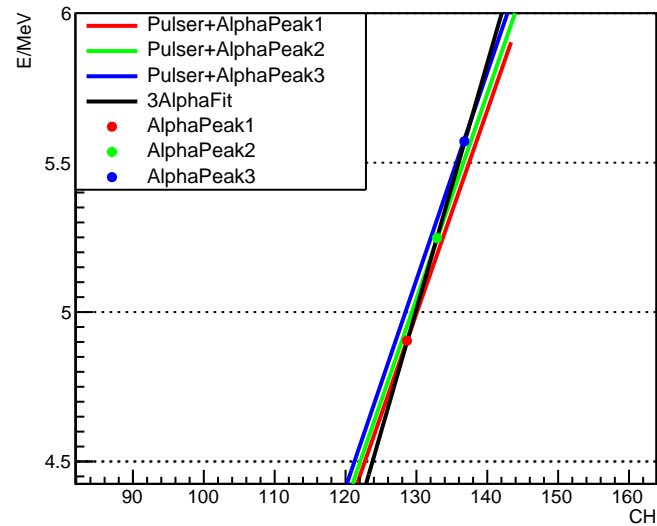
SSD1_L2F_Switch_AlphaCali00_32_CH04



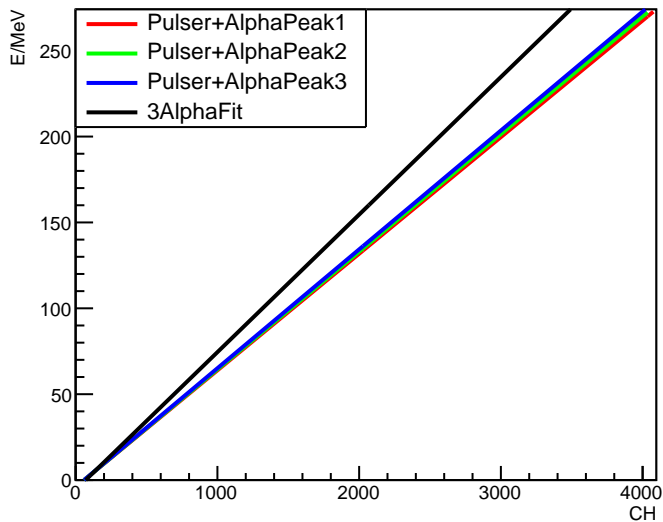
SSD1_L2F_Switch_AlphaCali00_32_CH04



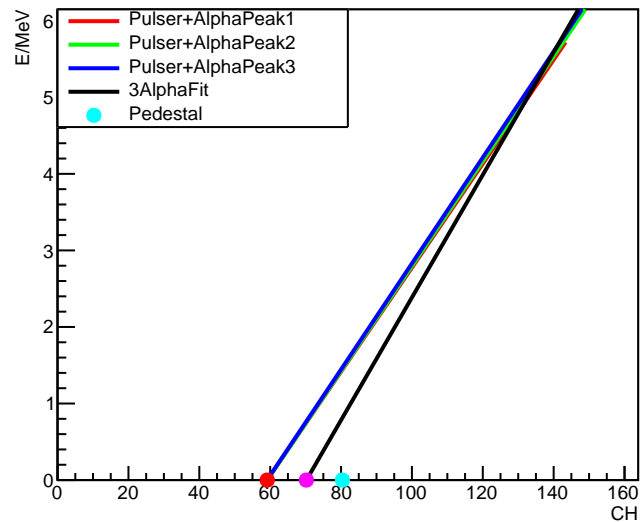
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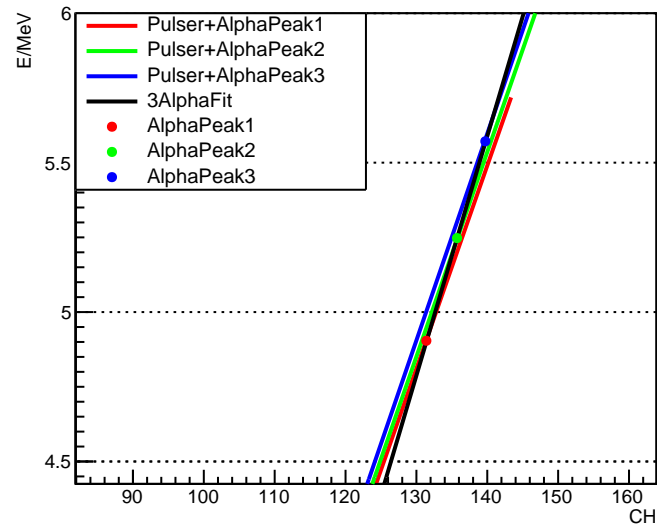
SSD1_L2F_Switch_AlphaCali00_32_CH05



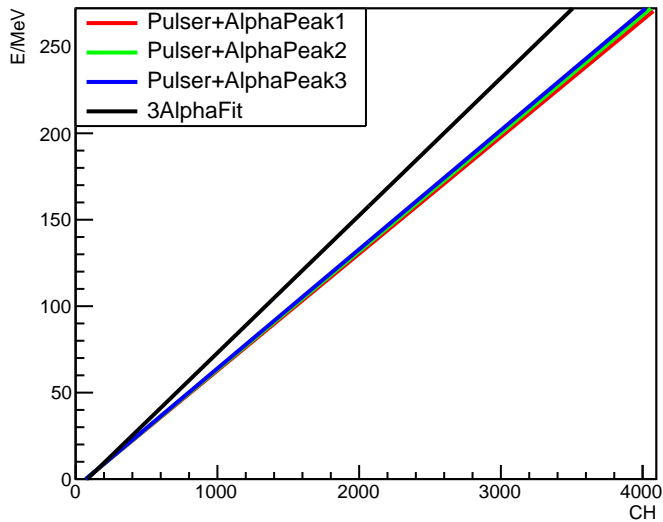
SSD1_L2F_Switch_AlphaCali00_32_CH05



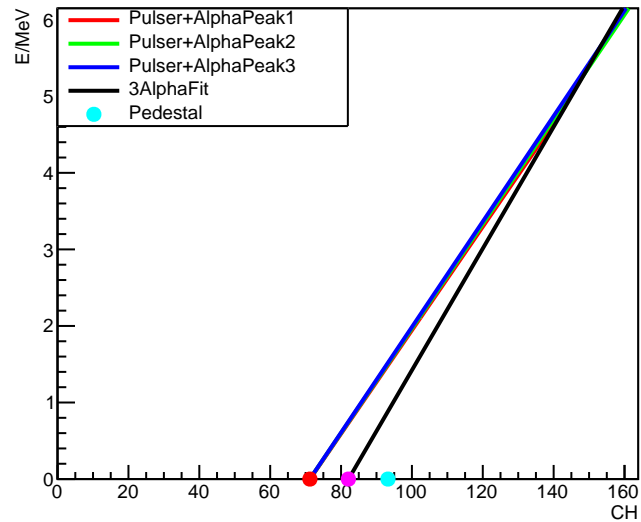
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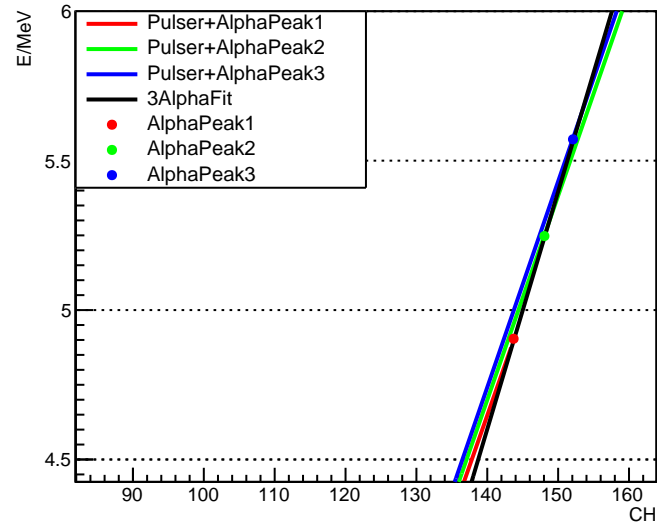
SSD1_L2F_Switch_AlphaCali00_32_CH06



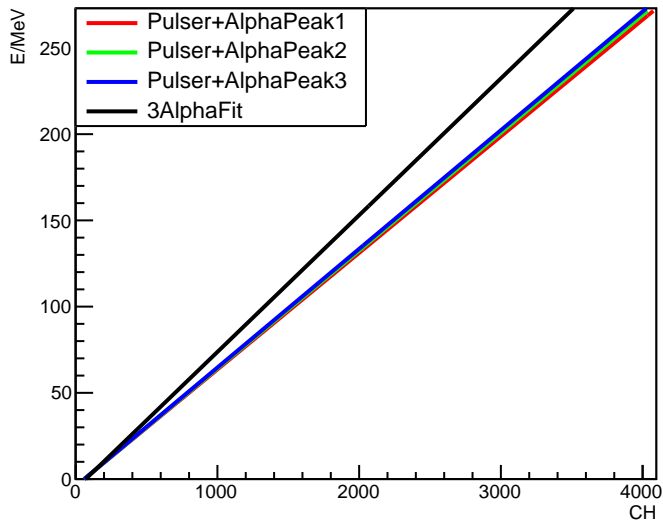
SSD1_L2F_Switch_AlphaCali00_32_CH06



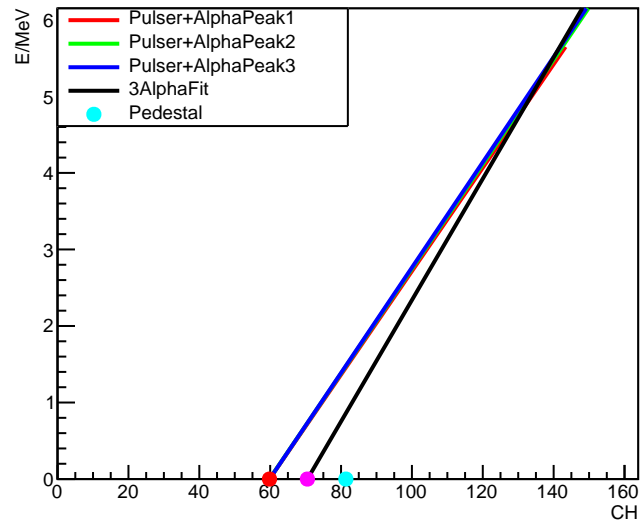
SSD1_L2F_Switch_AlphaCali00_32_CH06



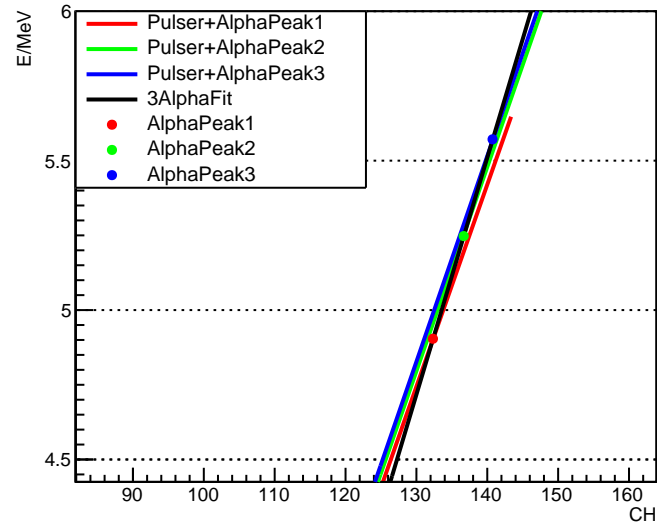
SSD1_L2F_Switch_AlphaCali00_32_CH07



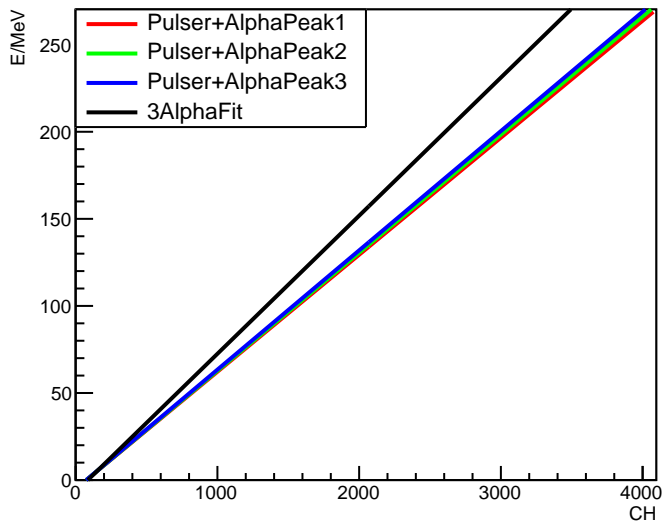
SSD1_L2F_Switch_AlphaCali00_32_CH07



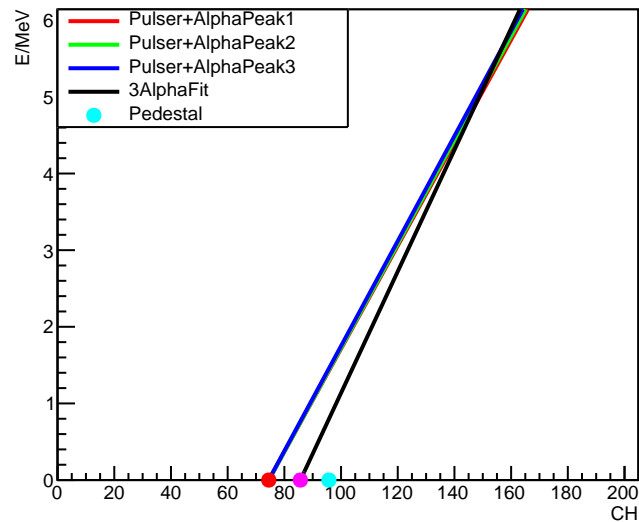
SSD1_L2F_Switch_AlphaCali00_32_CH07



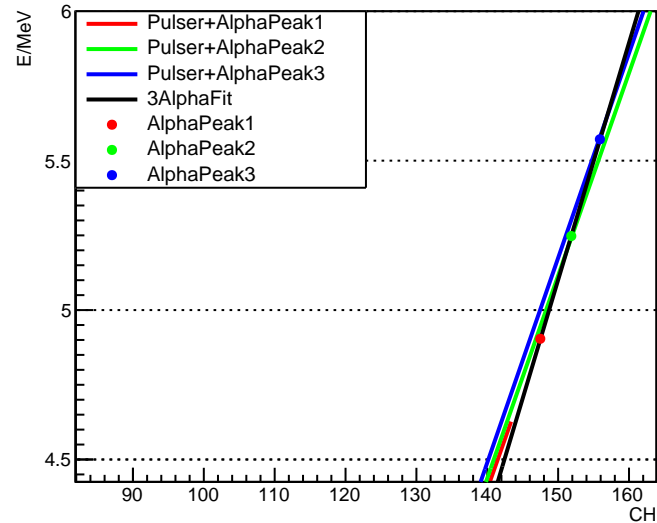
SSD1_L2F_Switch_AlphaCali00_32_CH08



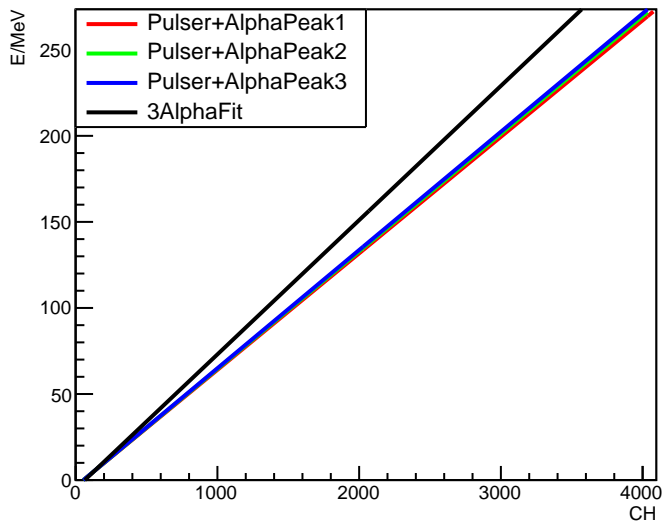
SSD1_L2F_Switch_AlphaCali00_32_CH08



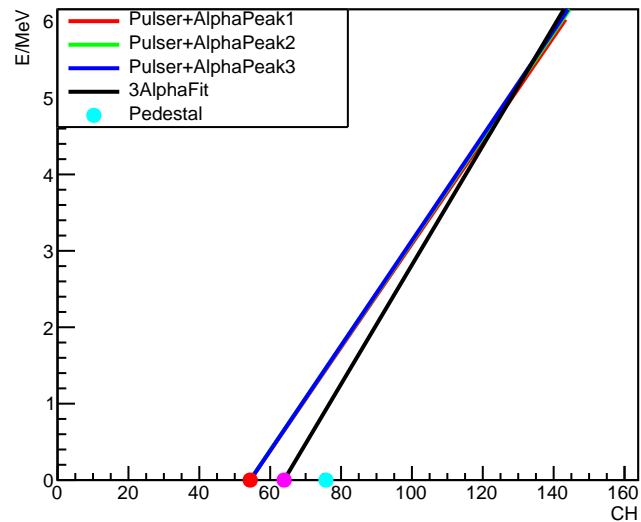
SSD1_L2F_Switch_AlphaCali00_32_CH08



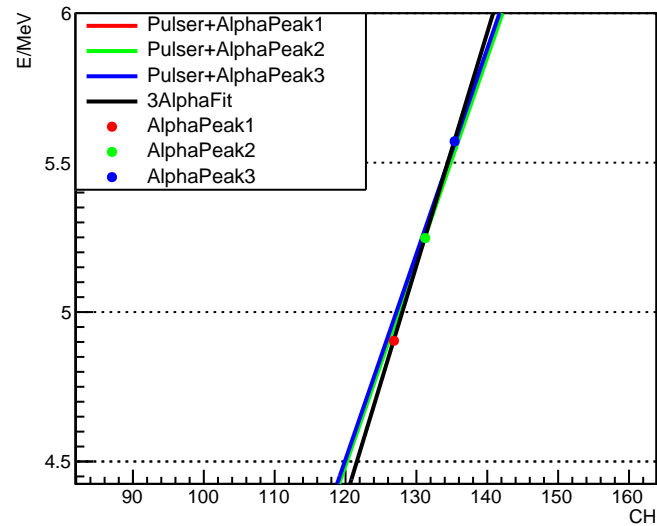
SSD1_L2F_Switch_AlphaCali00_32_CH09



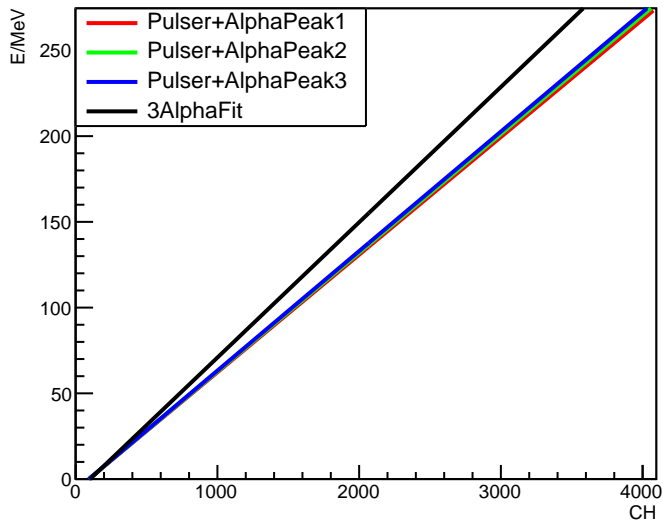
SSD1_L2F_Switch_AlphaCali00_32_CH09



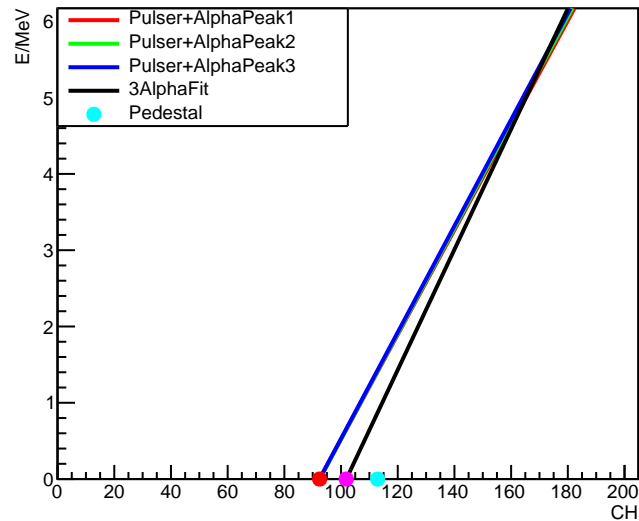
SSD1_L2F_Switch_AlphaCali00_32_CH09



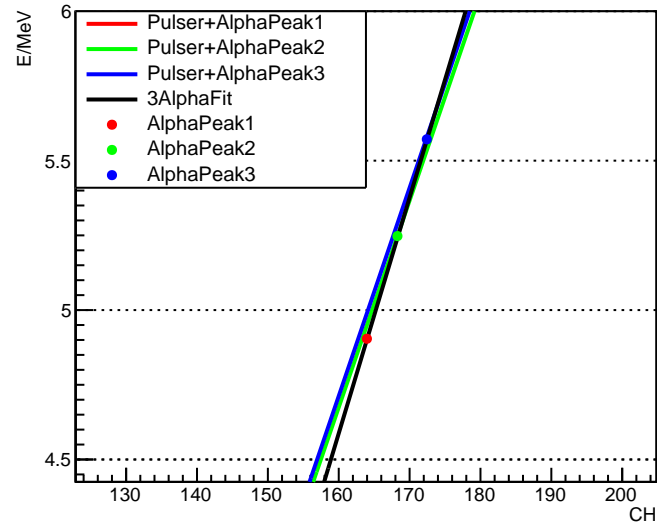
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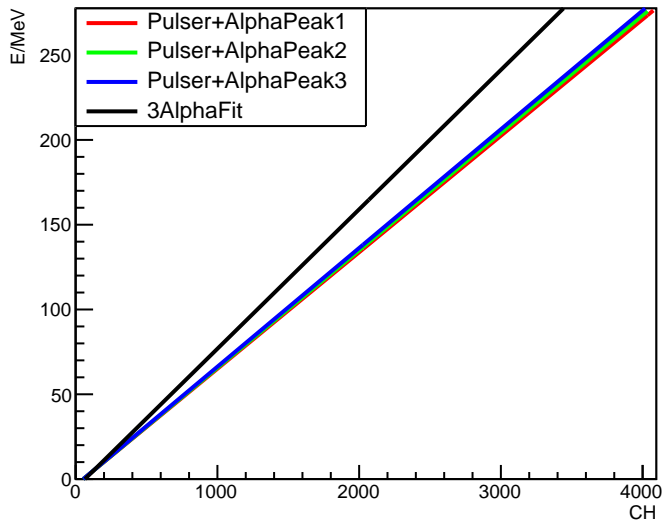
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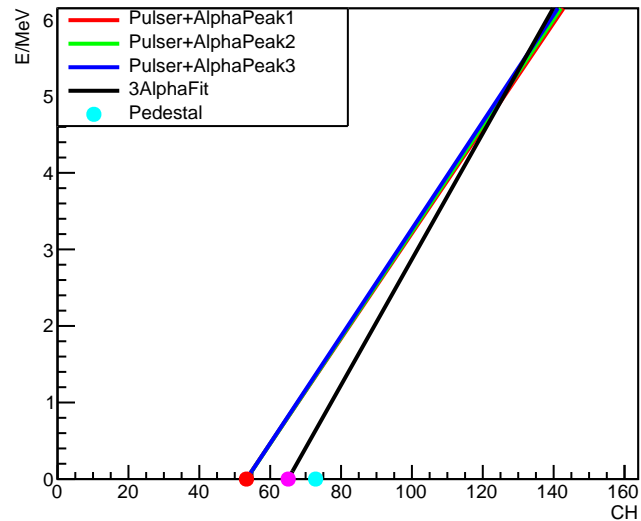
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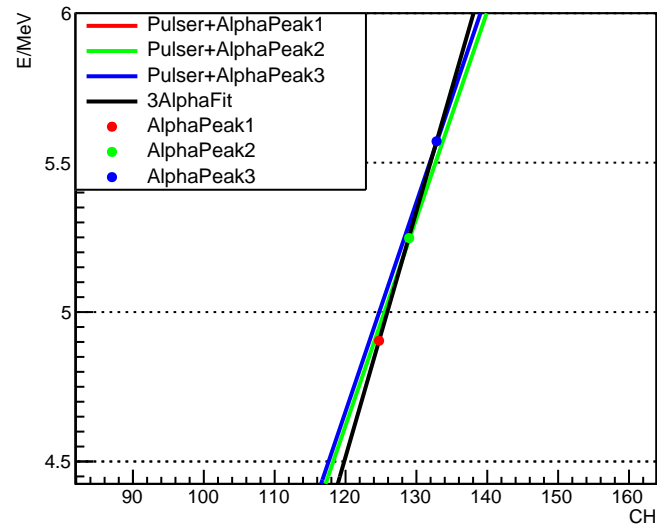
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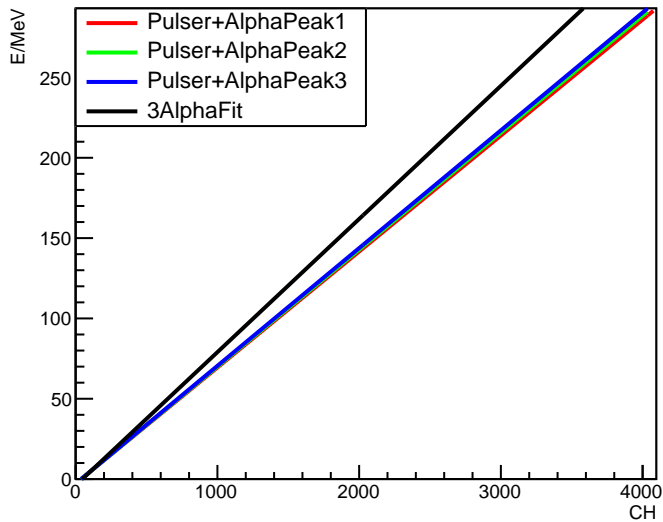
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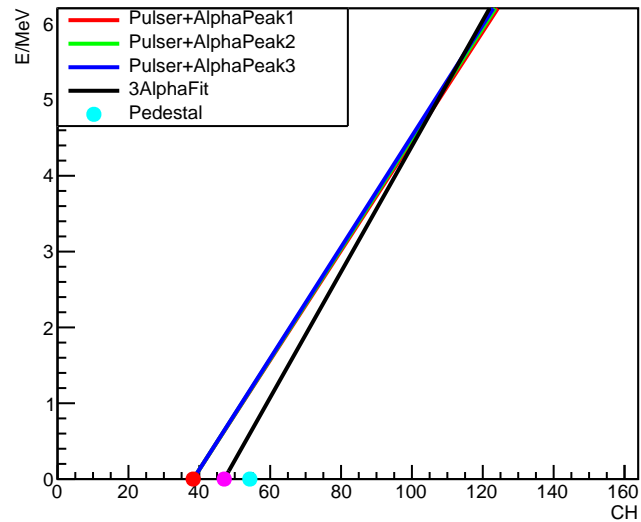
SSD1_L2F_Switch_AlphaCali00_32_CH11



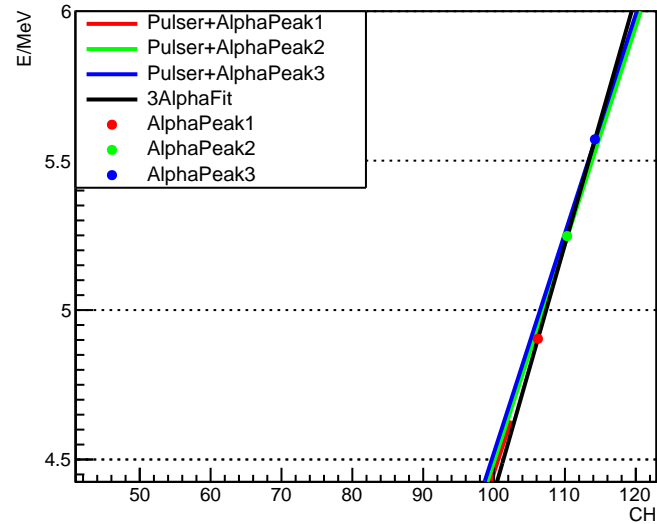
SSD1_L2F_Switch_AlphaCali00_32_CH12



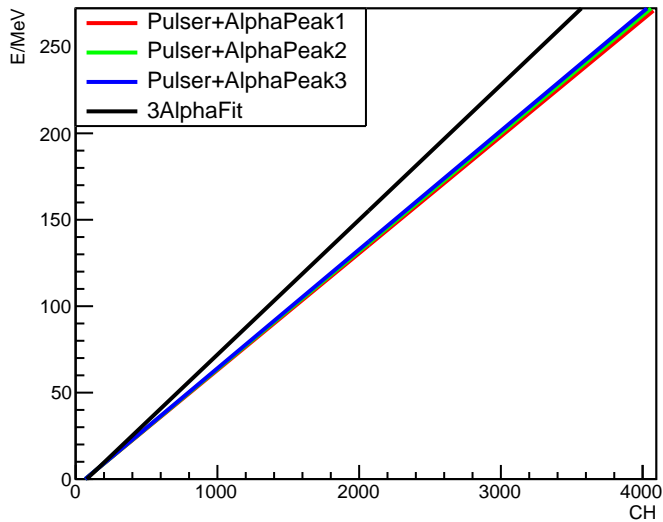
SSD1_L2F_Switch_AlphaCali00_32_CH12



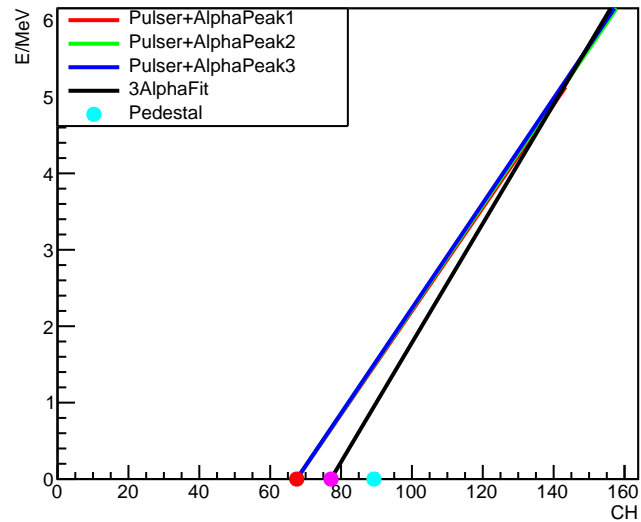
SSD1_L2F_Switch_AlphaCali00_32_CH12



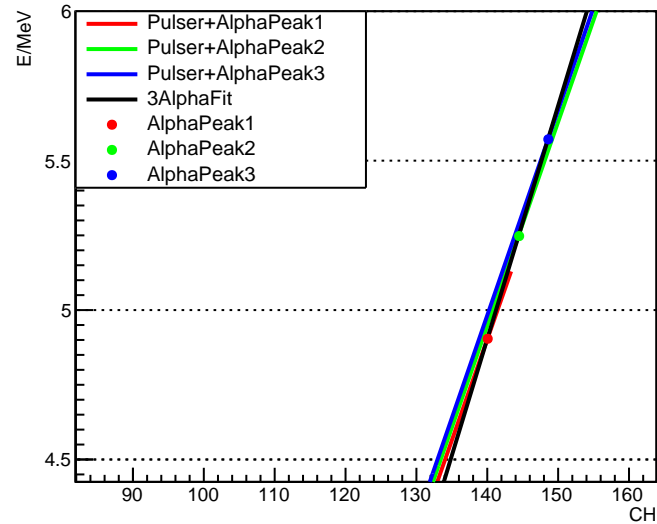
SSD1_L2F_Switch_AlphaCali00_32_CH13



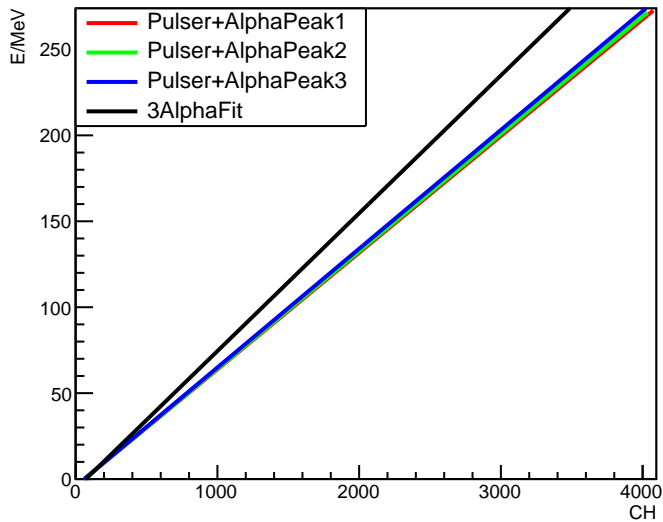
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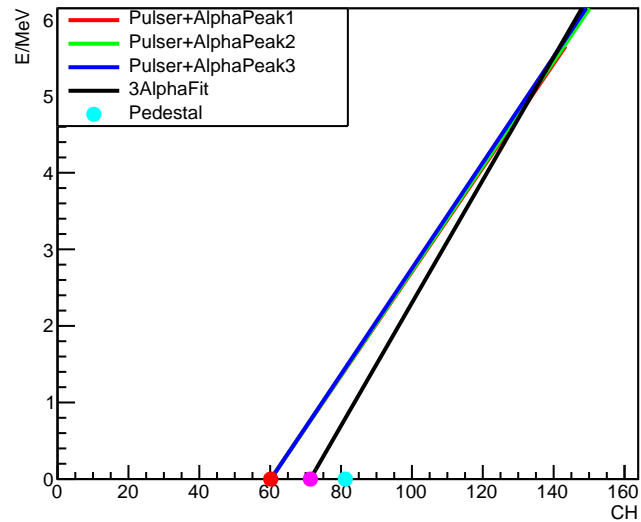
SSD1_L2F_Switch_AlphaCali00_32_CH13



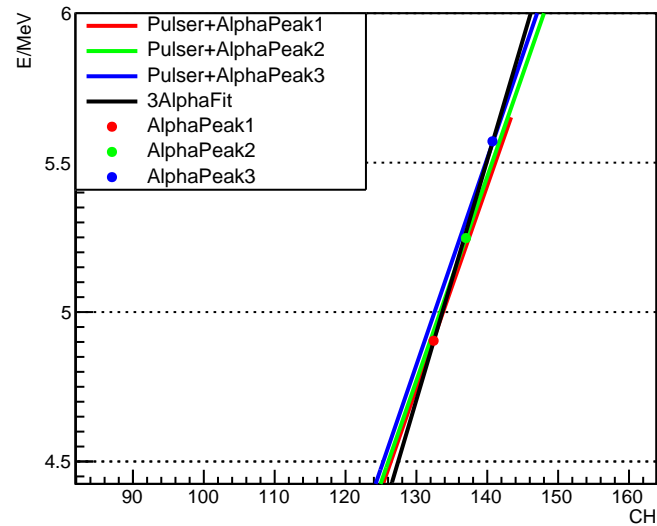
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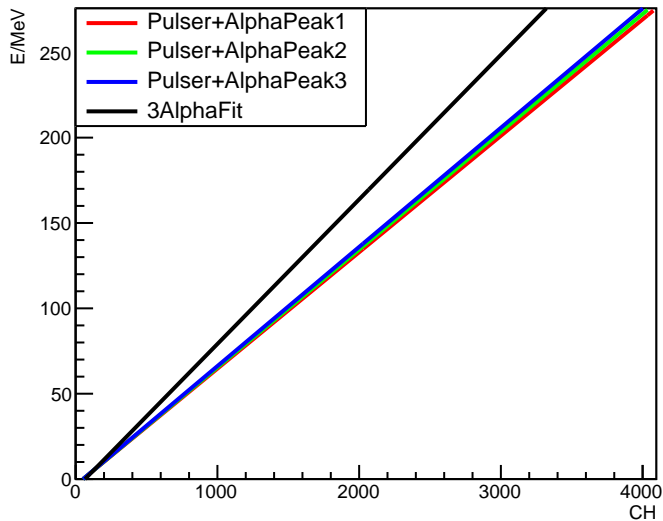
SSD1_L2F_Switch_AlphaCali00_32_CH14



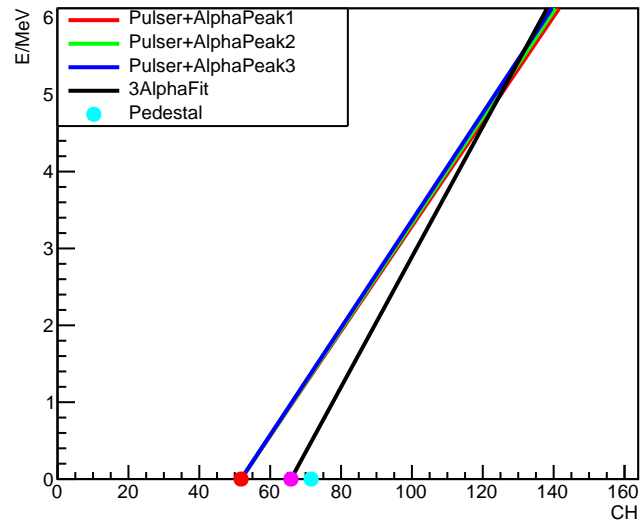
SSD1_L2F_Switch_AlphaCali00_32_CH14



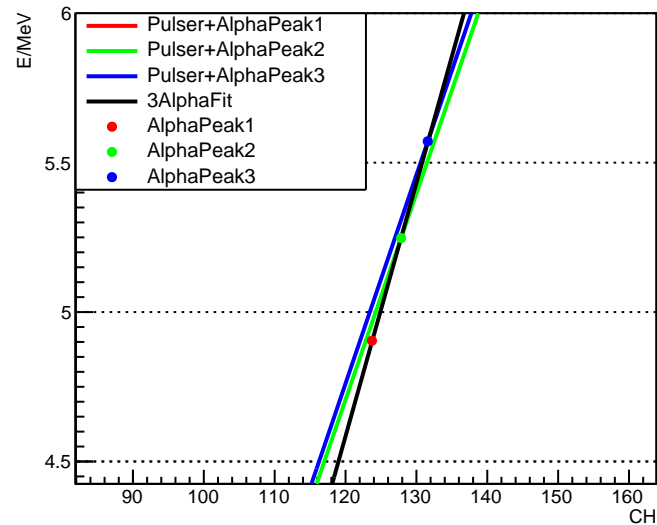
SSD1_L2F_Switch_AlphaCali00_32_CH15



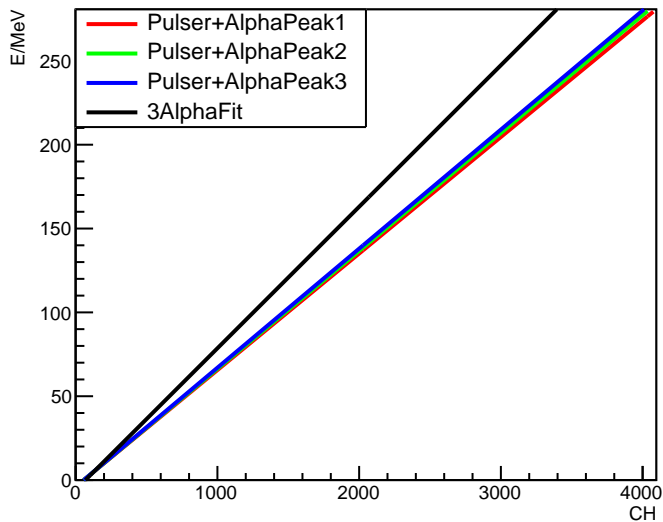
SSD1_L2F_Switch_AlphaCali00_32_CH15



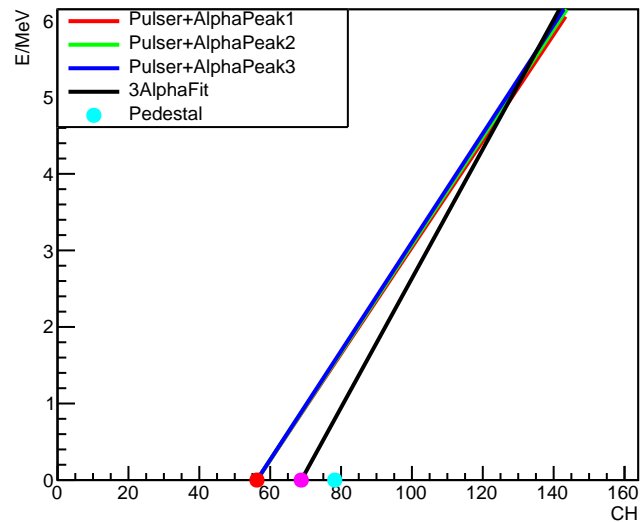
SSD1_L2F_Switch_AlphaCali00_32_CH15



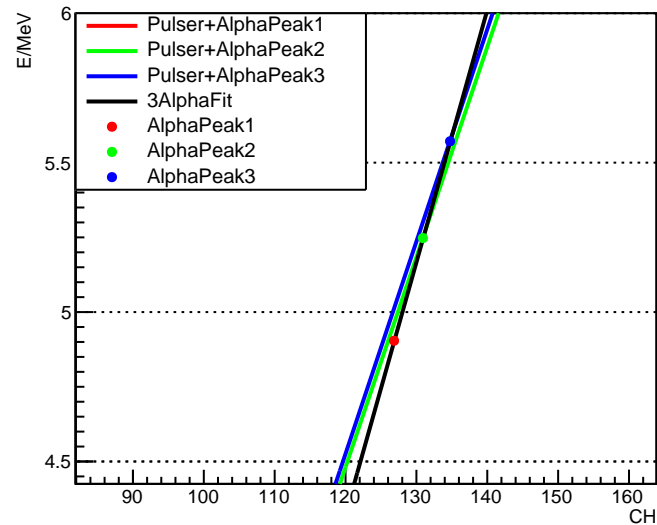
SSD2_L2F_Switch_AlphaCali00_32_CH00



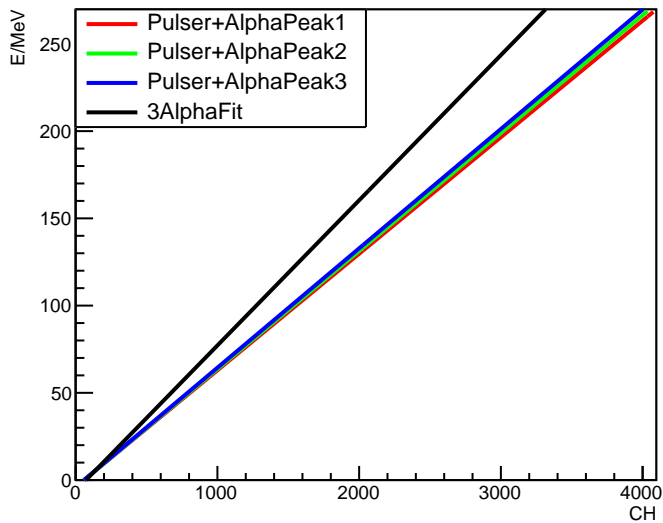
SSD2_L2F_Switch_AlphaCali00_32_CH00



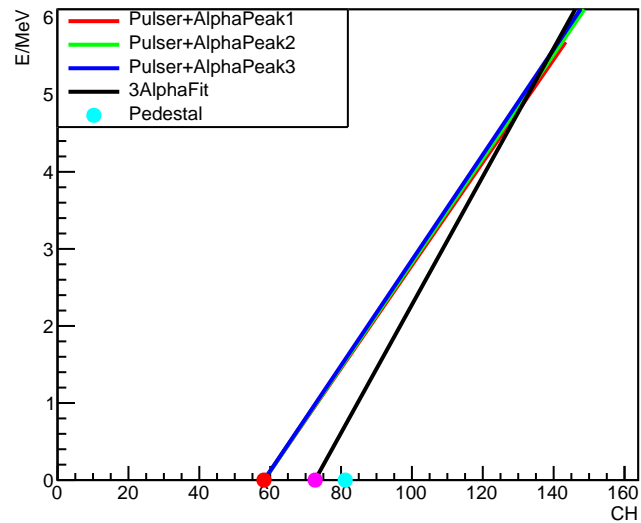
SSD2_L2F_Switch_AlphaCali00_32_CH00



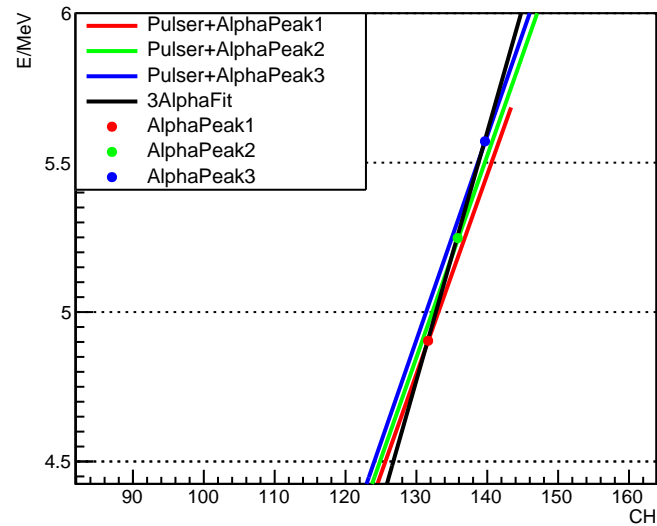
SSD2_L2F_Switch_AlphaCali00_32_CH01



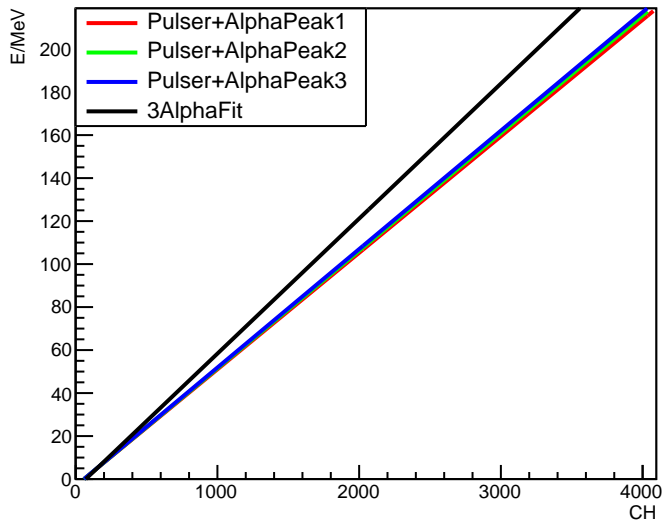
SSD2_L2F_Switch_AlphaCali00_32_CH01



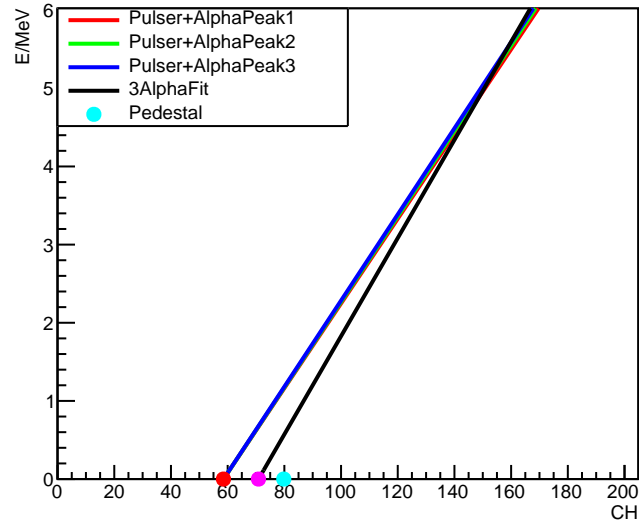
SSD2_L2F_Switch_AlphaCali00_32_CH01



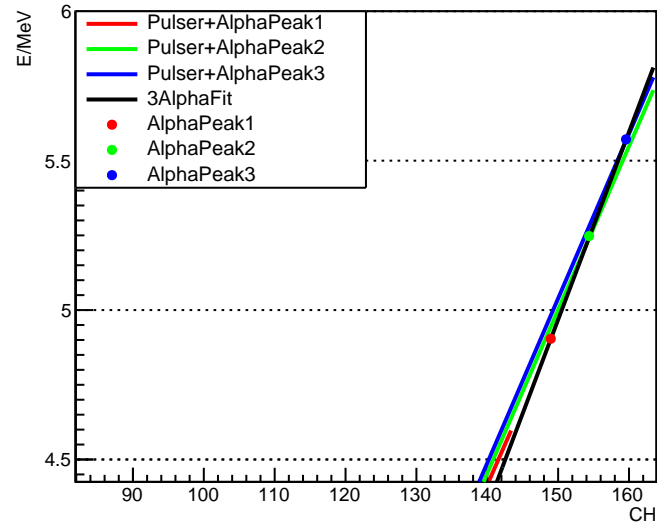
SSD2_L2F_Switch_AlphaCali00_32_CH02



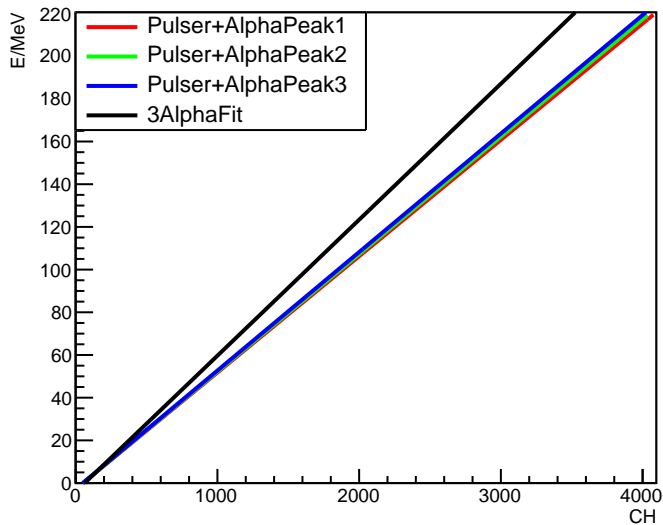
SSD2_L2F_Switch_AlphaCali00_32_CH02



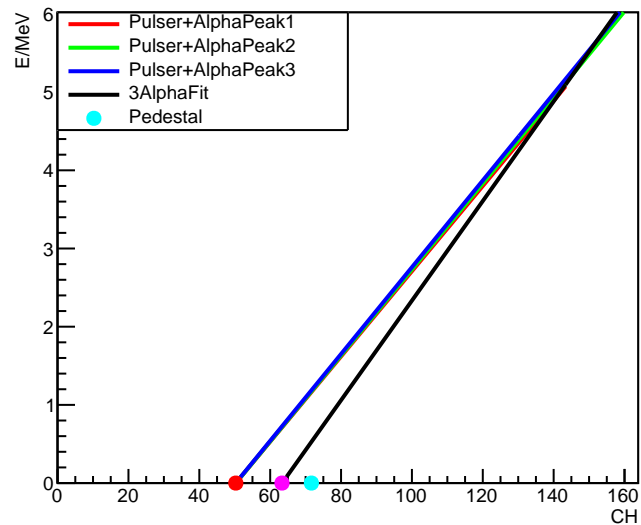
SSD2_L2F_Switch_AlphaCali00_32_CH02



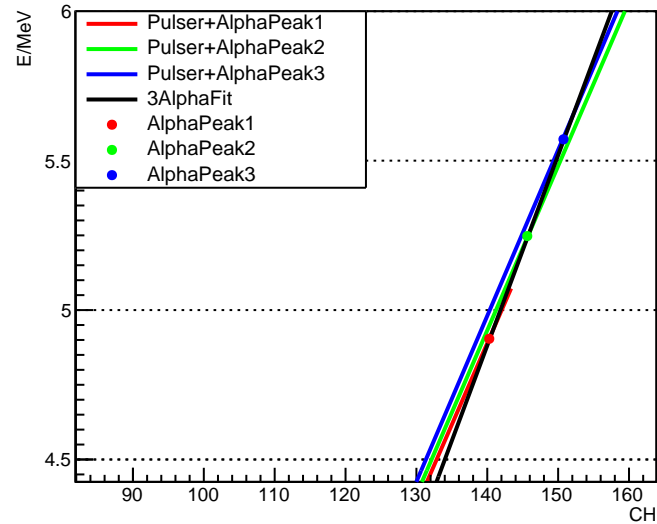
SSD2_L2F_Switch_AlphaCali00_32_CH03



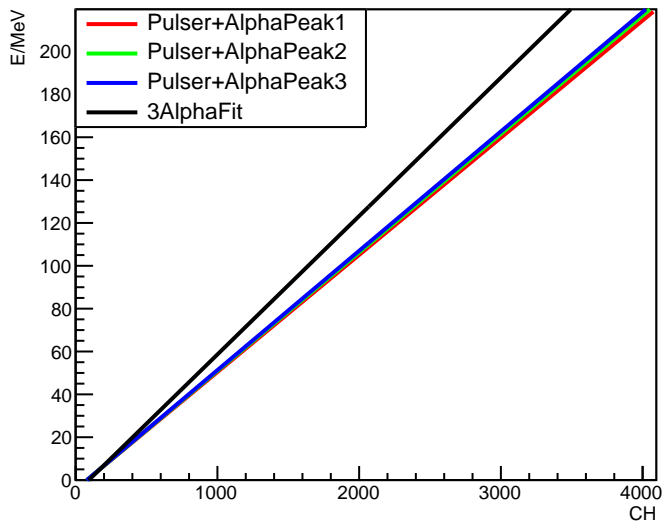
SSD2_L2F_Switch_AlphaCali00_32_CH03



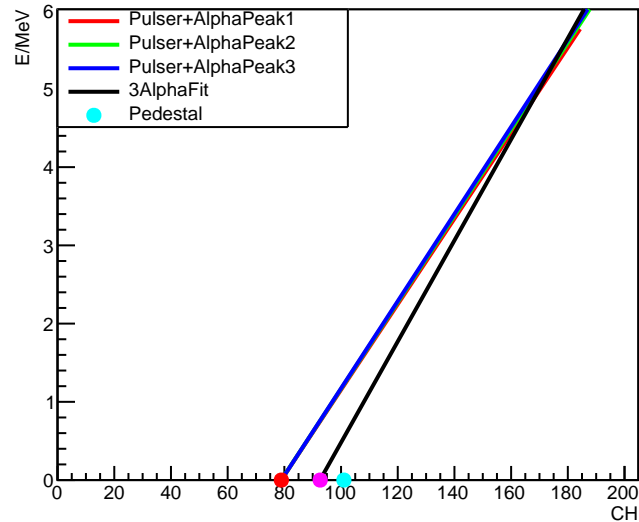
SSD2_L2F_Switch_AlphaCali00_32_CH03



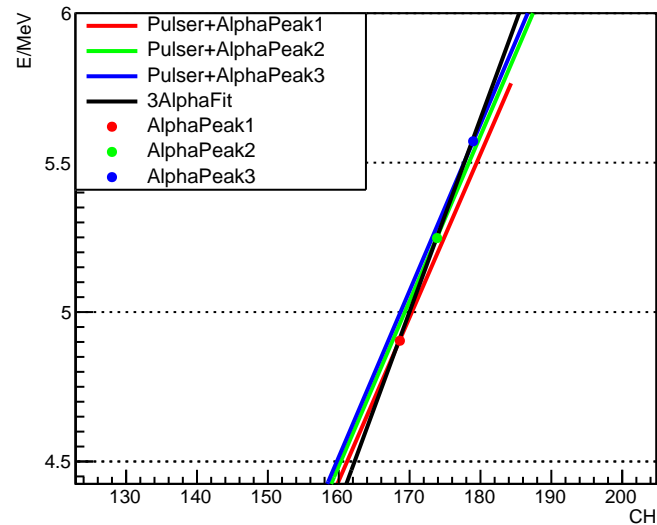
SSD2_L2F_Switch_AlphaCali00_32_CH04



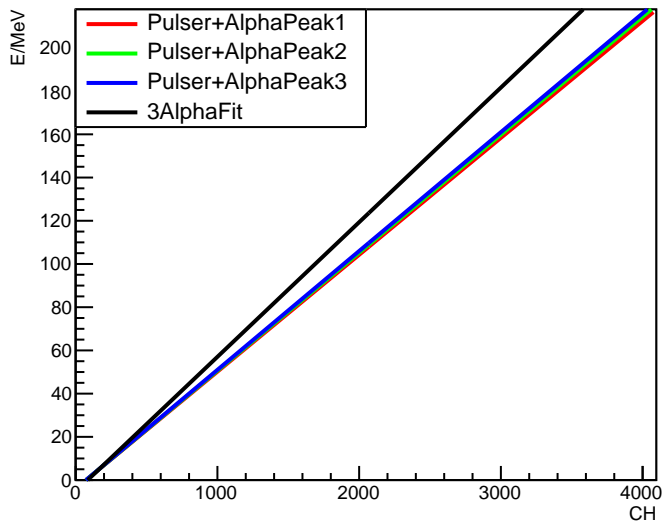
SSD2_L2F_Switch_AlphaCali00_32_CH04



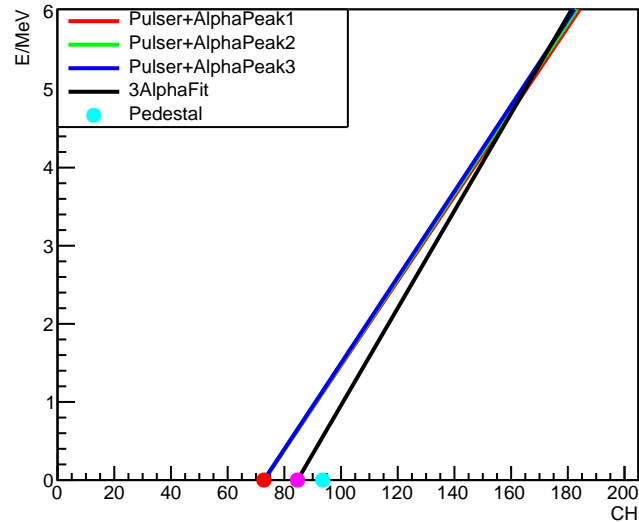
SSD2_L2F_Switch_AlphaCali00_32_CH04



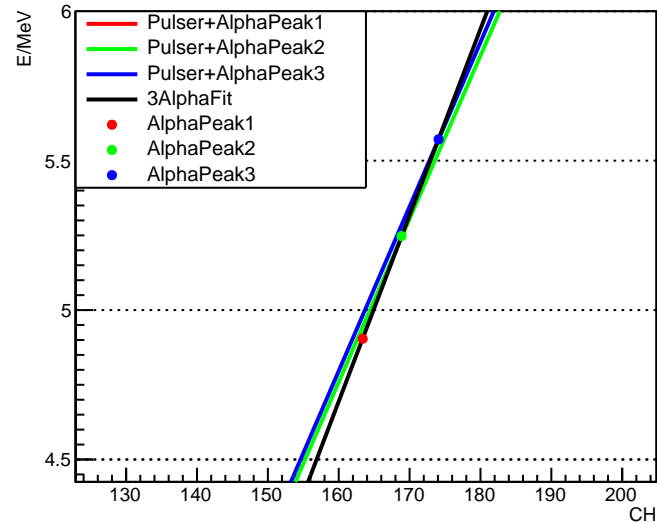
SSD2_L2F_Switch_AlphaCali00_32_CH05



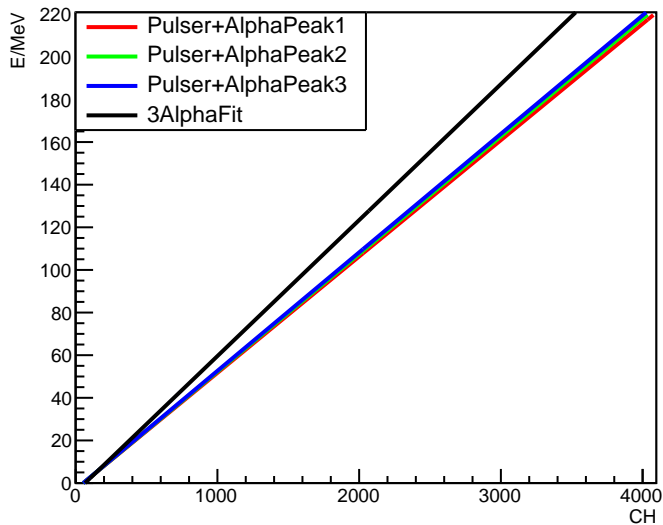
SSD2_L2F_Switch_AlphaCali00_32_CH05



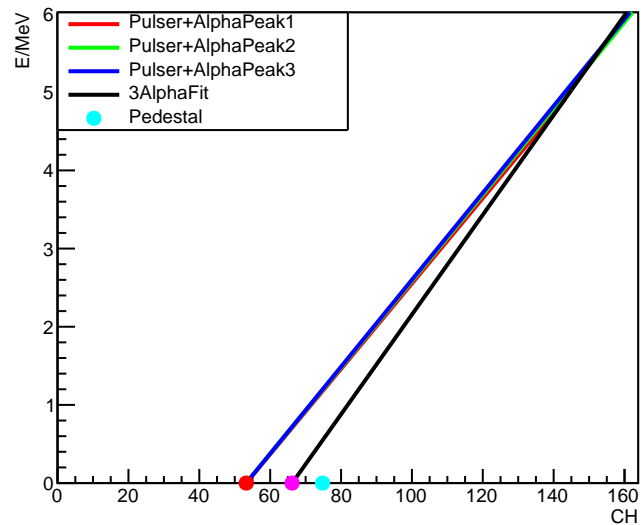
SSD2_L2F_Switch_AlphaCali00_32_CH05



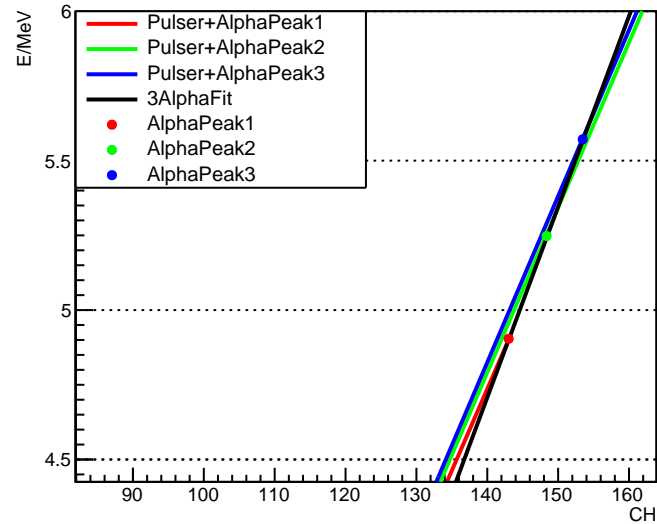
SSD2_L2F_Switch_AlphaCali00_32_CH06



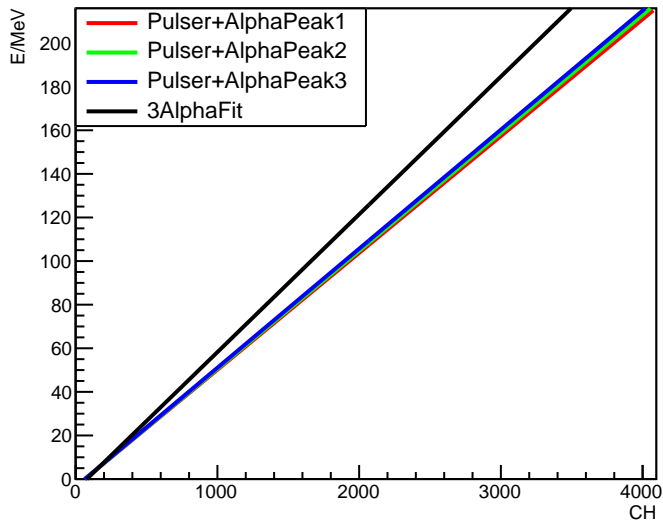
SSD2_L2F_Switch_AlphaCali00_32_CH06



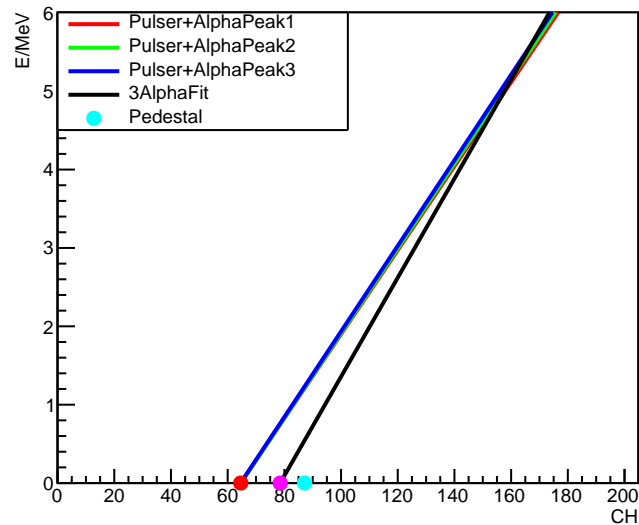
SSD2_L2F_Switch_AlphaCali00_32_CH06



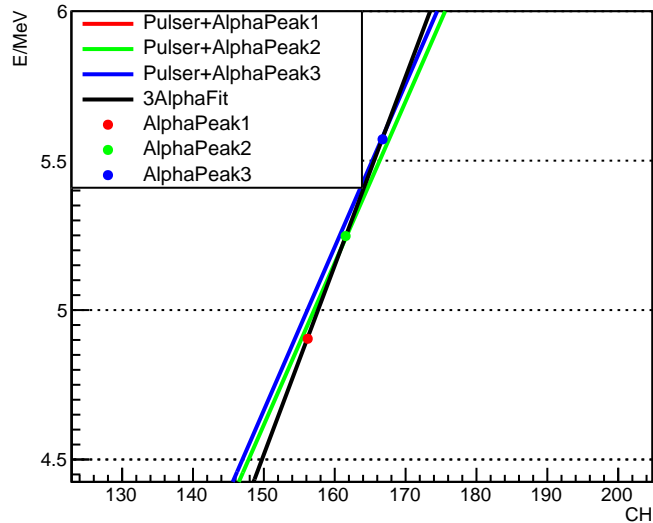
SSD2_L2F_Switch_AlphaCali00_32_CH07



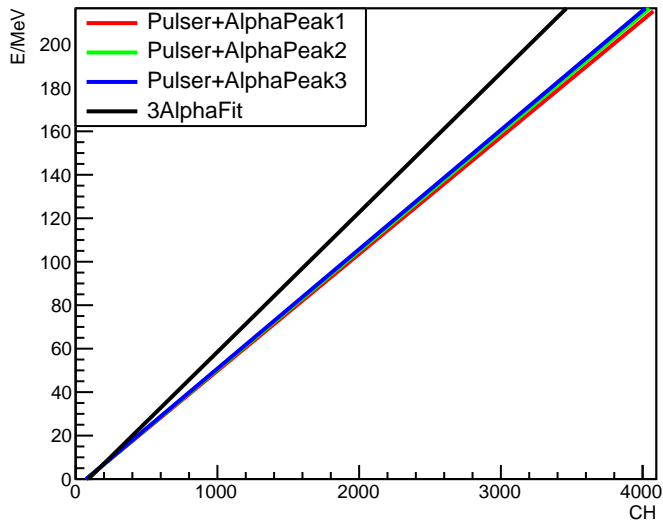
SSD2_L2F_Switch_AlphaCali00_32_CH07



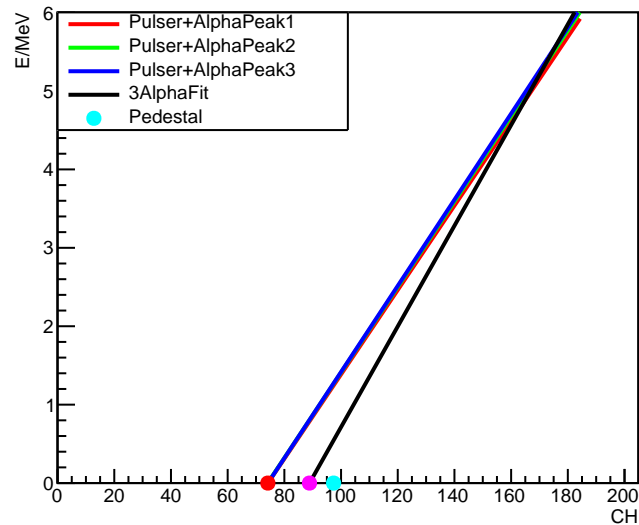
SSD2_L2F_Switch_AlphaCali00_32_CH07



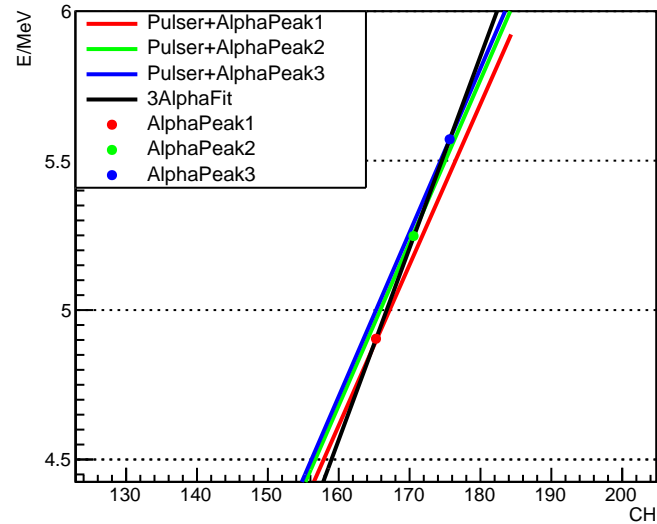
SSD2_L2F_Switch_AlphaCali00_32_CH08



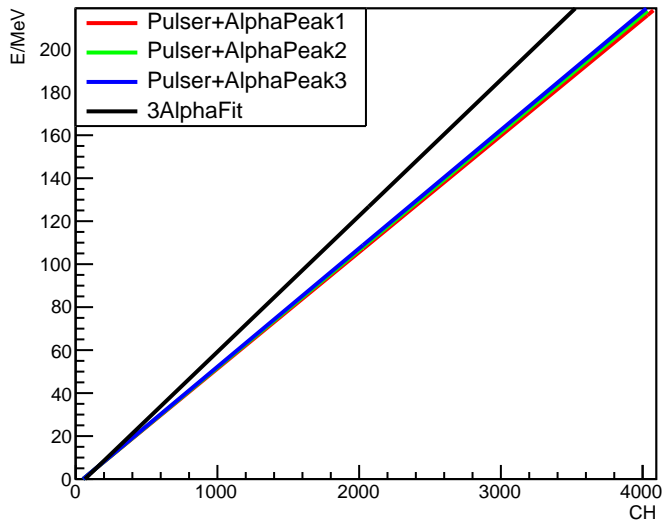
SSD2_L2F_Switch_AlphaCali00_32_CH08



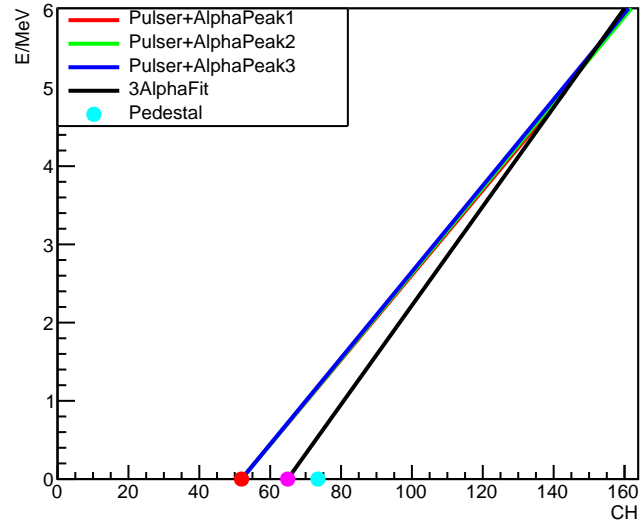
SSD2_L2F_Switch_AlphaCali00_32_CH08



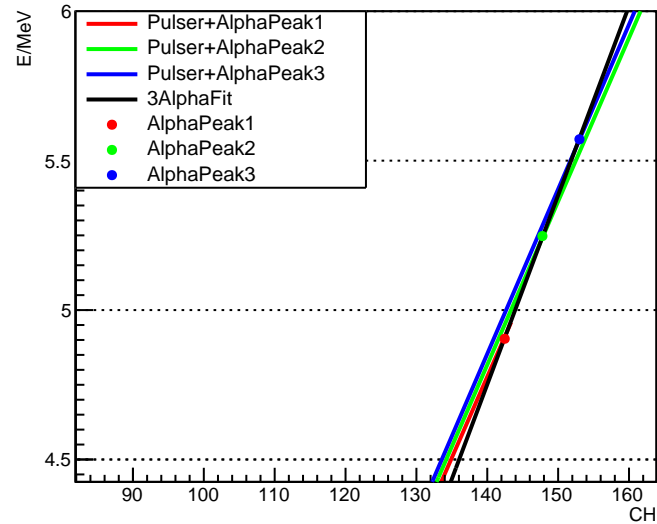
SSD2_L2F_Switch_AlphaCali00_32_CH09



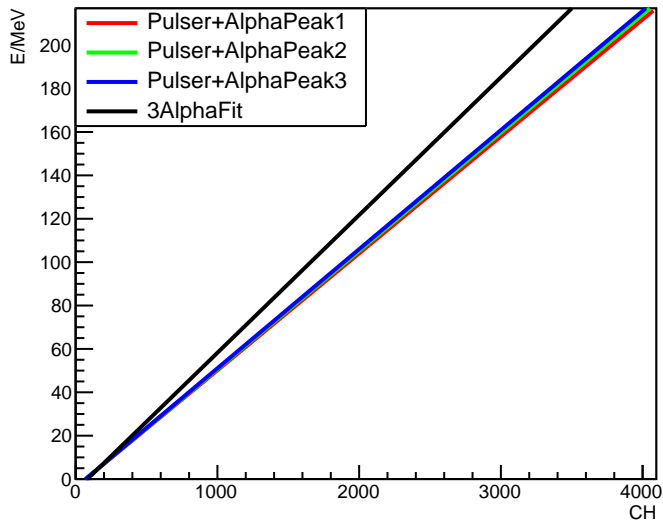
SSD2_L2F_Switch_AlphaCali00_32_CH09



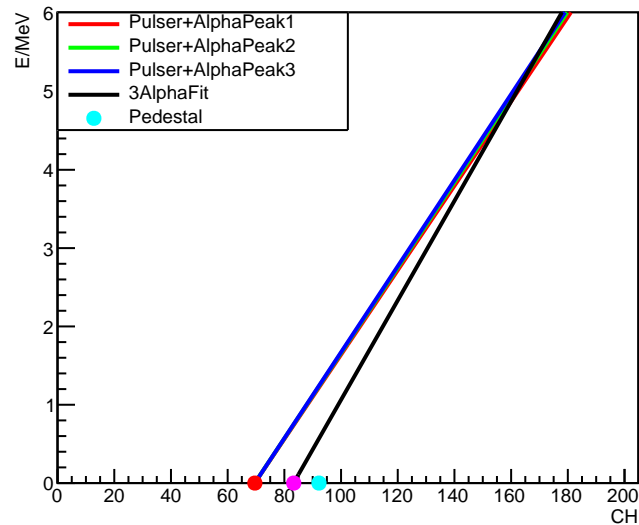
SSD2_L2F_Switch_AlphaCali00_32_CH09



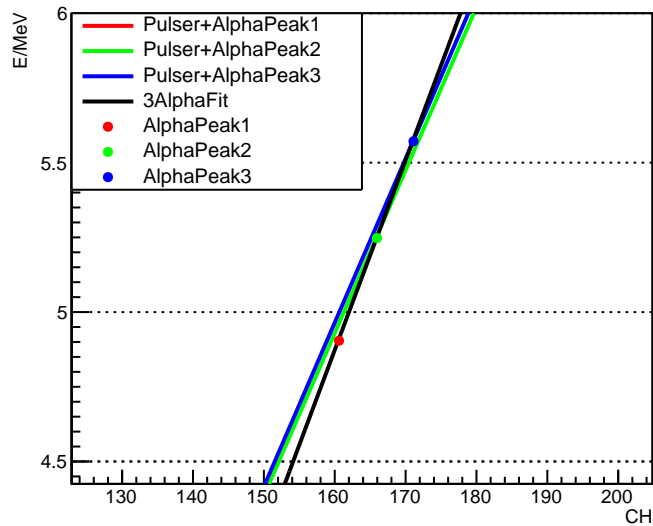
SSD2_L2F_Switch_AlphaCali00_32_CH10



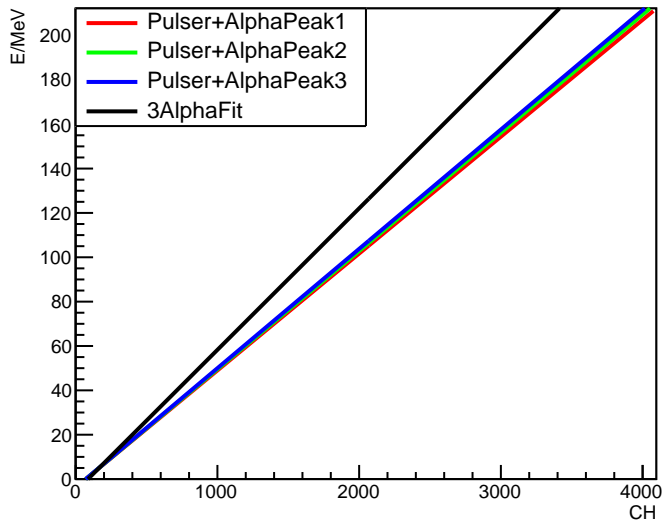
SSD2_L2F_Switch_AlphaCali00_32_CH10



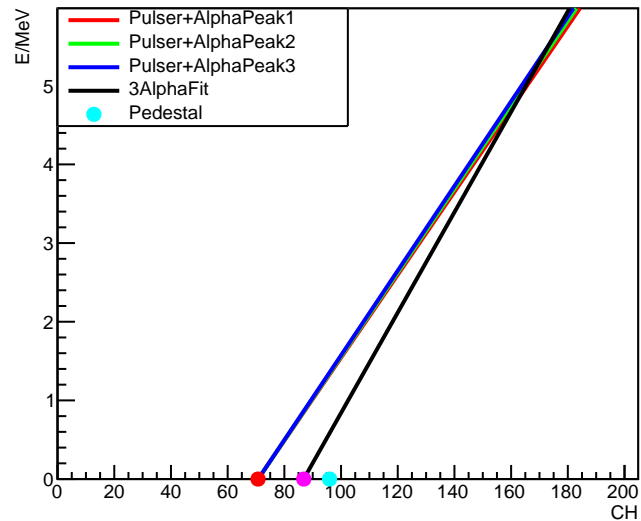
SSD2_L2F_Switch_AlphaCali00_32_CH10



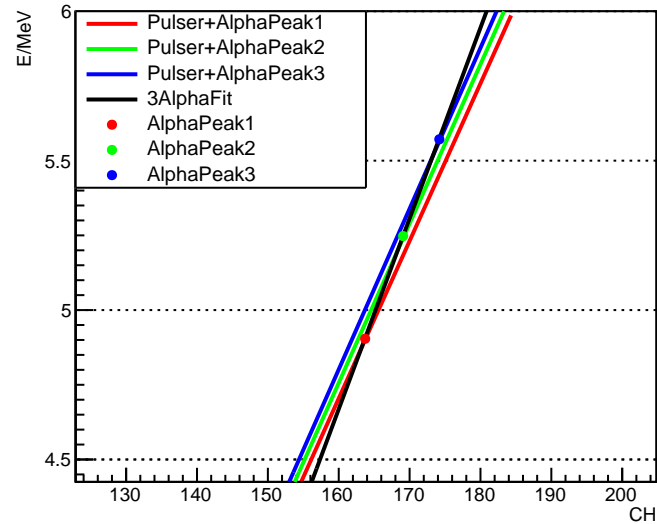
SSD2_L2F_Switch_AlphaCali00_32_CH11



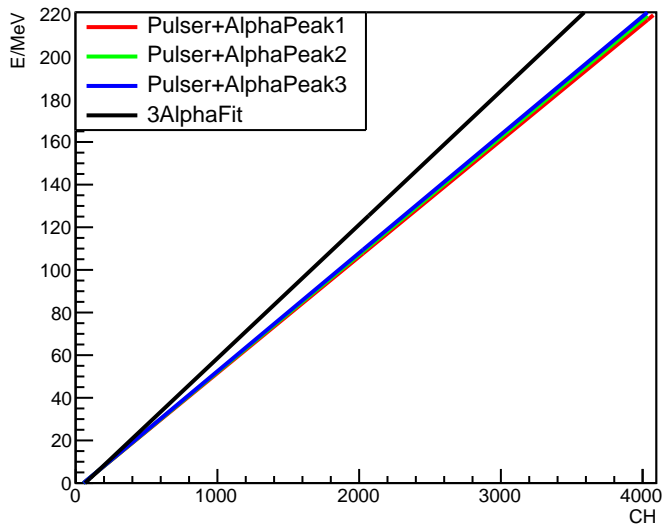
SSD2_L2F_Switch_AlphaCali00_32_CH11



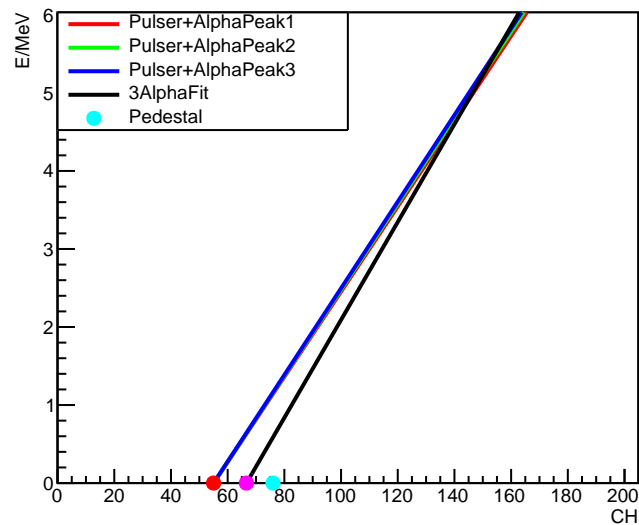
SSD2_L2F_Switch_AlphaCali00_32_CH11



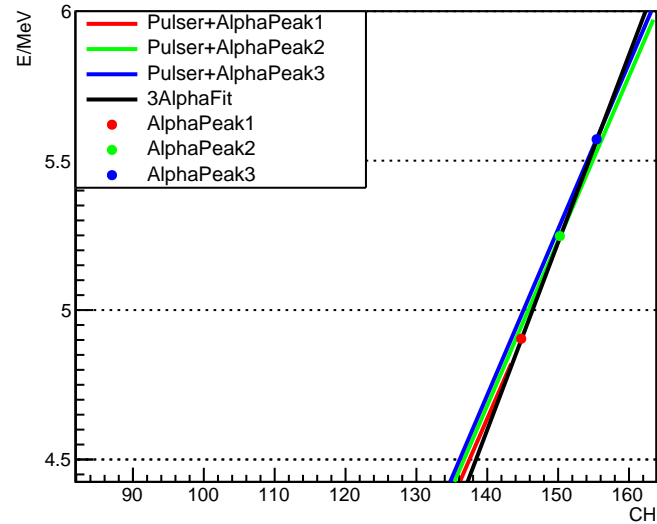
SSD2_L2F_Switch_AlphaCali00_32_CH12



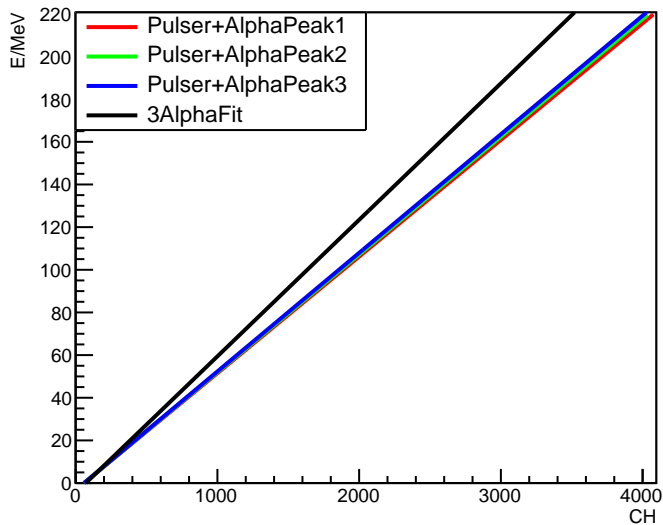
SSD2_L2F_Switch_AlphaCali00_32_CH12



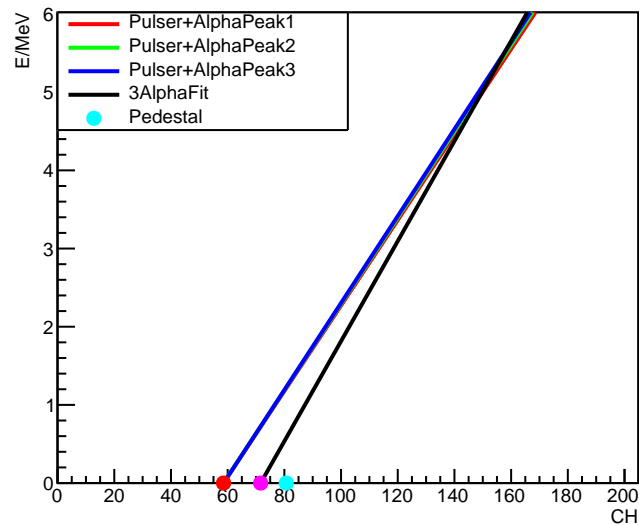
SSD2_L2F_Switch_AlphaCali00_32_CH12



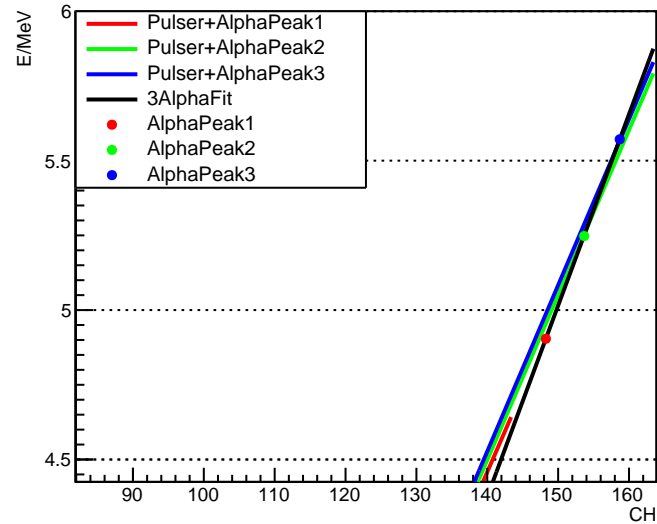
SSD2_L2F_Switch_AlphaCali00_32_CH13



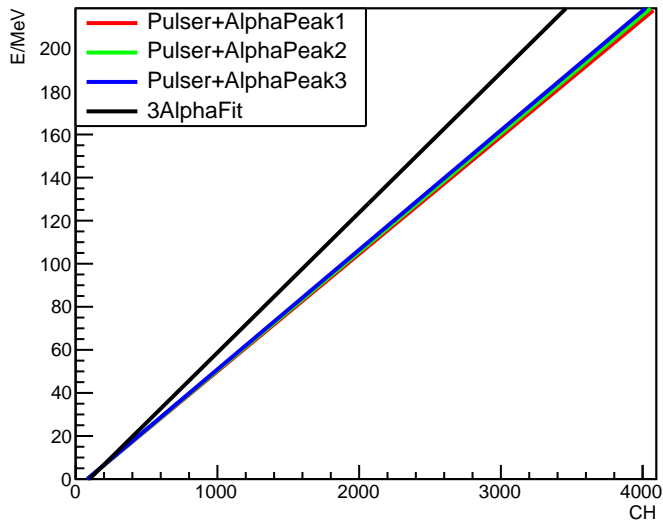
SSD2_L2F_Switch_AlphaCali00_32_CH13



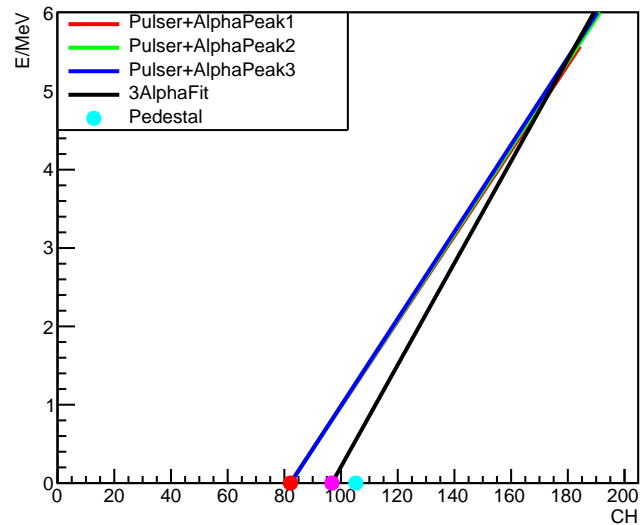
SSD2_L2F_Switch_AlphaCali00_32_CH13



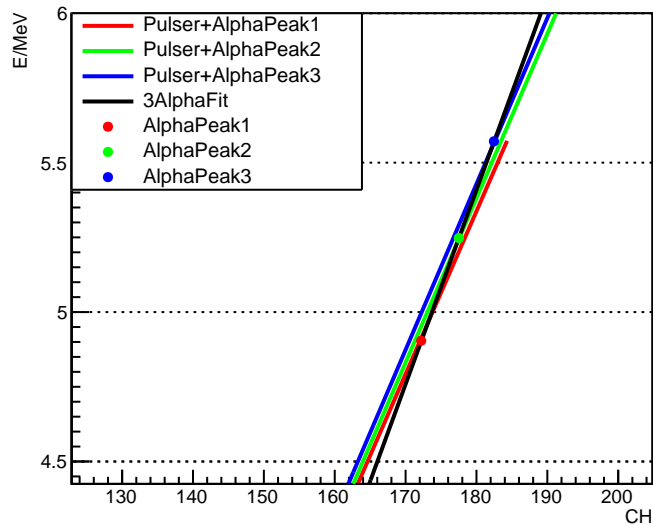
SSD2_L2F_Switch_AlphaCali00_32_CH14



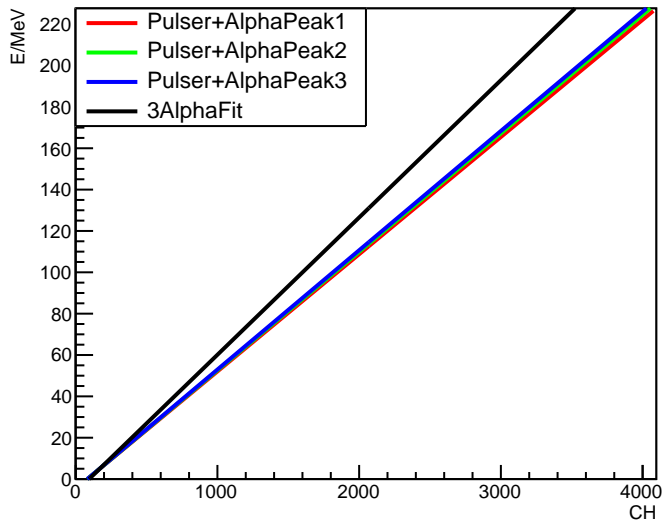
SSD2_L2F_Switch_AlphaCali00_32_CH14



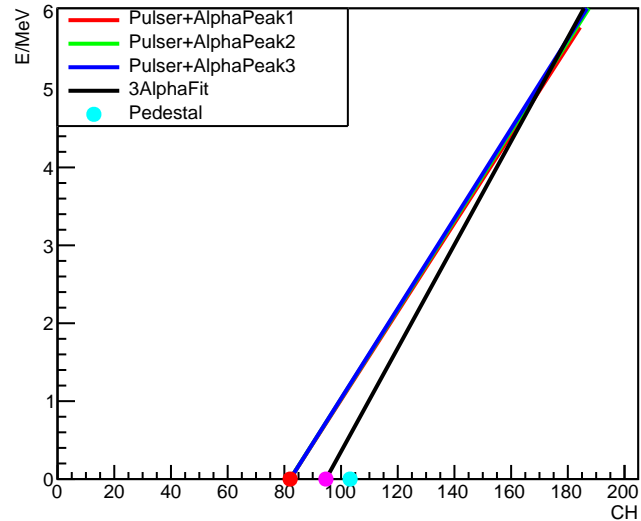
SSD2_L2F_Switch_AlphaCali00_32_CH14



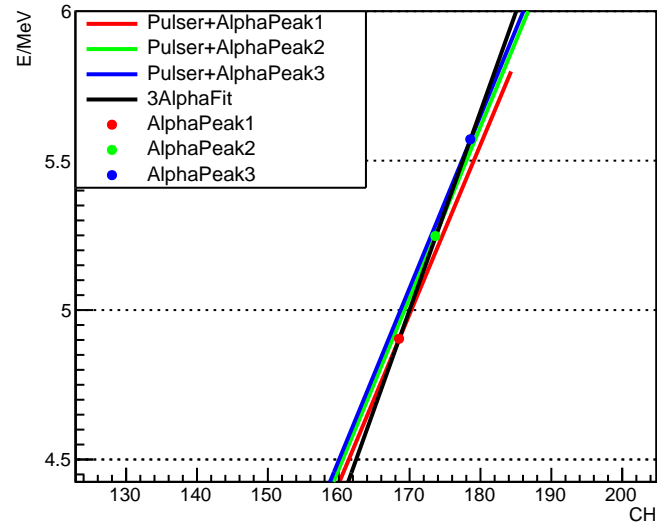
SSD2_L2F_Switch_AlphaCali00_32_CH15



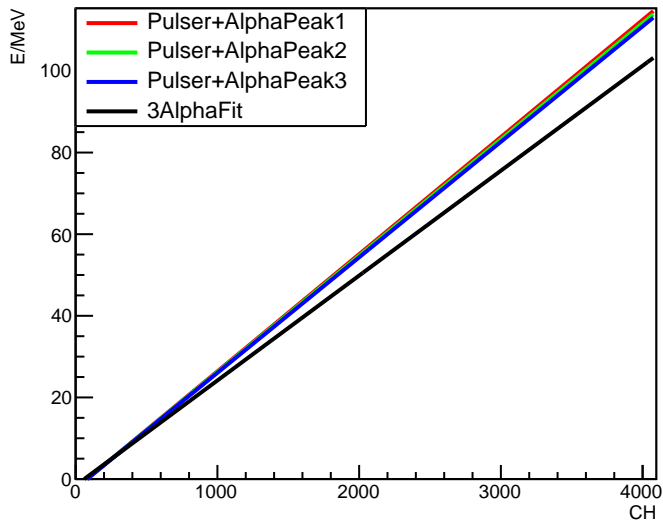
SSD2_L2F_Switch_AlphaCali00_32_CH15



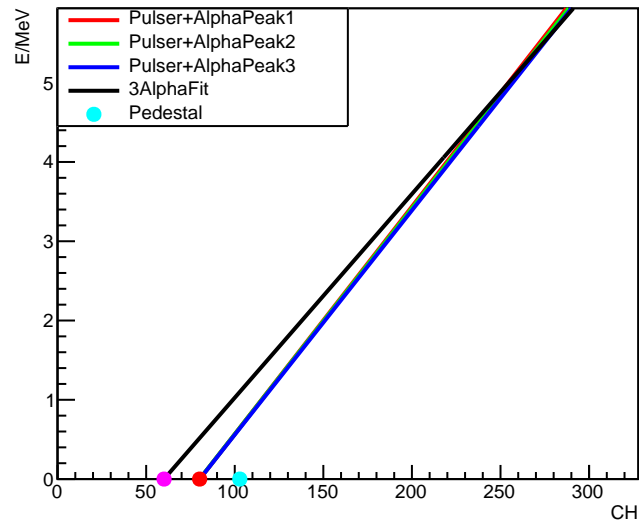
SSD2_L2F_Switch_AlphaCali00_32_CH15



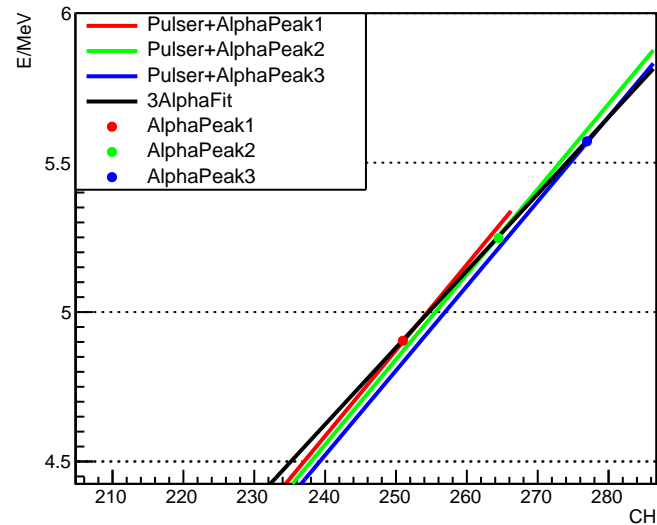
SSD3_L2F_Switch_AlphaCali00_32_CH00



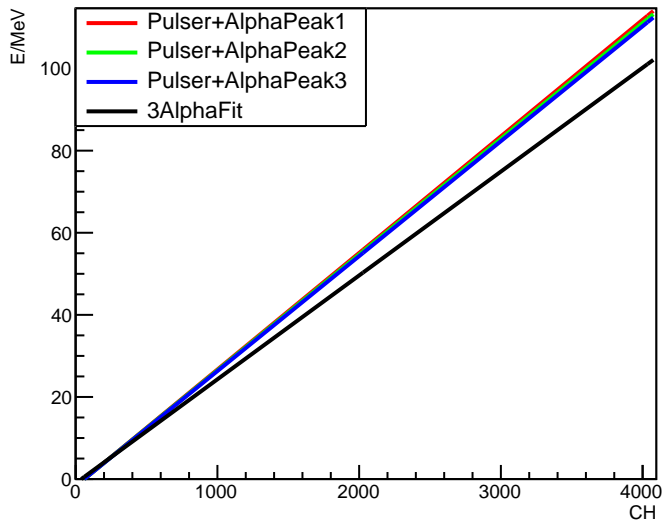
SSD3_L2F_Switch_AlphaCali00_32_CH00



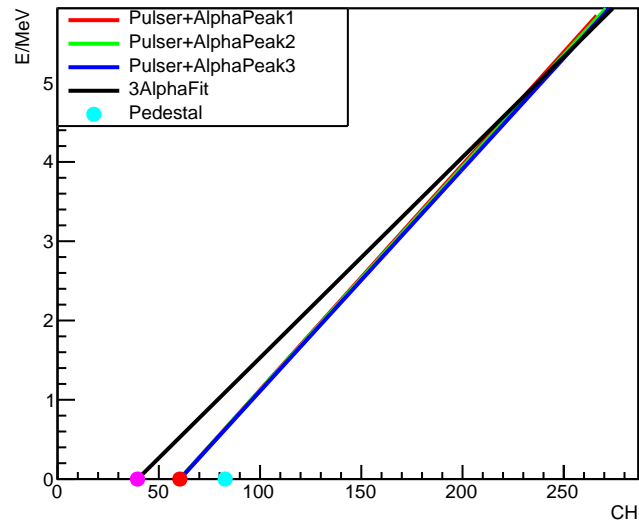
SSD3_L2F_Switch_AlphaCali00_32_CH00



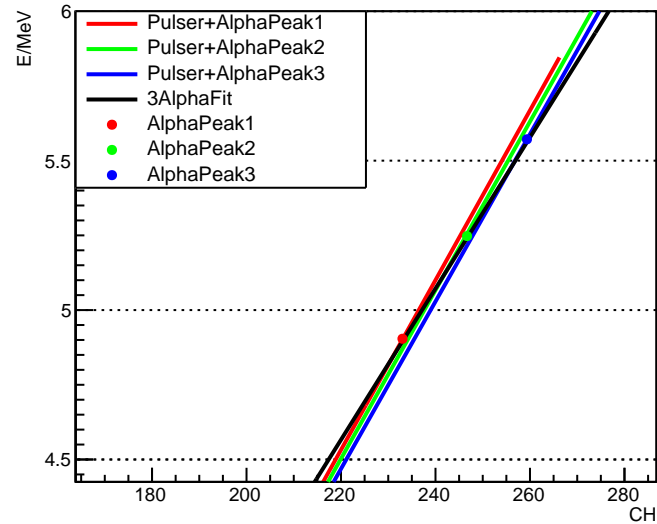
SSD3_L2F_Switch_AlphaCali00_32_CH01



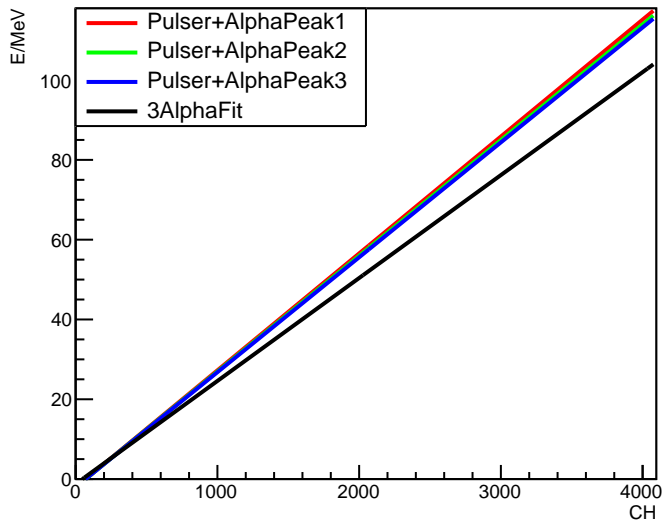
SSD3_L2F_Switch_AlphaCali00_32_CH01



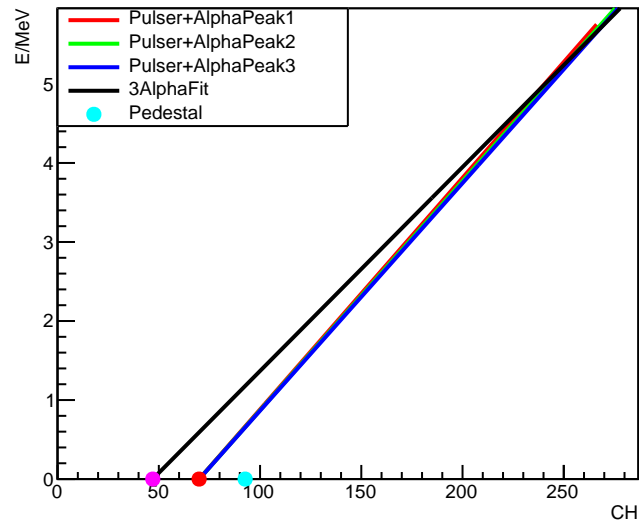
SSD3_L2F_Switch_AlphaCali00_32_CH01



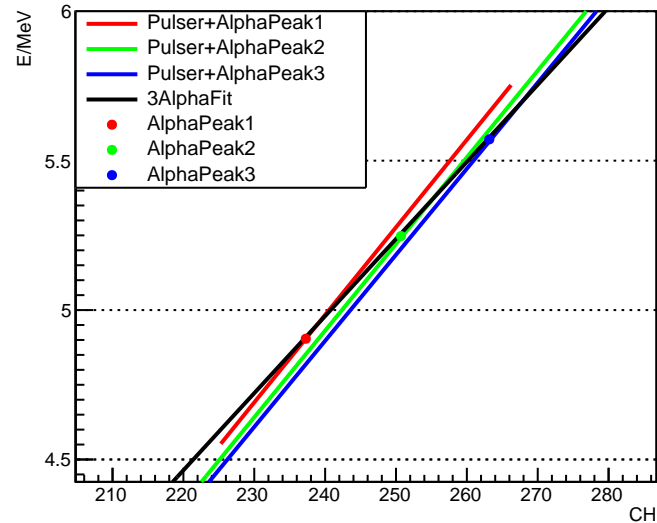
SSD3_L2F_Switch_AlphaCali00_32_CH02



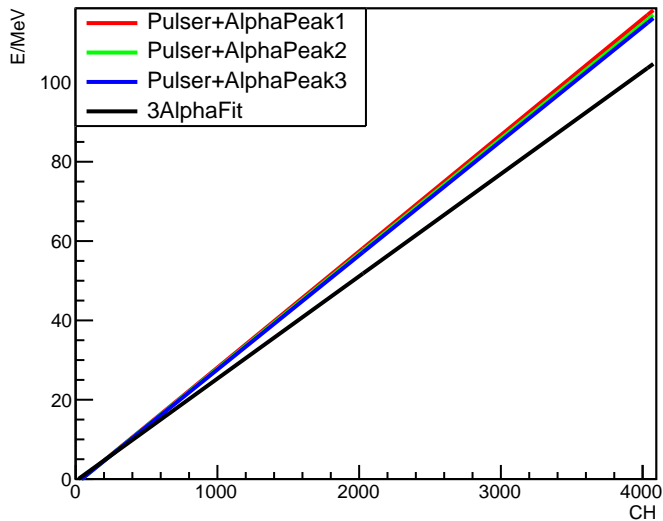
SSD3_L2F_Switch_AlphaCali00_32_CH02



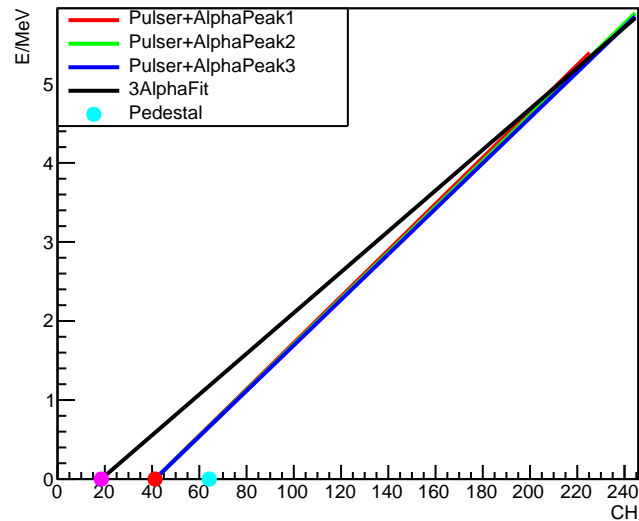
SSD3_L2F_Switch_AlphaCali00_32_CH02



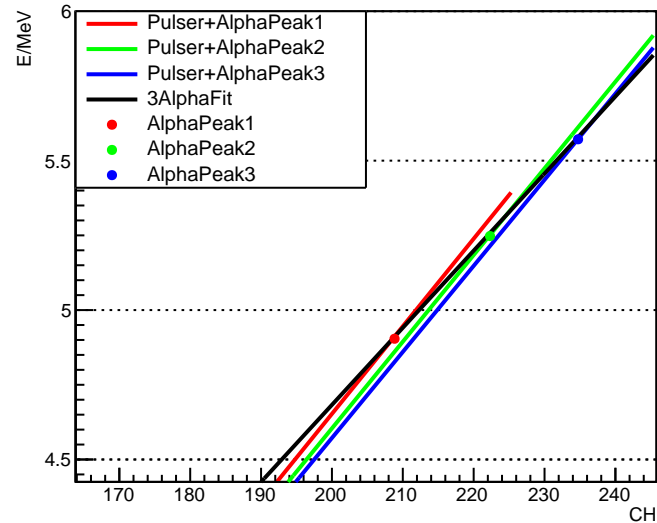
SSD3_L2F_Switch_AlphaCali00_32_CH03



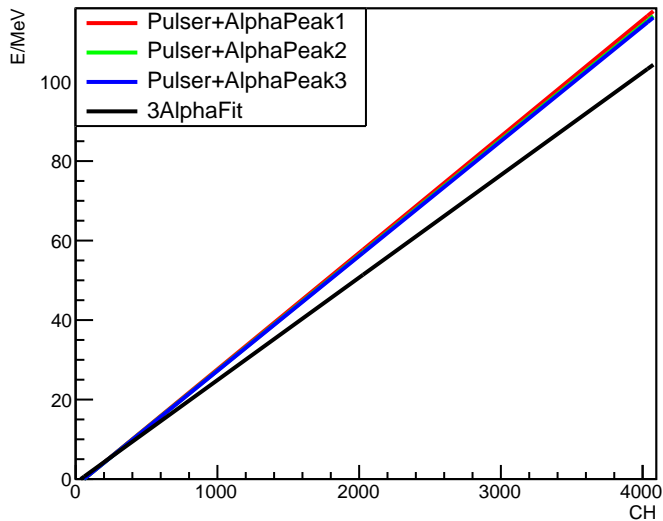
SSD3_L2F_Switch_AlphaCali00_32_CH03



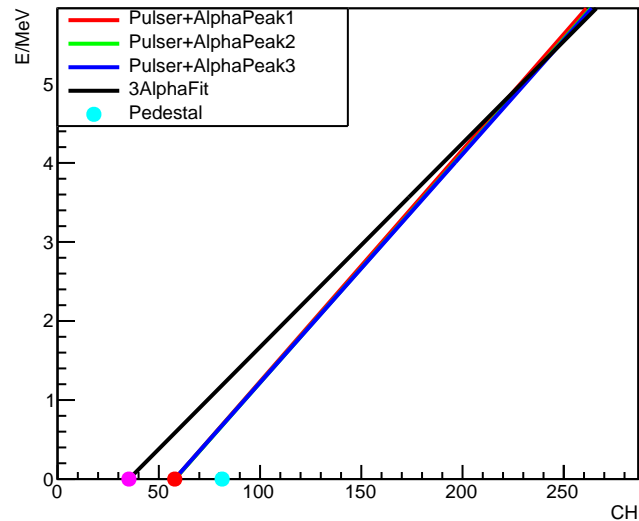
SSD3_L2F_Switch_AlphaCali00_32_CH03



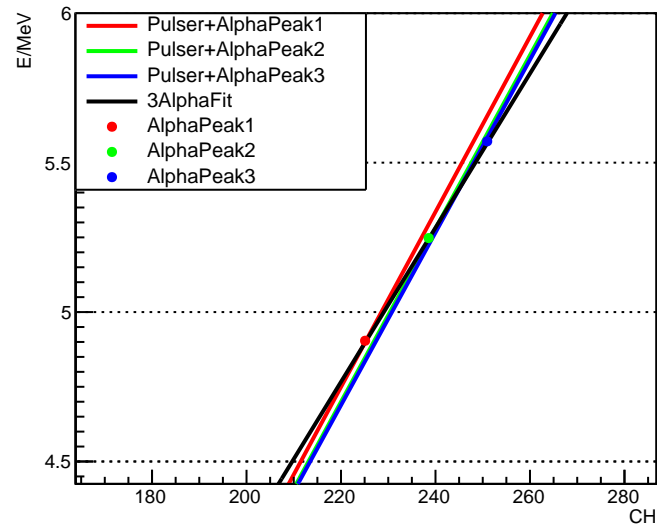
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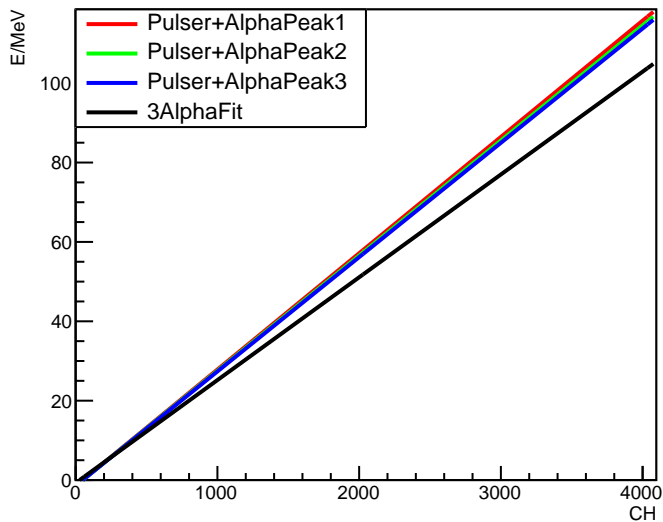
SSD3_L2F_Switch_AlphaCali00_32_CH04



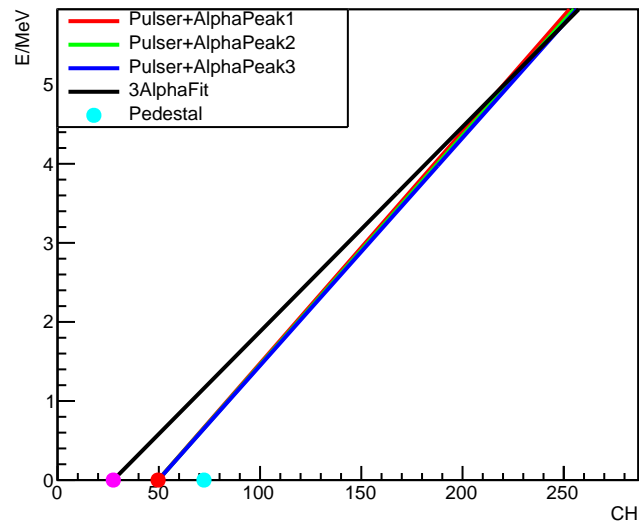
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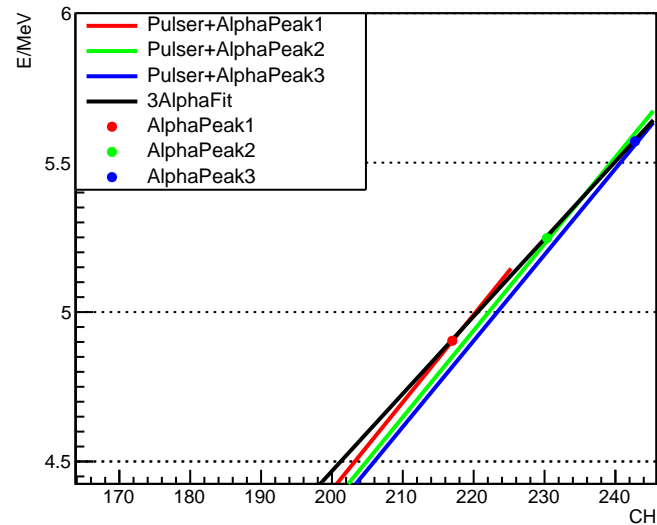
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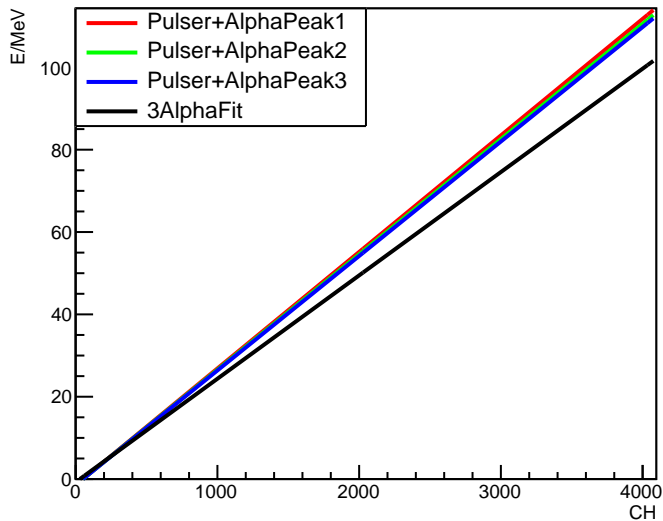
SSD3_L2F_Switch_AlphaCali00_32_CH05



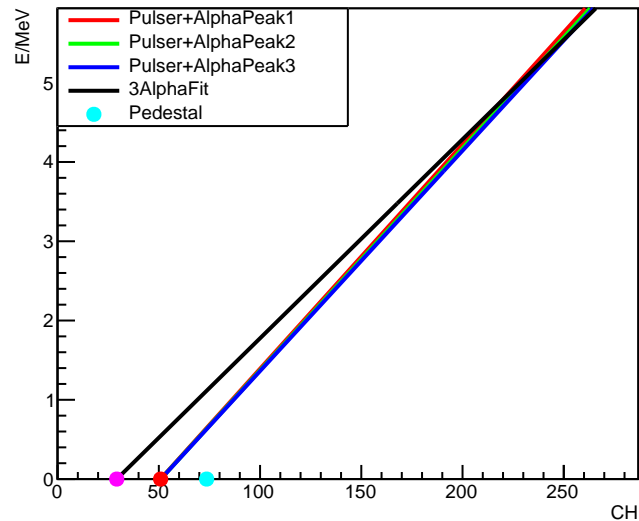
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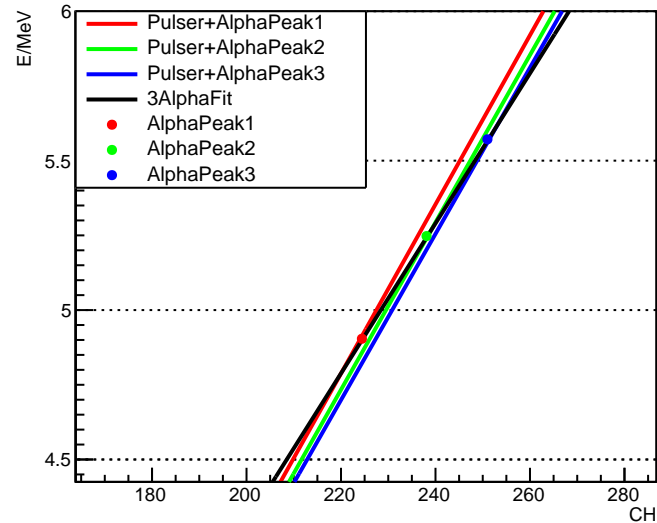
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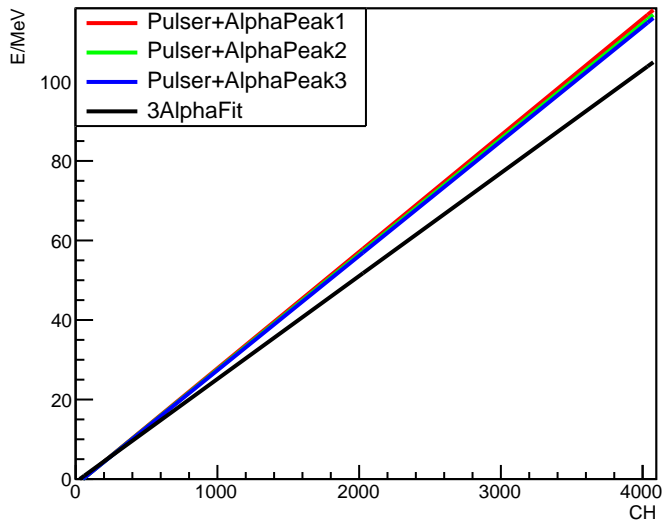
SSD3_L2F_Switch_AlphaCali00_32_CH06



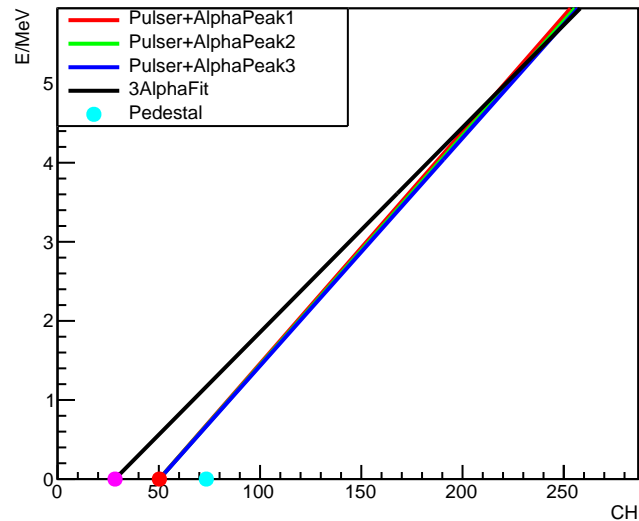
SSD3_L2F_Switch_AlphaCali00_32_CH06



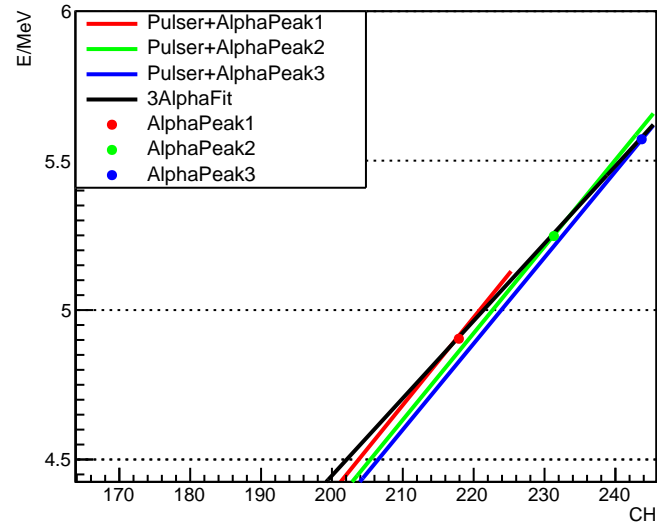
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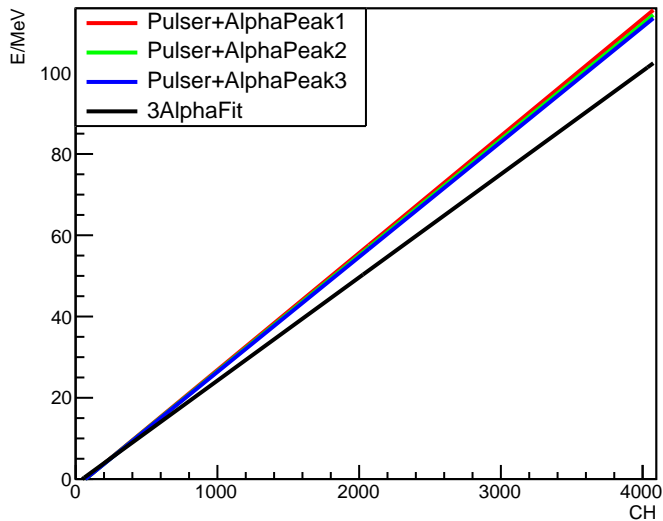
SSD3_L2F_Switch_AlphaCali00_32_CH07



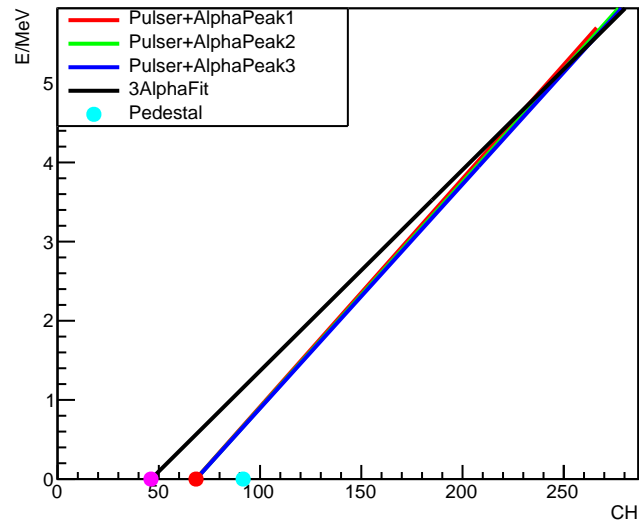
SSD3_L2F_Switch_AlphaCali00_32_CH07



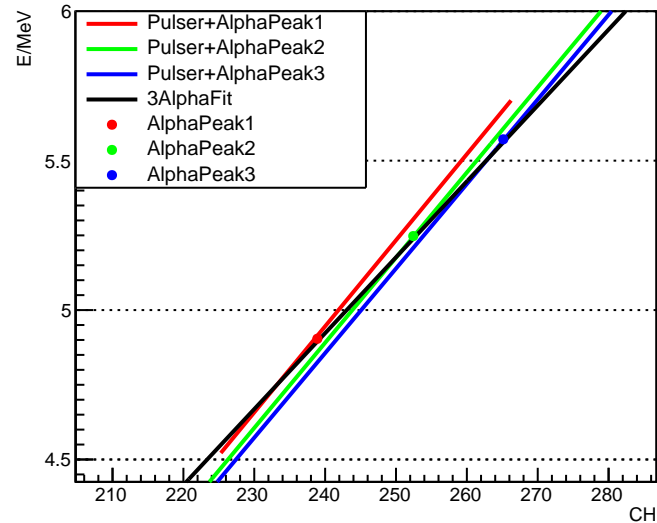
SSD3_L2F_Switch_AlphaCali00_32_CH08



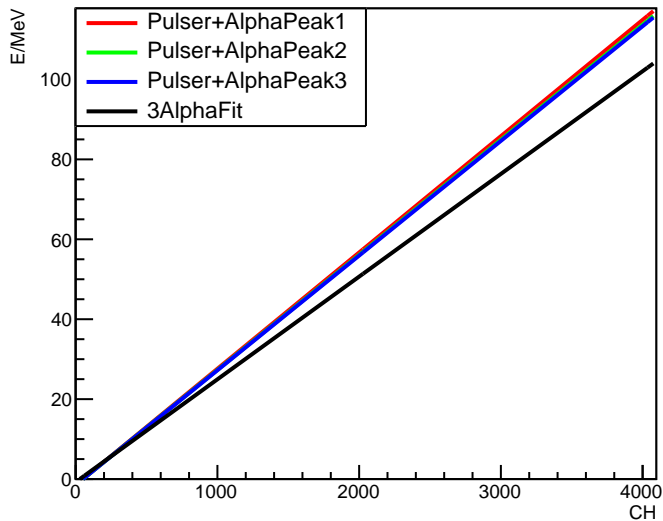
SSD3_L2F_Switch_AlphaCali00_32_CH08



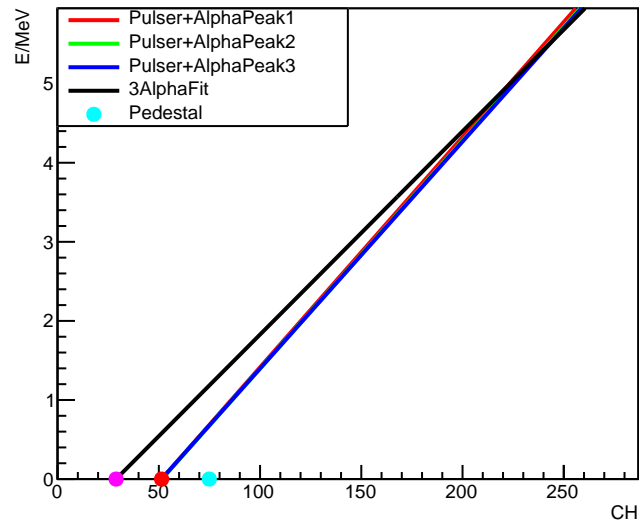
SSD3_L2F_Switch_AlphaCali00_32_CH08



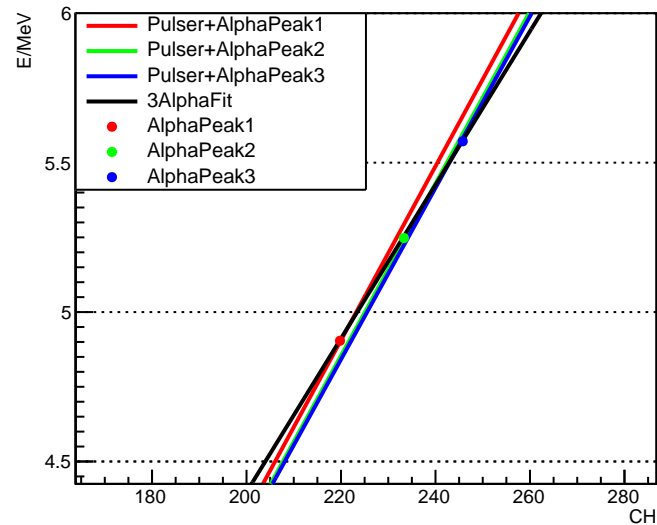
SSD3_L2F_Switch_AlphaCali00_32_CH09



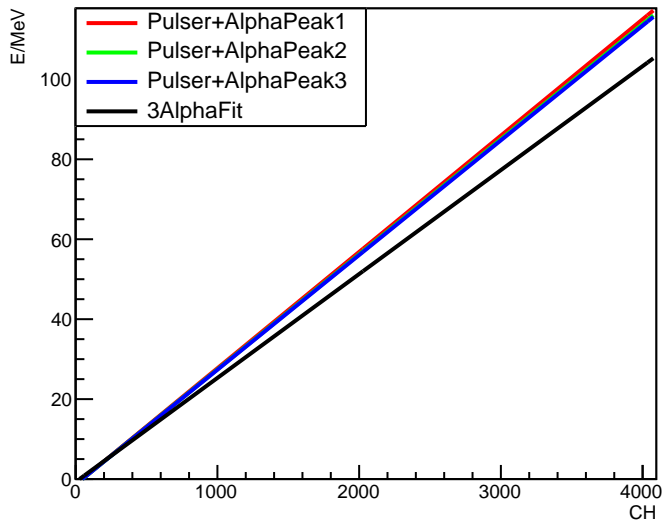
SSD3_L2F_Switch_AlphaCali00_32_CH09



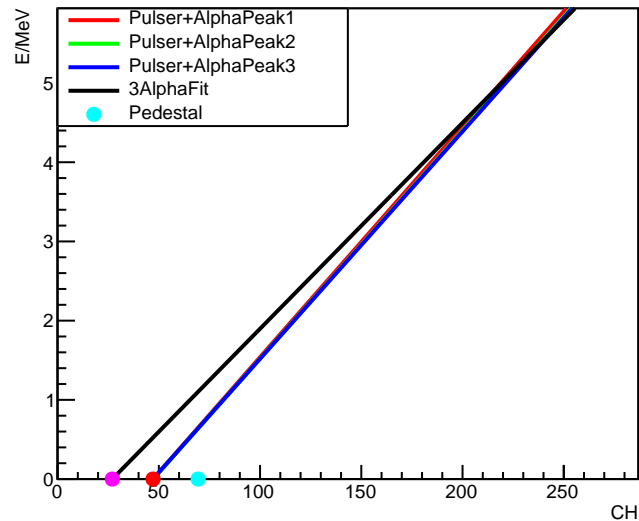
SSD3_L2F_Switch_AlphaCali00_32_CH09



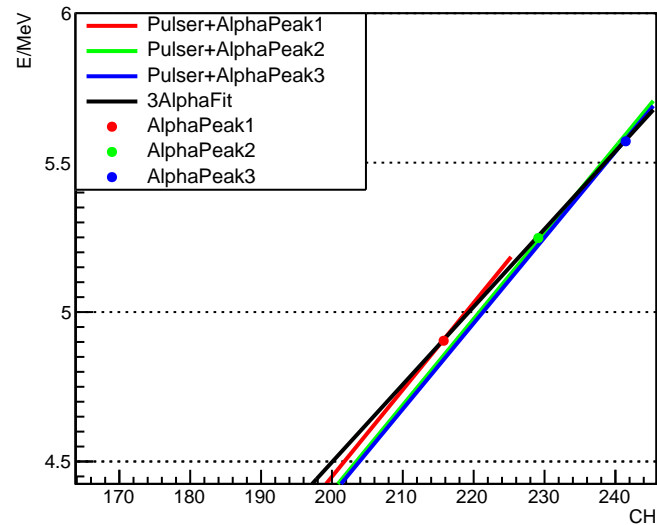
SSD3_L2F_Switch_AlphaCali00_32_CH10



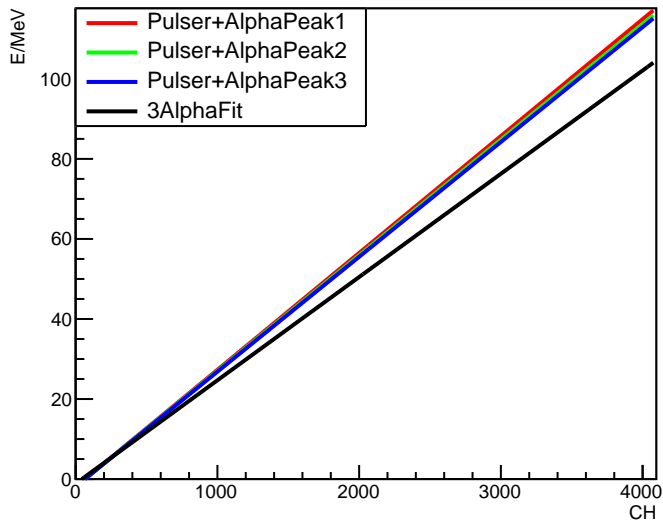
SSD3_L2F_Switch_AlphaCali00_32_CH10



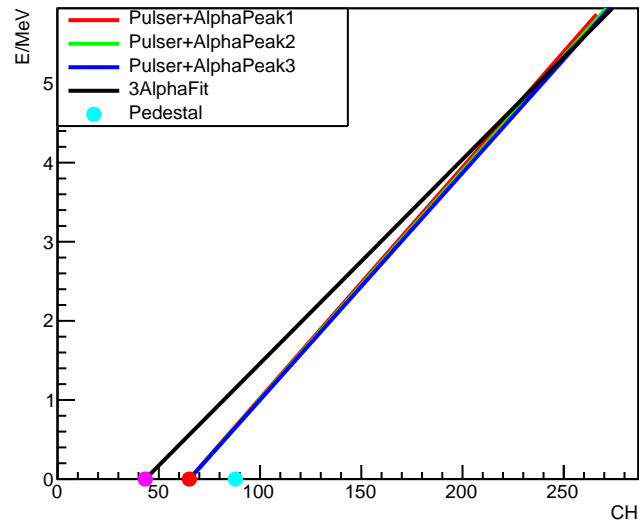
SSD3_L2F_Switch_AlphaCali00_32_CH10



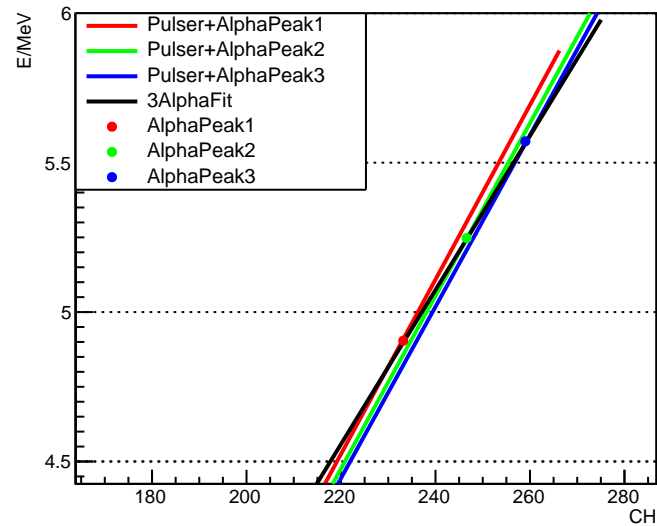
SSD3_L2F_Switch_AlphaCali00_32_CH11



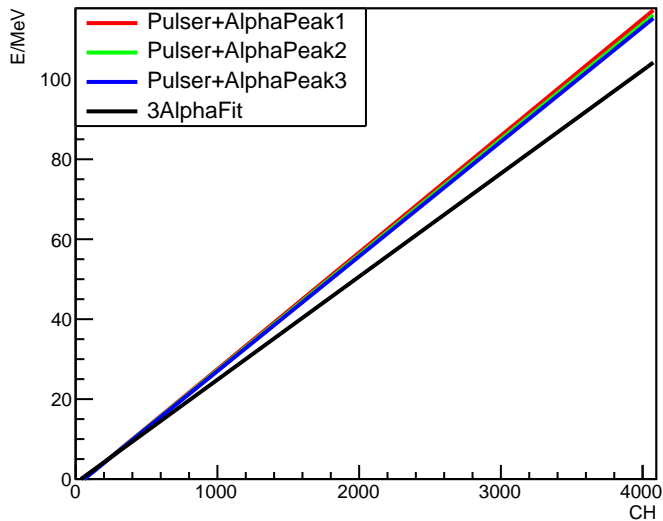
SSD3_L2F_Switch_AlphaCali00_32_CH11



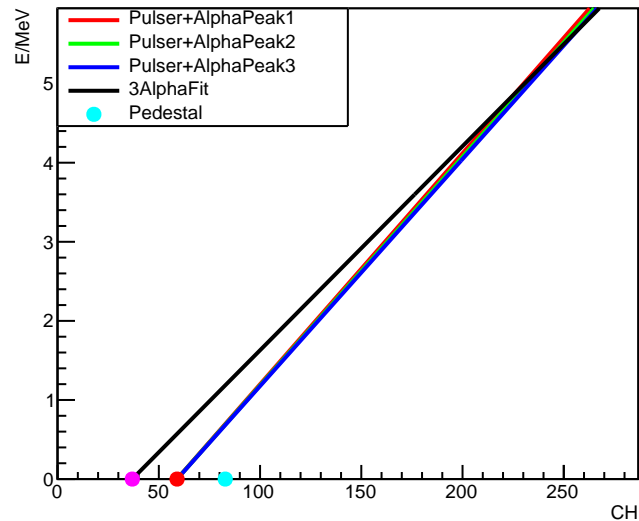
SSD3_L2F_Switch_AlphaCali00_32_CH11



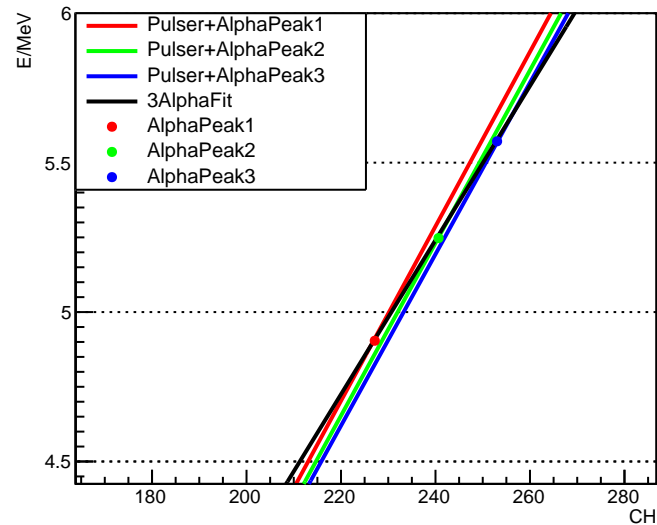
SSD3_L2F_Switch_AlphaCali00_32_CH12



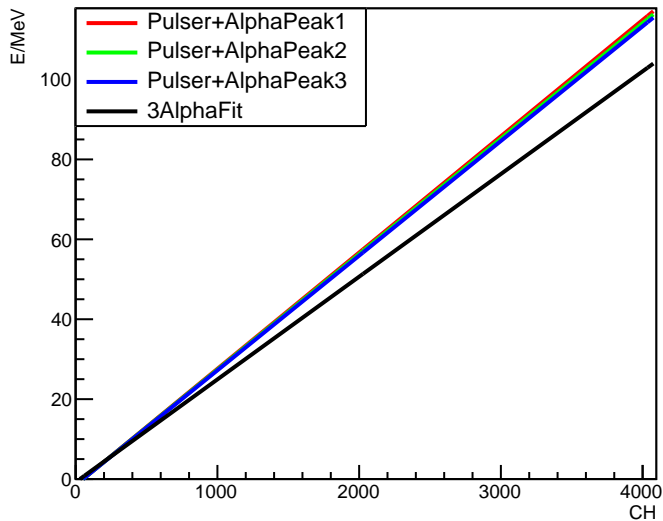
SSD3_L2F_Switch_AlphaCali00_32_CH12



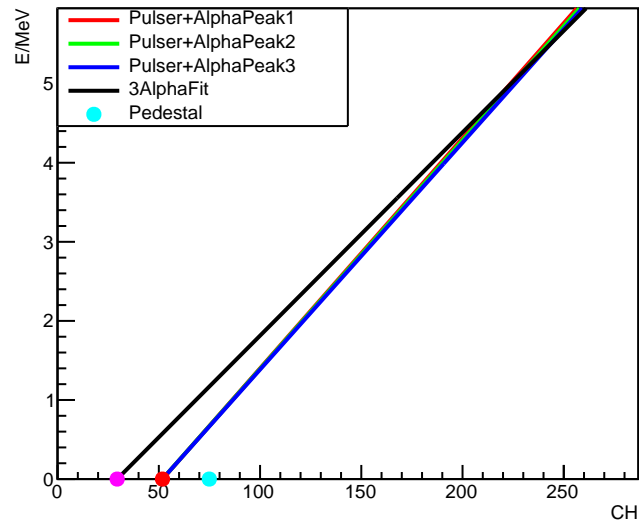
SSD3_L2F_Switch_AlphaCali00_32_CH12



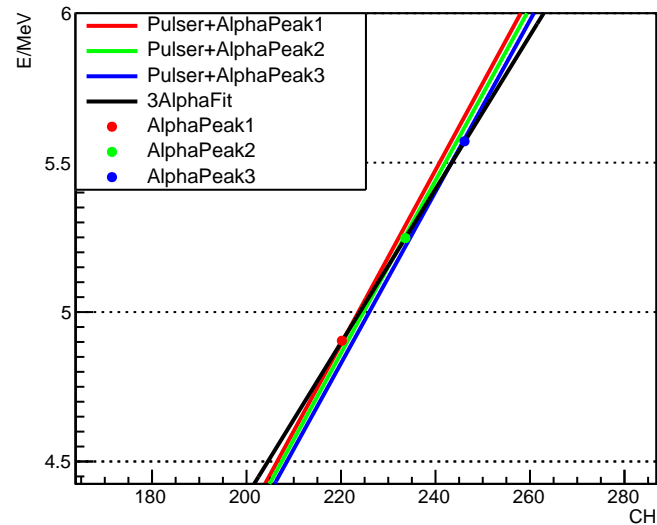
SSD3_L2F_Switch_AlphaCali00_32_CH13



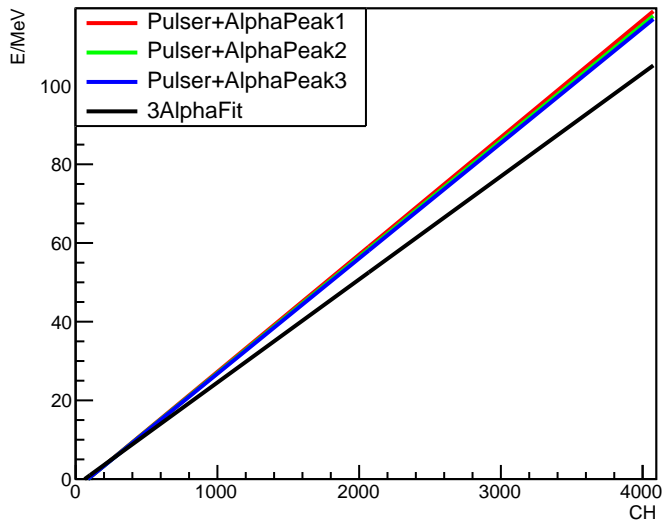
SSD3_L2F_Switch_AlphaCali00_32_CH13



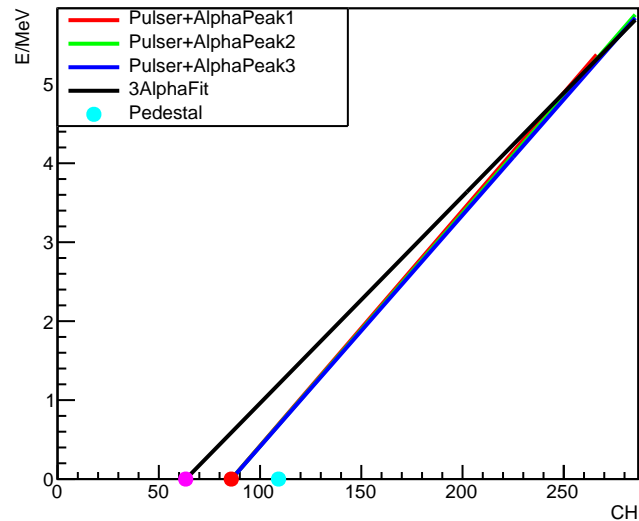
SSD3_L2F_Switch_AlphaCali00_32_CH13



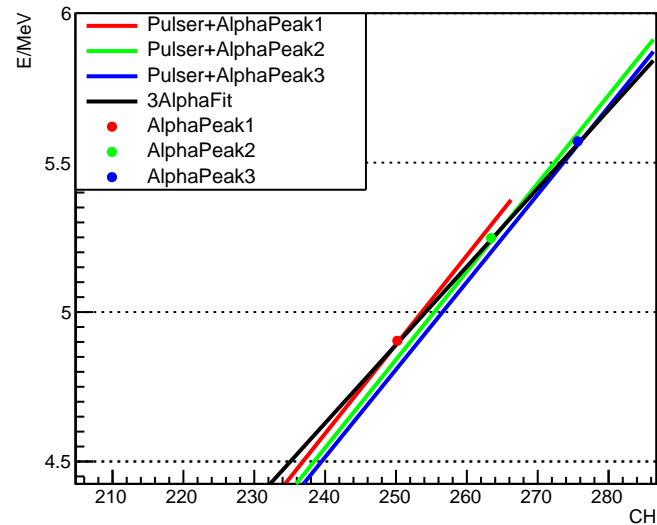
SSD3_L2F_Switch_AlphaCali00_32_CH14



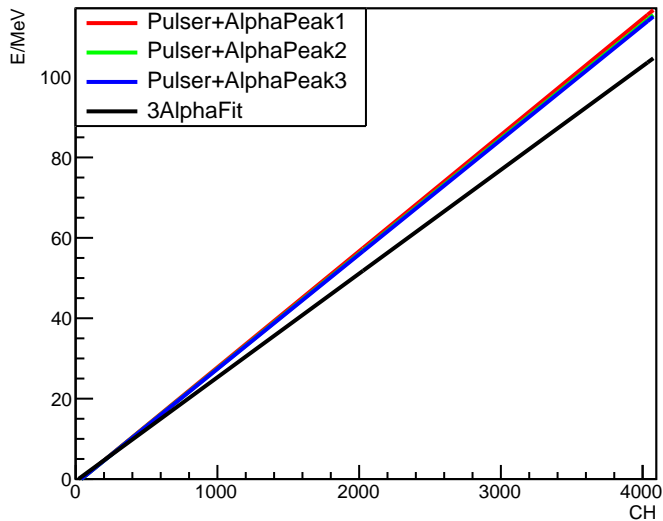
SSD3_L2F_Switch_AlphaCali00_32_CH14



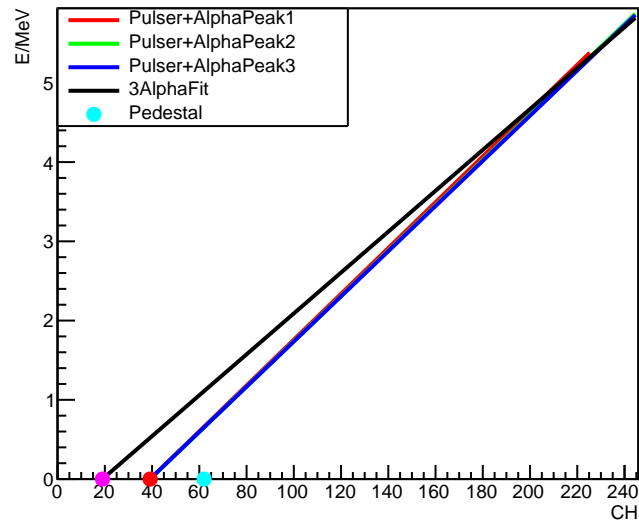
SSD3_L2F_Switch_AlphaCali00_32_CH14



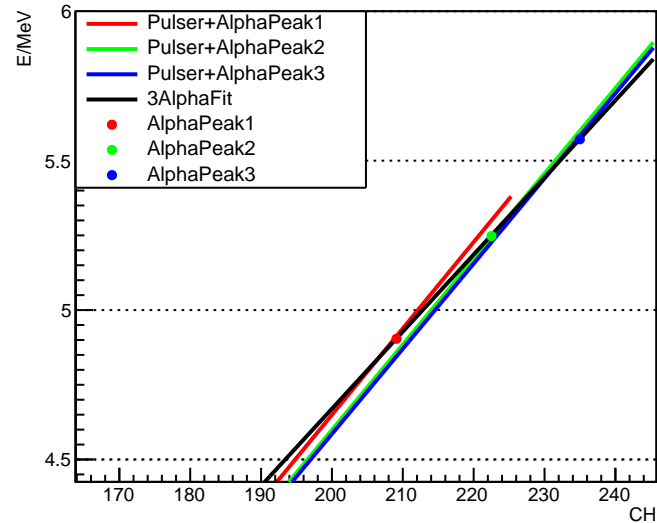
SSD3_L2F_Switch_AlphaCali00_32_CH15



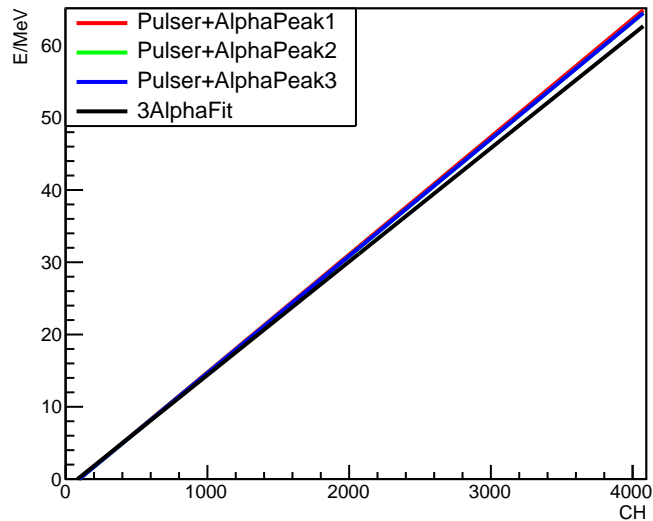
SSD3_L2F_Switch_AlphaCali00_32_CH15



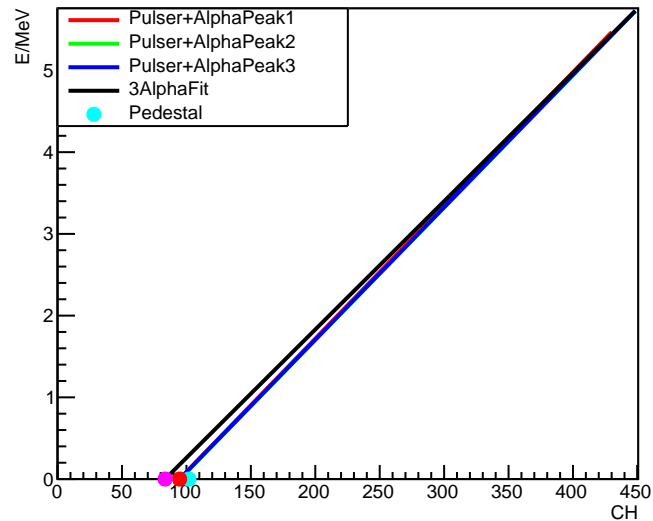
SSD3_L2F_Switch_AlphaCali00_32_CH15



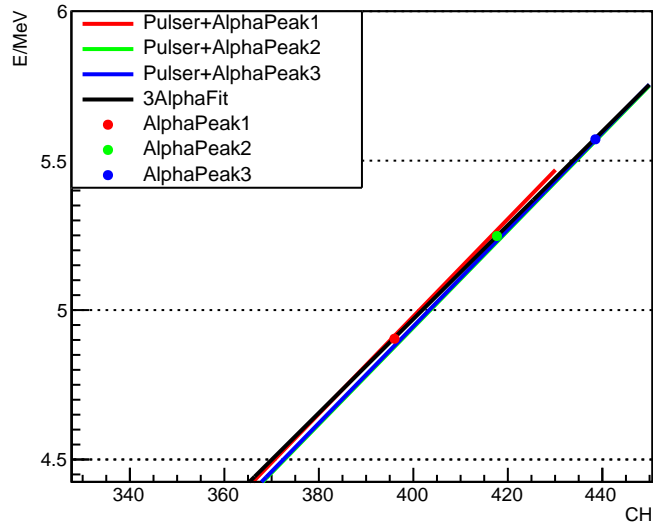
SSD4_L2F_Switch_AlphaCali00_32_CH00



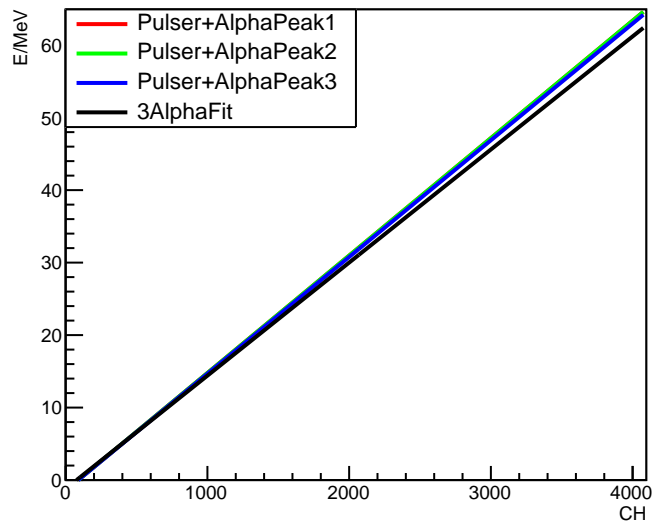
SSD4_L2F_Switch_AlphaCali00_32_CH00



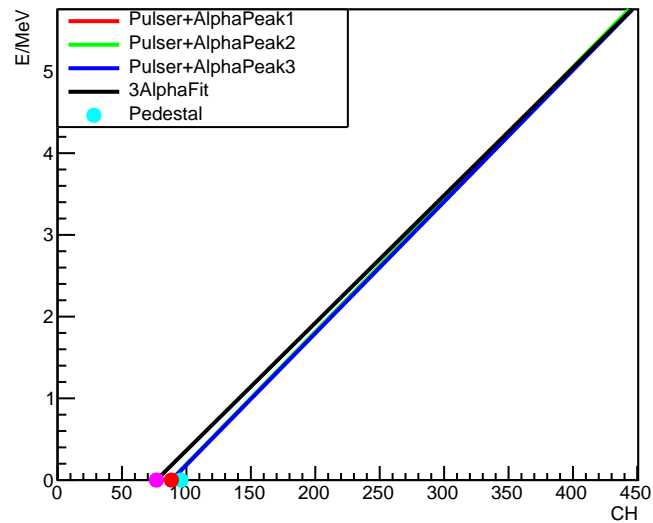
SSD4_L2F_Switch_AlphaCali00_32_CH00



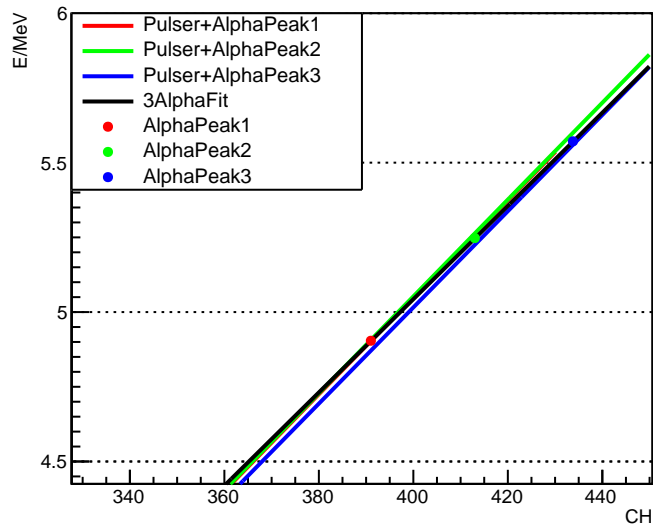
SSD4_L2F_Switch_AlphaCali00_32_CH01



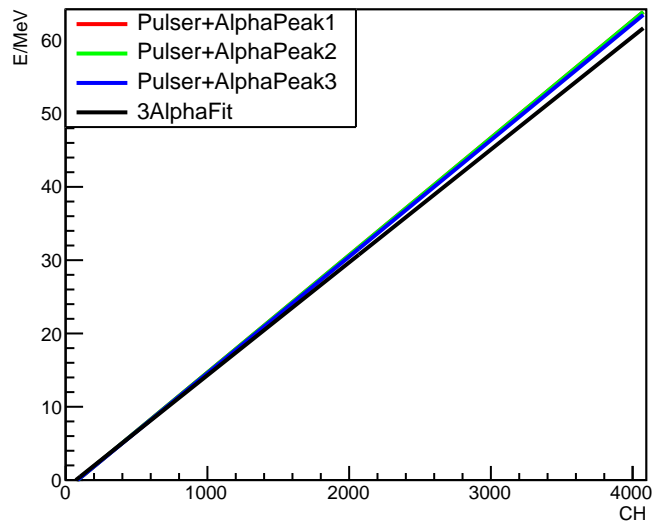
SSD4_L2F_Switch_AlphaCali00_32_CH01



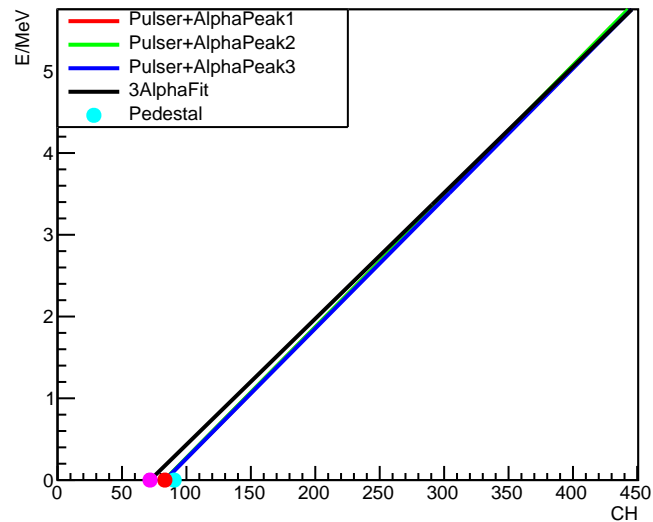
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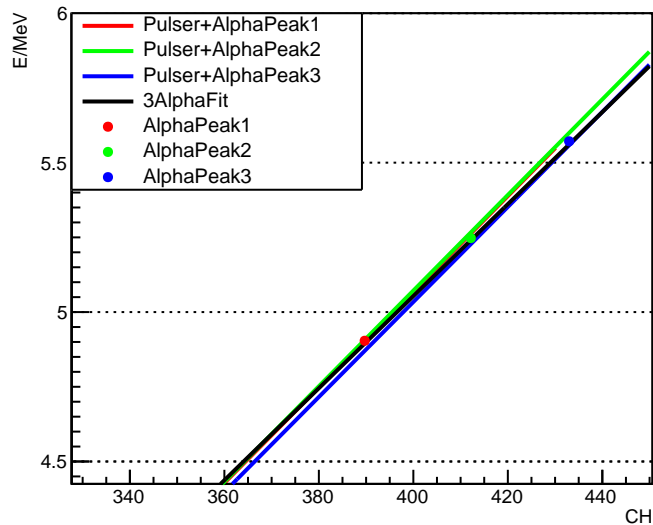
SSD4_L2F_Switch_AlphaCali00_32_CH02



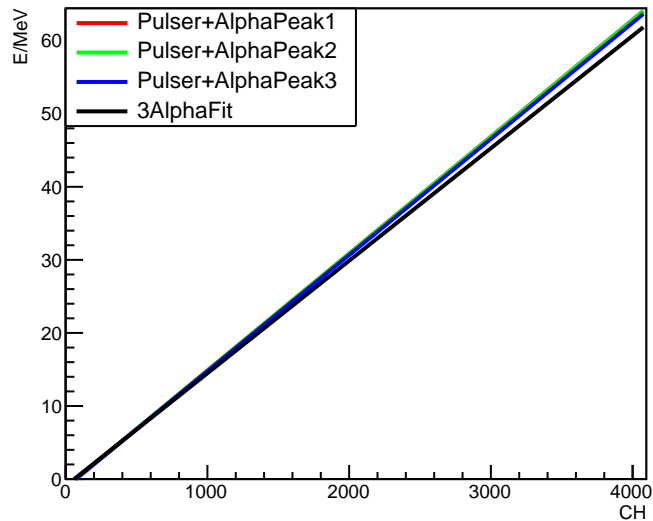
SSD4_L2F_Switch_AlphaCali00_32_CH02



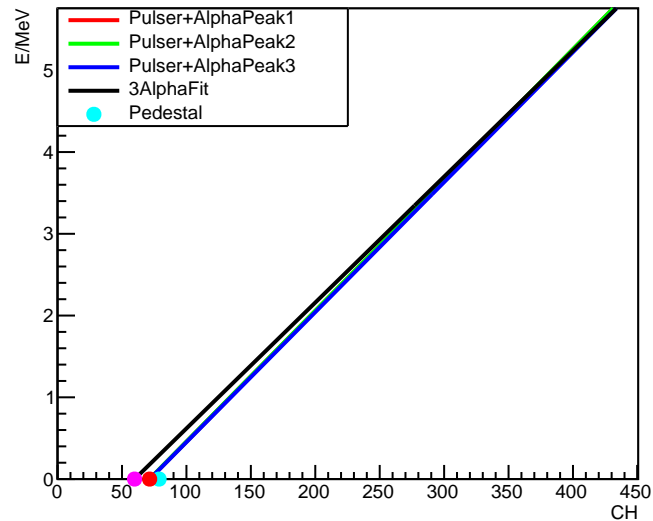
SSD4_L2F_Switch_AlphaCali00_32_CH02



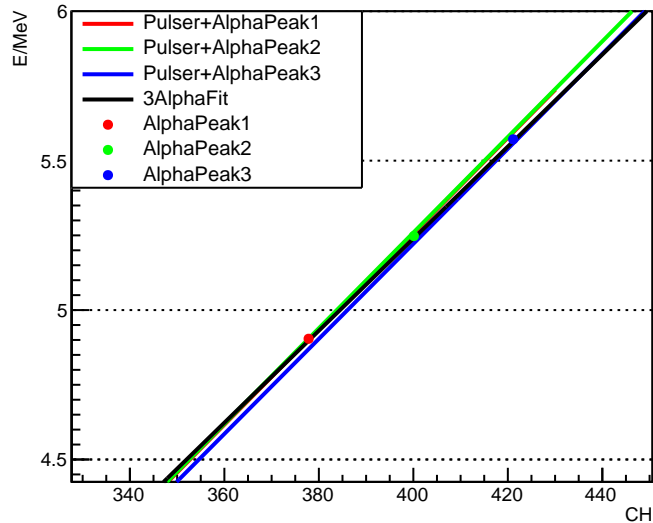
SSD4_L2F_Switch_AlphaCali00_32_CH03



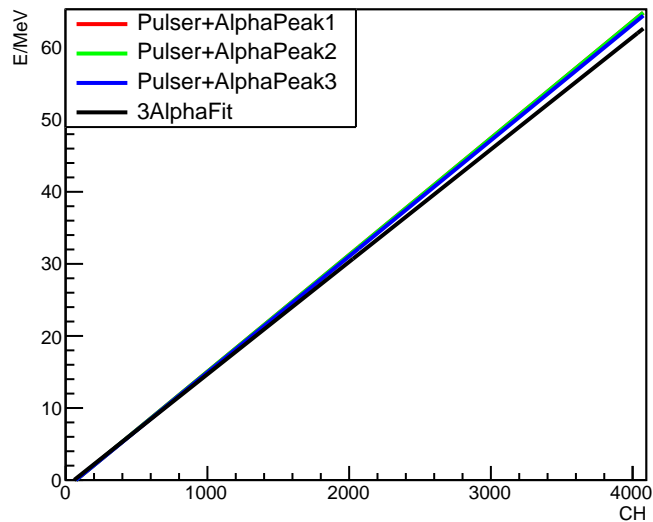
SSD4_L2F_Switch_AlphaCali00_32_CH03



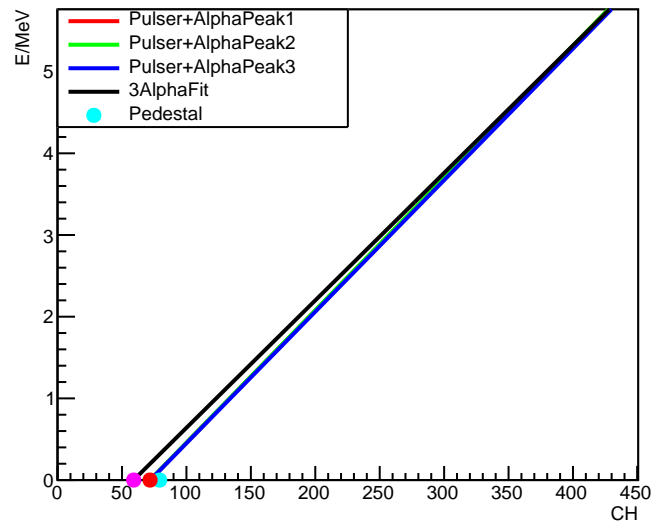
SSD4_L2F_Switch_AlphaCali00_32_CH03



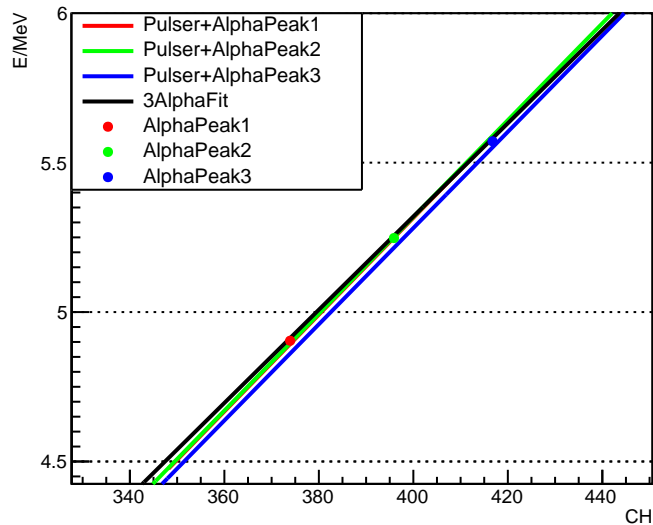
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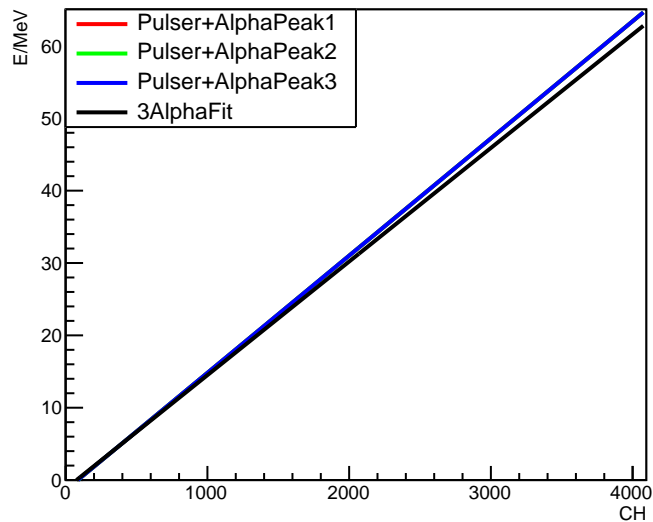
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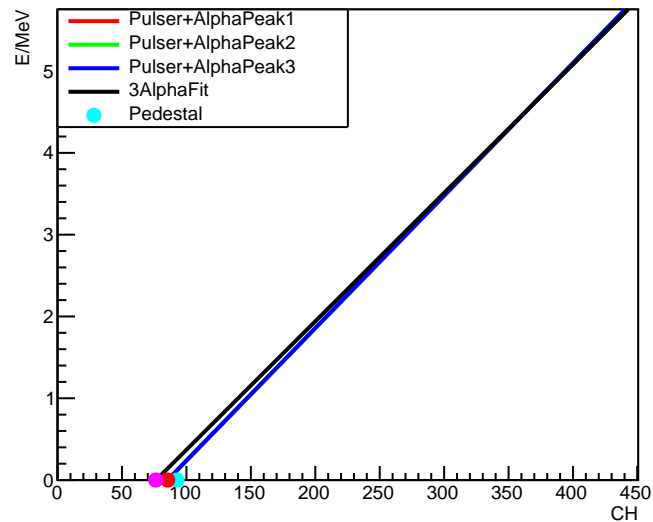
SSD4_L2F_Switch_AlphaCali00_32_CH04



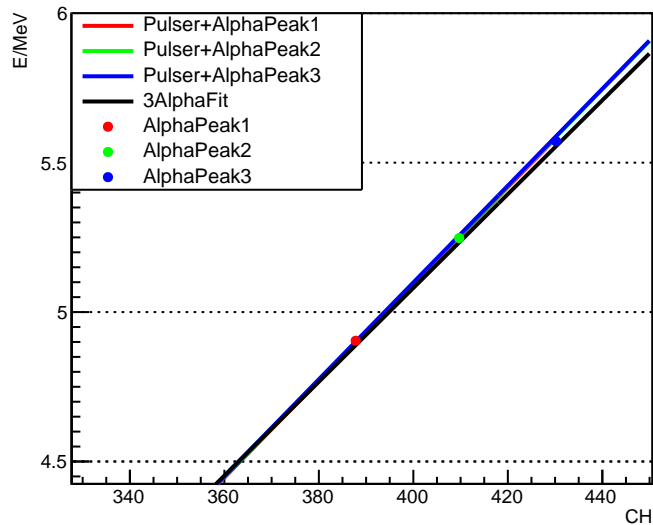
SSD4_L2F_Switch_AlphaCali00_32_CH05



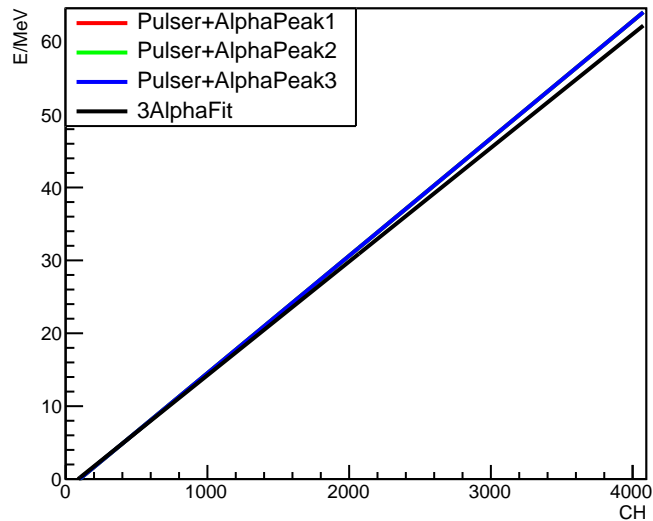
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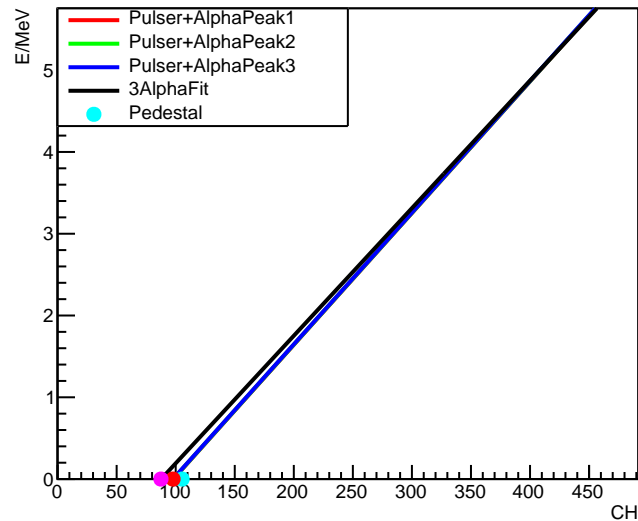
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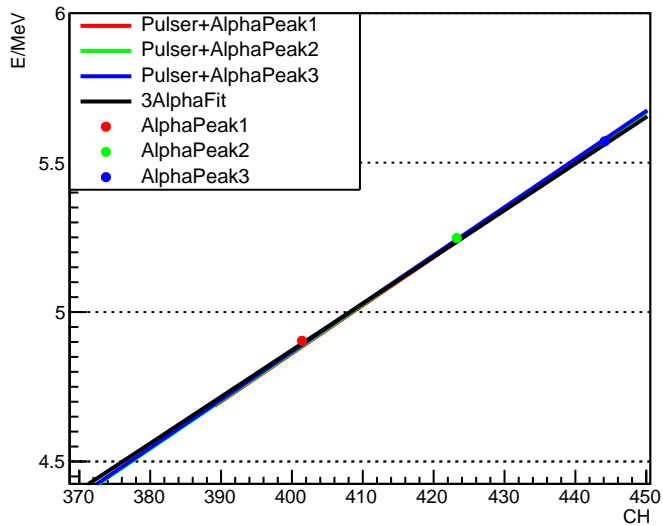
SSD4_L2F_Switch_AlphaCali00_32_CH06



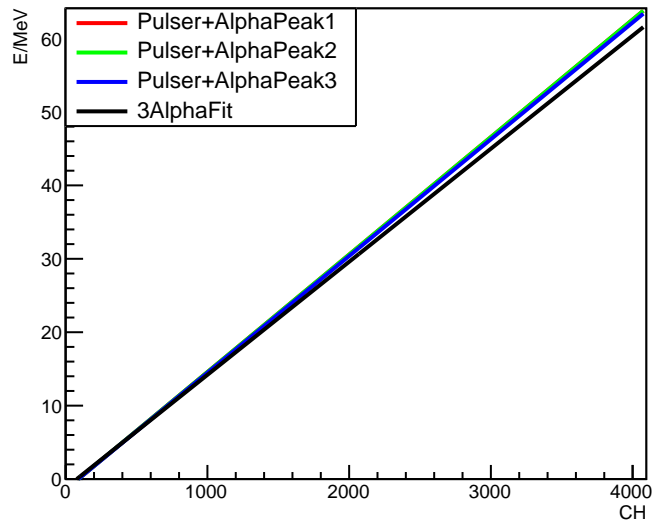
SSD4_L2F_Switch_AlphaCali00_32_CH06



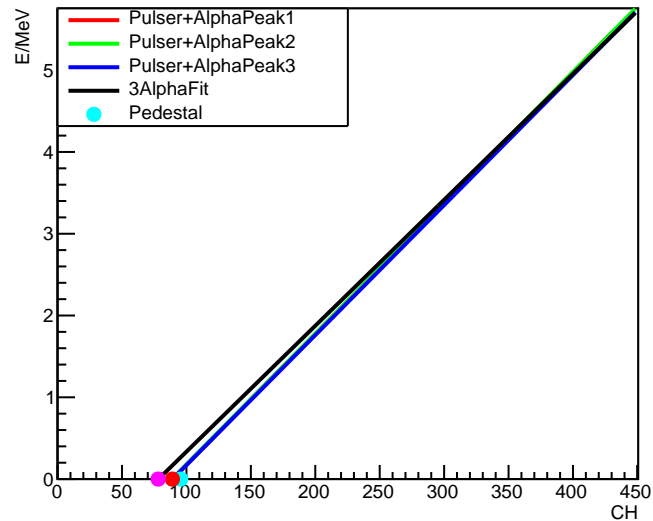
SSD4_L2F_Switch_AlphaCali00_32_CH06



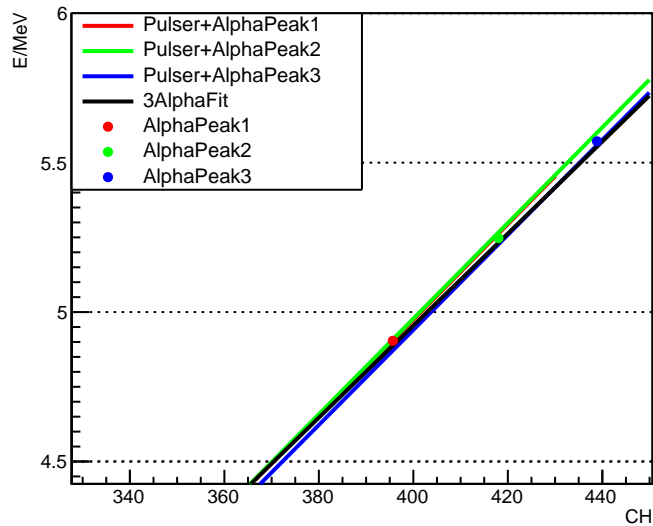
SSD4_L2F_Switch_AlphaCali00_32_CH07



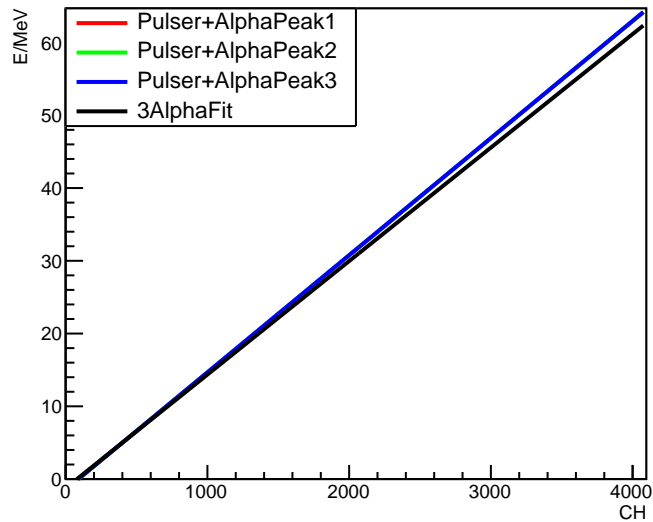
SSD4_L2F_Switch_AlphaCali00_32_CH07



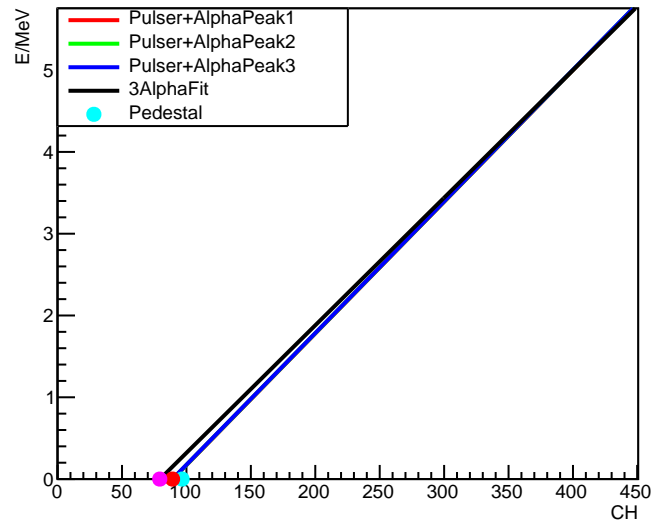
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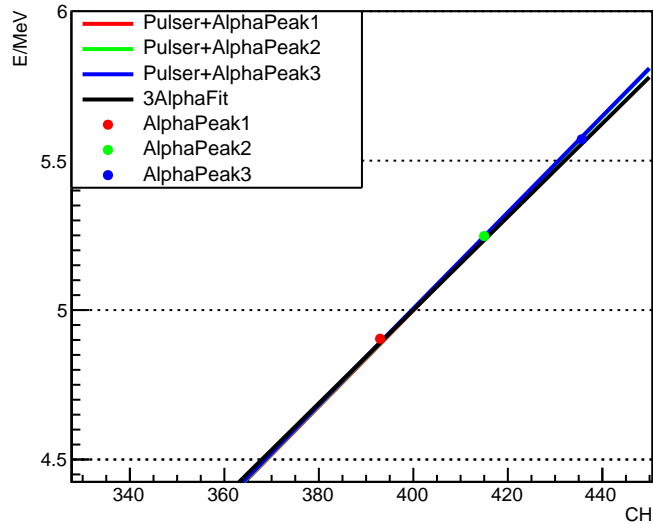
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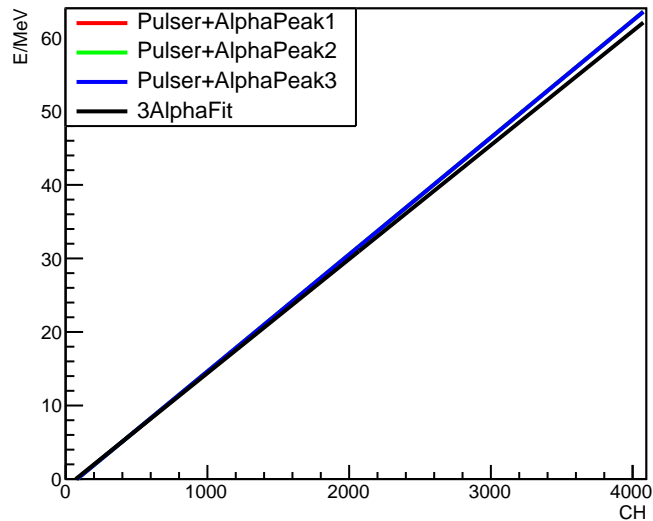
SSD4_L2F_Switch_AlphaCali00_32_CH08



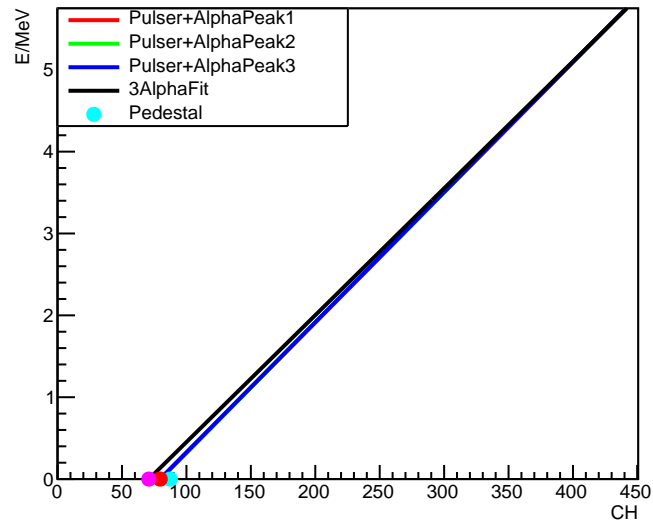
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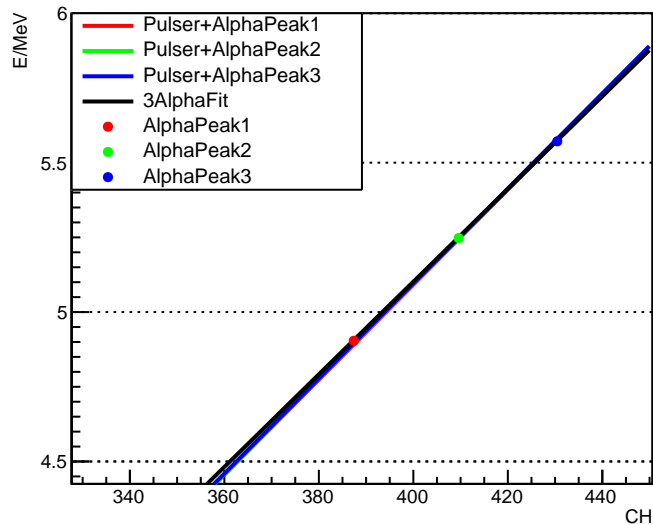
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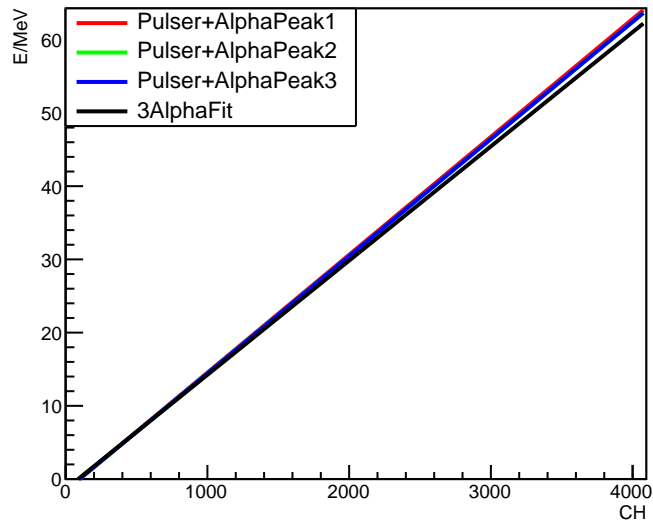
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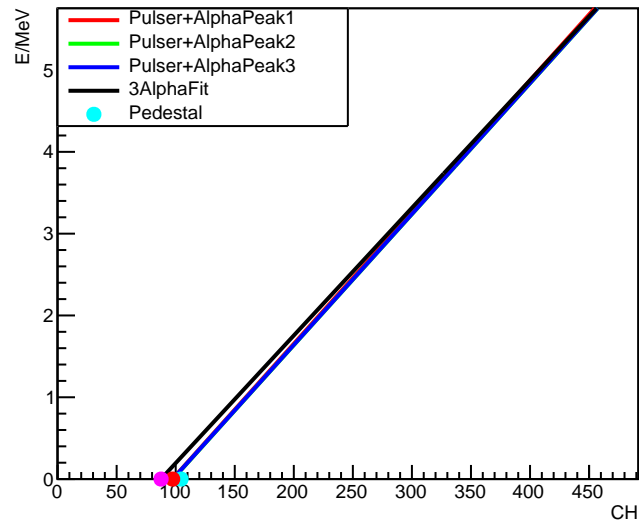
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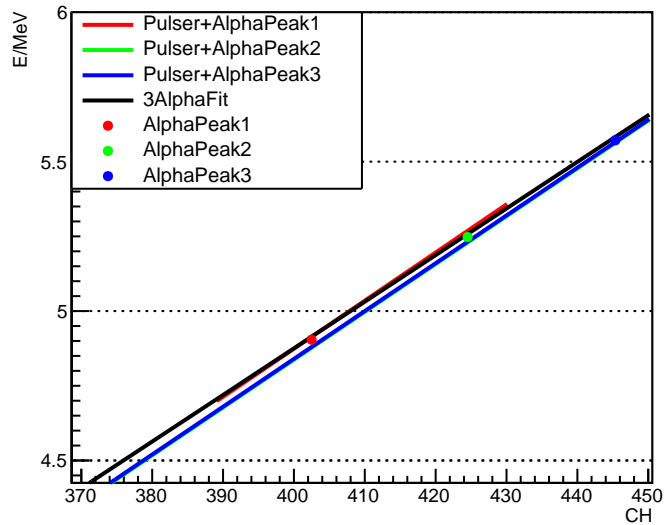
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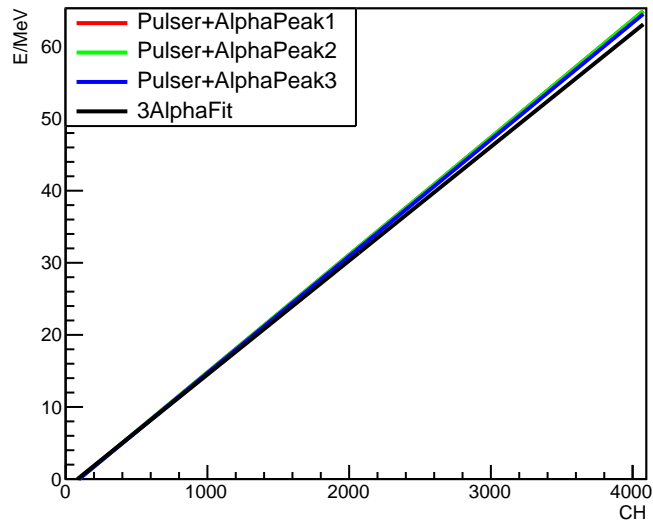
SSD4_L2F_Switch_AlphaCali00_32_CH10



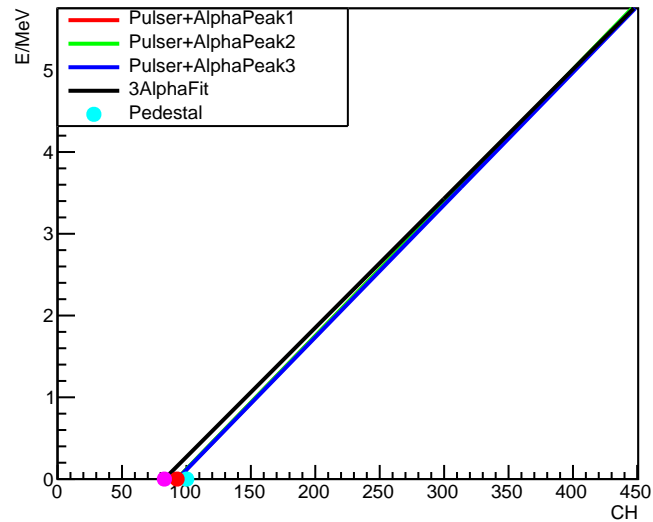
SSD4_L2F_Switch_AlphaCali00_32_CH10



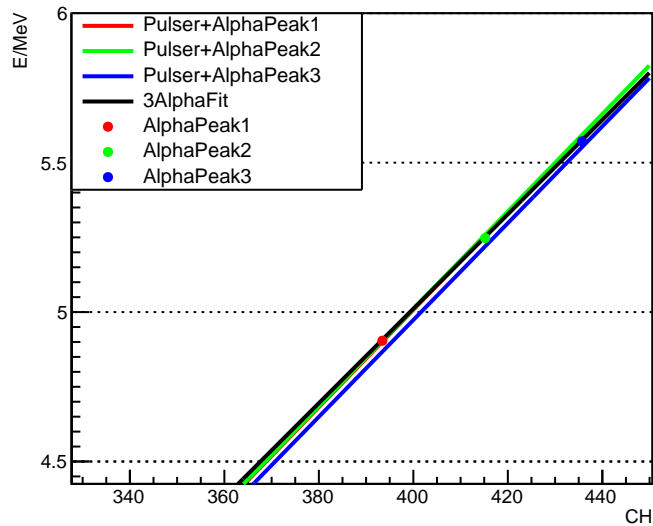
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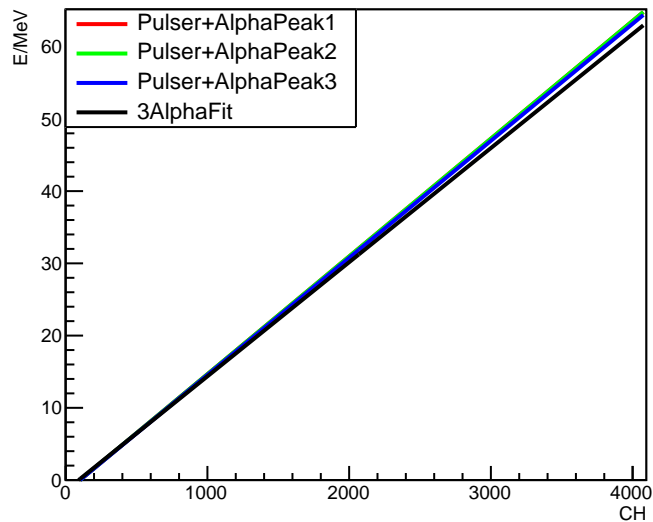
SSD4_L2F_Switch_AlphaCali00_32_CH11



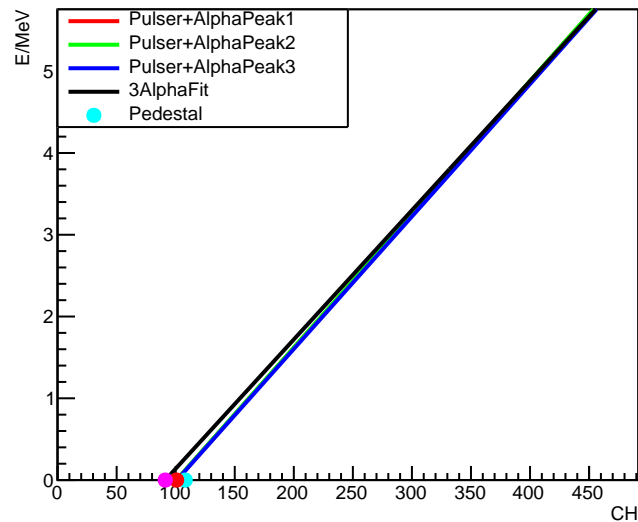
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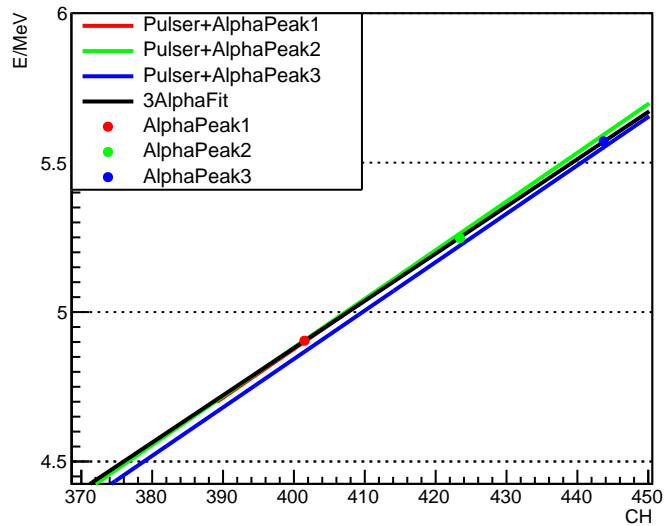
SSD4_L2F_Switch_AlphaCali00_32_CH12



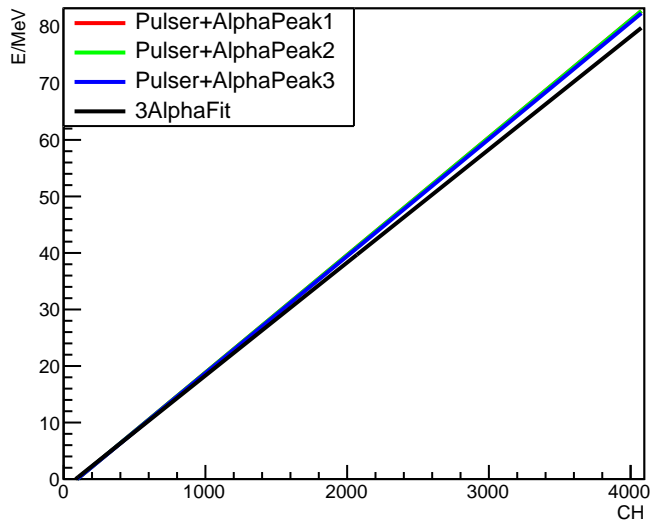
SSD4_L2F_Switch_AlphaCali00_32_CH12



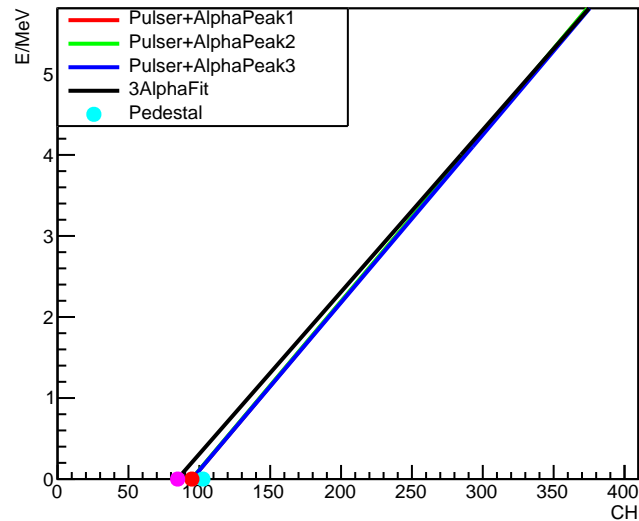
SSD4_L2F_Switch_AlphaCali00_32_CH12



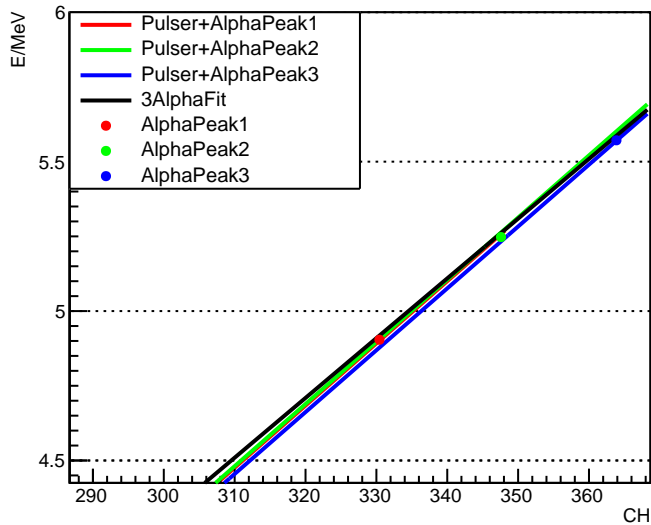
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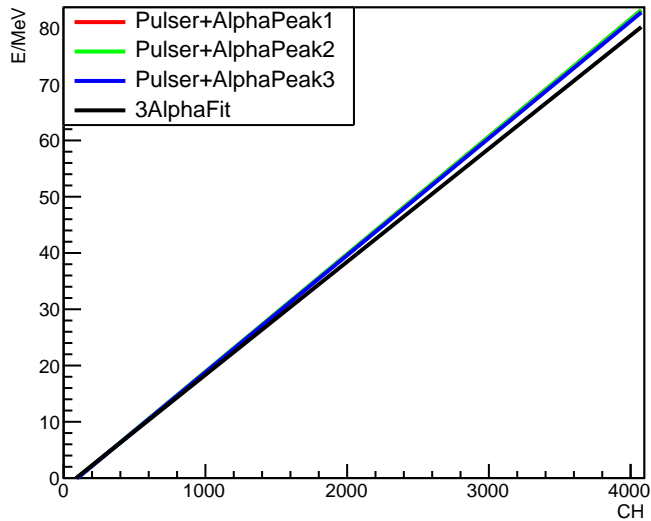
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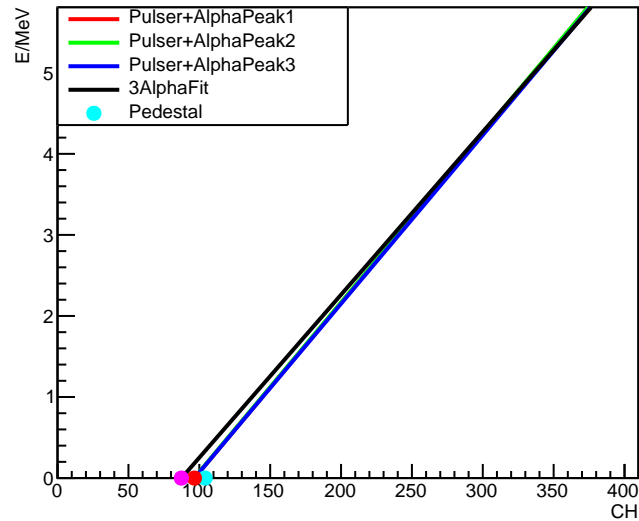
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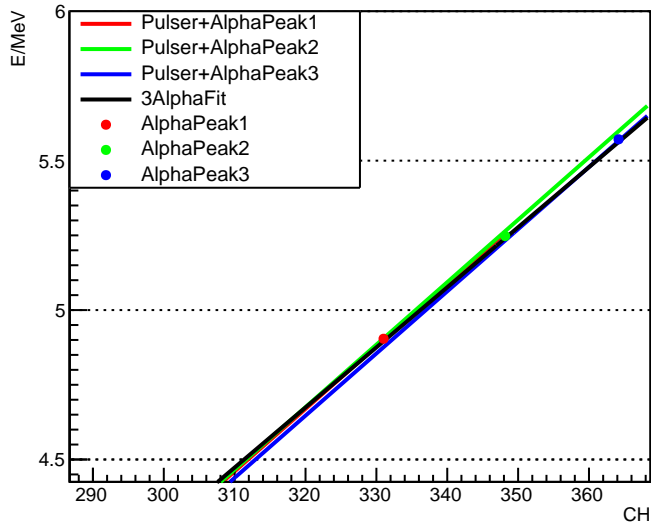
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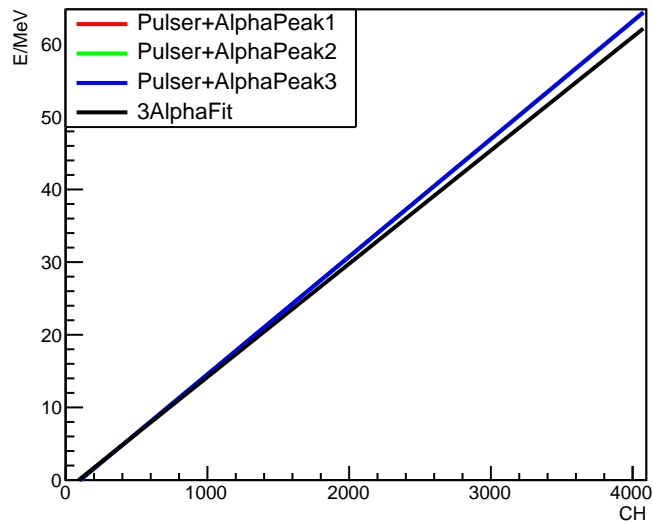
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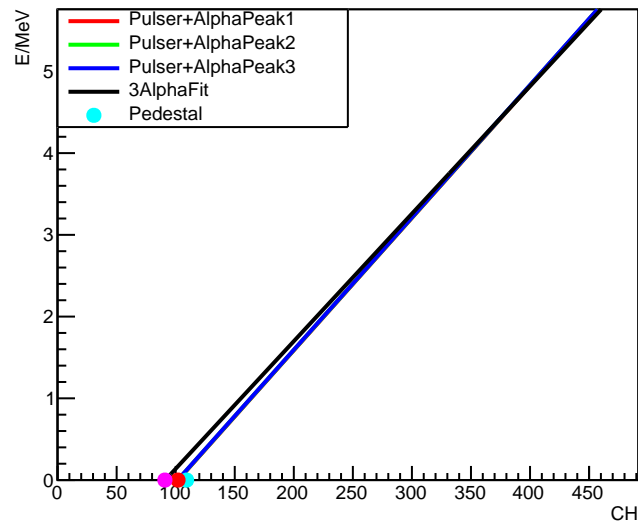
SSD4_L2F_Switch_AlphaCali00_32_CH14



SSD4_L2F_Switch_AlphaCali00_32_CH15



SSD4_L2F_Switch_AlphaCali00_32_CH15



SSD4_L2F_Switch_AlphaCali00_32_CH15

