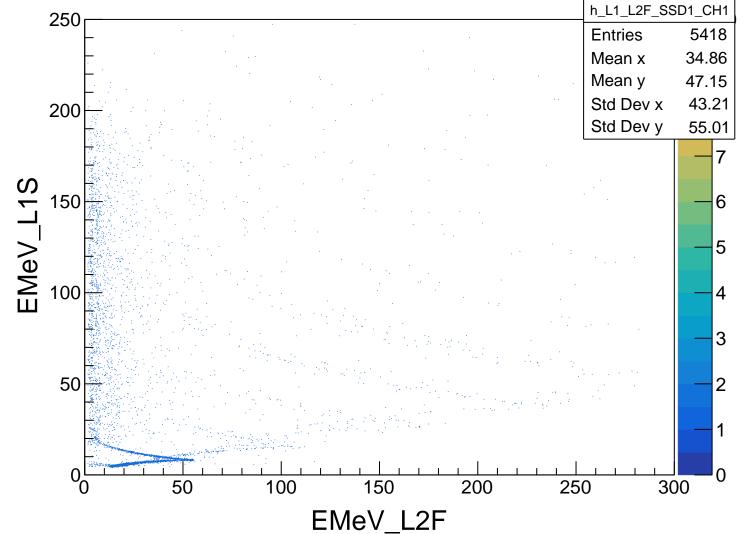
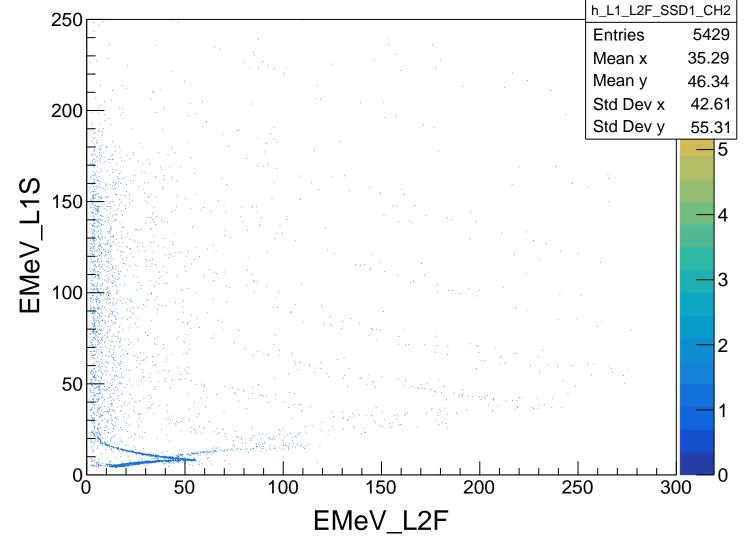
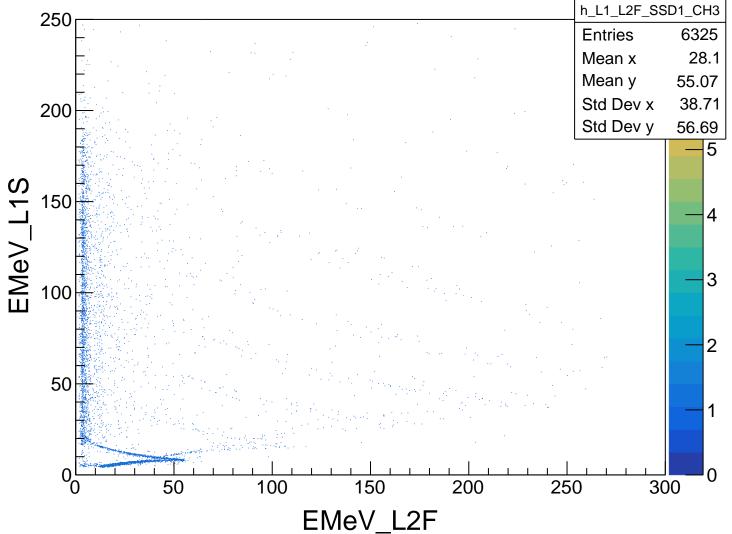
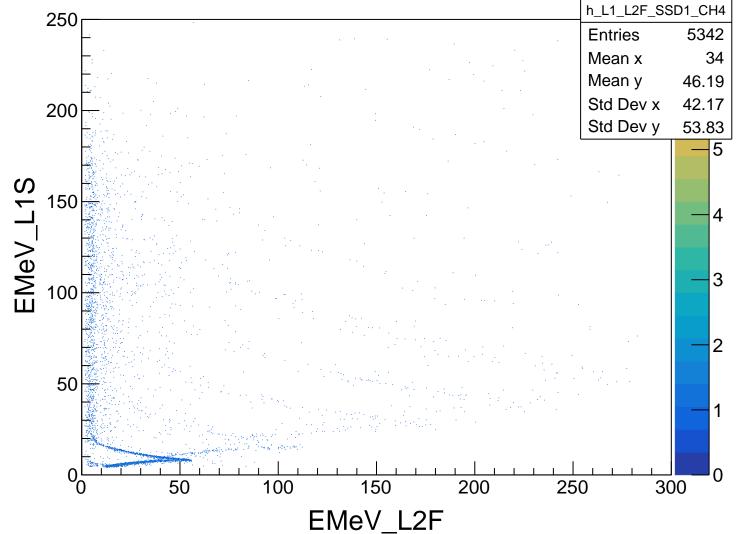
h_L1_L2F_SSD1_CH0 h_L1_L2F_SSD1_CH0 250 **Entries** 4749 Mean x 27.09 46.79 Mean y 32.67 Std Dev x 200 Std Dev y 54.14 3.5 EMeV_L1S 150 2.5 100 1.5 50 0.5 300 50 200 250 100 150 EMeV_L2F









h_L1_L2F_SSD1_CH5 h_L1_L2F_SSD1_CH5 **Entries** 6151 Mean x 28.2 Mean y 55.14 Std Dev x 38.6 Std Dev y 56.37

250

200

150

100

50

50

100

EMeV_L1S

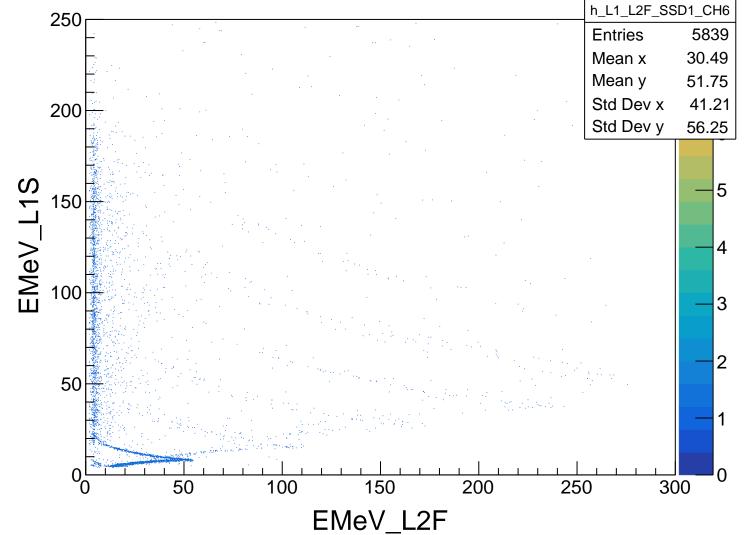


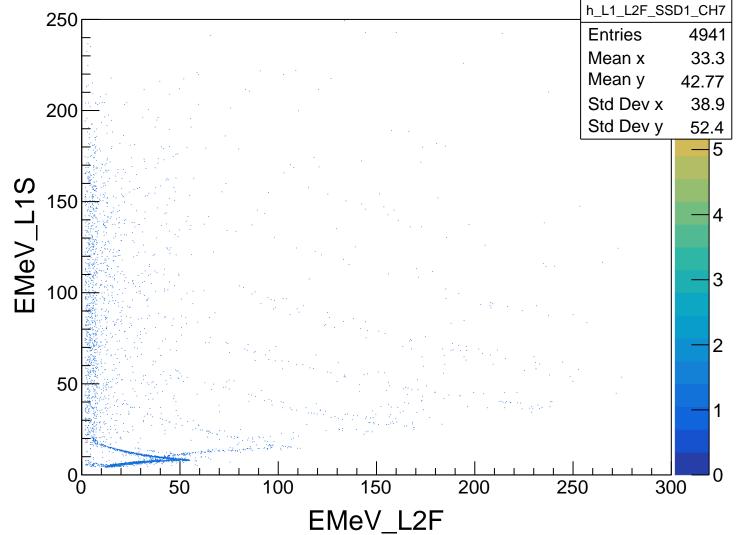
150

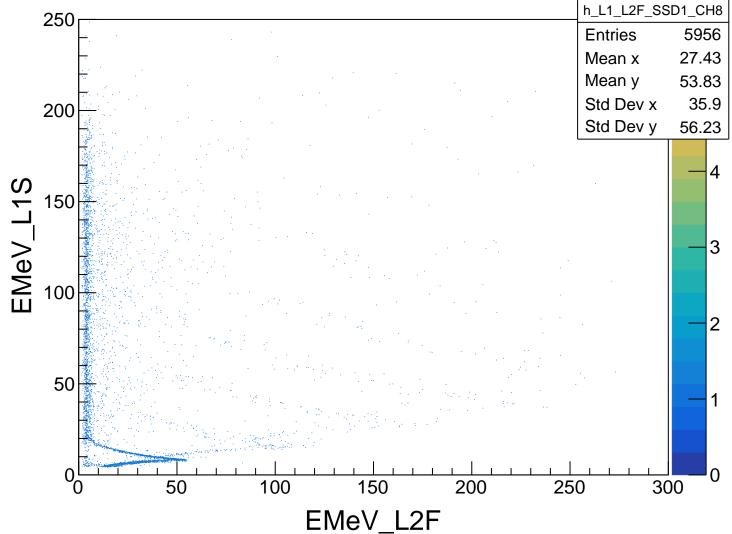
200

300

250

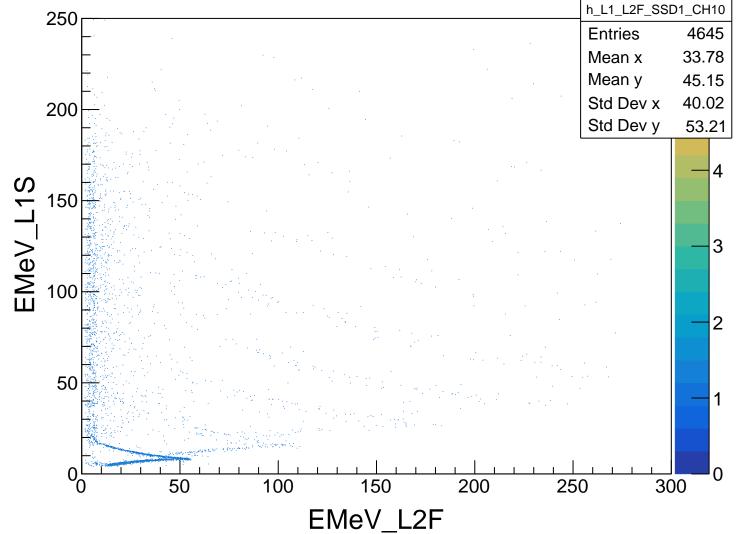


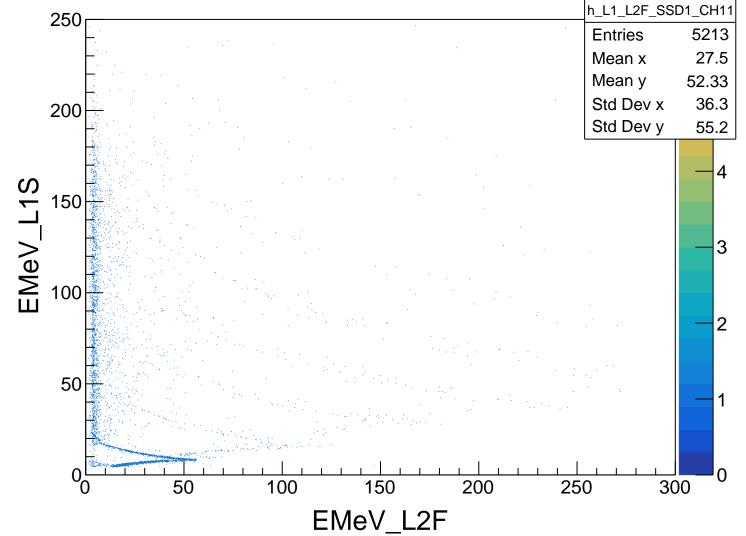




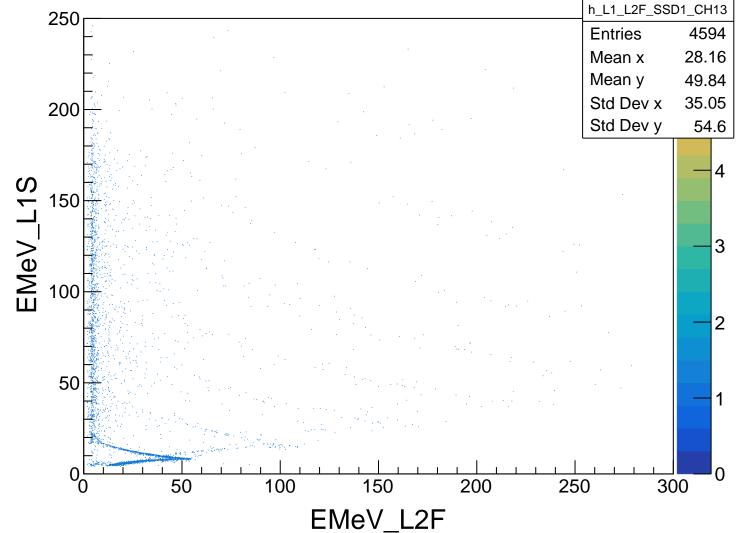
h_L1_L2F_SSD1_CH9 h_L1_L2F_SSD1_CH9 250 **Entries** 5520 Mean x 29.26 Mean y 53.24 40.46 Std Dev x 200 Std Dev y 56.08 EMeV_L1S 150 100 50 300 50 150 200 250 100

EMeV_L2F





h_L1_L2F_SSD1_CH12 h_L1_L2F_SSD1_CH12 250 **Entries** 4465 31.52 Mean x Mean y 47.7 Std Dev x 36.8 200 Std Dev y 54.2 3.5 EMeV_L1S 150 2.5 100 1.5 50 0.5 300 50 150 200 250 100 EMeV_L2F



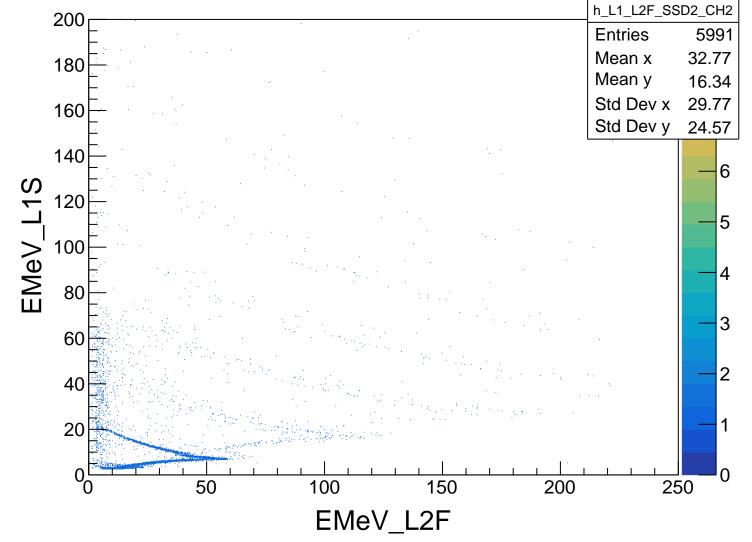
h_L1_L2F_SSD1_CH14 h_L1_L2F_SSD1_CH14 250 **Entries** 3779 Mean x 32.82 Mean y 42.39 Std Dev x 35.4 200 Std Dev y 51.78 3.5 EMeV_L1S 150 3 2.5 100 1.5 50 0.5 300 50 150 200 250 100 EMeV_L2F

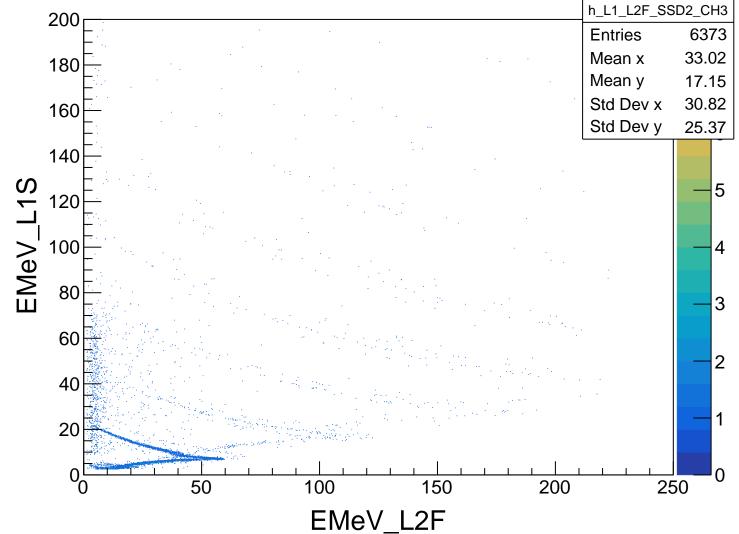
h_L1_L2F_SSD1_CH15 h_L1_L2F_SSD1_CH15 250 **Entries** 3773 Mean x 27.88 Mean y 49.64 Std Dev x 32.61 200 Std Dev y 55.72 3.5 EMeV_L1S 150 2.5 100 1.5 50 0.5 300 50 200 250 100 150 EMeV_L2F

h_L1_L2F_SSD2_CH0 h_L1_L2F_SSD2_CH0 200 **Entries** 900 Mean x 10.71 180 Mean y 33.79 Std Dev x 12.36 160 Std Dev y 28.91 140 EMeV_L1S 120 100 1.5 80 60 40 0.5 20 50 150 200 250 100 EMeV_L2F

h_L1_L2F_SSD2_CH1 h_L1_L2F_SSD2_CH1 200 **Entries** 841 Mean x 7.772 180 Mean y 33.93 Std Dev x 8.785 160 Std Dev y 28.77 140 EMeV_L1S 120 100 3 80 60 40 20 50 100 150 200 250

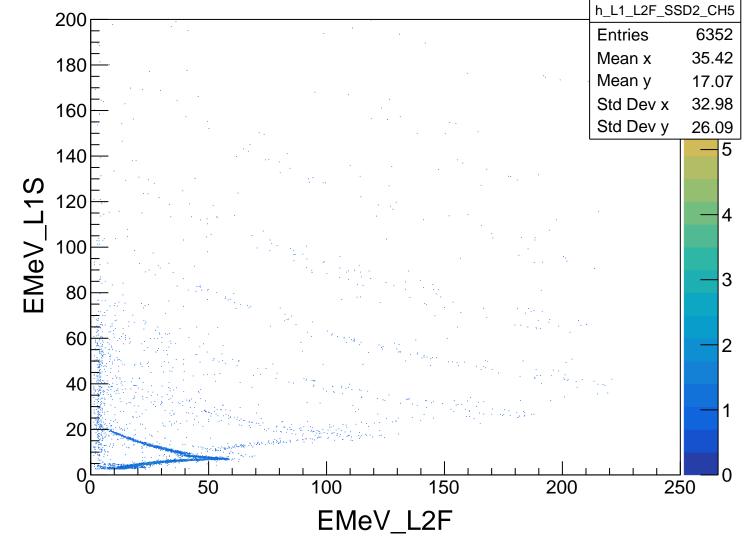
EMeV_L2F

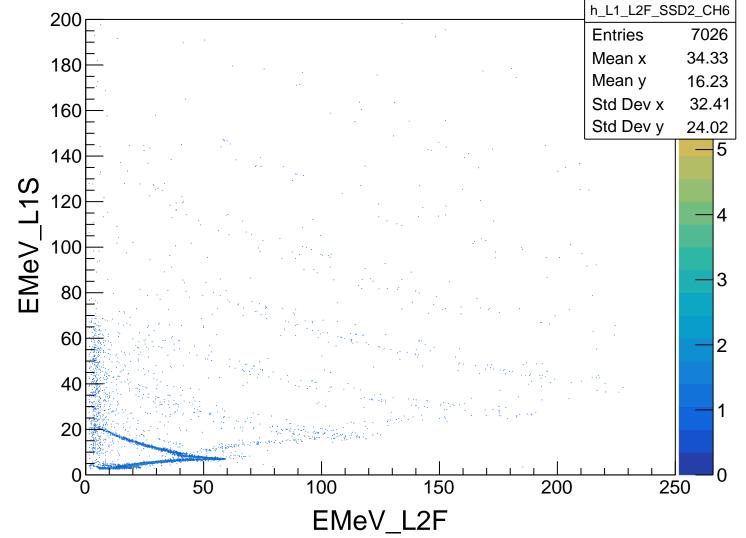


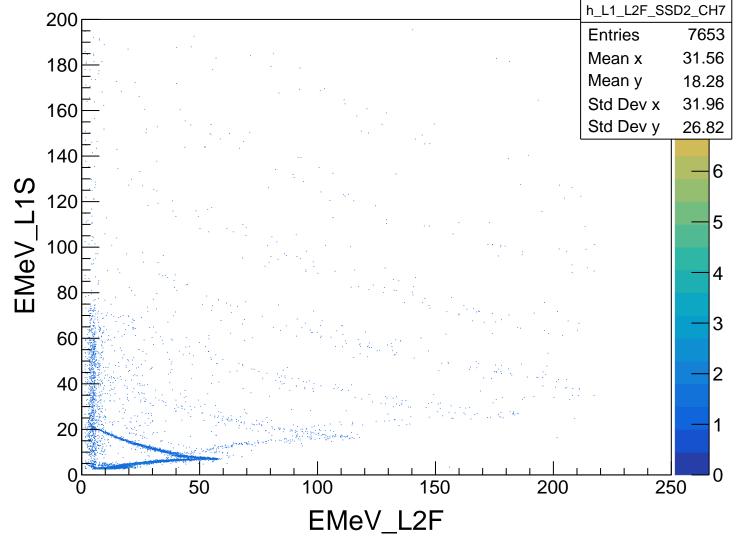


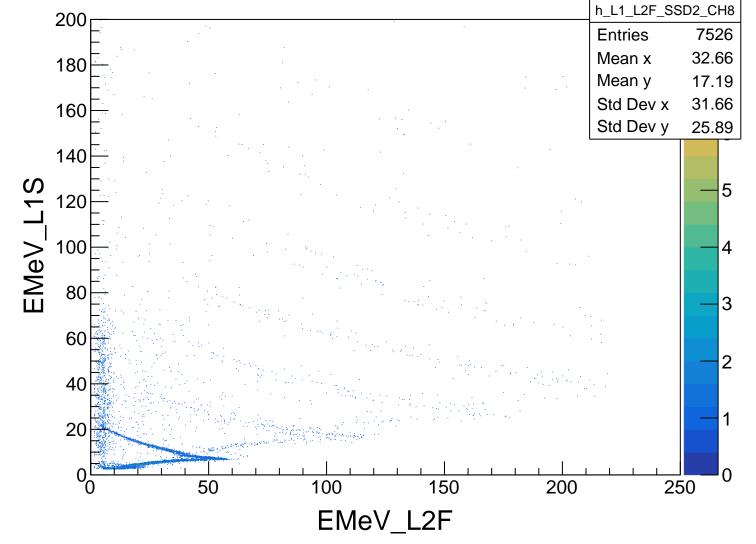
h_L1_L2F_SSD2_CH4 h_L1_L2F_SSD2_CH4 200 **Entries** 6256 Mean x 33.45 180 Mean y 16.89 Std Dev x 30.5 160 Std Dev y 25.26 5 140 EMeV_L1S 120 100 80 60 40 20 50 150 200 250 100

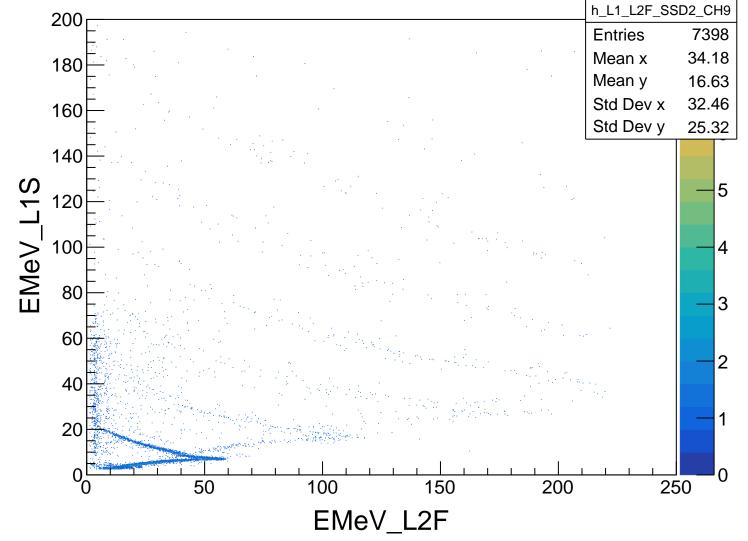
EMeV_L2F

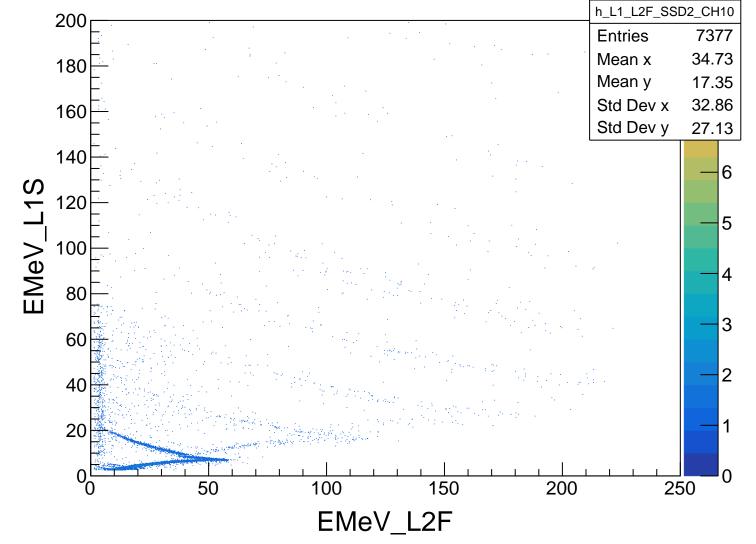


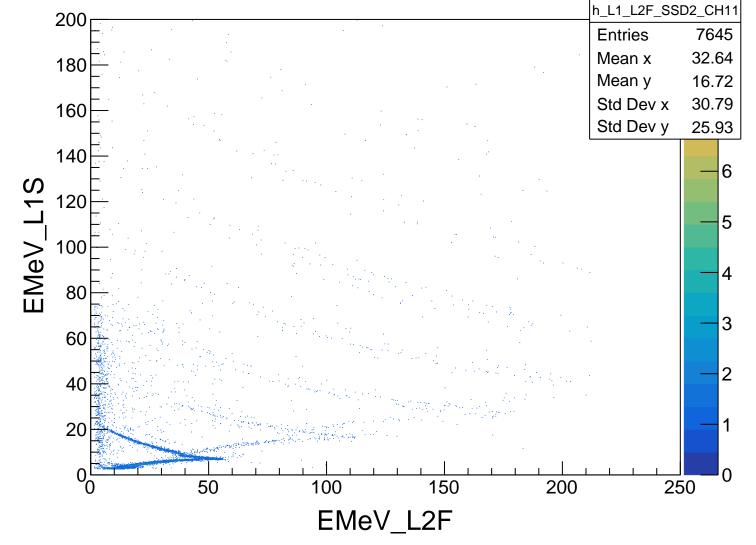


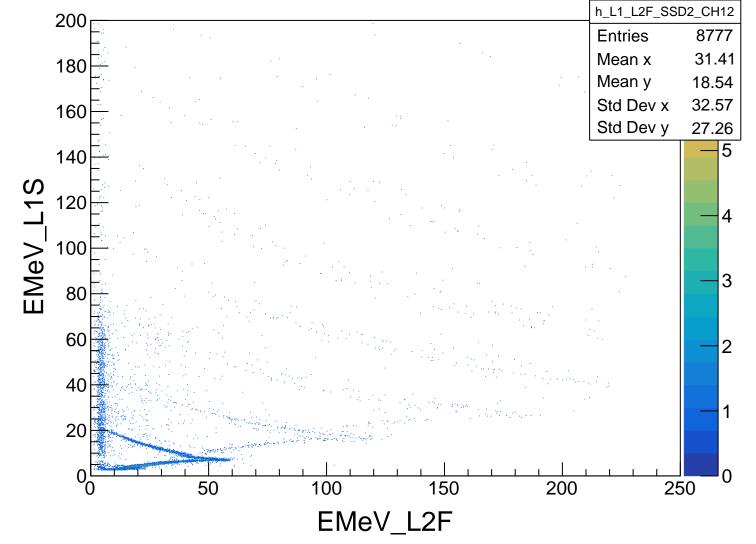


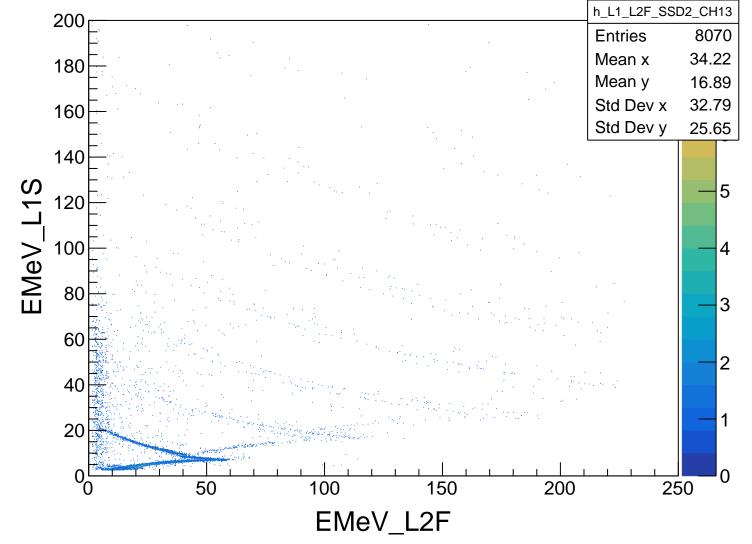


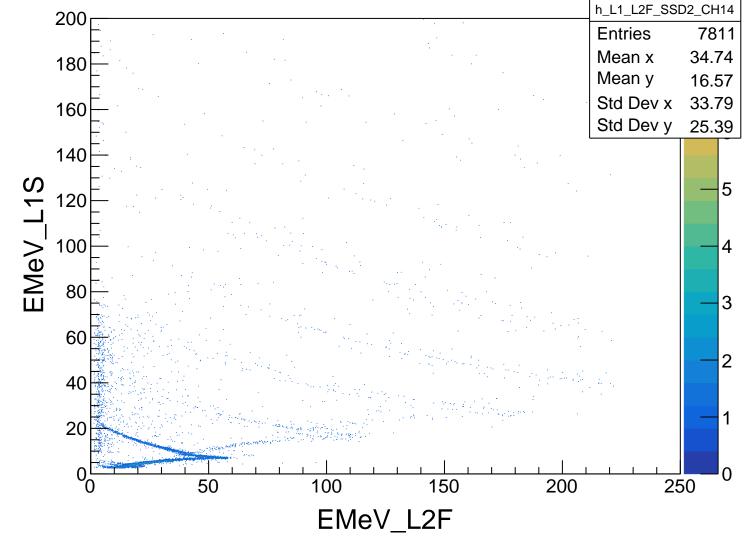


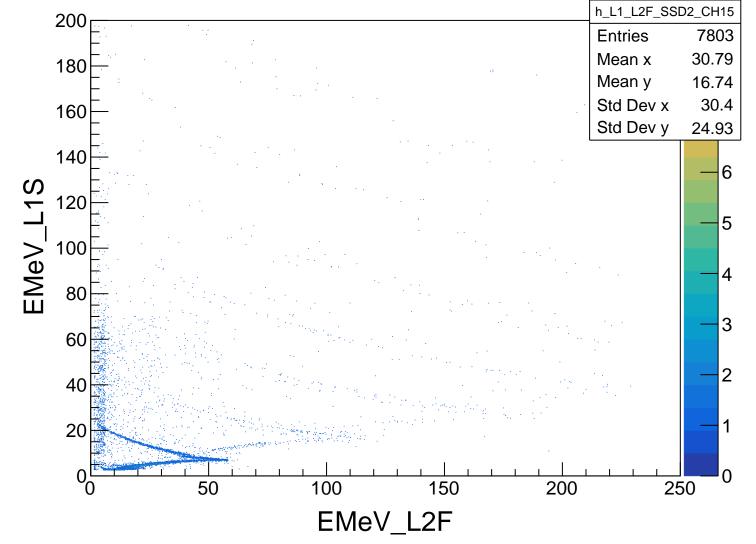


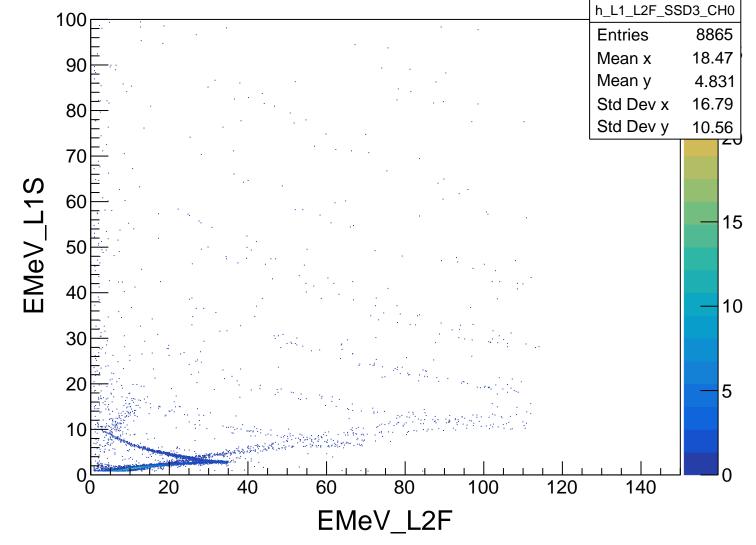


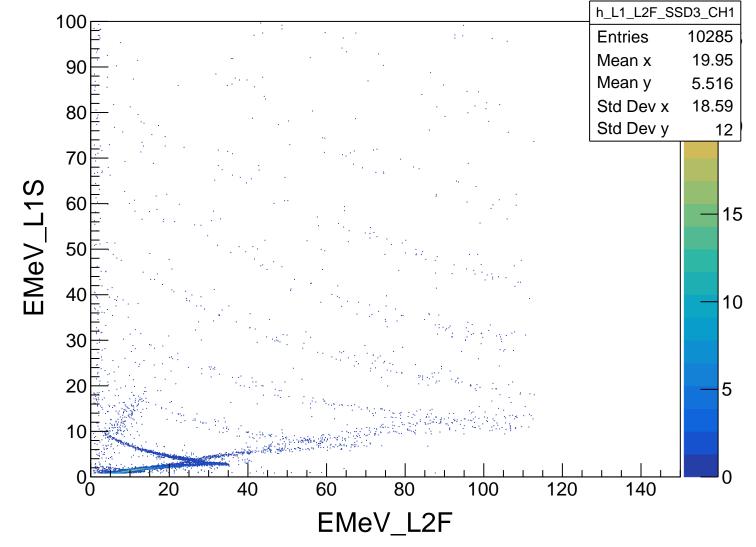


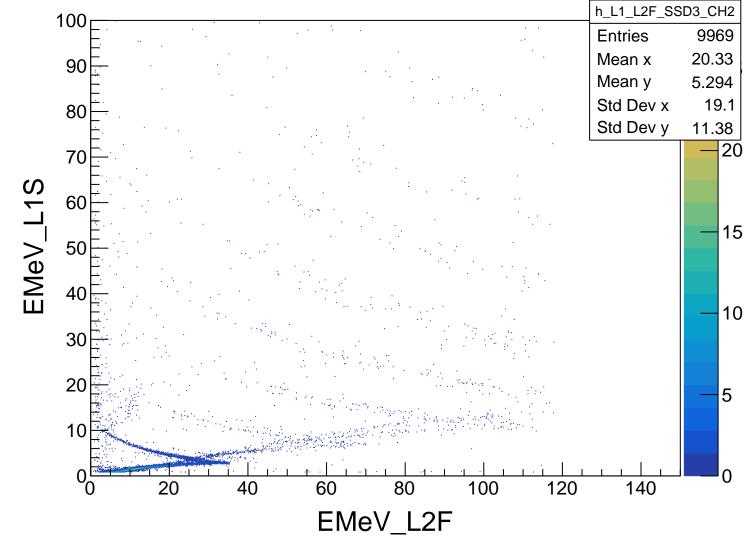


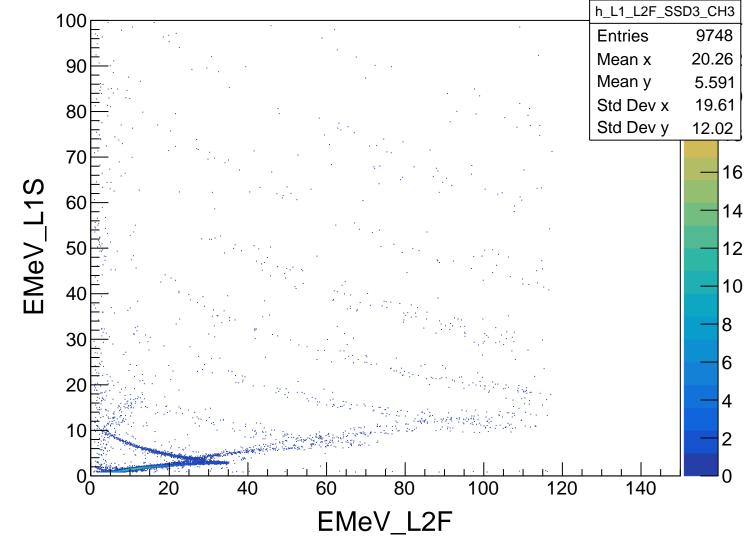


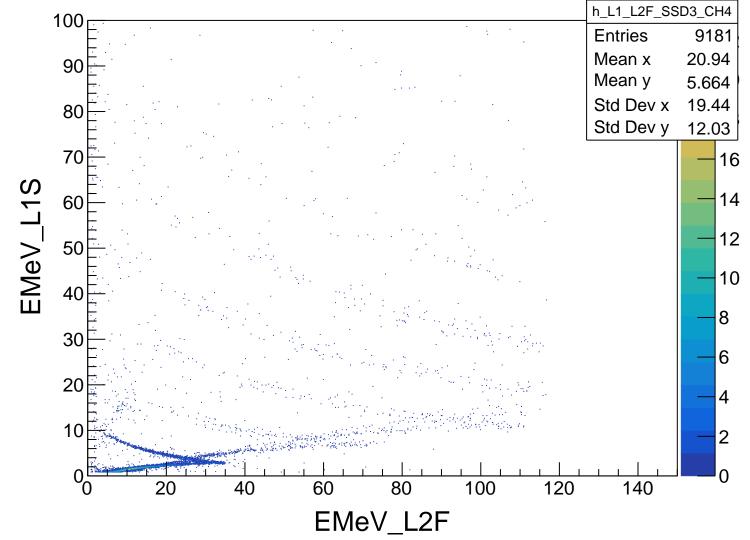


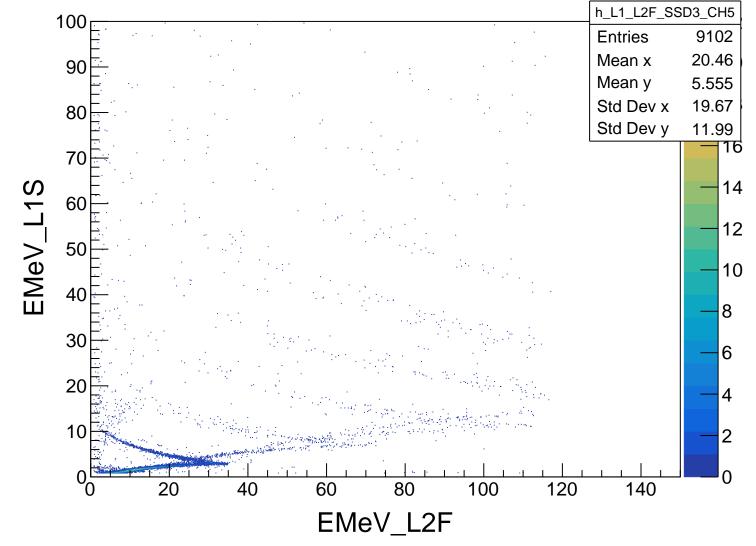


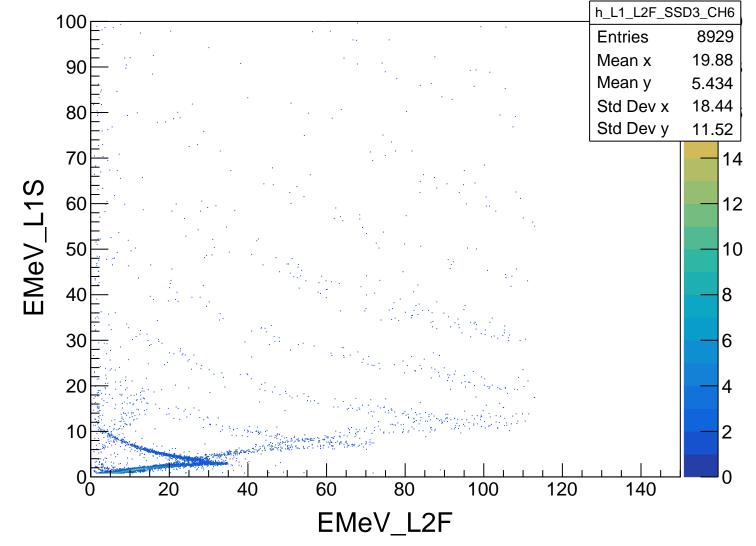


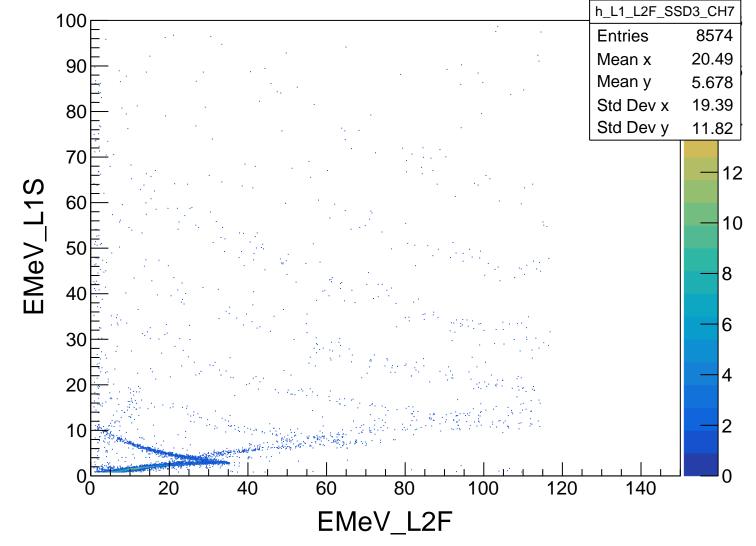


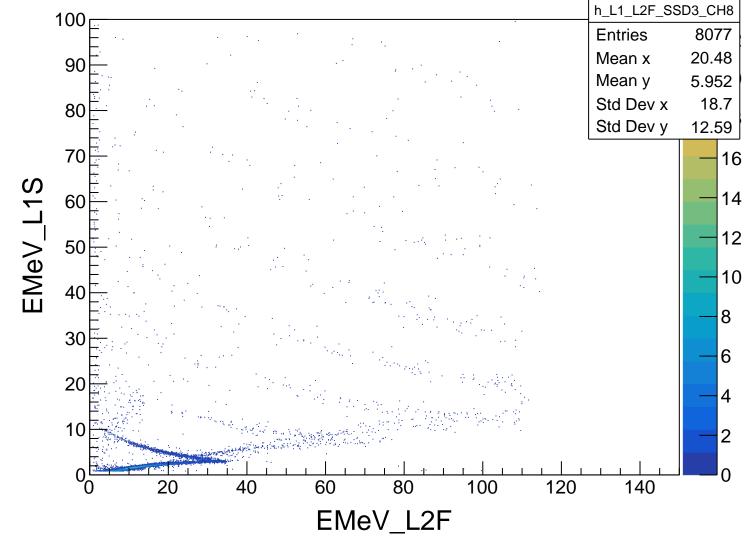


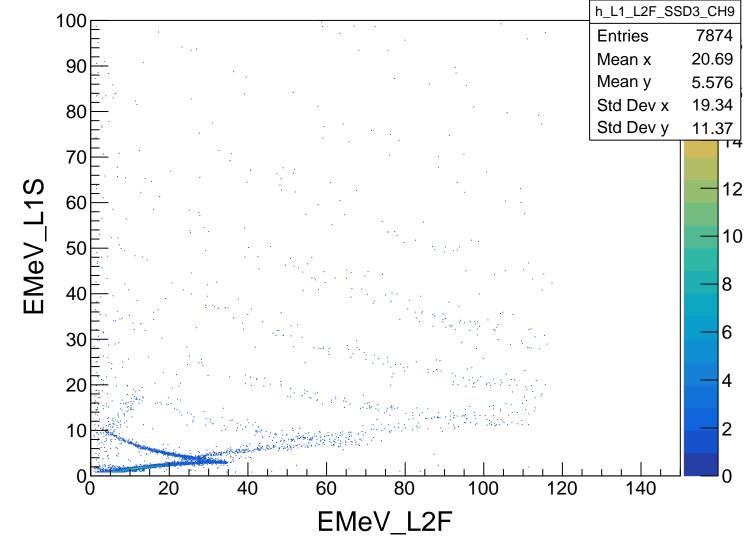


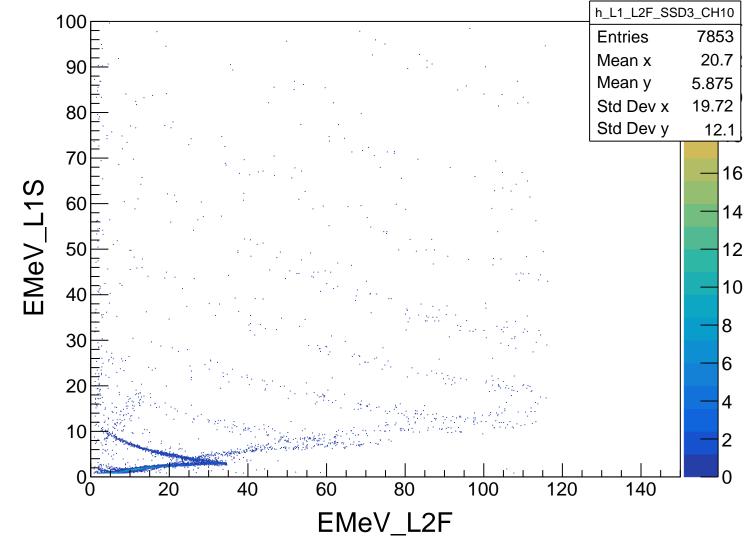


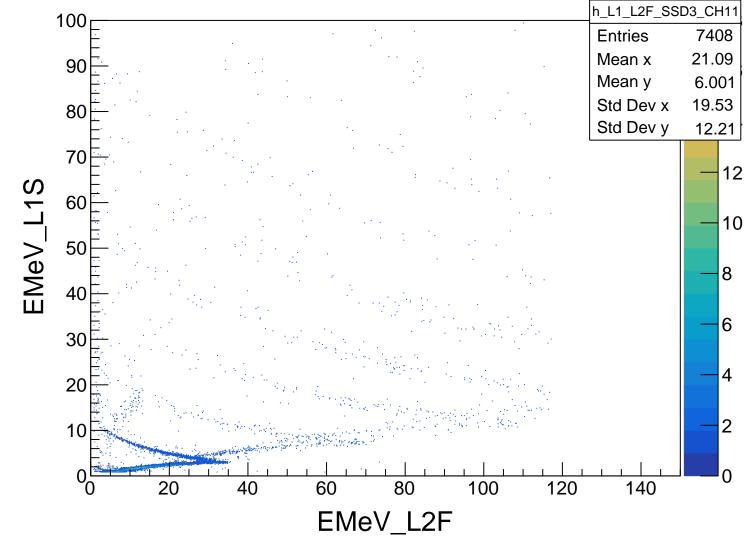


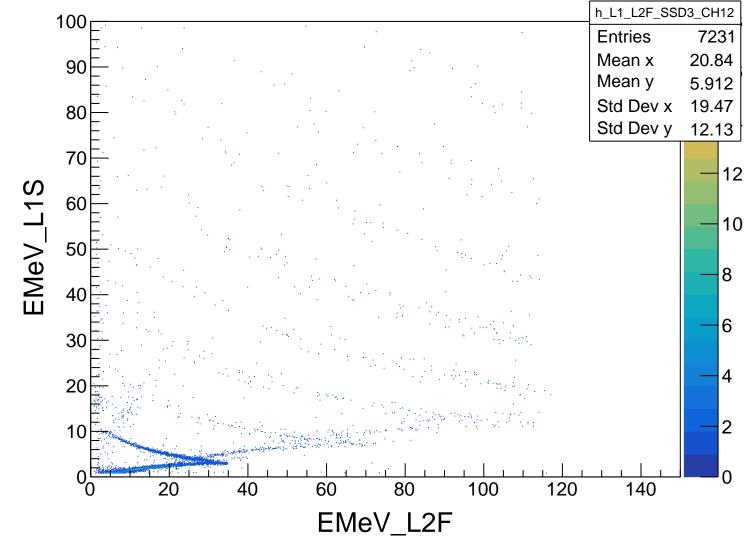


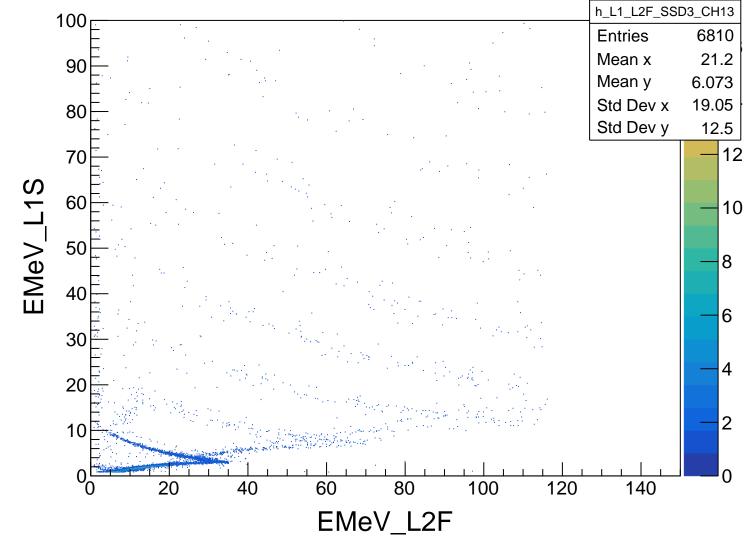


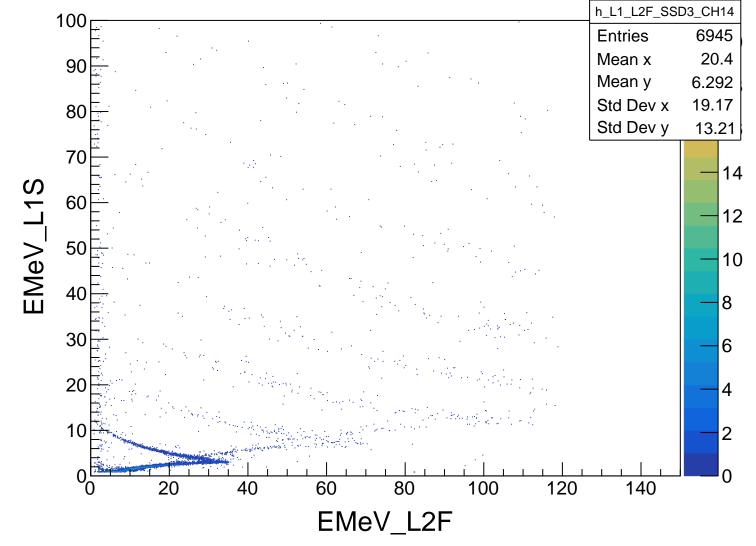


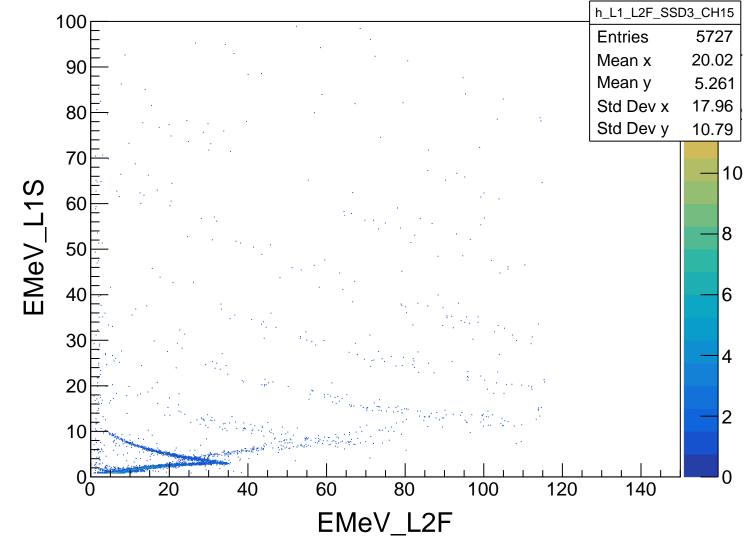




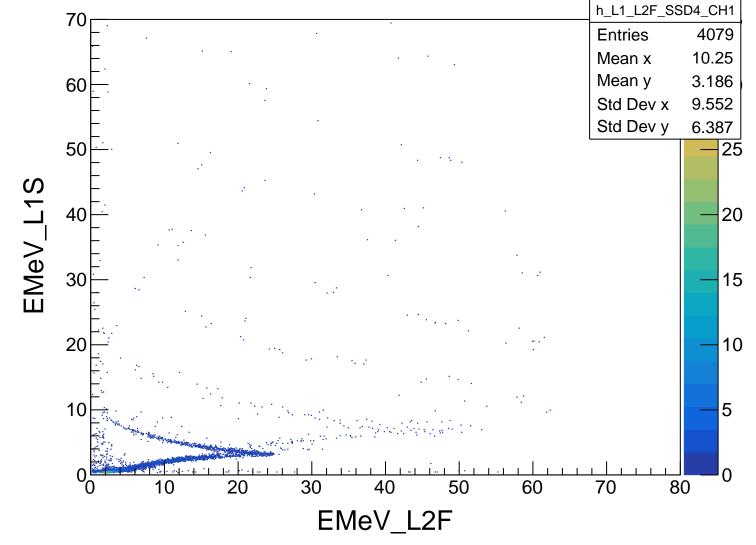


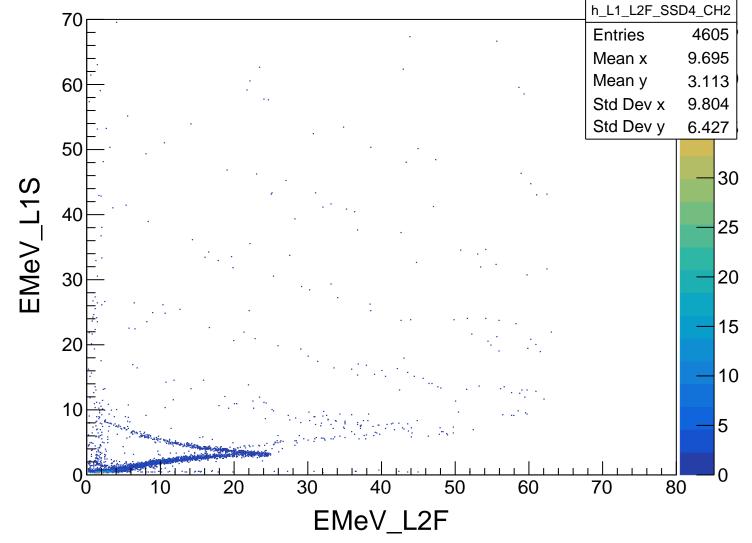


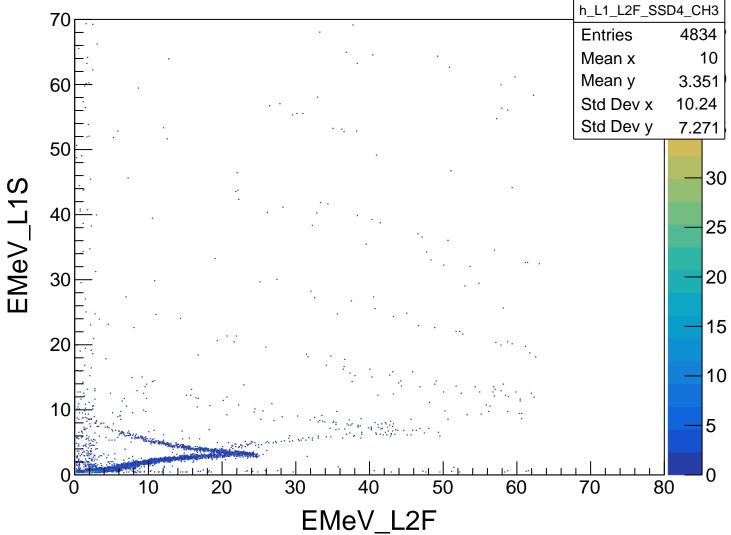


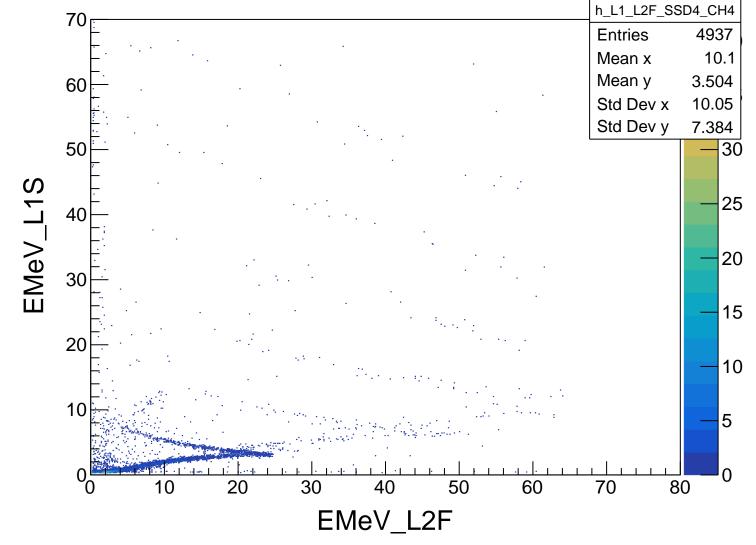


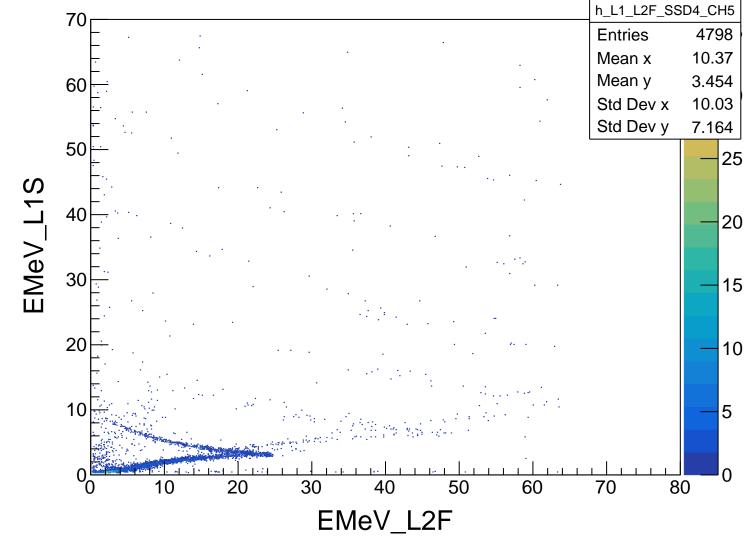
h_L1_L2F_SSD4_CH0 h_L1_L2F_SSD4_CH0 **Entries** Mean x 10.5 Mean y 3.42 10.11 Std Dev x Std Dev y 7.182 EMeV_L1S EMeV_L2F

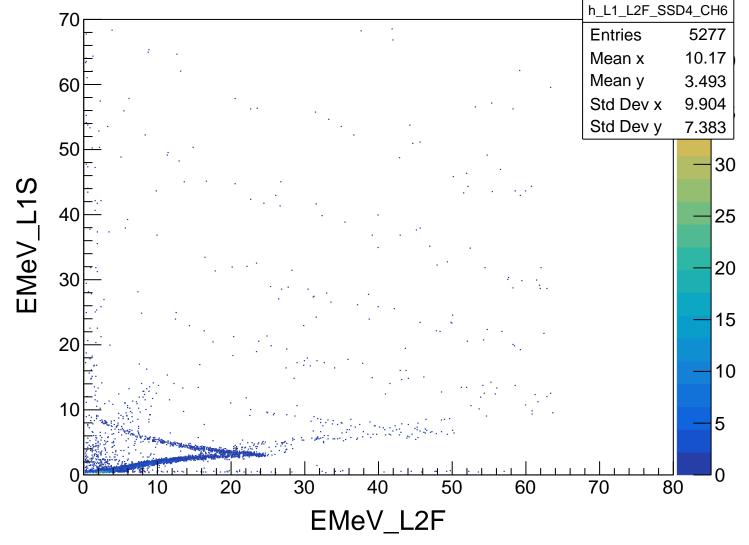


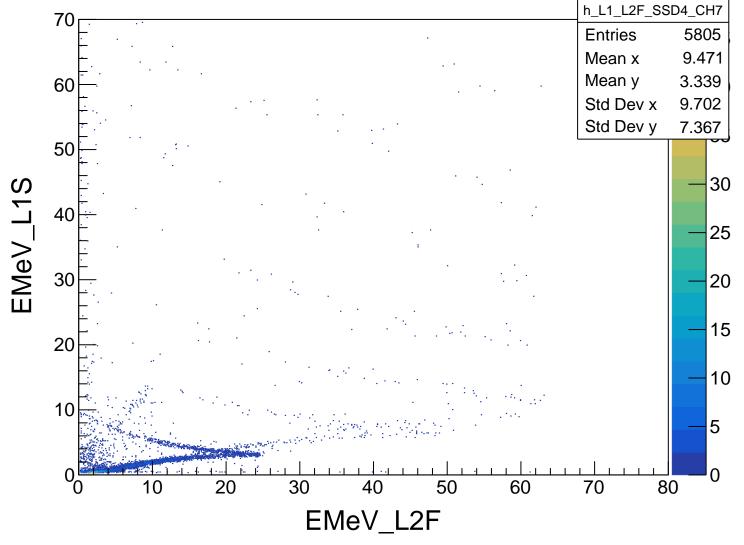


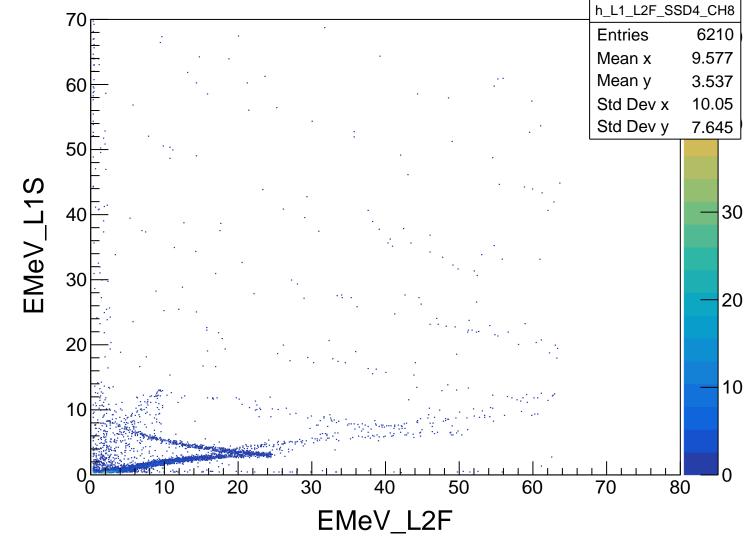


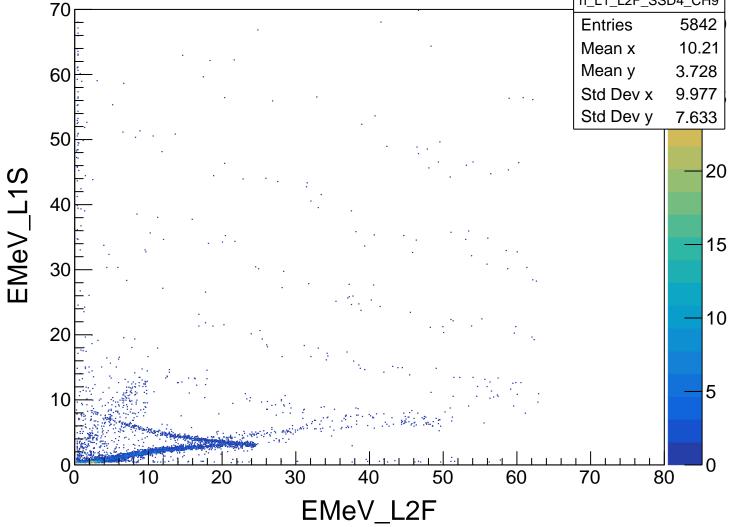


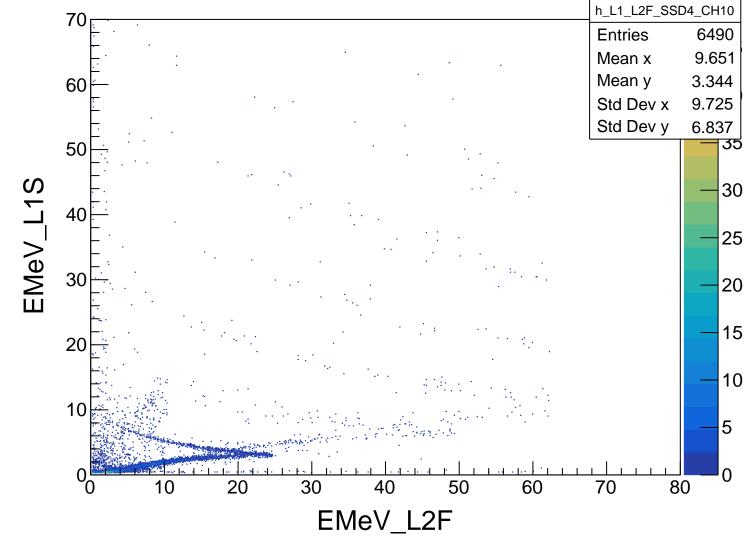


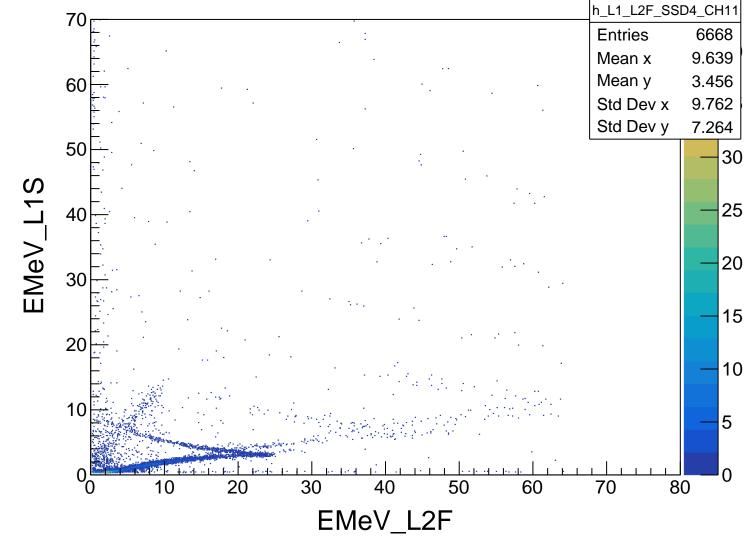


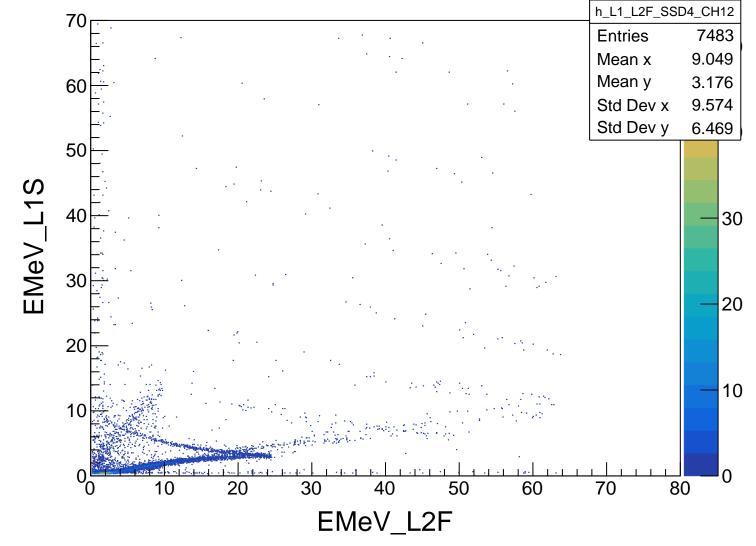












h_L1_L2F_SSD4_CH13 h_L1_L2F_SSD4_CH13 **Entries** Mean x 21.68 Mean y 17.34 16.09 Std Dev x Std Dev y 18.56 EMeV_L1S EMeV_L2F

h_L1_L2F_SSD4_CH14 h_L1_L2F_SSD4_CH14 70 **Entries** 1885 Mean x 19.43 Mean y 60 17.21 Std Dev x 16.09 Std Dev y 17.83 50 EMeV_L1S 15 40 30 10 20 5 10 80 10 20 30 50 60 70 40 EMeV_L2F

h_L1_L2F_SSD4_CH15 h_L1_L2F_SSD4_CH15 **Entries** Mean x 9.209 Mean y 3.031 Std Dev x 9.392 Std Dev y 6.015

EMeV_L1S

