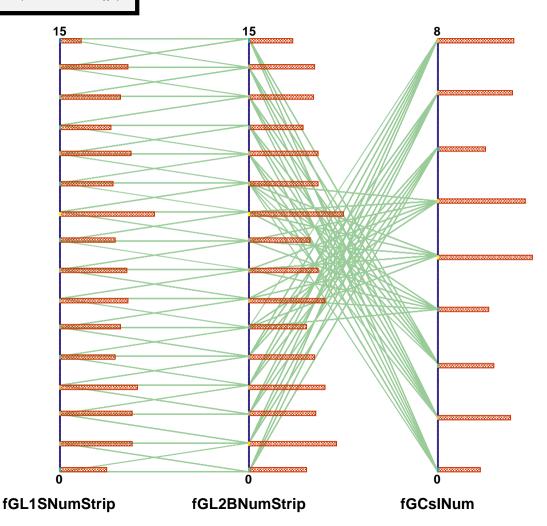
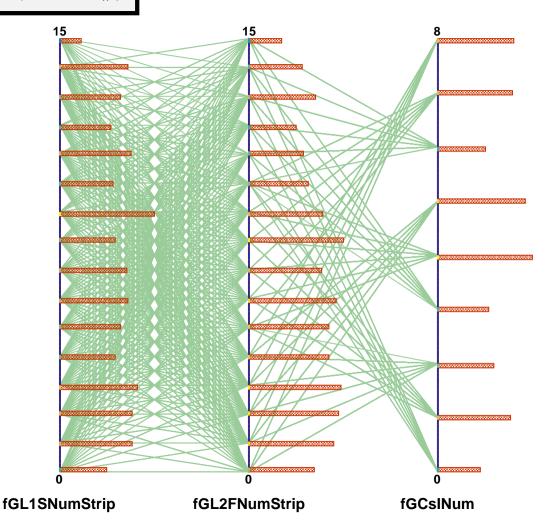


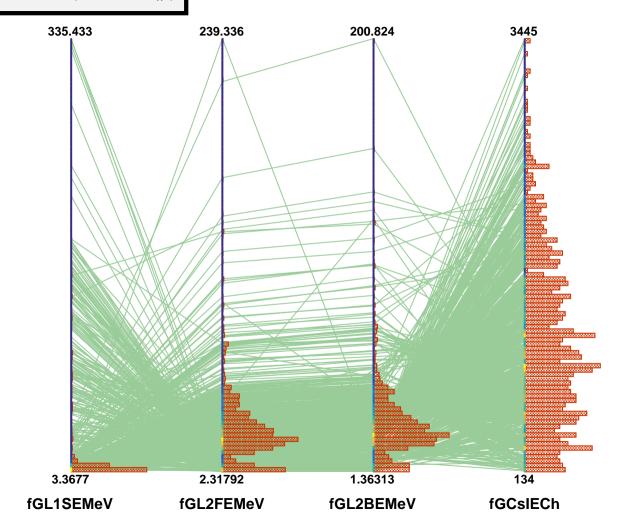
fGL1SNumStripfGL2FNumStripfGL2BNumStrip fGCslNum

fGL1SNumStrip:fGL2BNumStrip:fGCslNum{fGSSDNum==0 && fSSDGlobalMulti[0]==1}

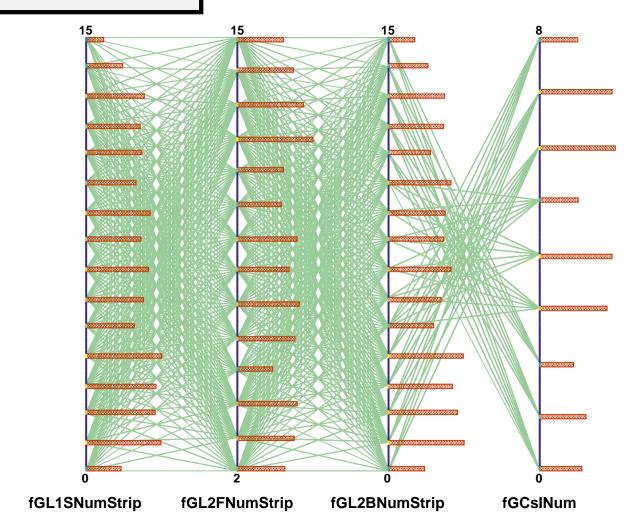


fGL1SNumStrip:fGL2FNumStrip:fGCslNum{fGSSDNum==0 && fSSDGlobalMulti[0]==1}

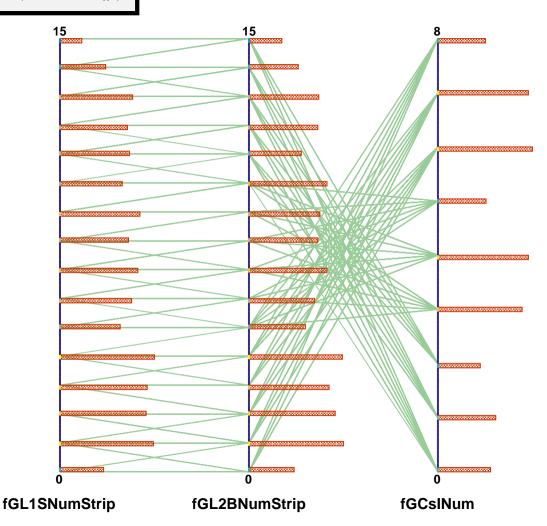




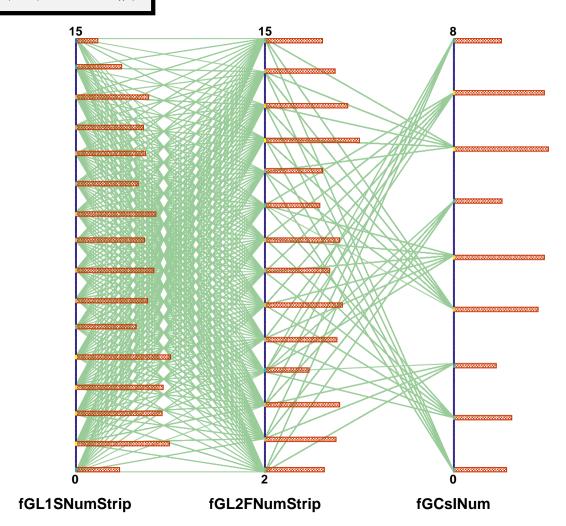
fGL1SNumStrip:fGL2FNumStrip:fGL2BNumStrip:fGCslNum(fGSSDNum==1 && fSSDGlobalMulti[1]==1)

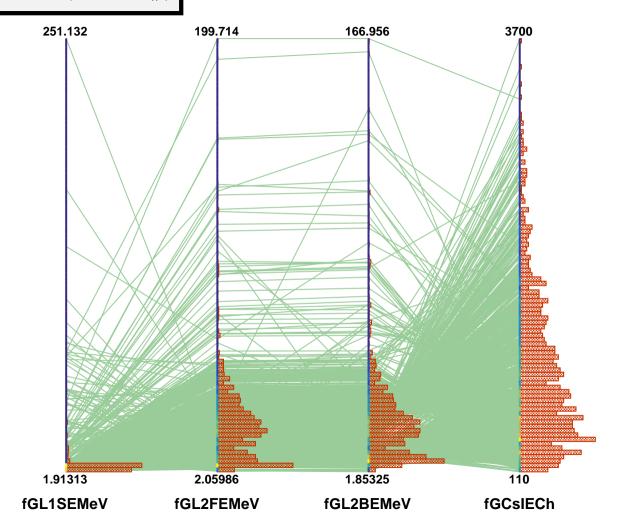


fGL1SNumStrip:fGL2BNumStrip:fGCslNum{fGSSDNum==1 && fSSDGlobalMulti[1]==1}

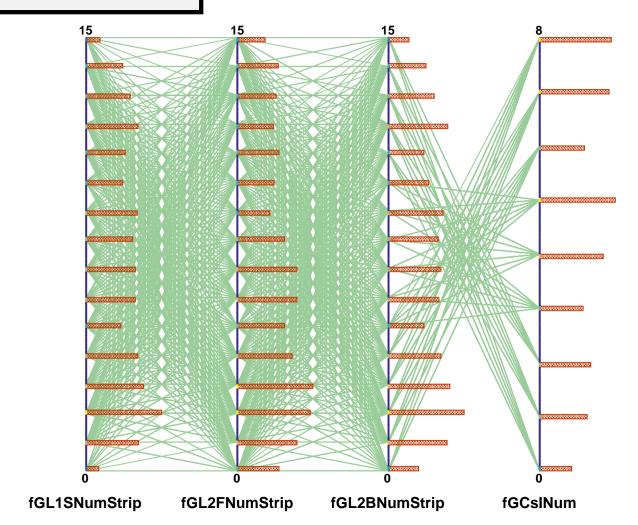


fGL1SNumStrip:fGL2FNumStrip:fGCslNum{fGSSDNum==1 && fSSDGlobalMulti[1]==1}

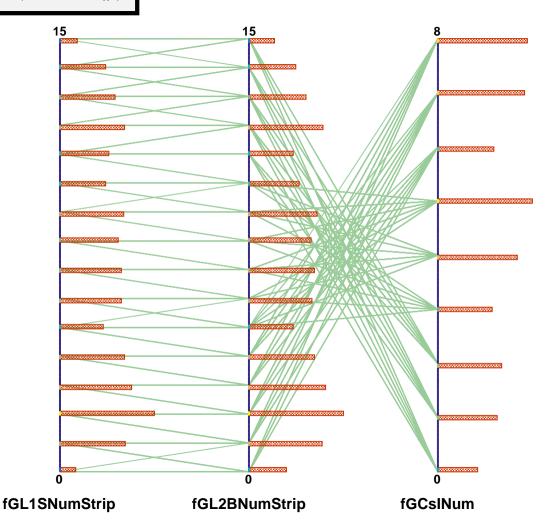




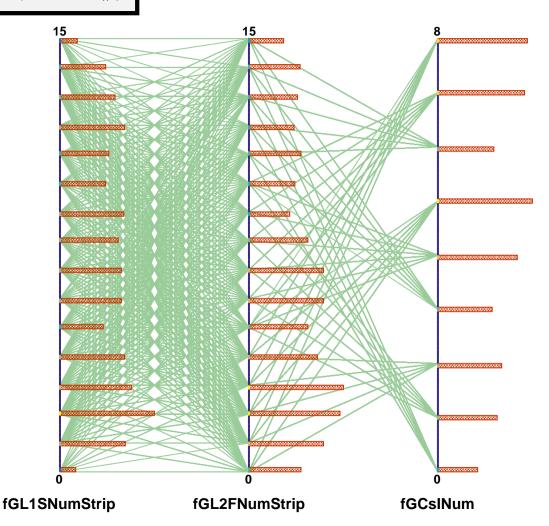
fGL1SNumStrip:fGL2FNumStrip:fGL2BNumStrip:fGCslNum(fGSSDNum==2 && fSSDGlobalMulti[2]==1)

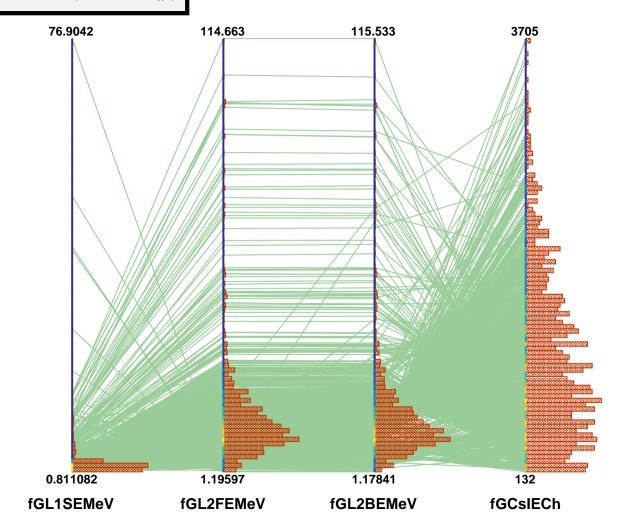


fGL1SNumStrip:fGL2BNumStrip:fGCslNum{fGSSDNum==2 && fSSDGlobalMulti[2]==1}

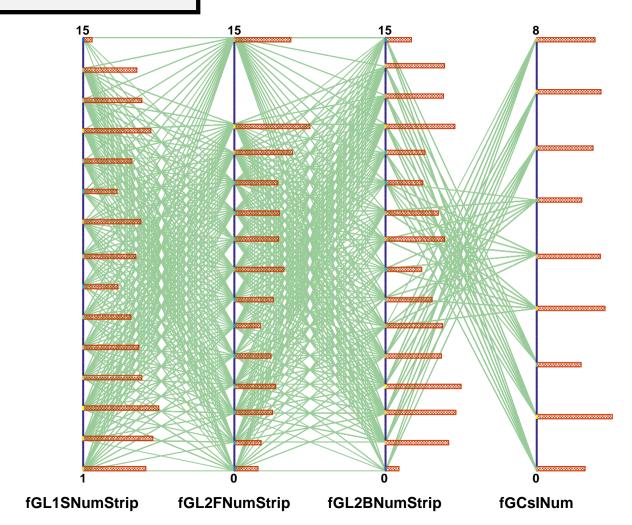


fGL1SNumStrip:fGL2FNumStrip:fGCslNum{fGSSDNum==2 && fSSDGlobalMulti[2]==1}

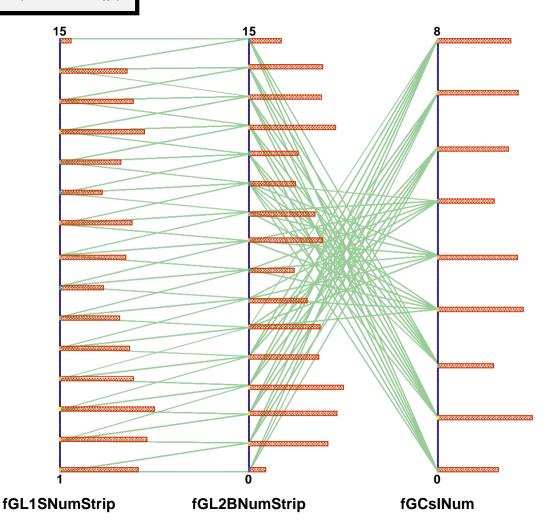




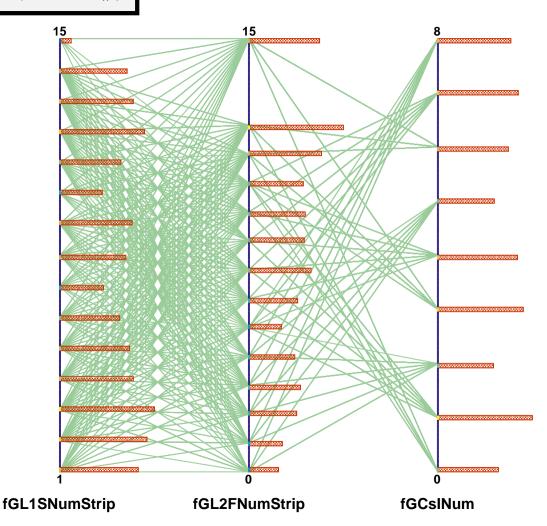
fGL1SNumStrip:fGL2FNumStrip:fGL2BNumStrip:fGCslNum(fGSSDNum==3 && fSSDGlobalMulti[3]==1)



fGL1SNumStrip:fGL2BNumStrip:fGCslNum{fGSSDNum==3 && fSSDGlobalMulti[3]==1}



fGL1SNumStrip:fGL2FNumStrip:fGCslNum{fGSSDNum==3 && fSSDGlobalMulti[3]==1}



fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh{fGSSDNum==3 && fSSDGlobalMulti[3]==1}

