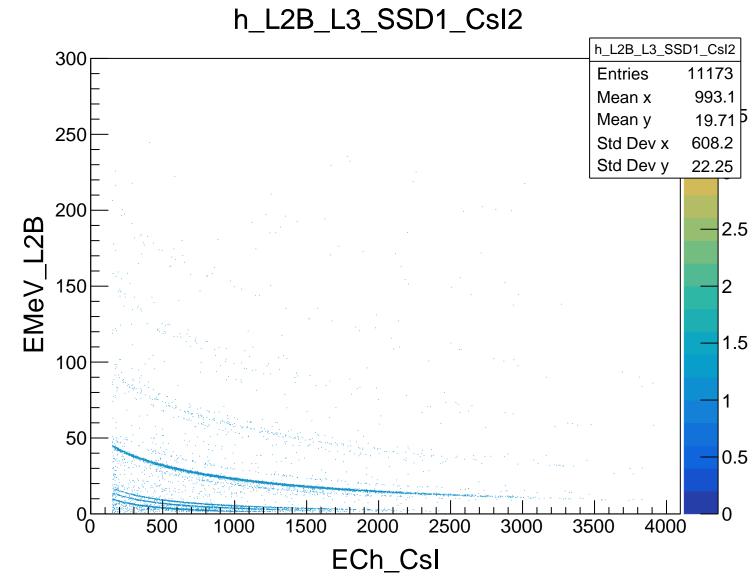
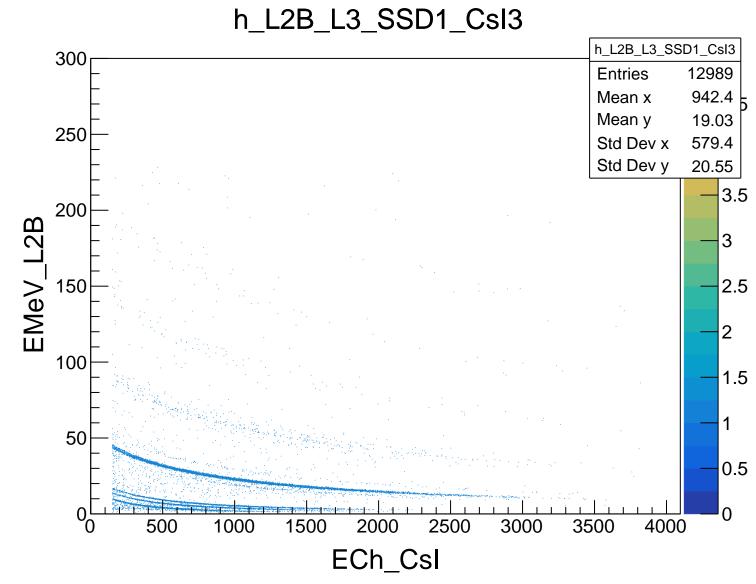
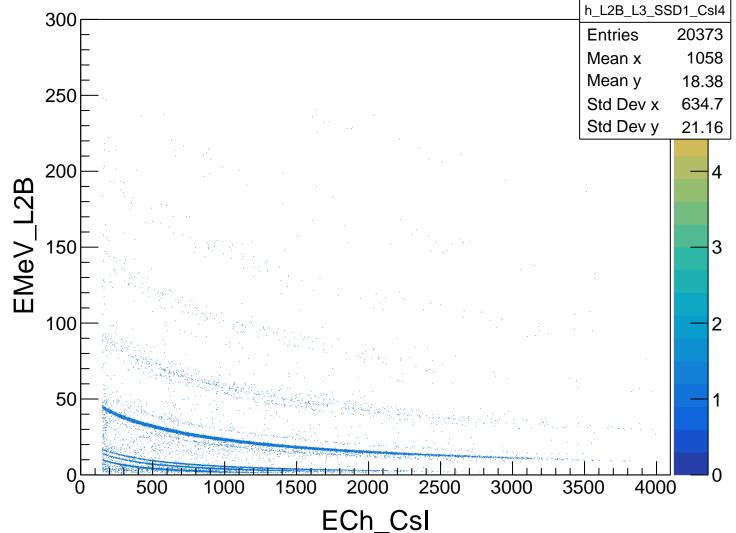


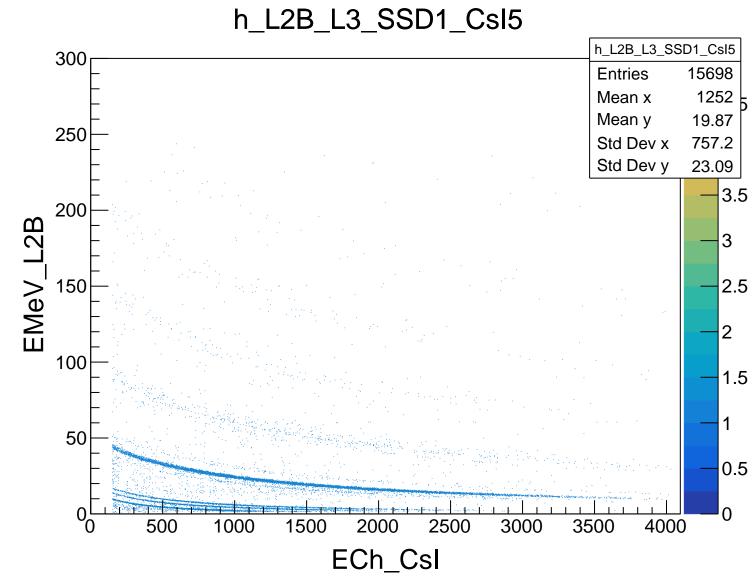
h_L2B_L3_SSD1_Csl1 h_L2B_L3_SSD1_Csl1 300 14538 **Entries** Mean x 936.1 Mean y 17.81 250 Std Dev x 568.1 Std Dev y 20.59 5 200 EMeV_L2B 4 150 100 50 4000 500 1500 3000 3500 1000 2000 2500 ECh_Csl

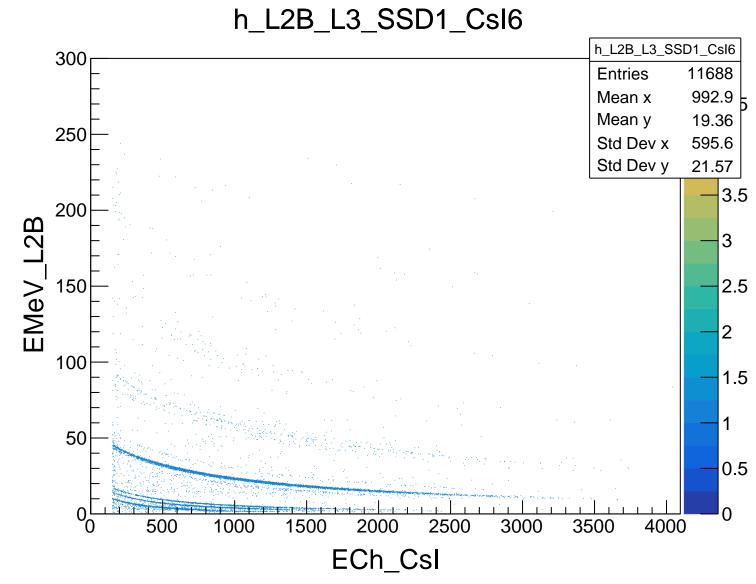


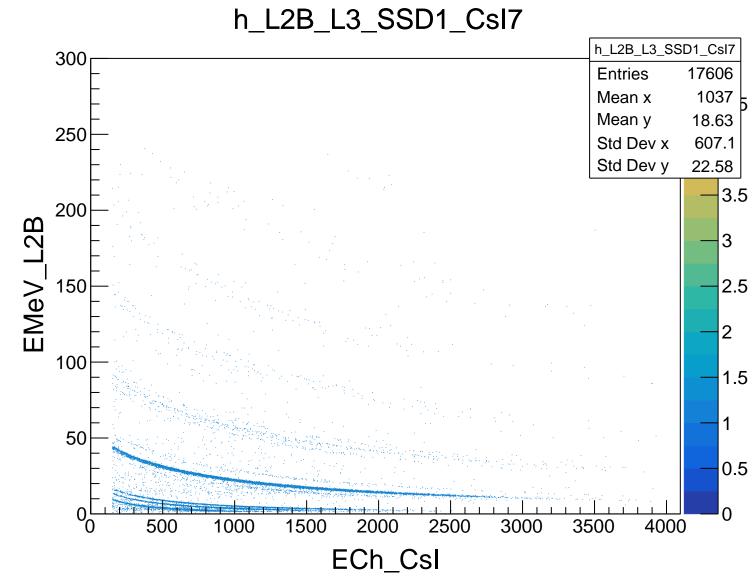


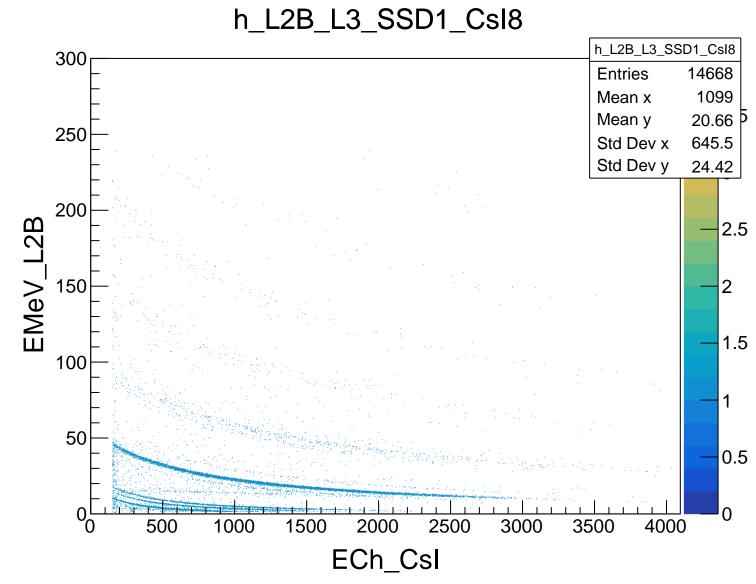
h_L2B_L3_SSD1_CsI4

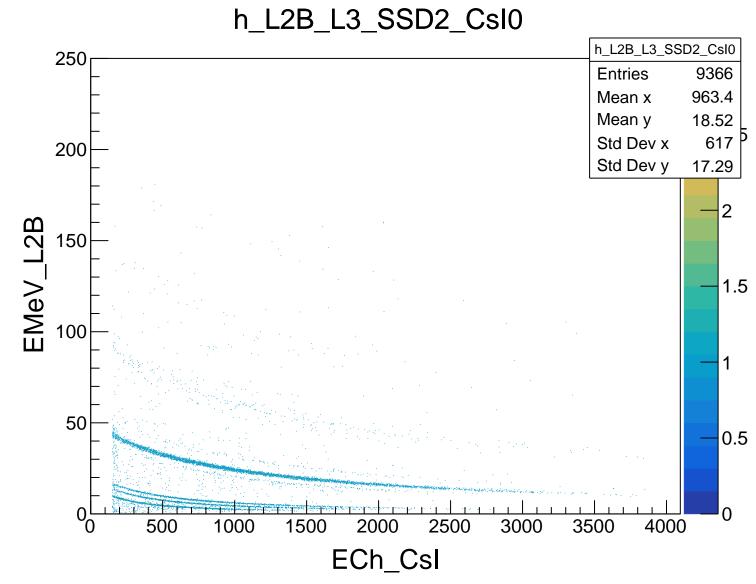


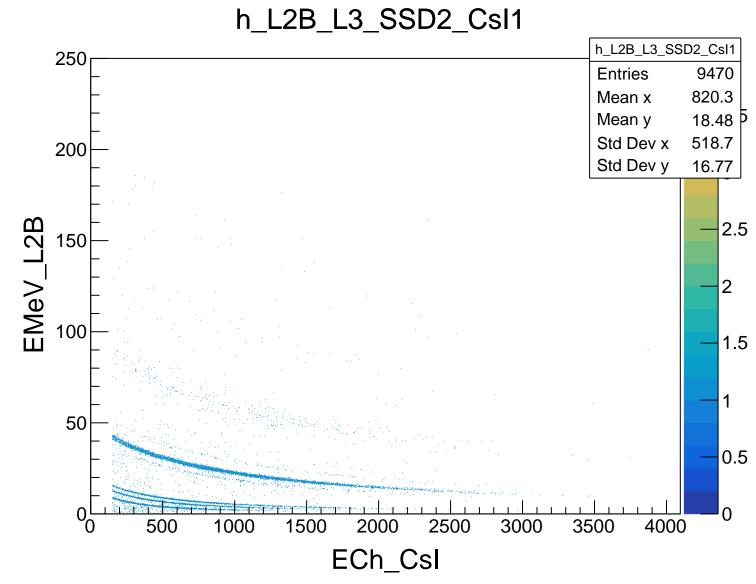


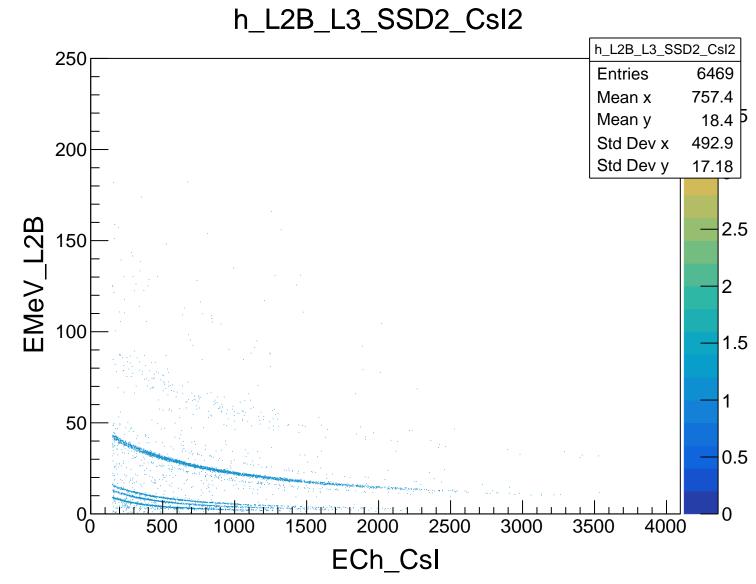


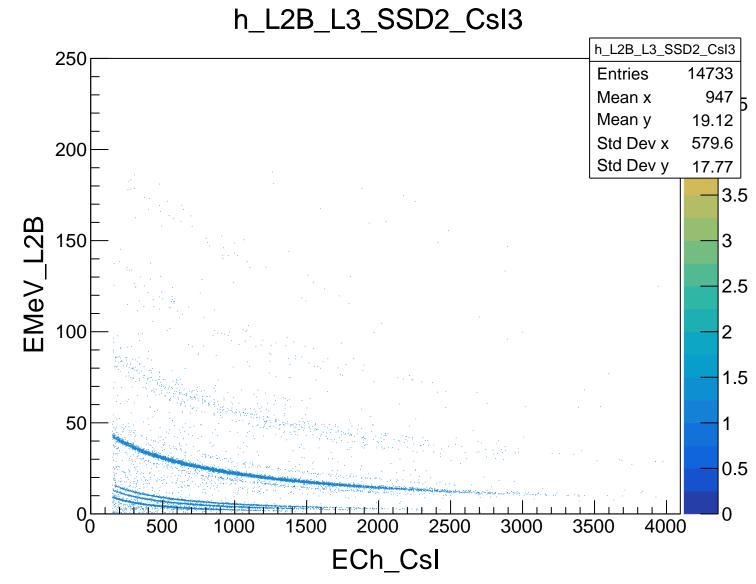


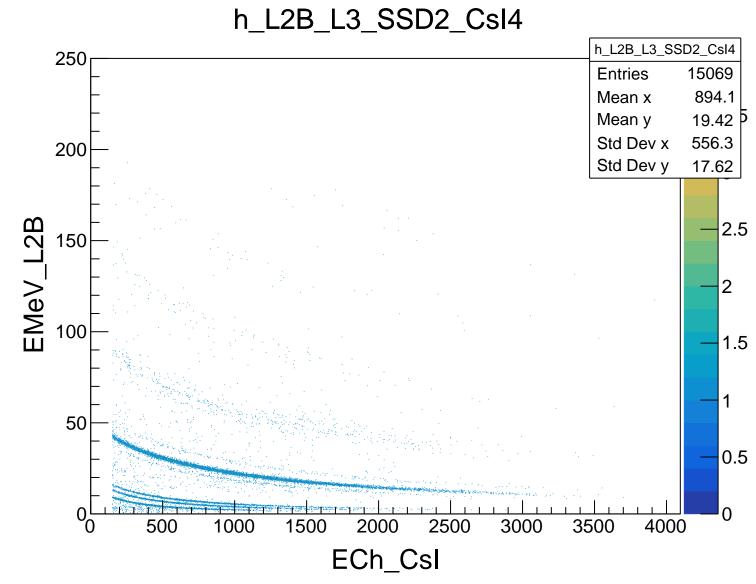


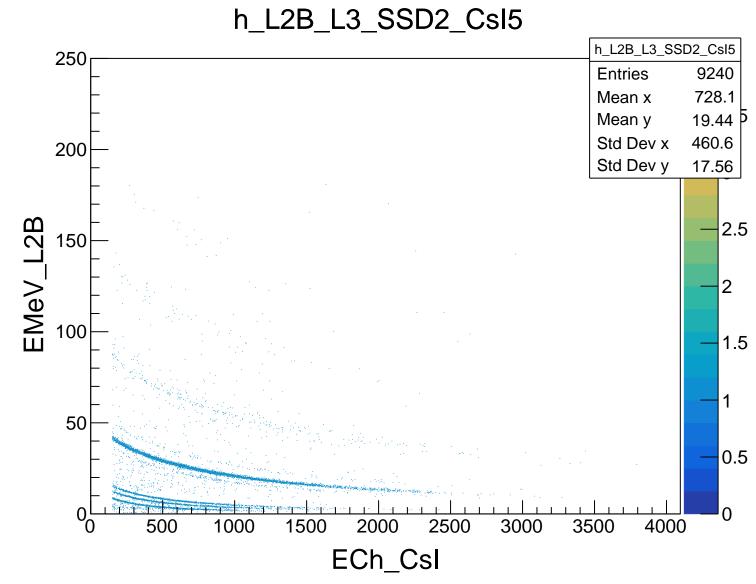


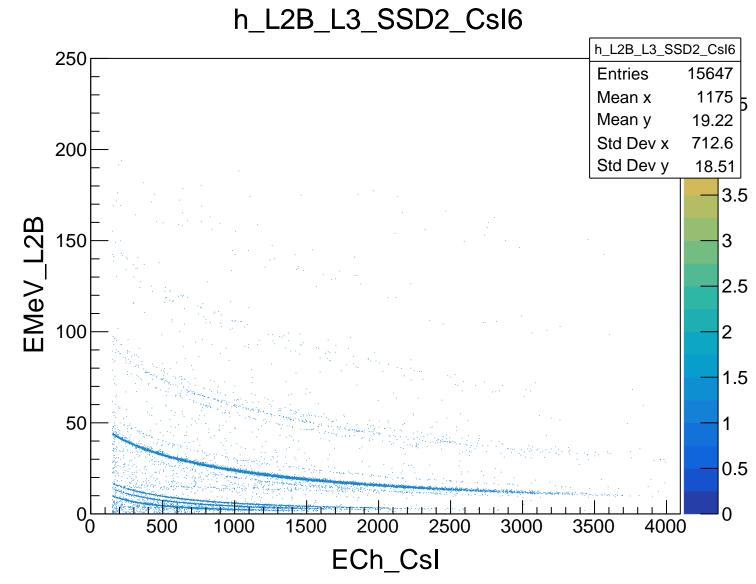


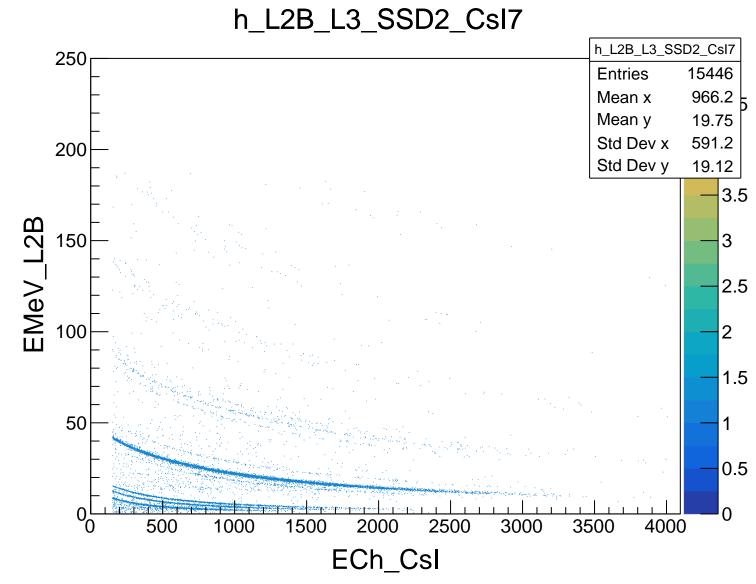


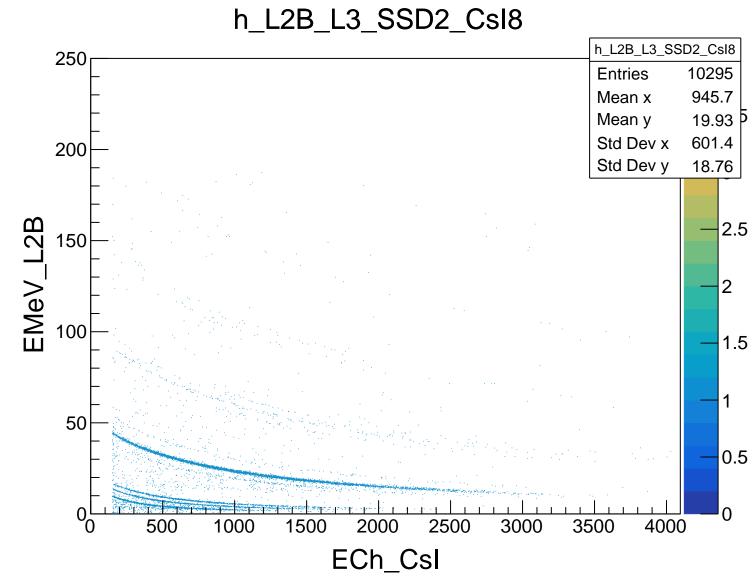


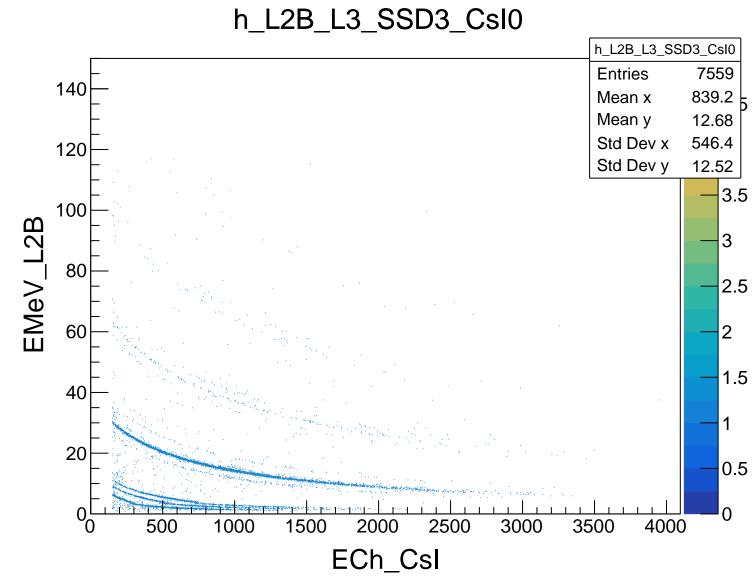




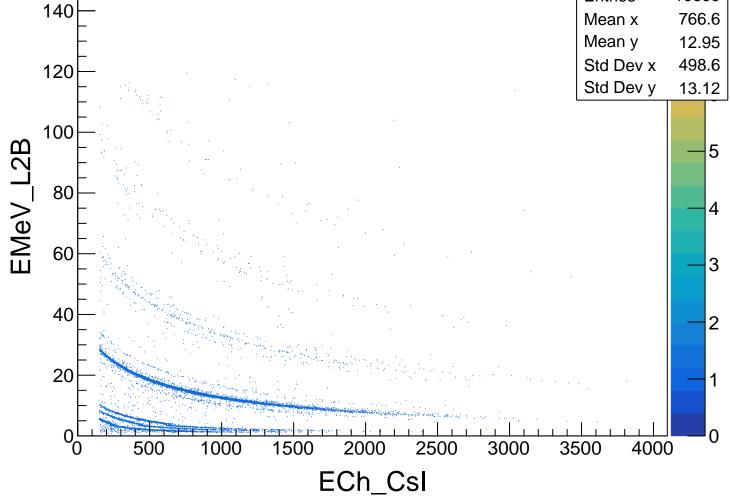


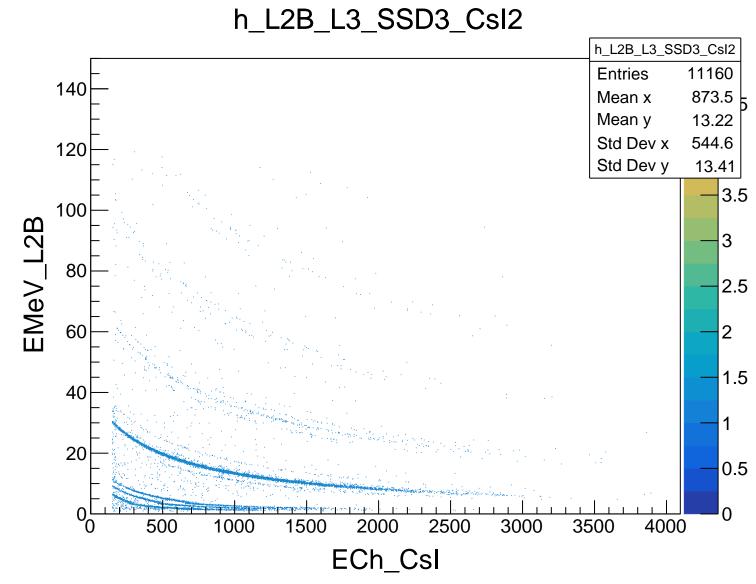


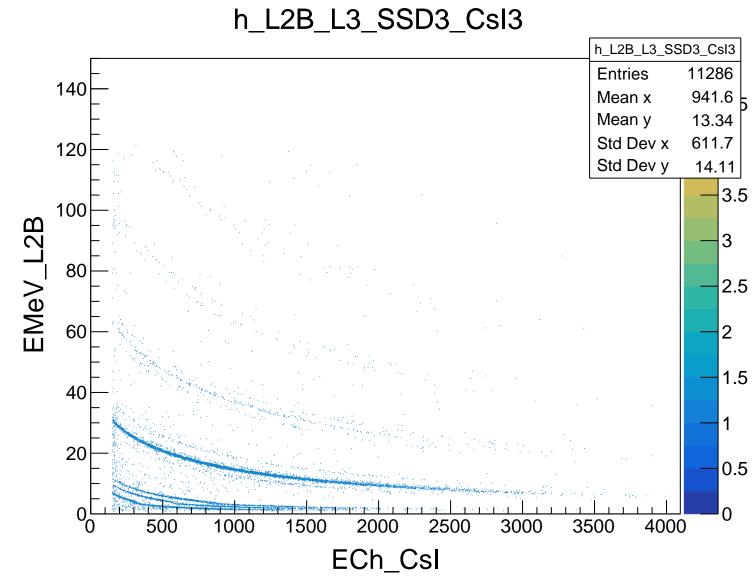


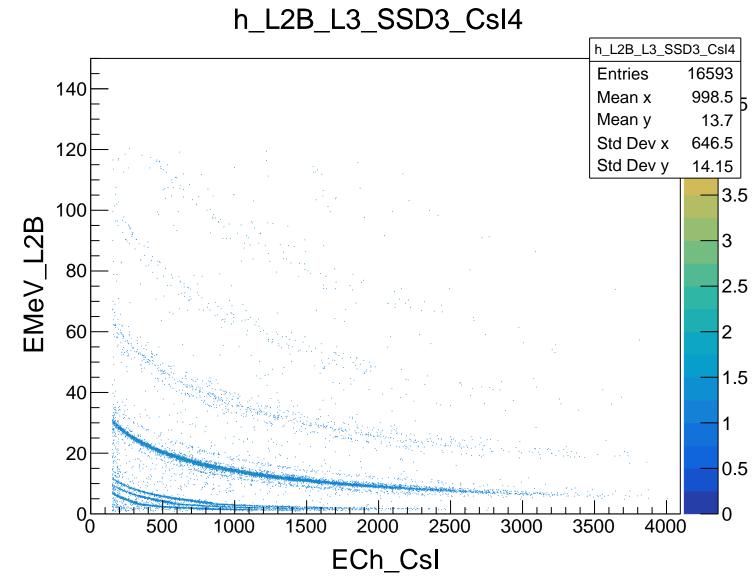


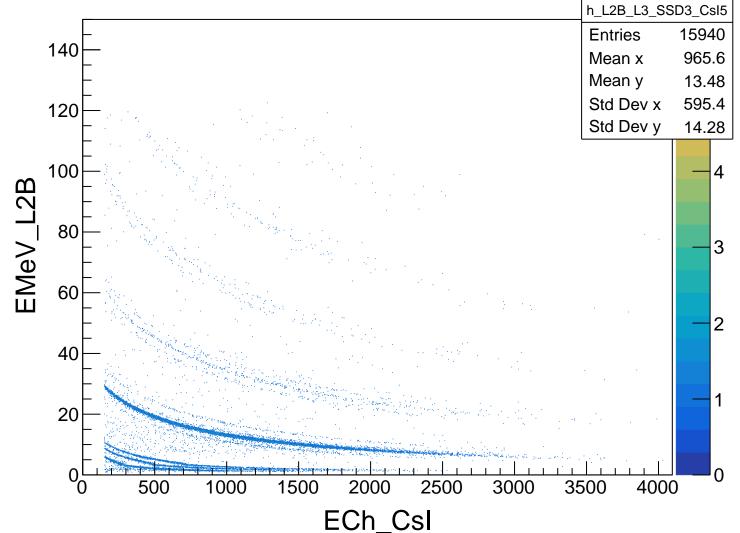
h_L2B_L3_SSD3_Csl1 h_L2B_L3_SSD3_Csl1 10699 **Entries** Mean x 766.6 Mean y 12.95 Std Dev x 498.6 Std Dev y 13.12 5 3

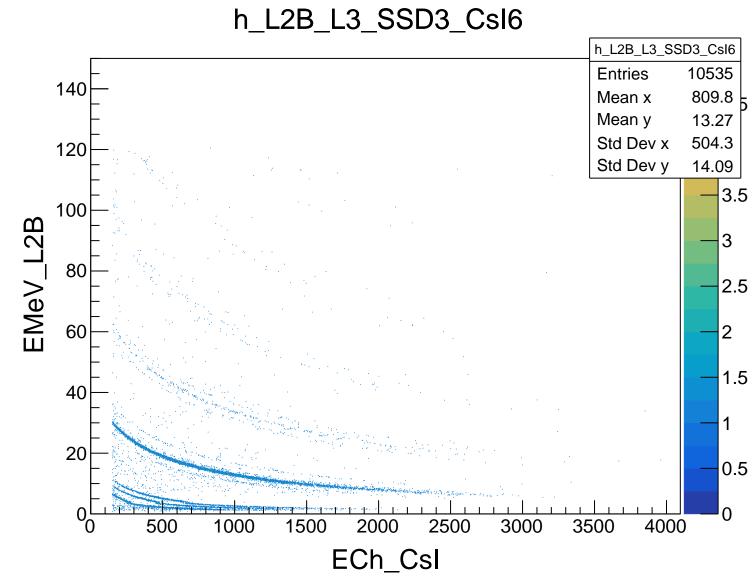












h_L2B_L3_SSD3_CsI7

