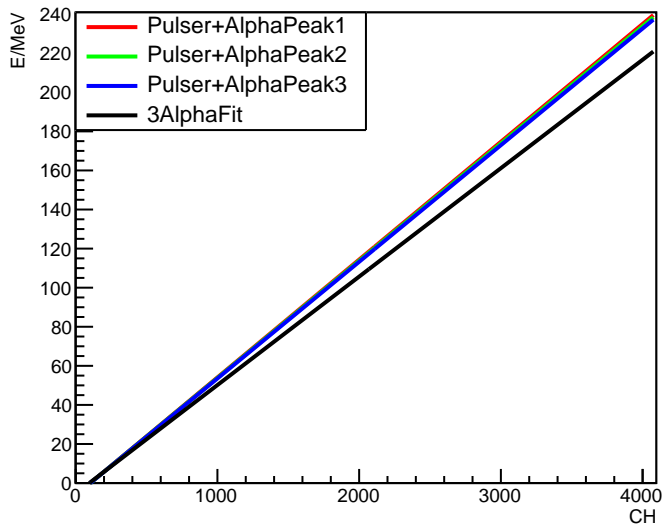
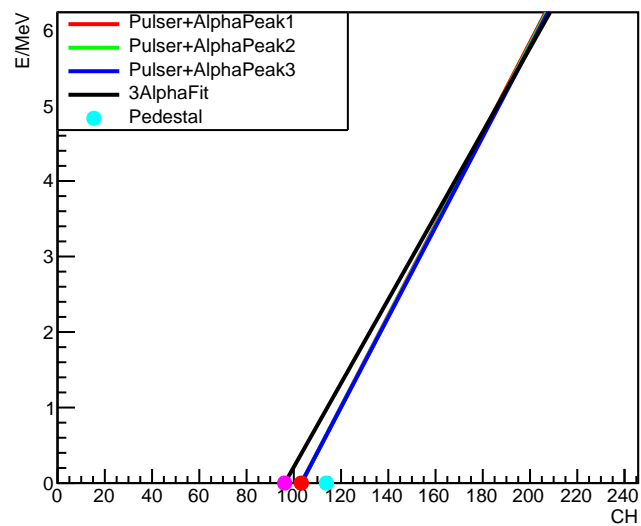


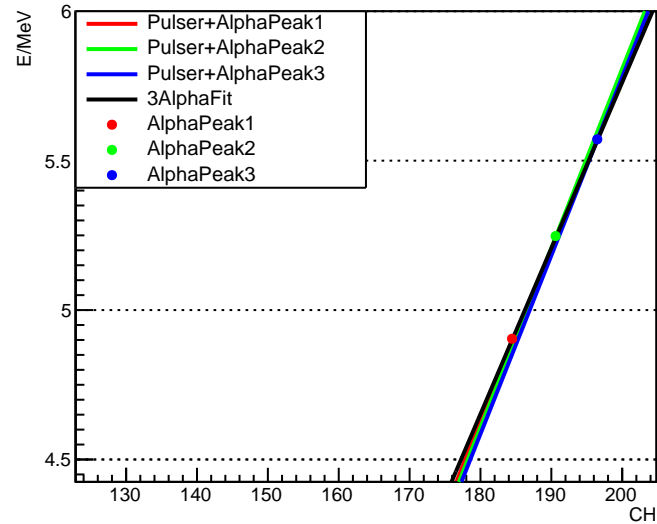
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH00



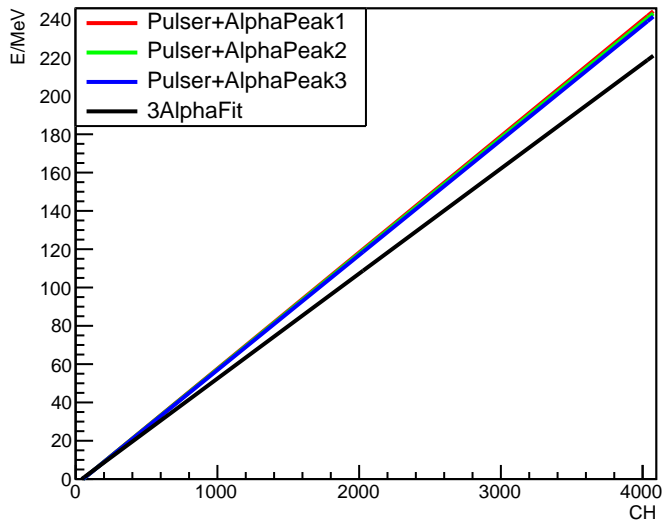
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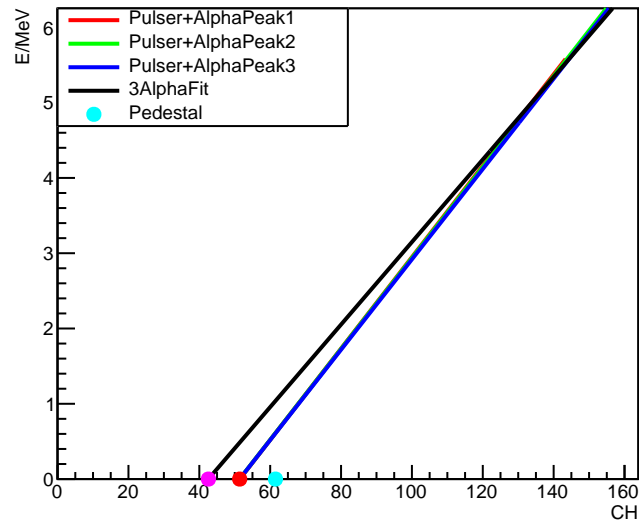
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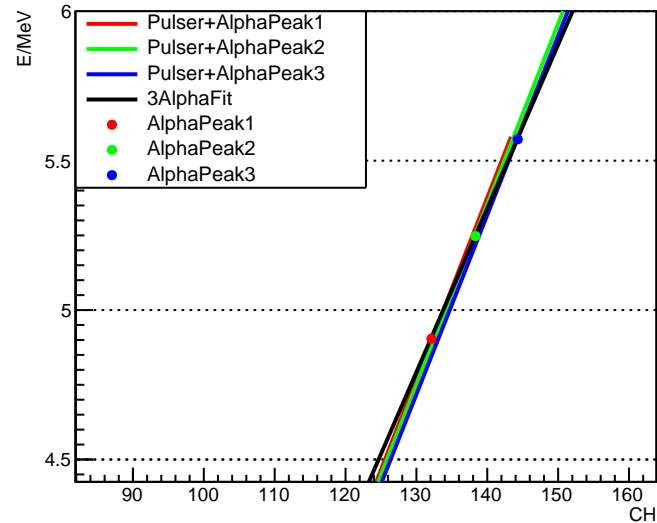
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH01



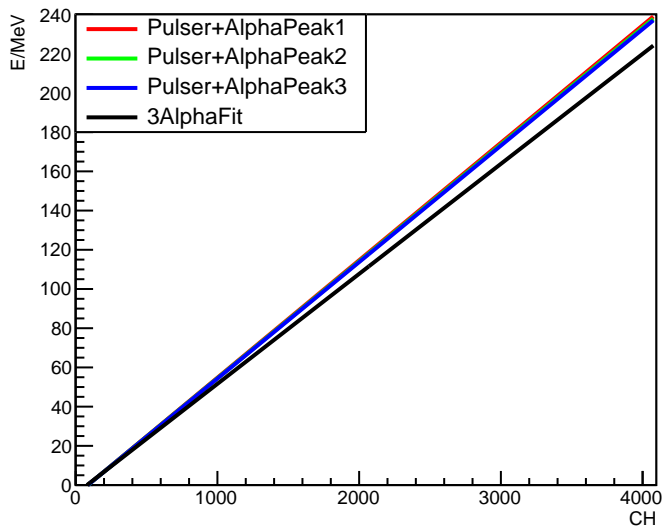
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH01



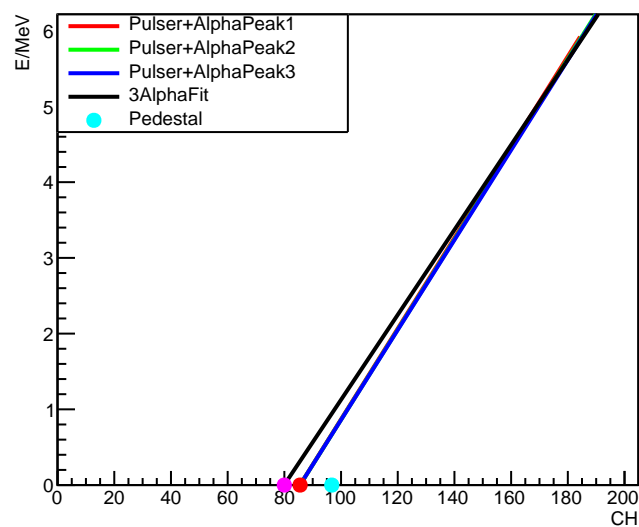
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH01



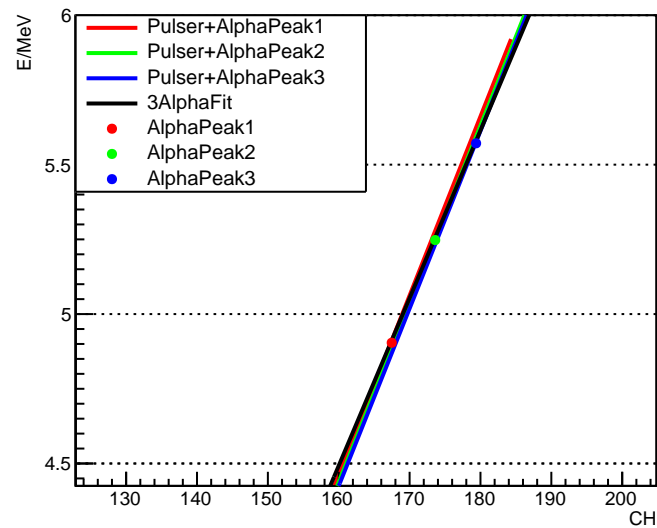
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH02



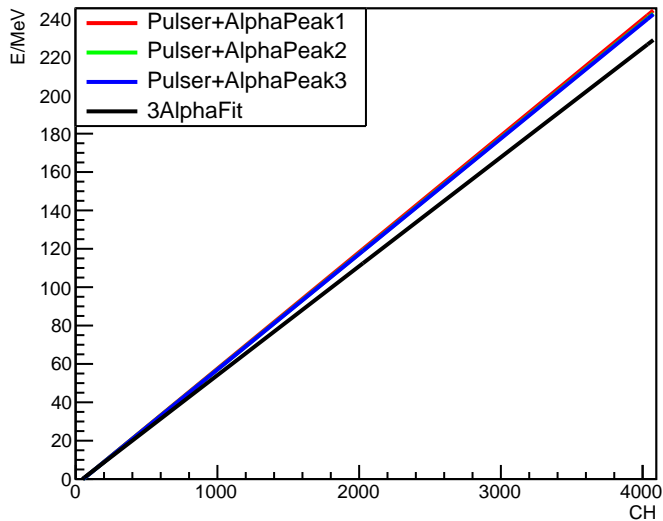
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH02



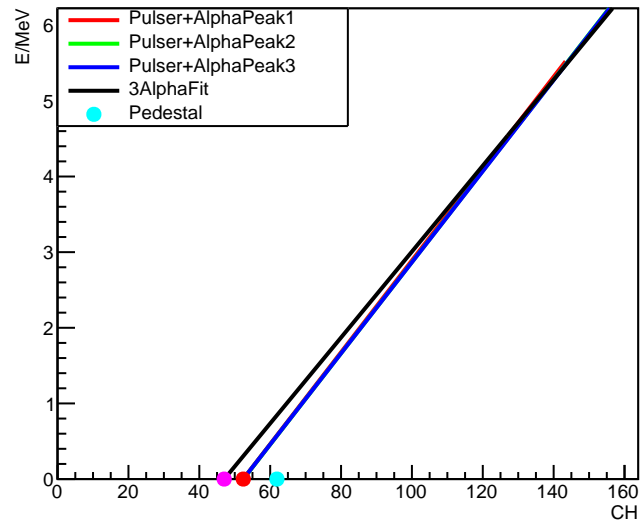
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH02



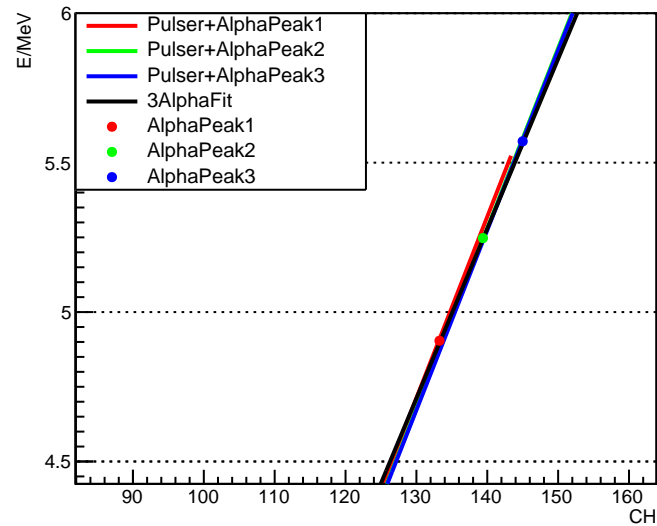
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH03



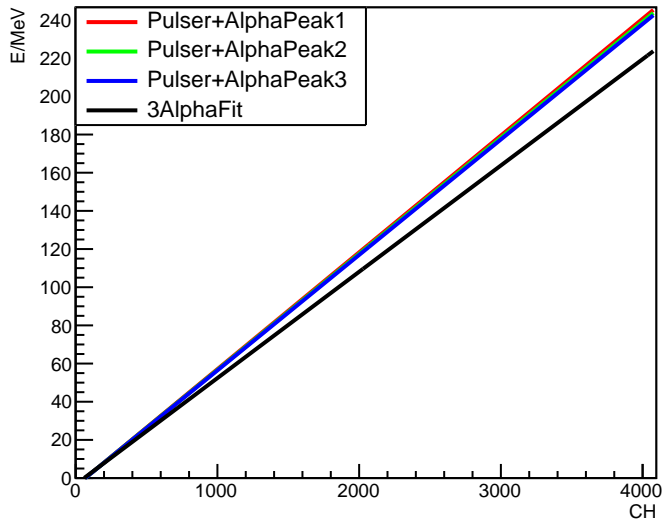
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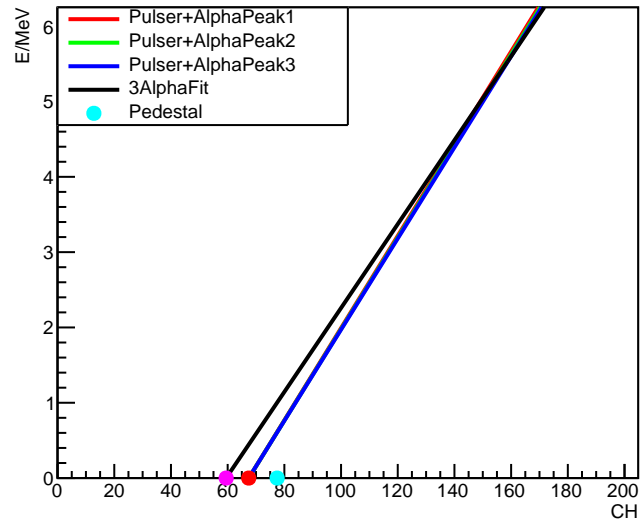
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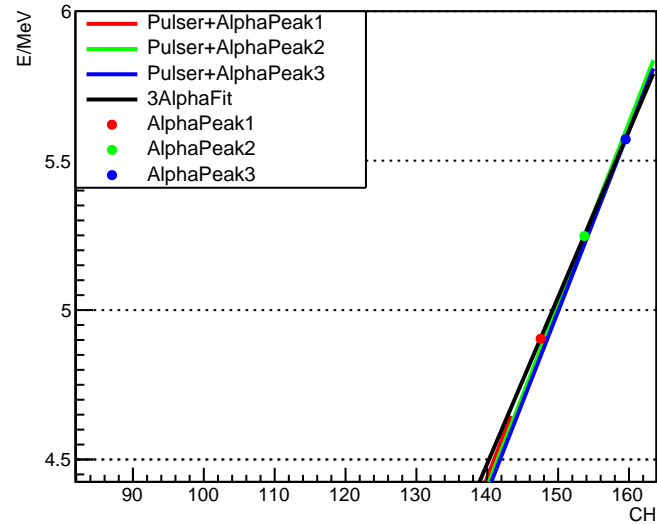
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH04



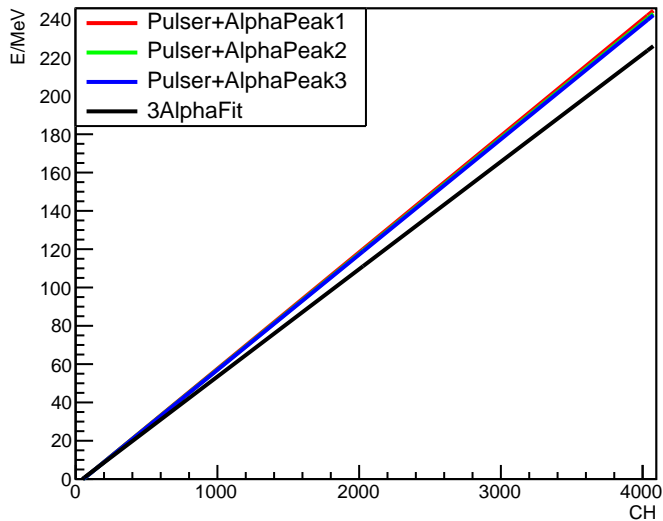
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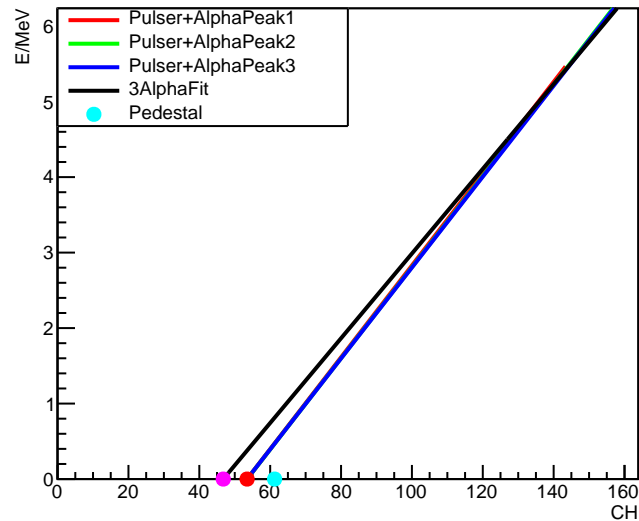
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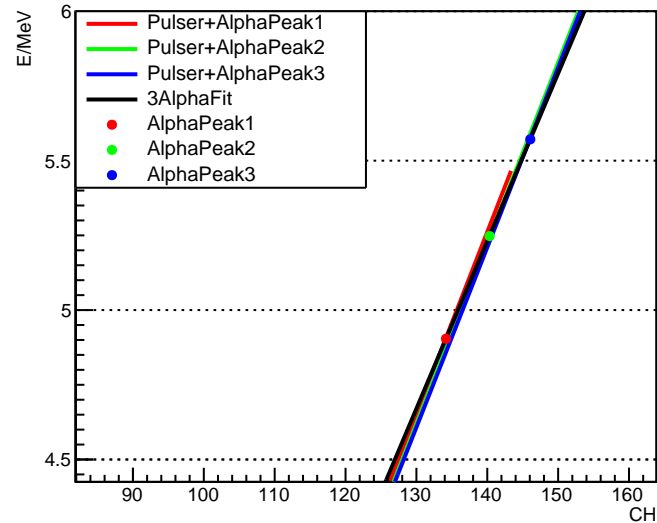
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH05



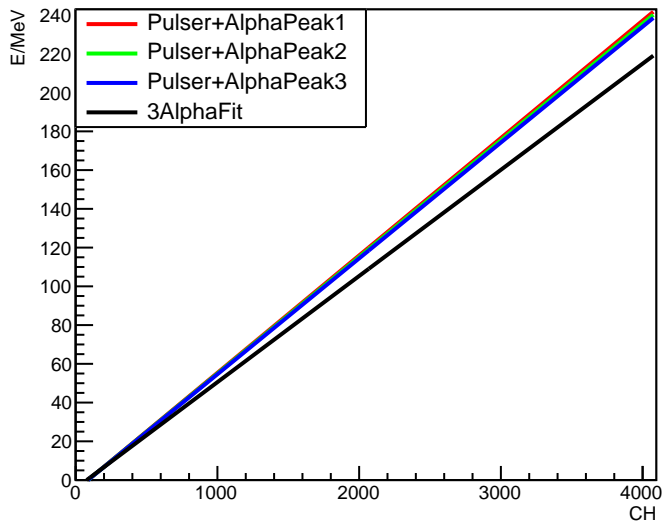
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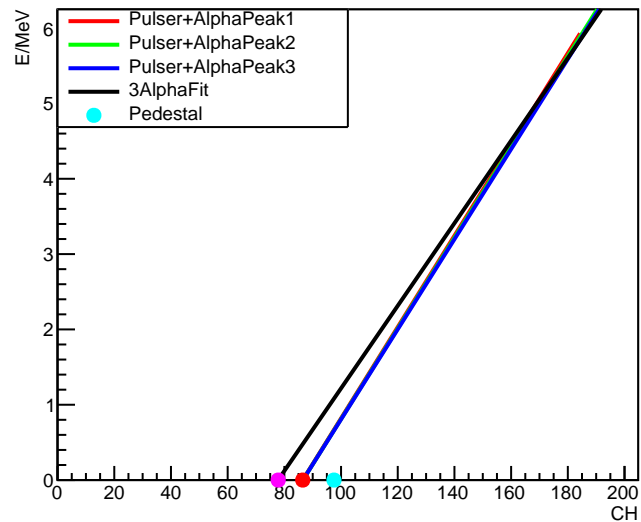
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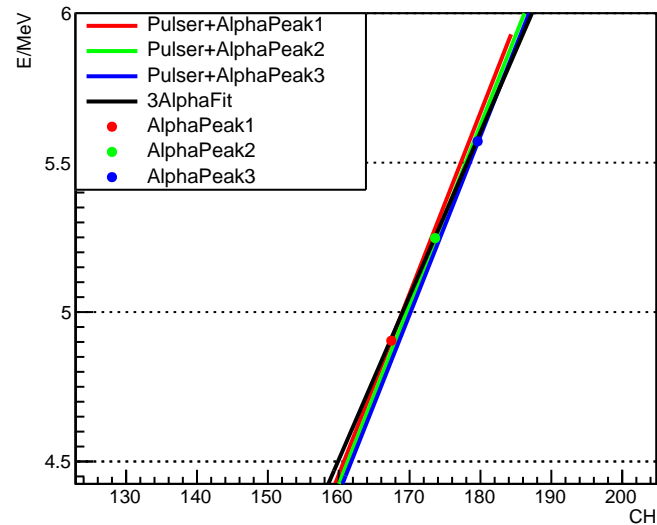
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH06



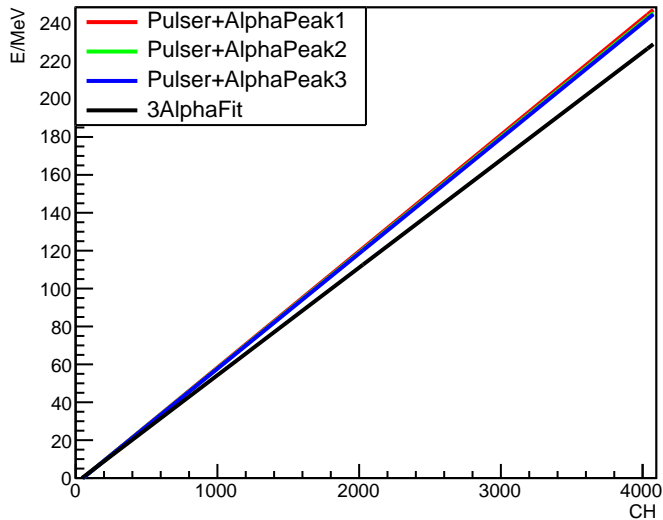
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH06



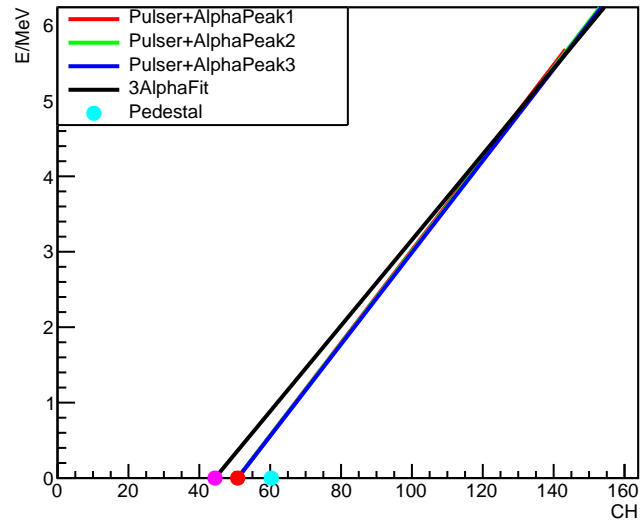
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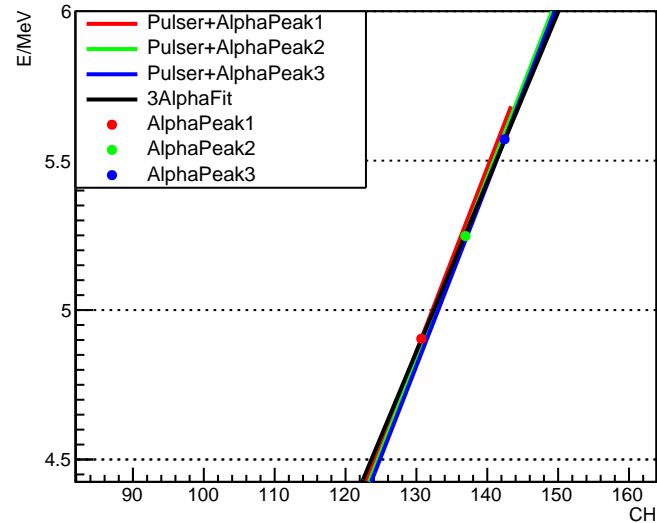
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH07



SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH07

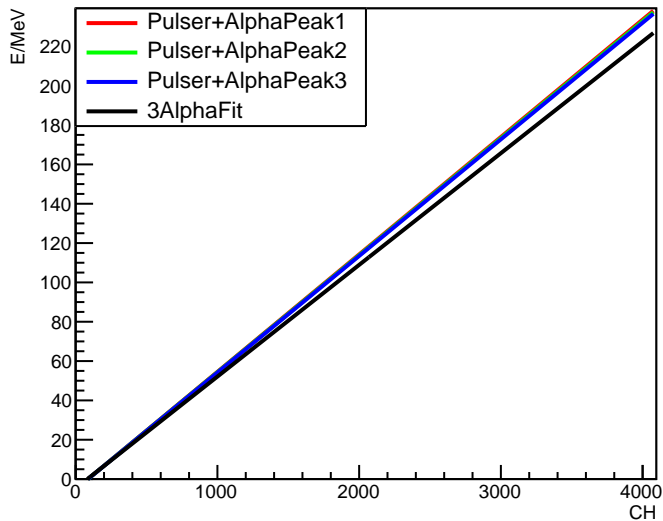


SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH07

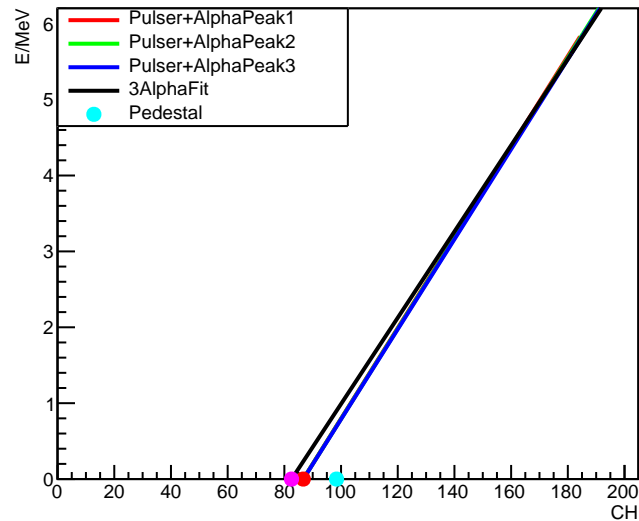




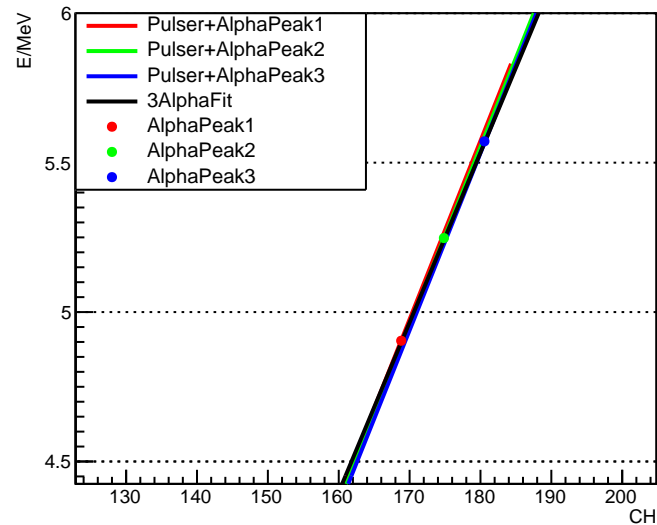
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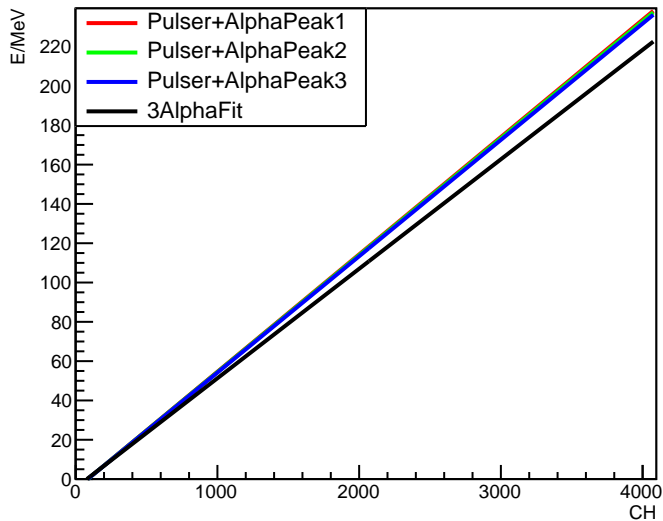
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH08



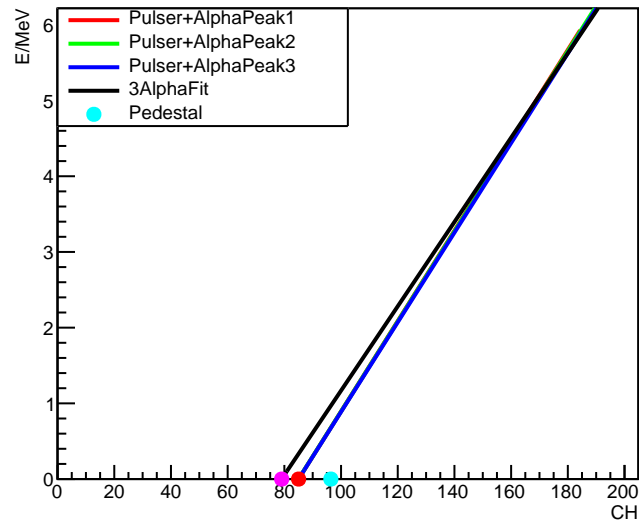
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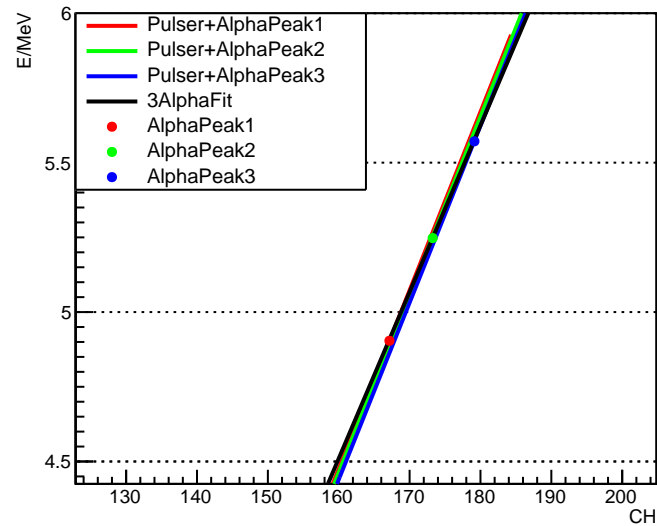
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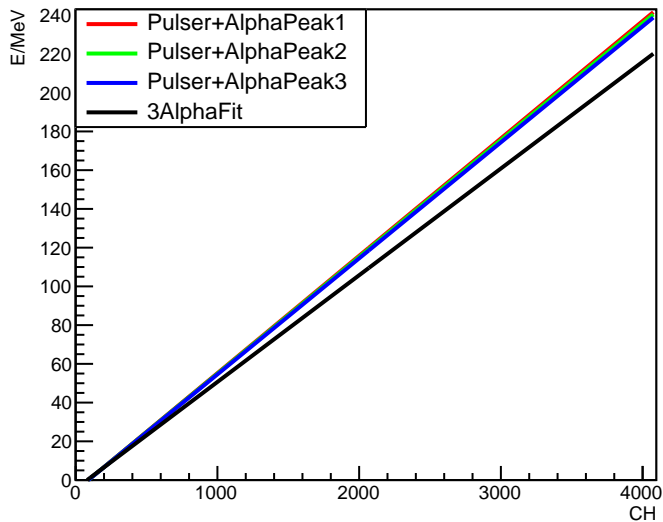
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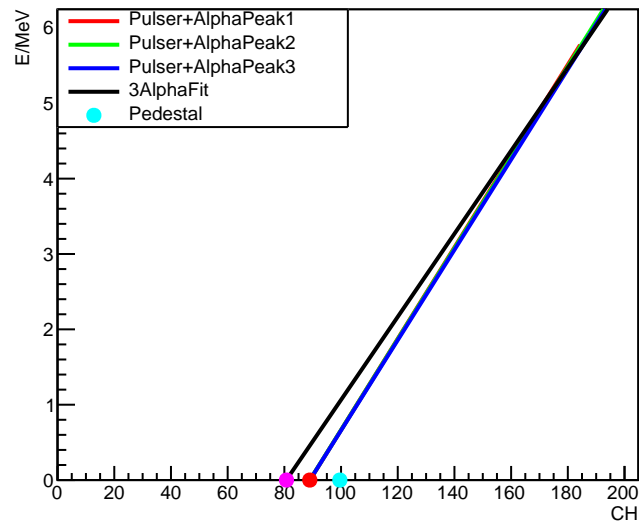
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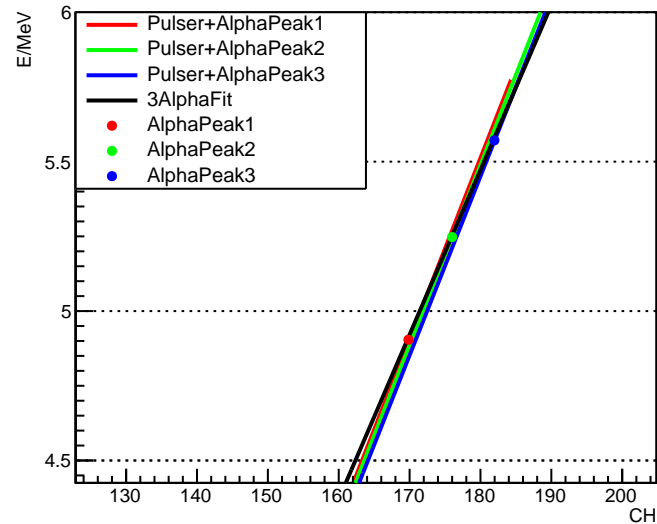
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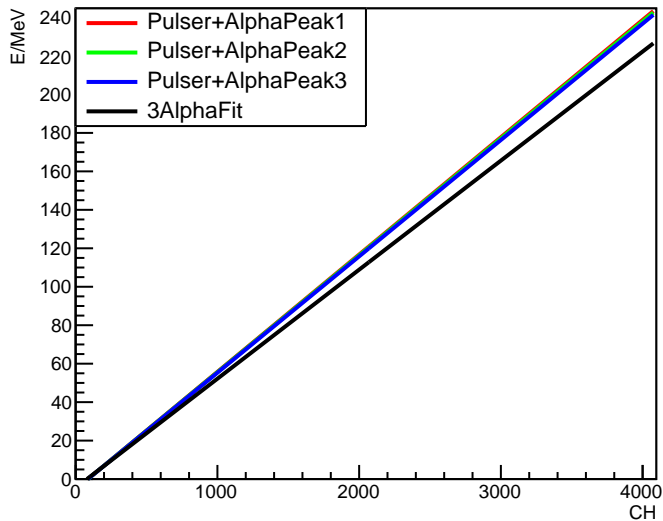
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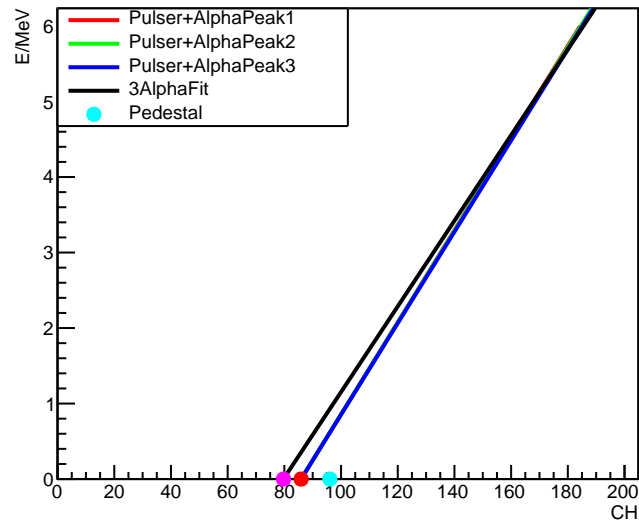
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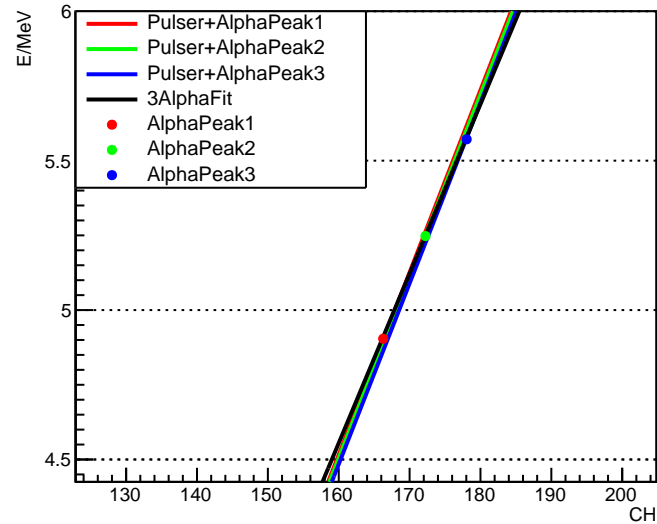
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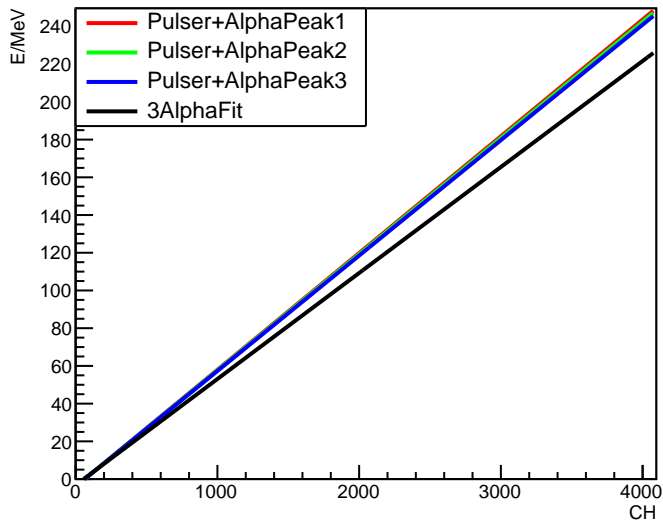
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH11



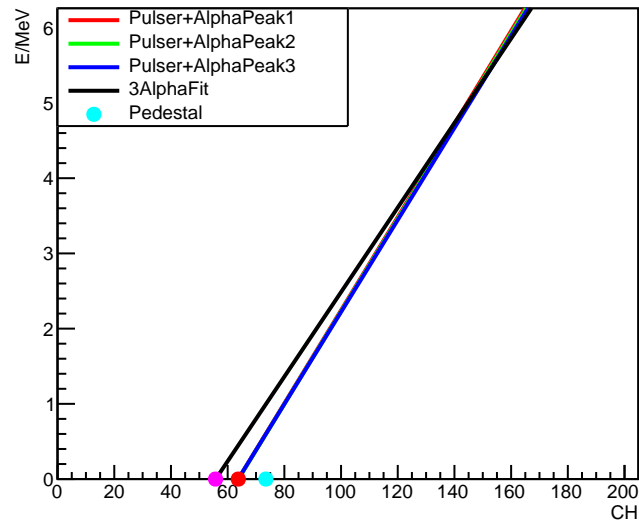
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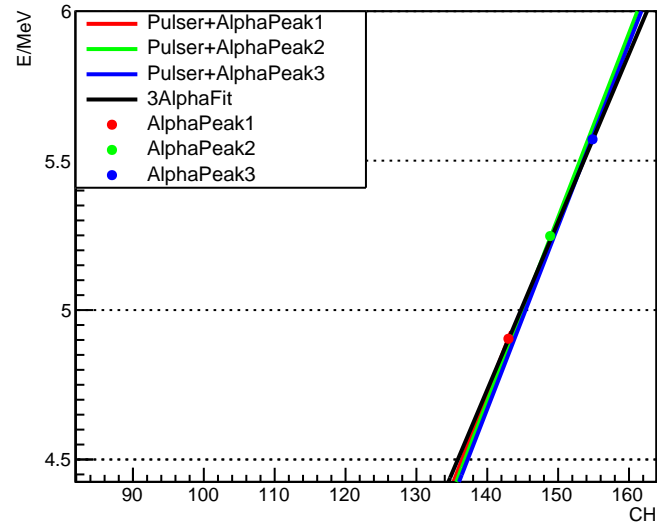
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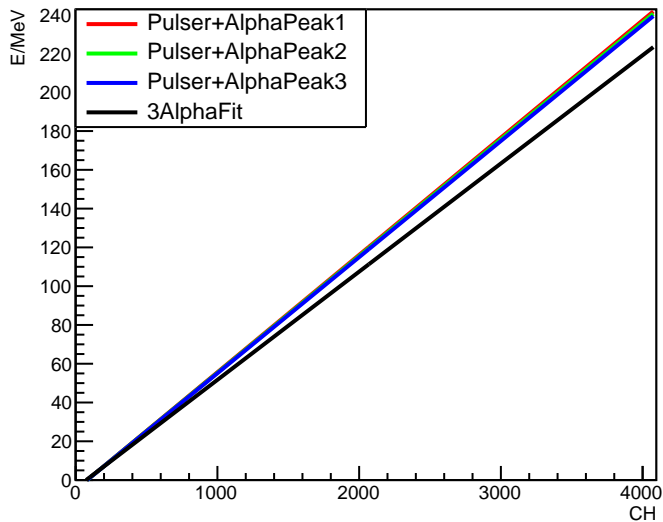
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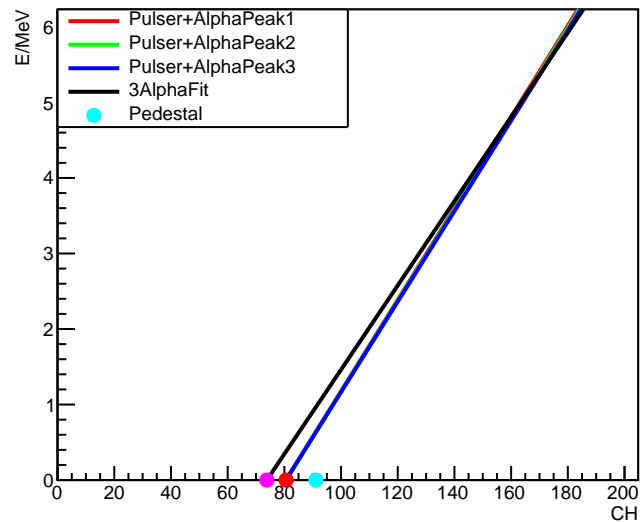
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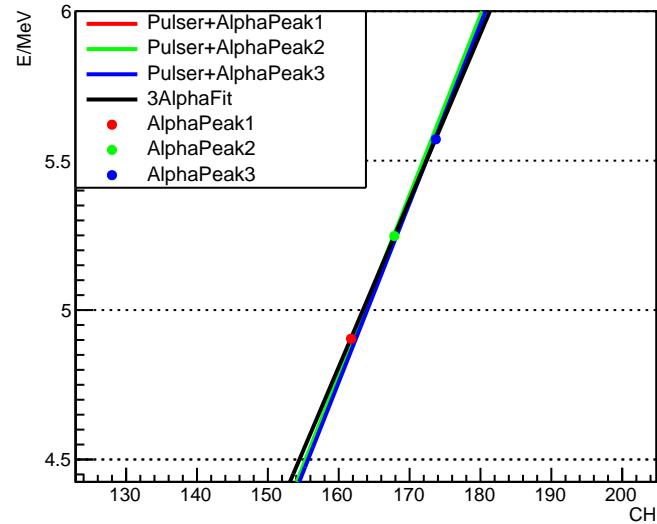
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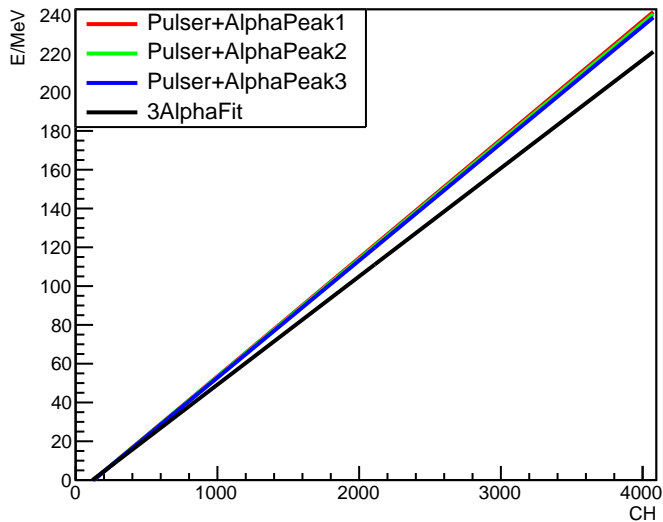
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH13



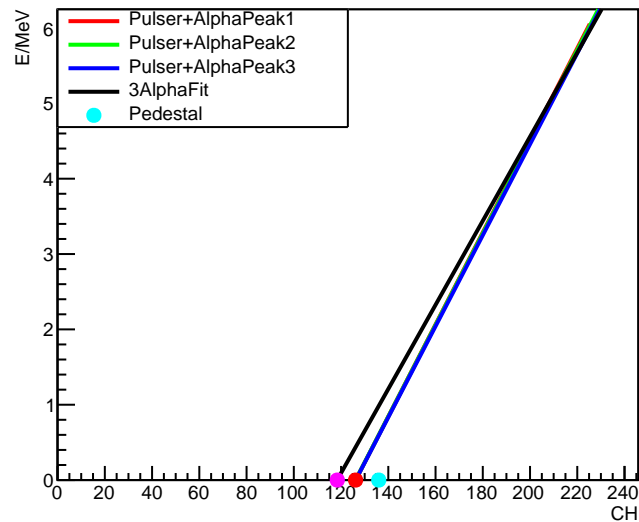
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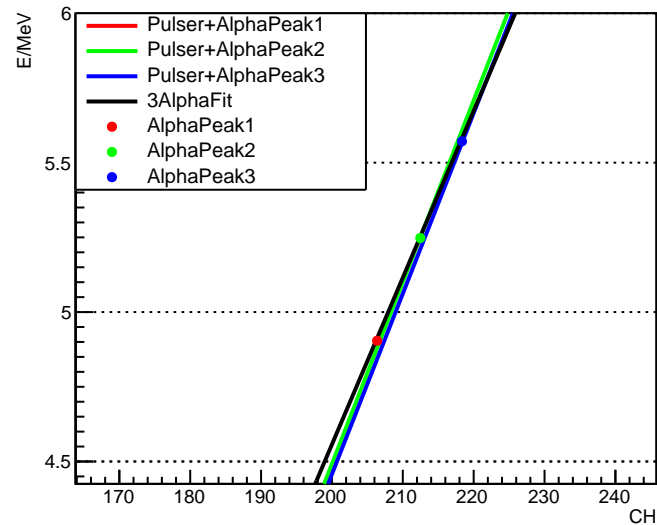
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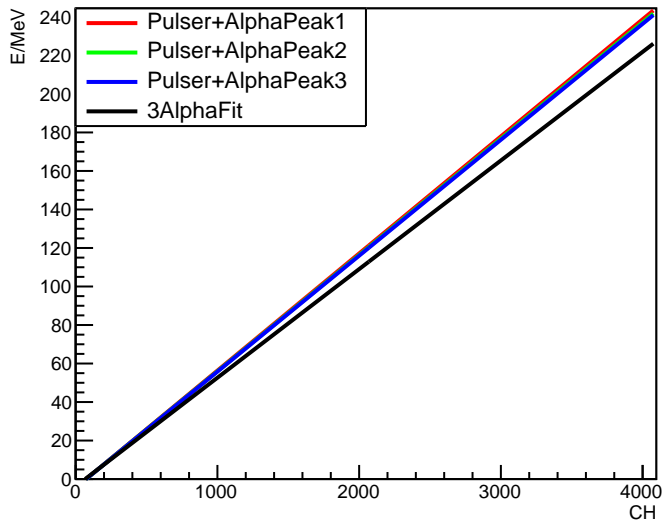
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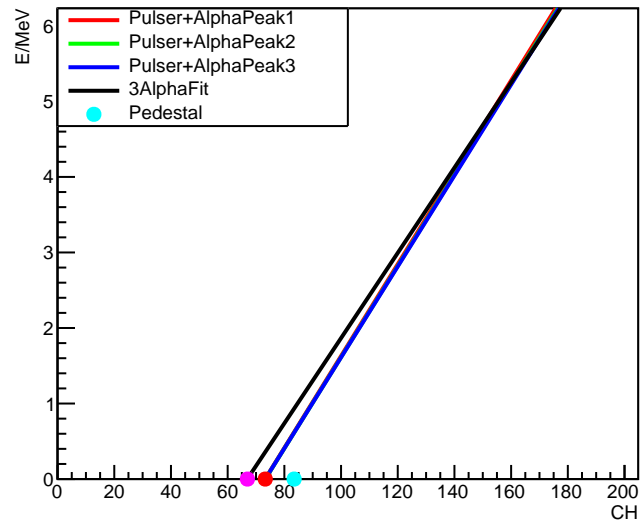
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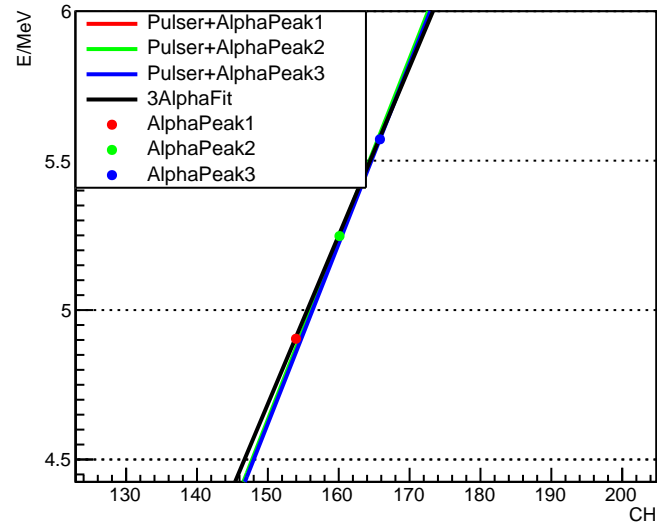
SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH15



SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH15

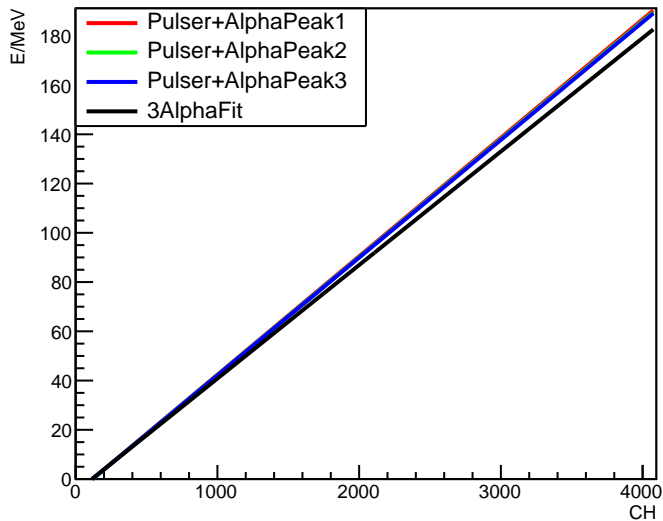


SSD1\_L2B\_Switch\_AlphaCali33\_48\_CH15

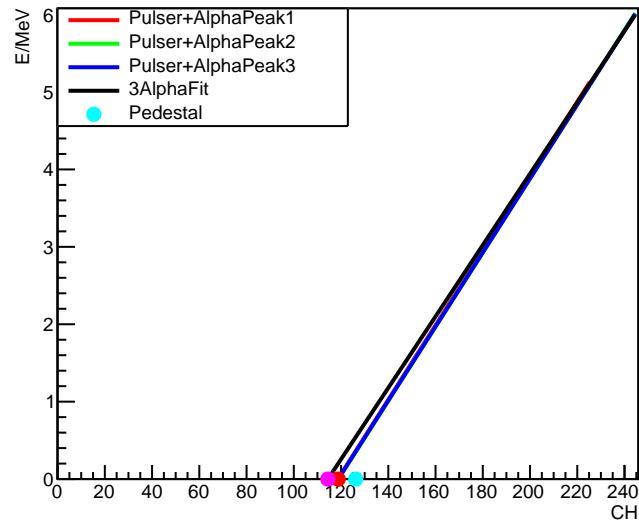




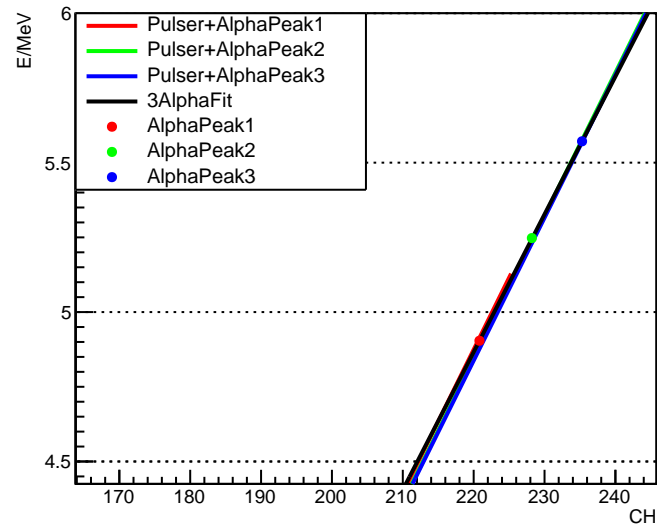
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH00



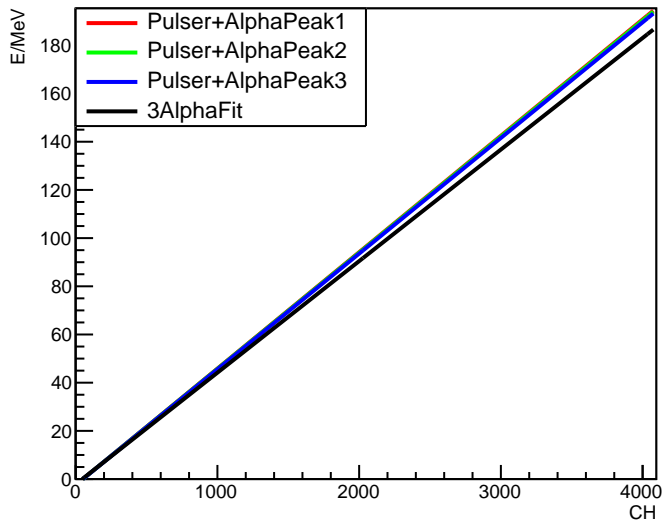
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH00



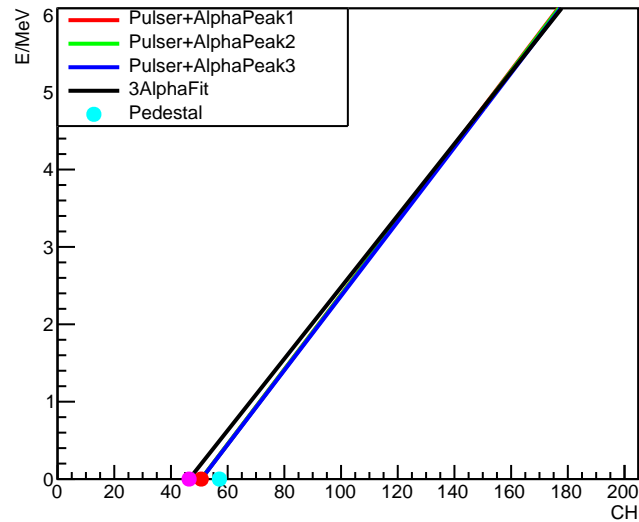
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH00



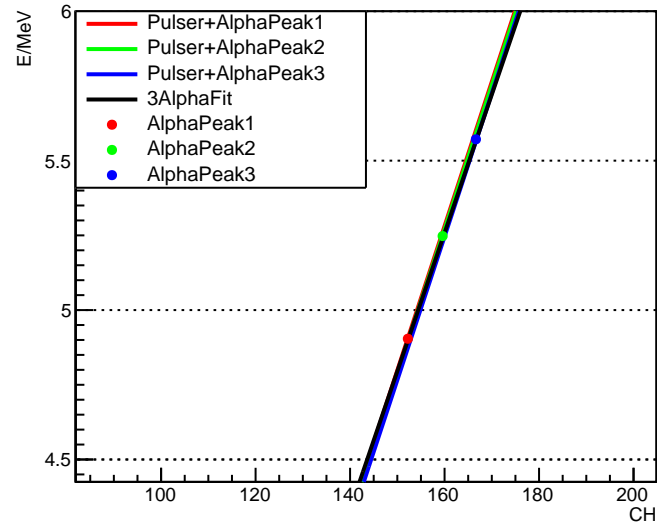
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH01



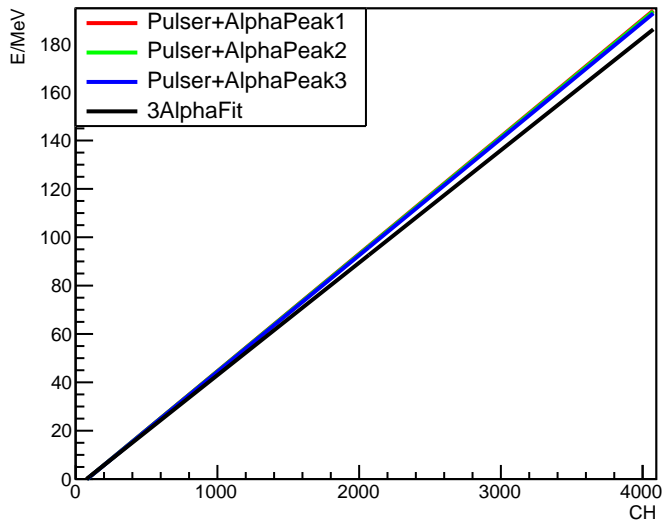
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH01



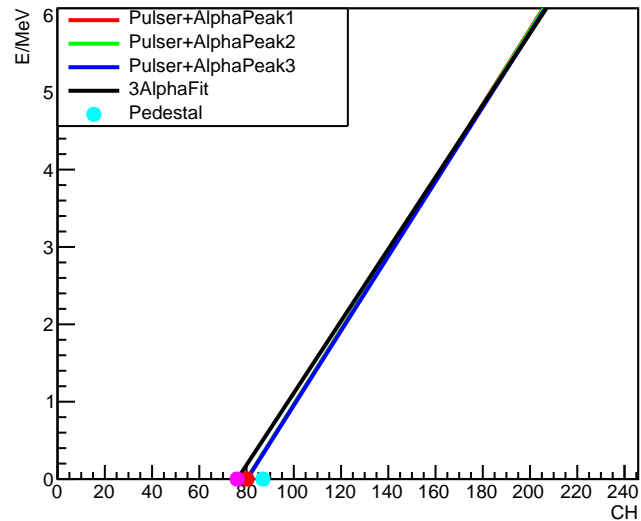
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH01



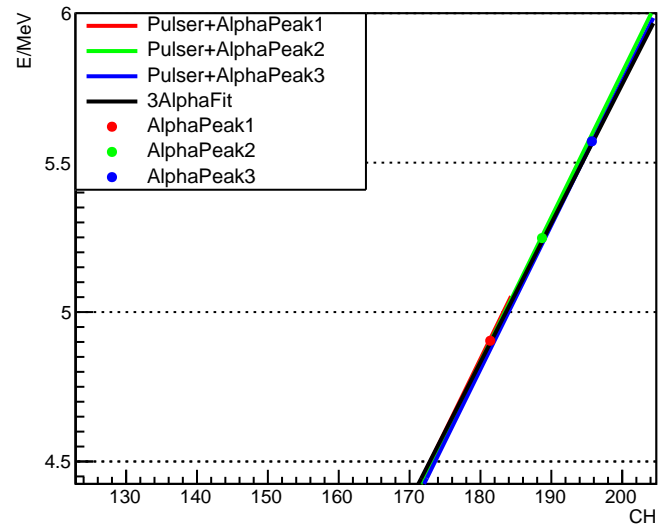
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH02



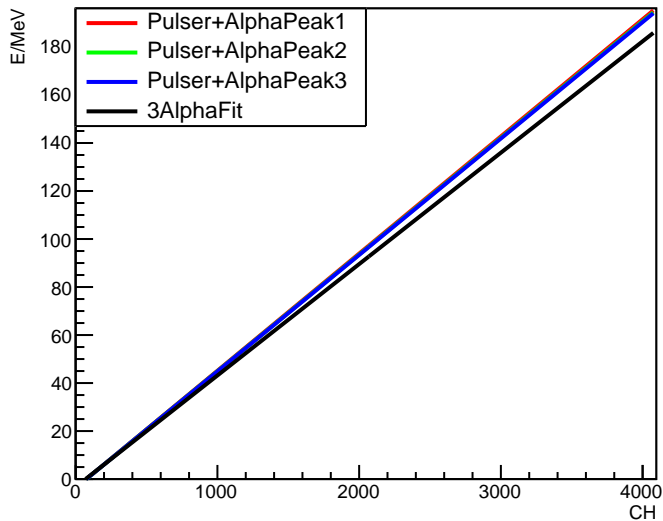
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH02



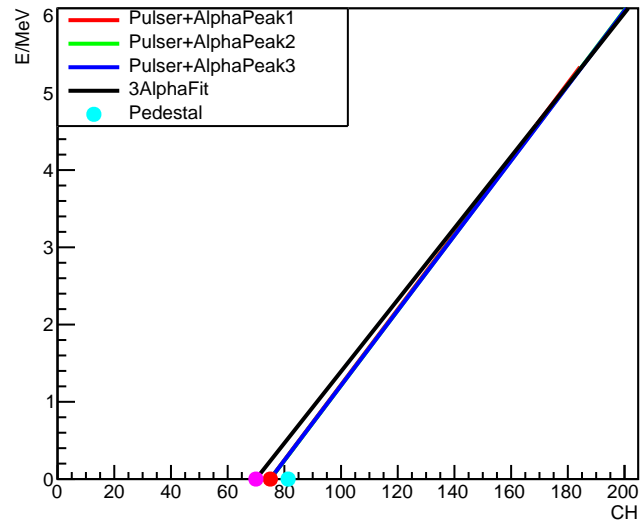
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH02



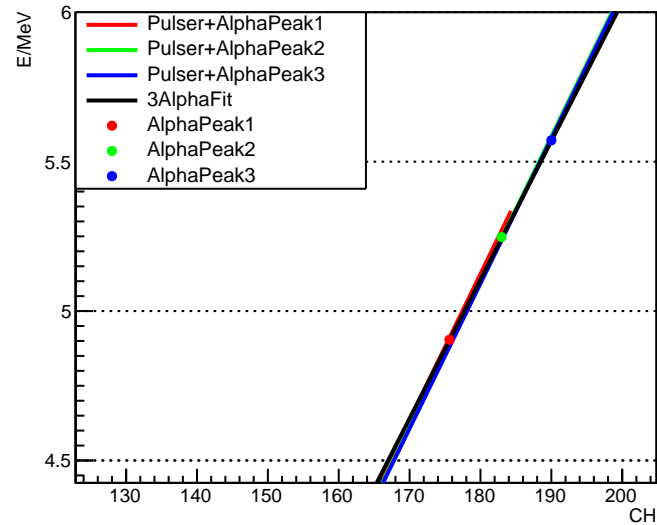
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH03



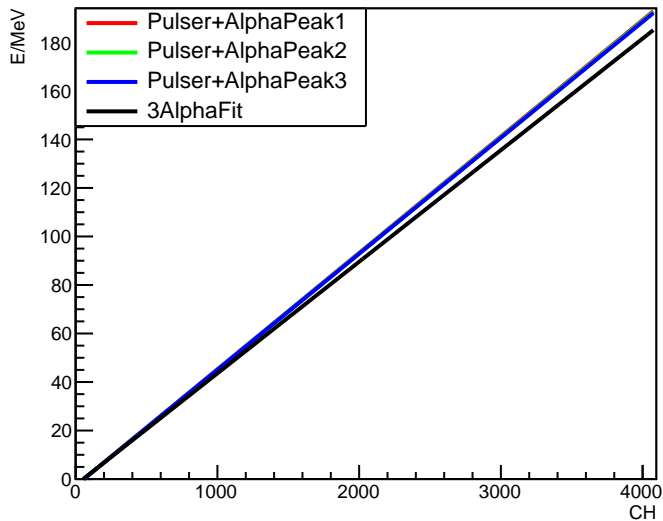
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH03



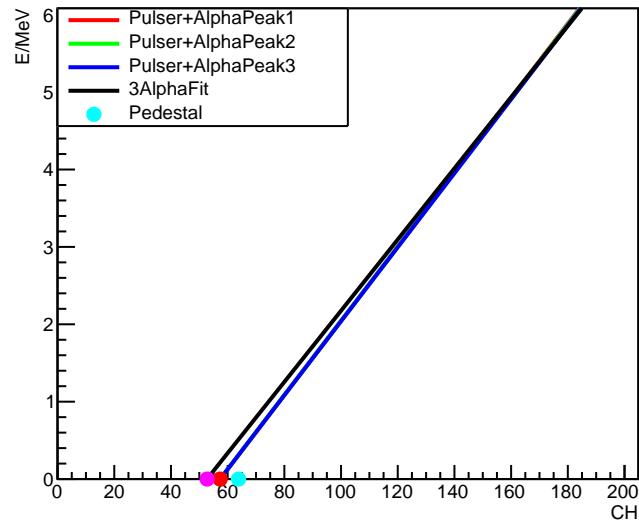
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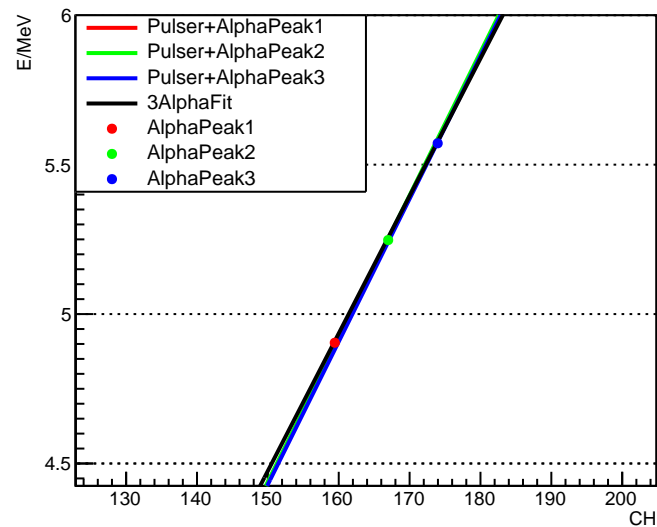
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH04



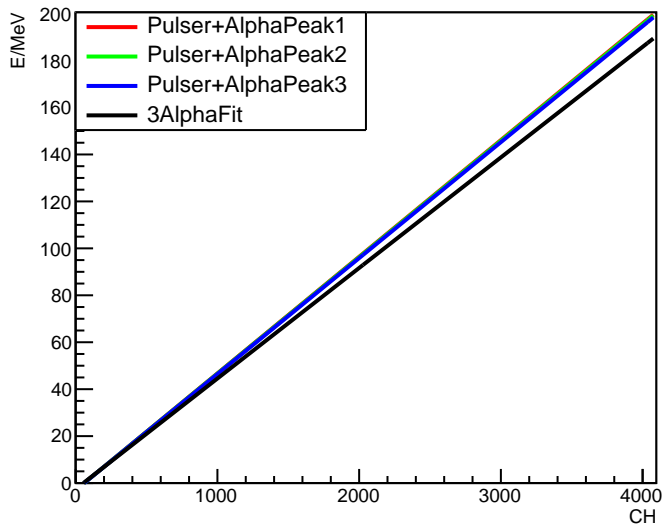
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH04



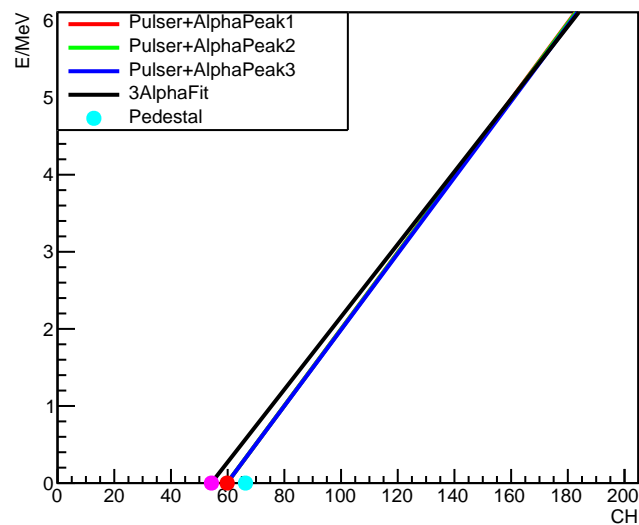
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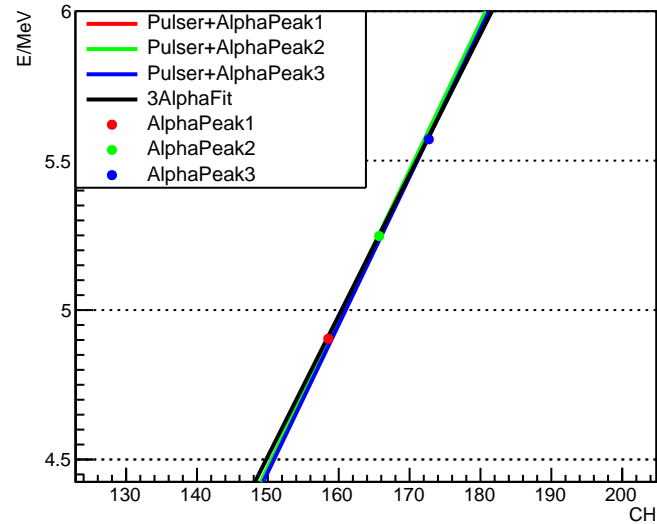
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH05



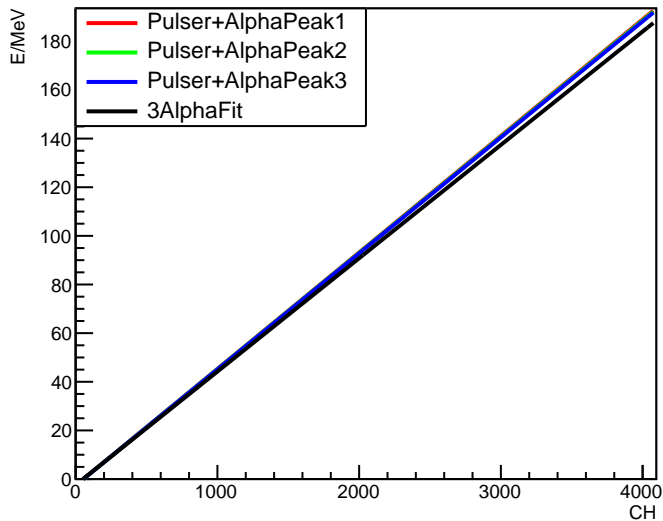
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH05



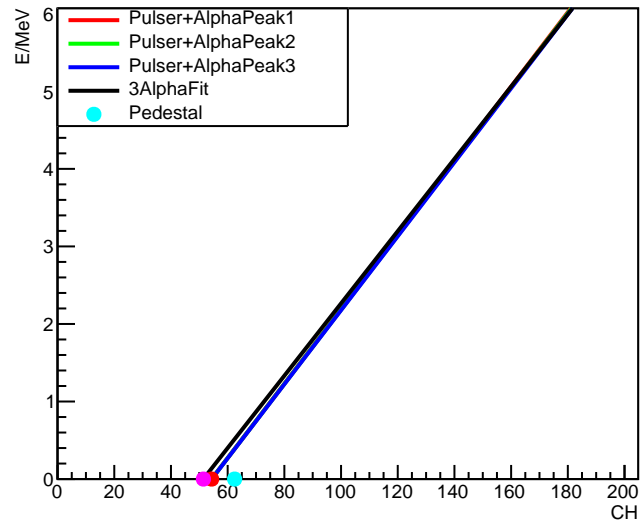
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH05



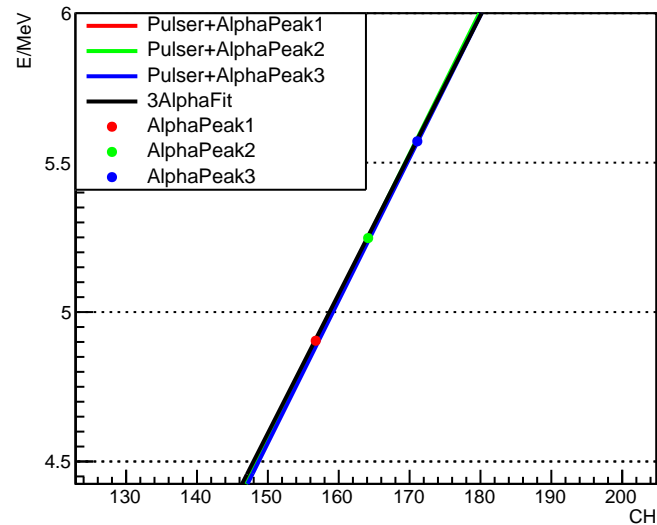
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH06



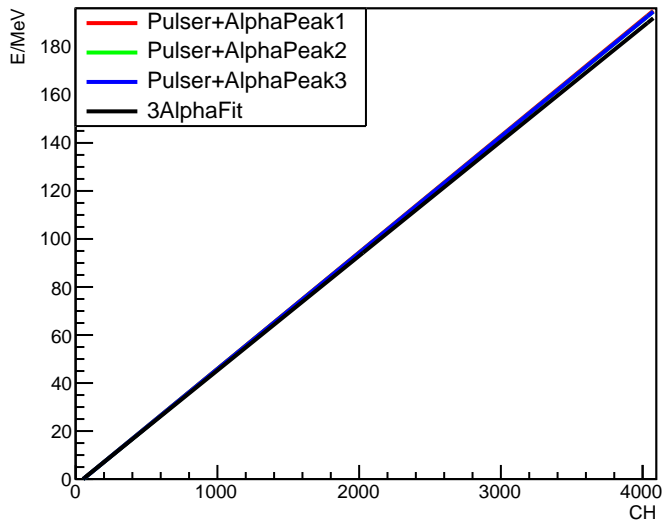
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH06



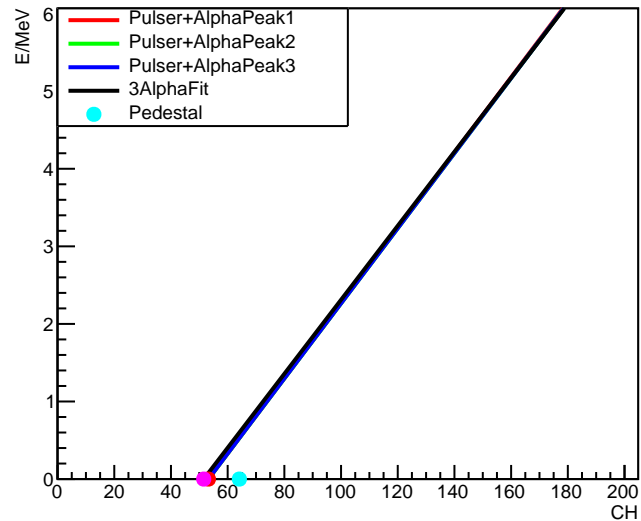
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH06



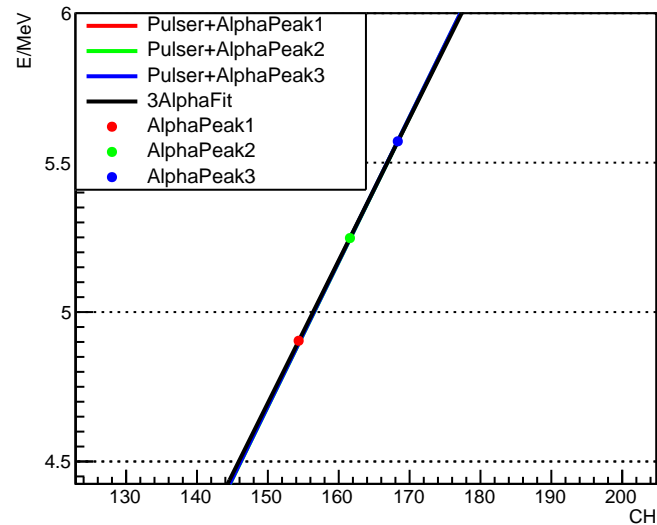
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH07



SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH07

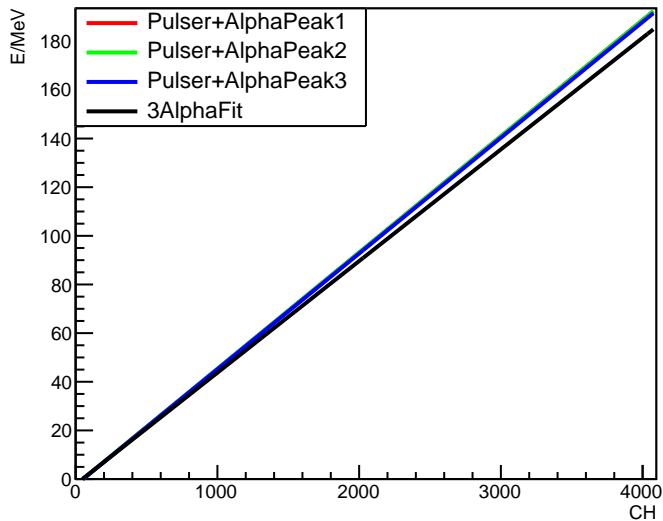


SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH07

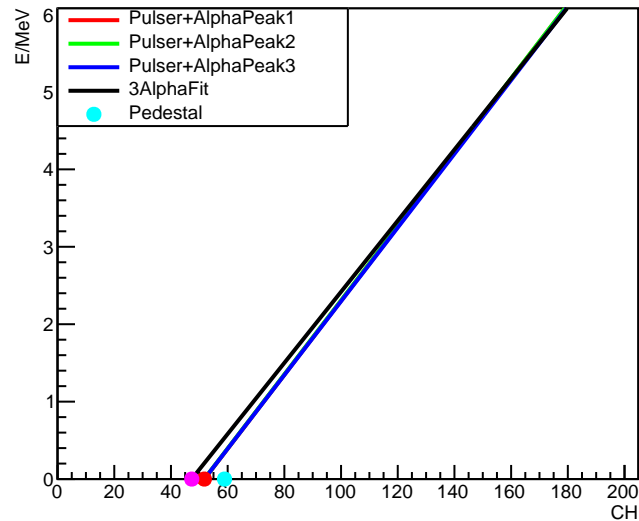




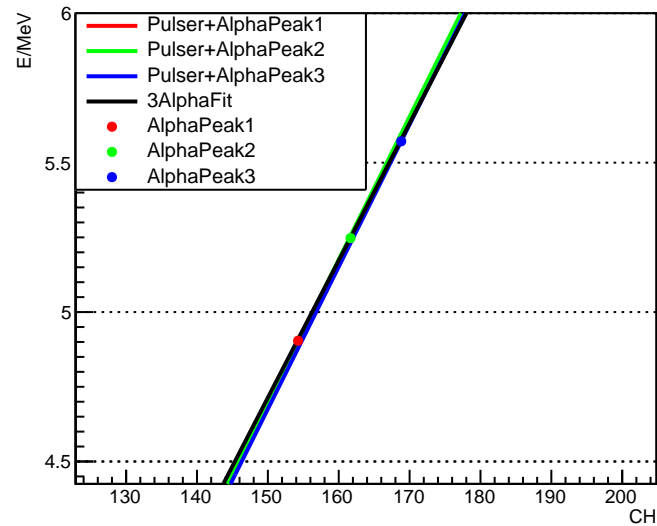
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH08



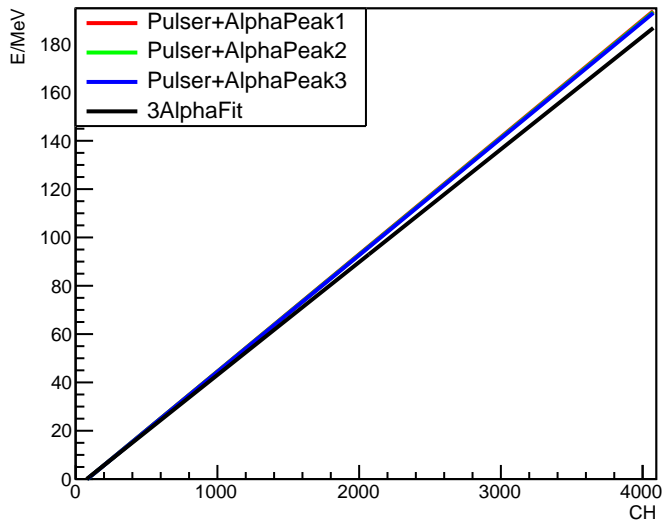
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH08



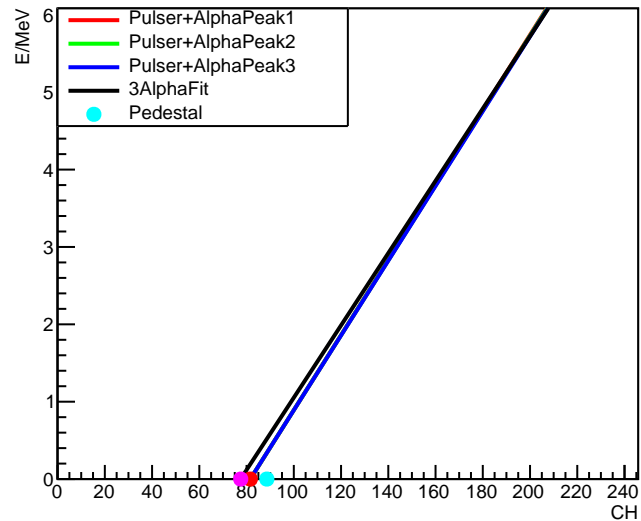
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH08



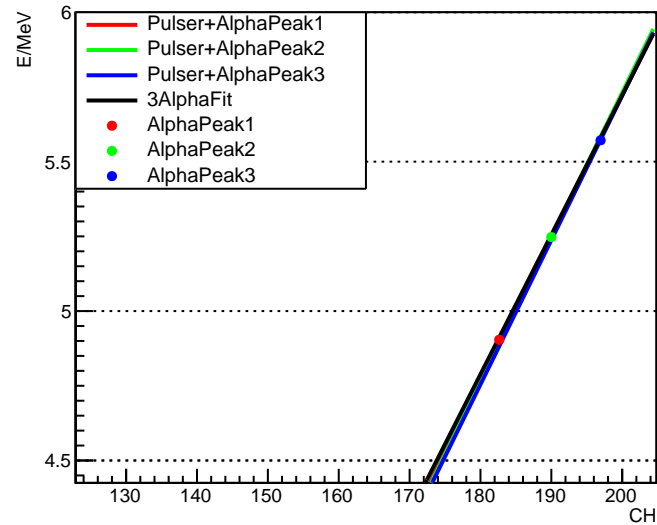
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH09



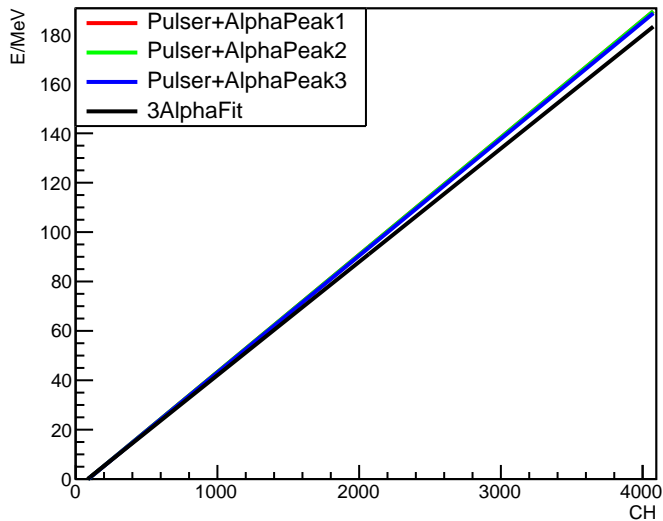
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH09



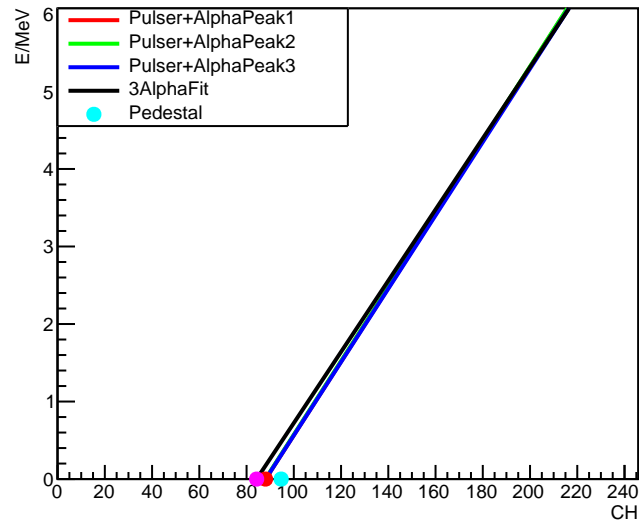
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH09



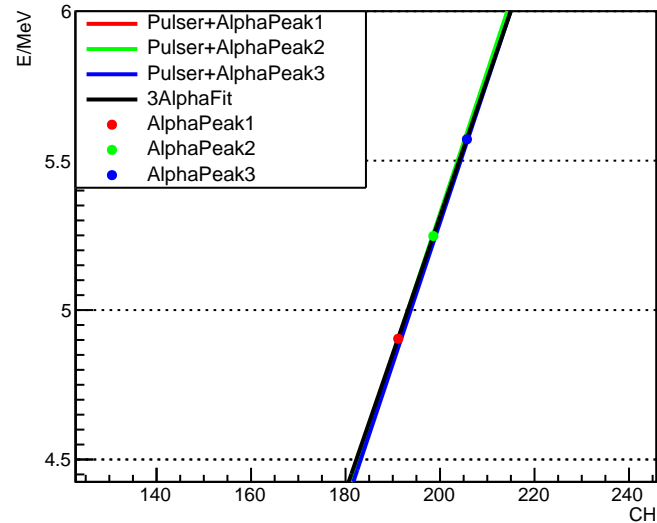
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH10



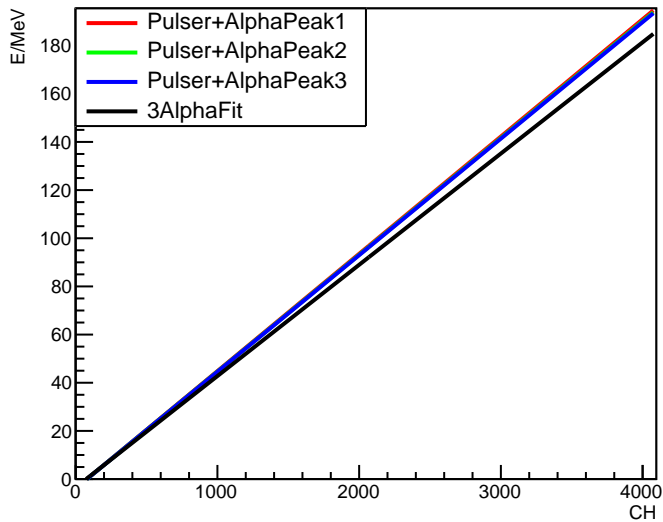
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH10



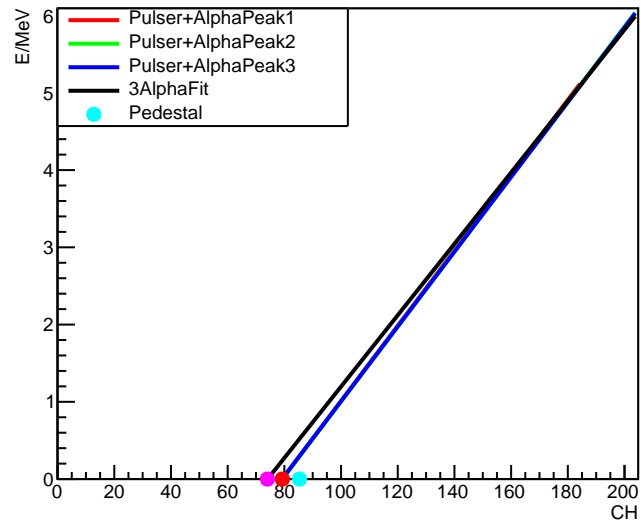
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH10



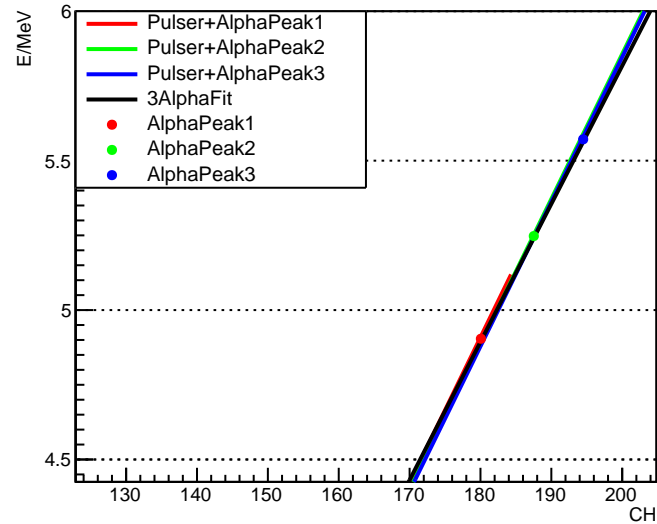
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH11



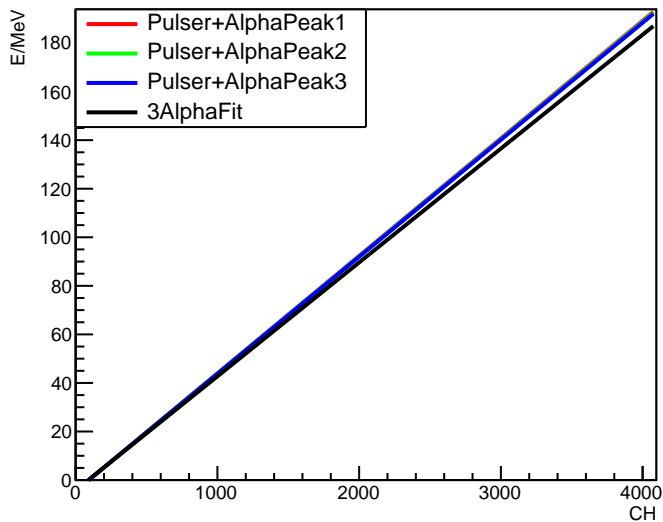
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH11



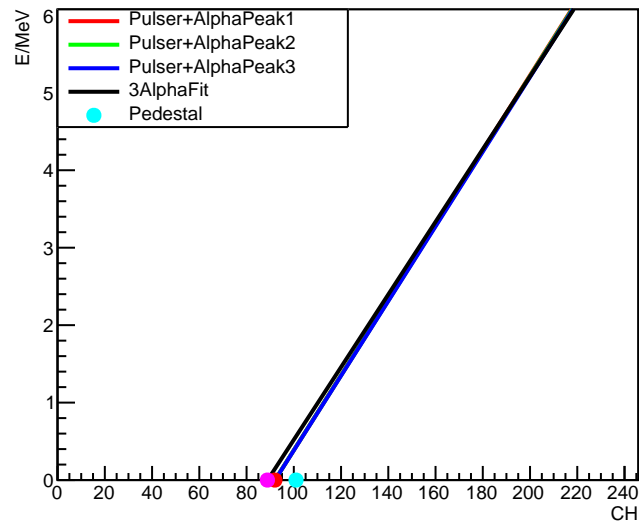
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH11



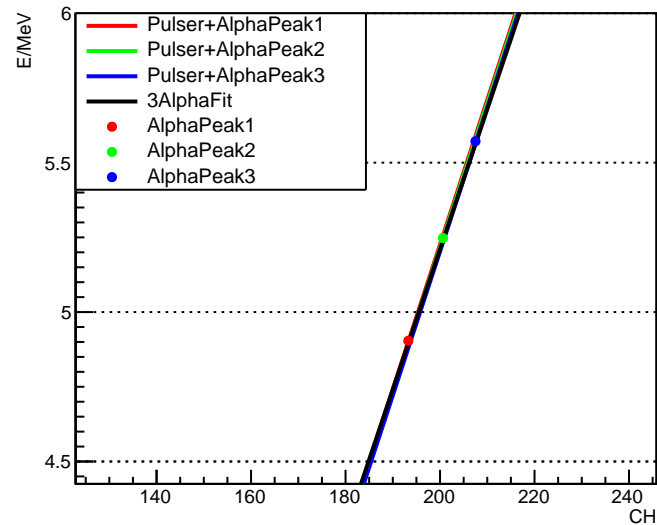
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH12



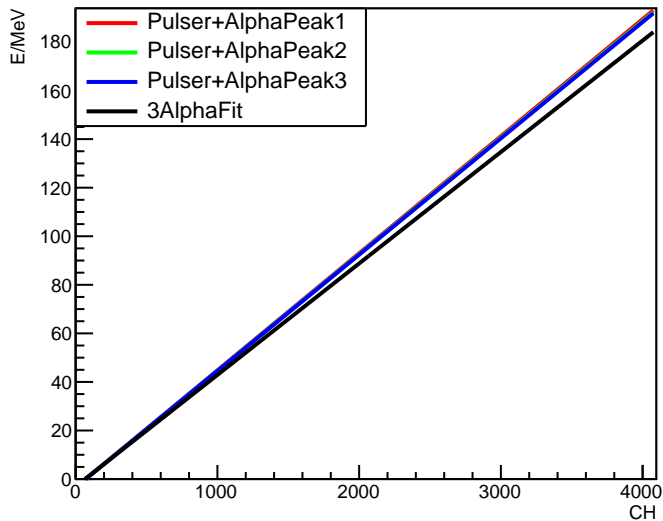
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH12



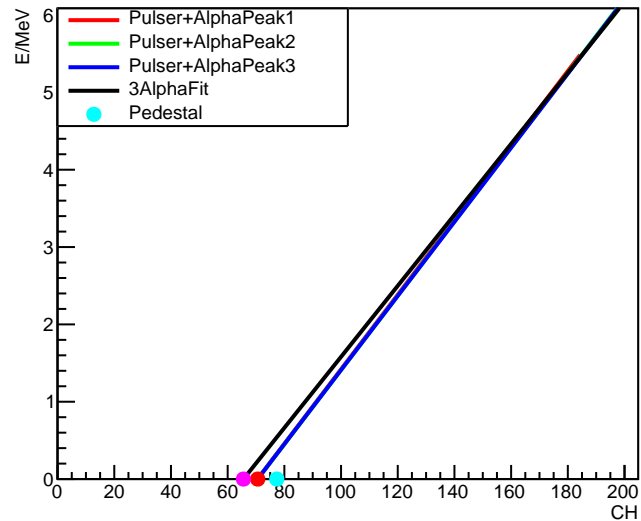
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH12



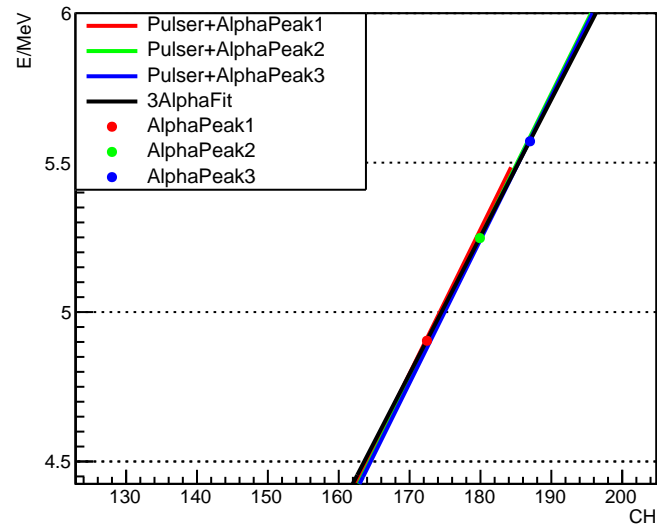
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH13



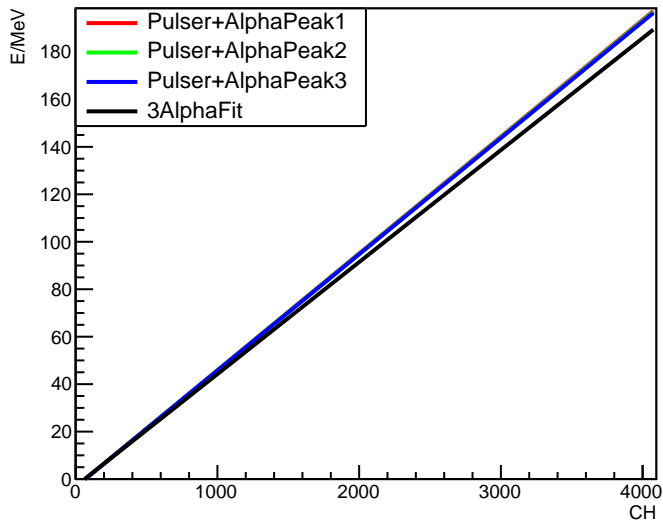
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH13



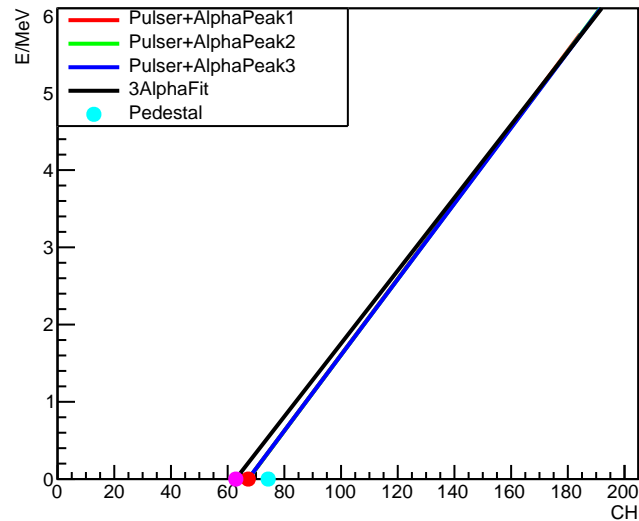
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH13



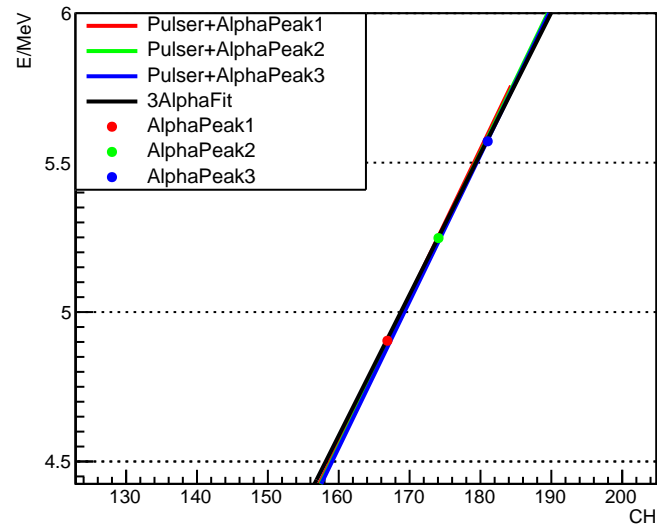
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH14



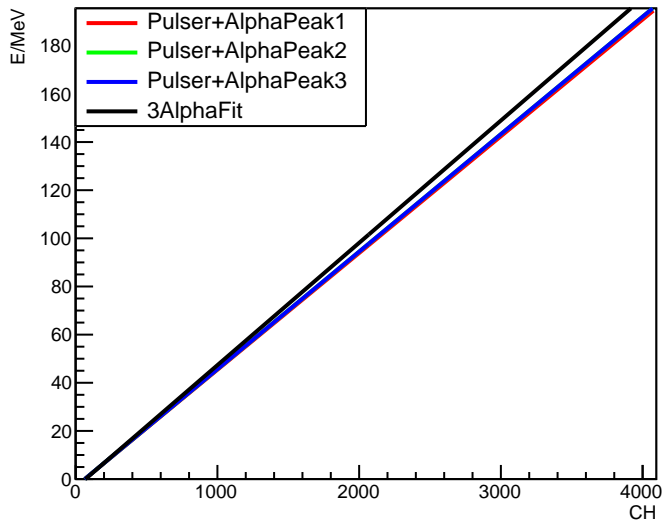
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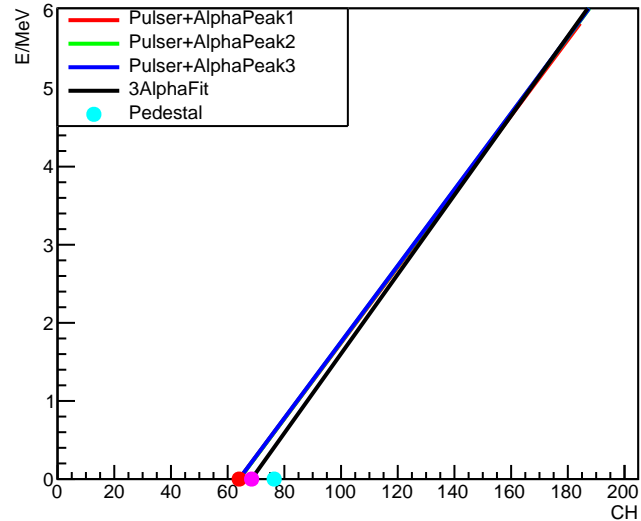
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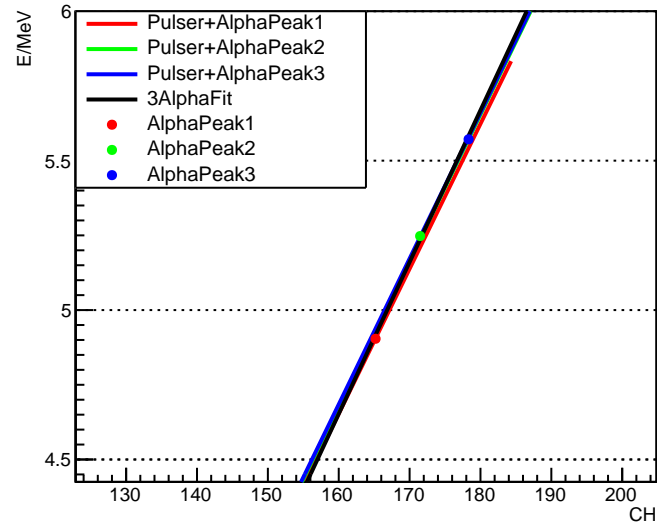
SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH15



SSD2\_L2B\_Switch\_AlphaCali33\_48\_CH15

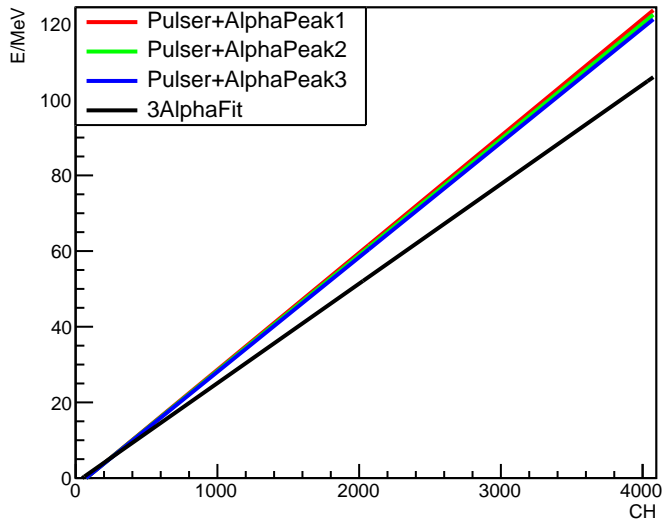


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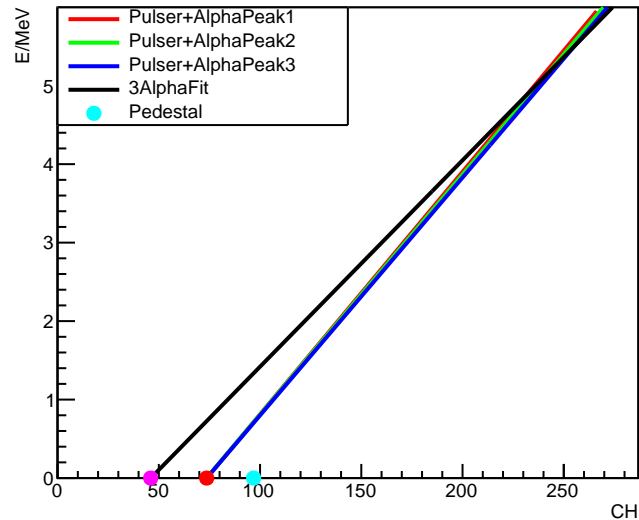




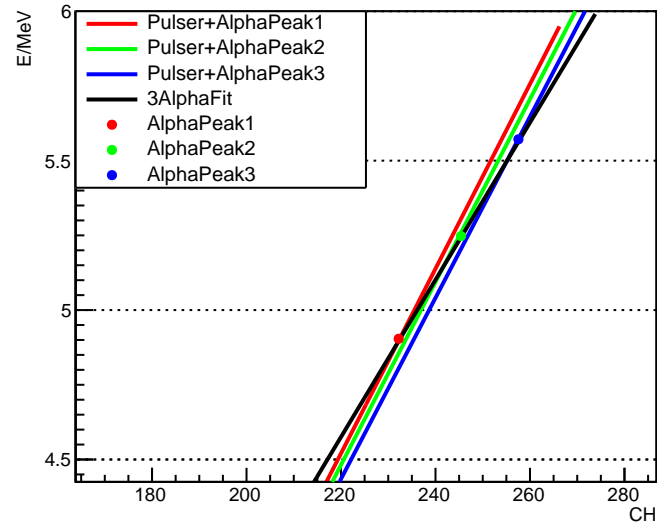
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH00



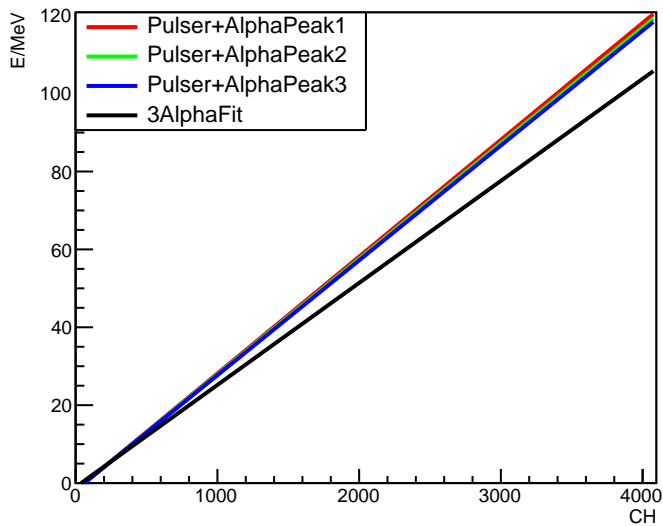
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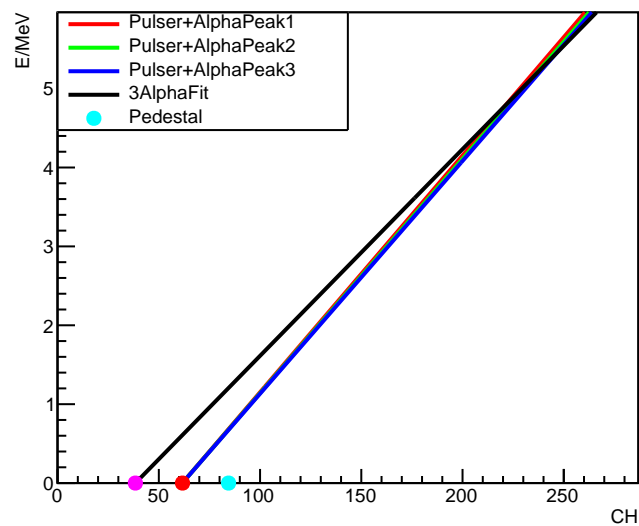
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH00



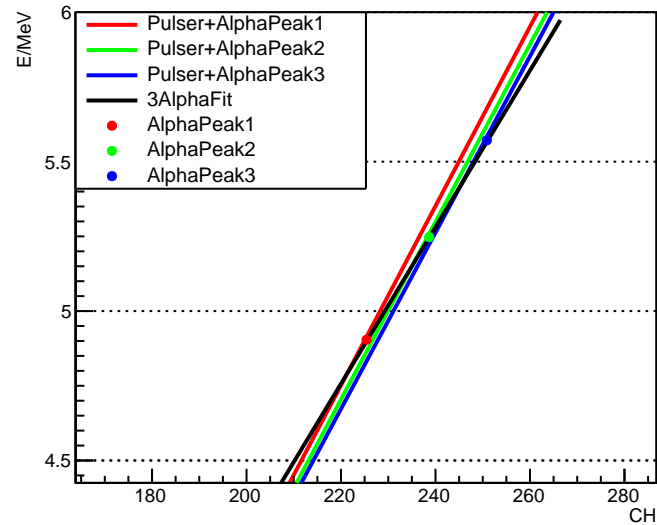
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH01



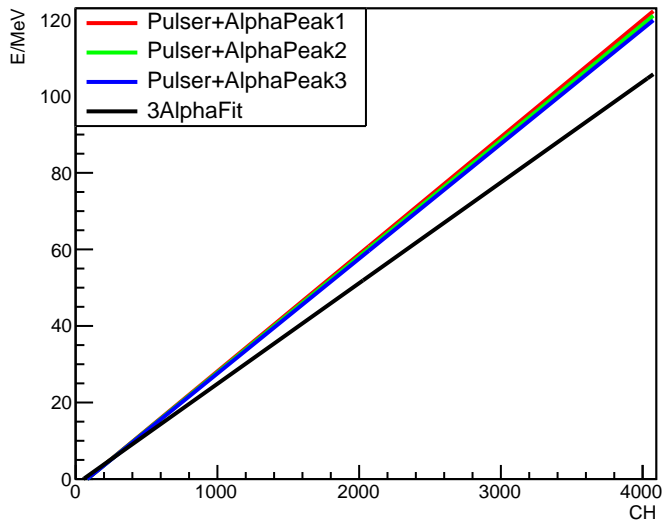
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH01



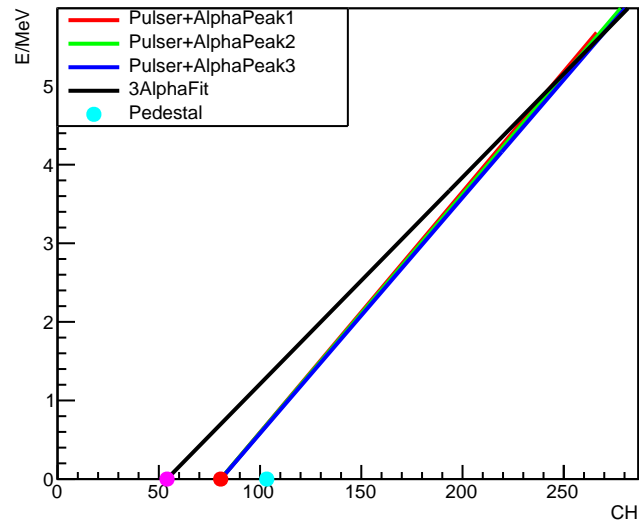
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH01



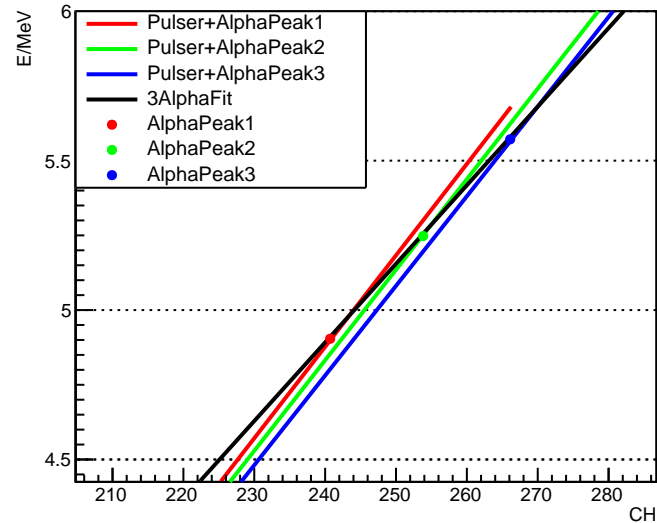
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH02



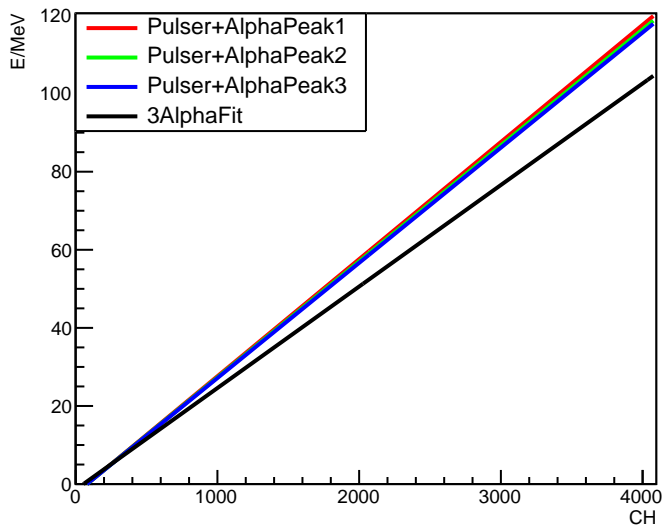
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH02



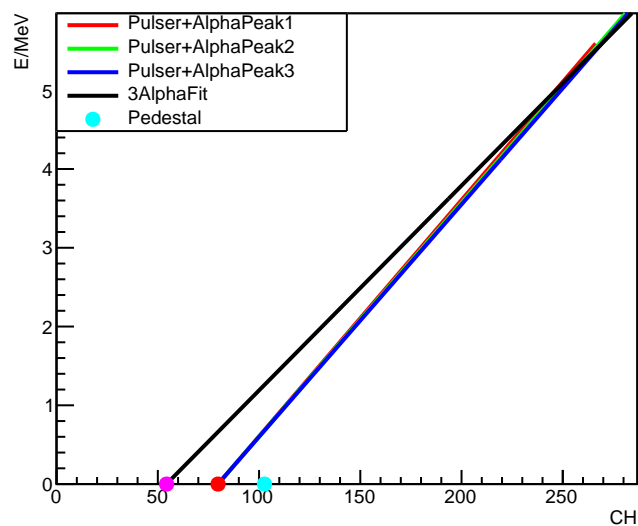
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH02



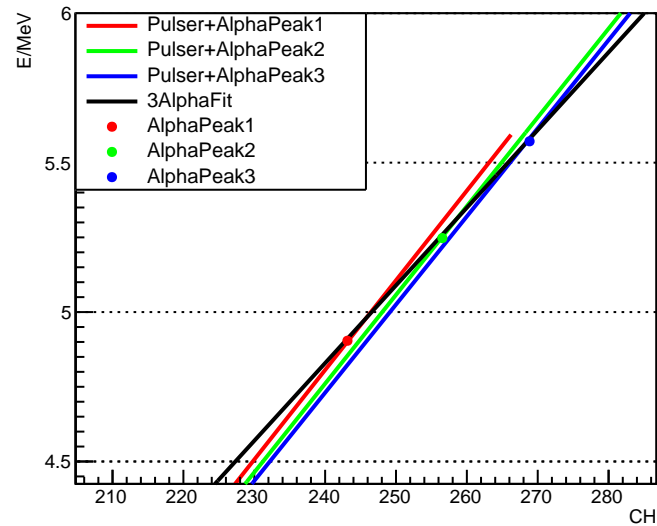
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH03



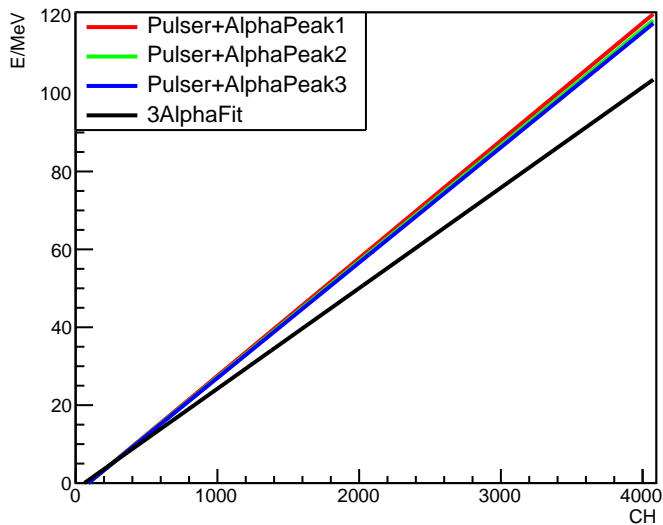
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH03



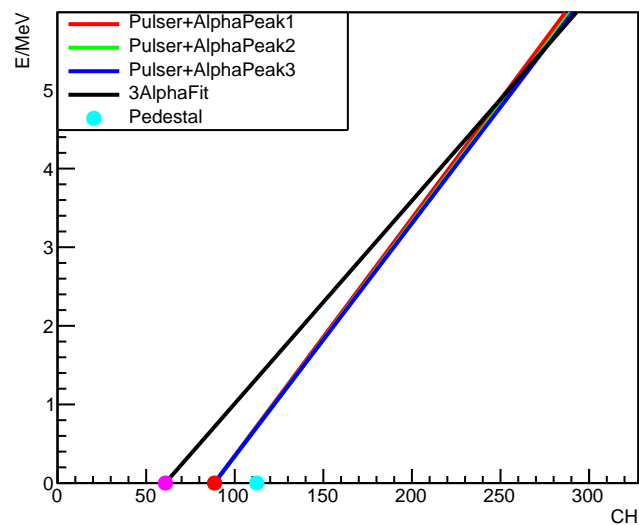
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH03



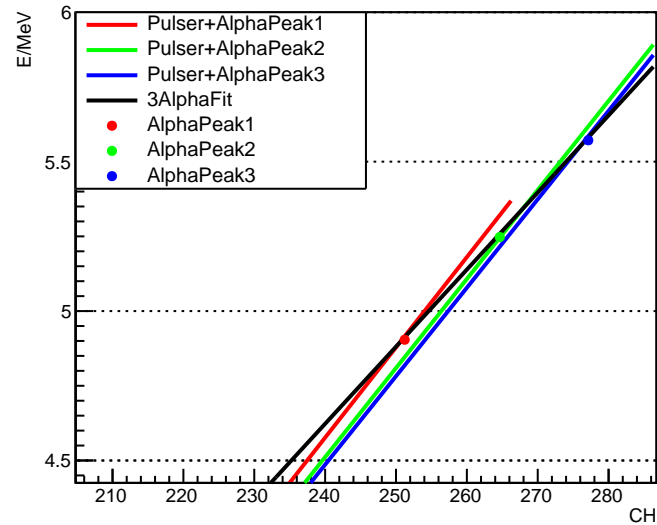
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH04



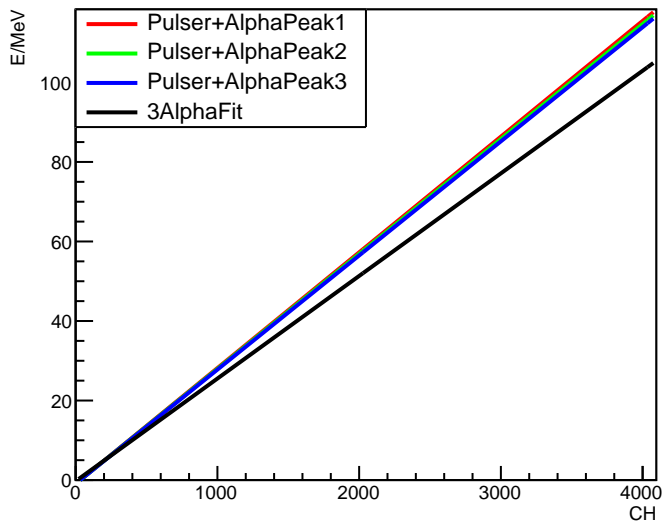
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH04



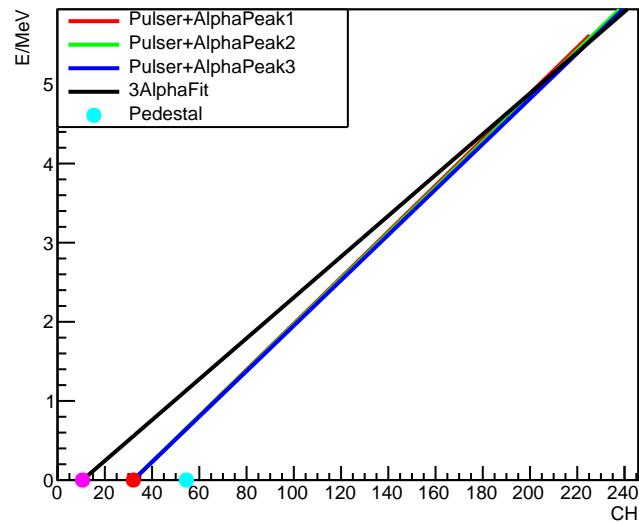
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH04



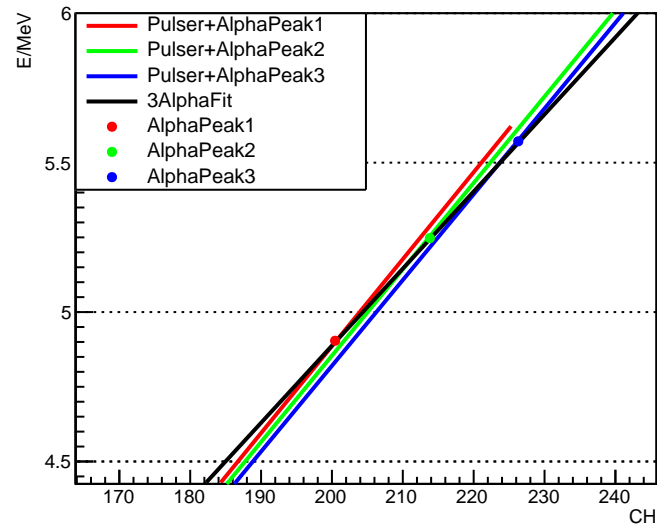
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH05



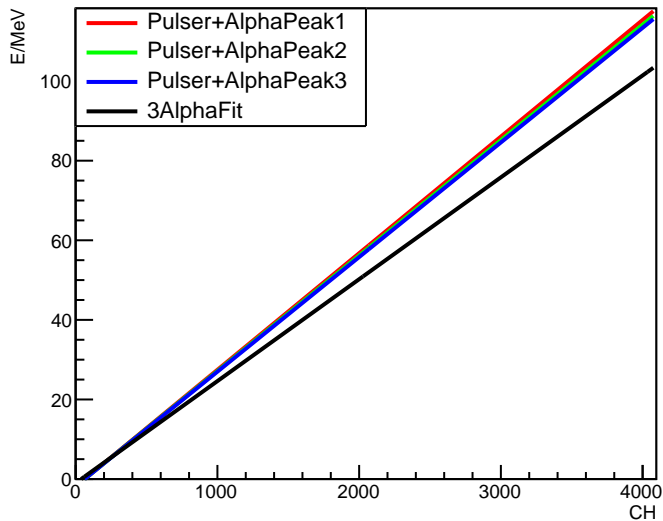
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH05



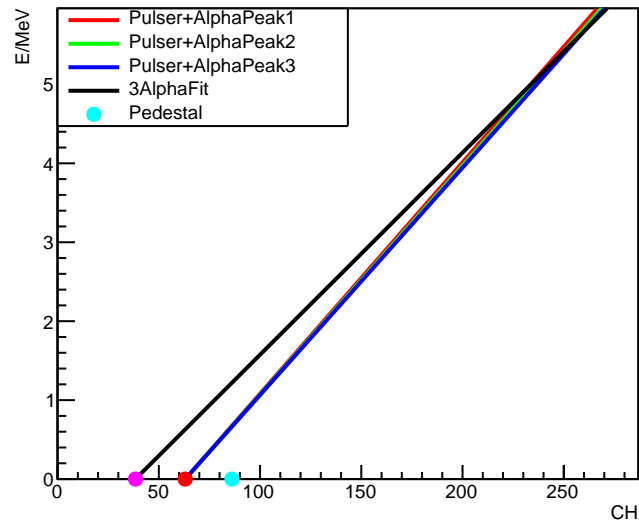
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH05



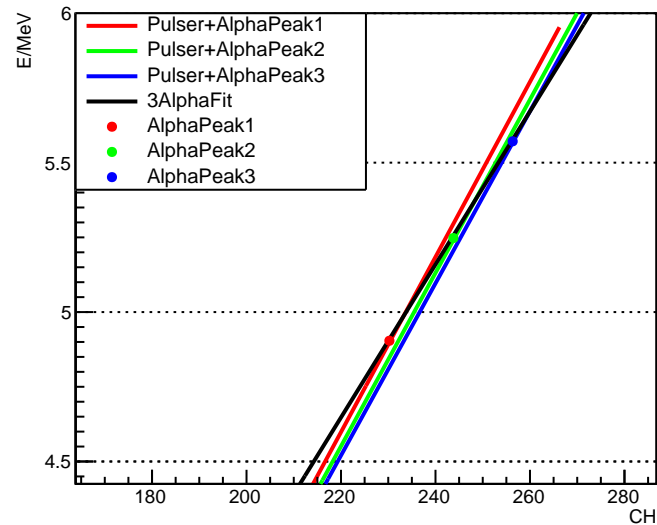
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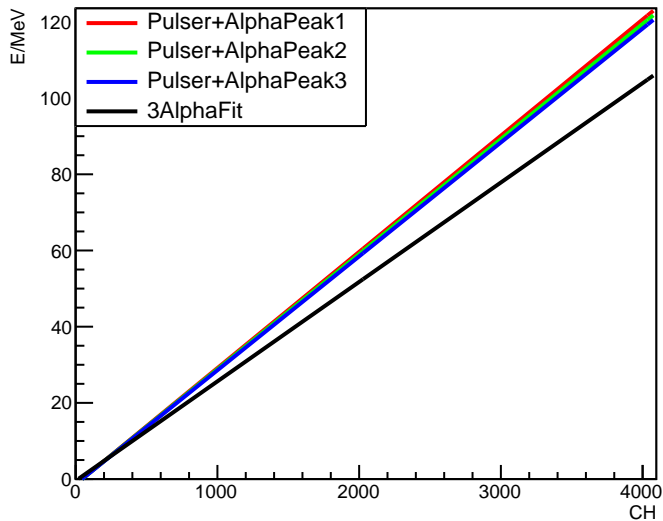
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH06



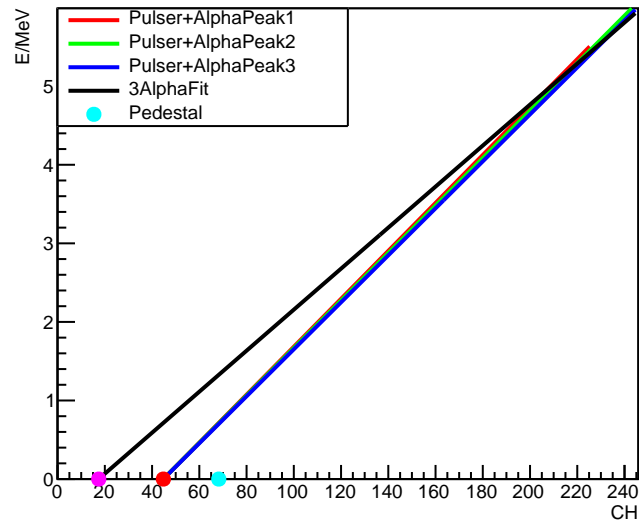
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH06



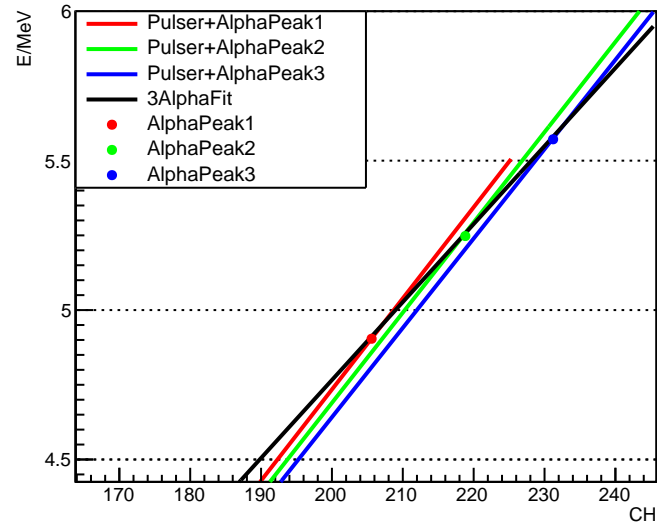
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH07



SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH07

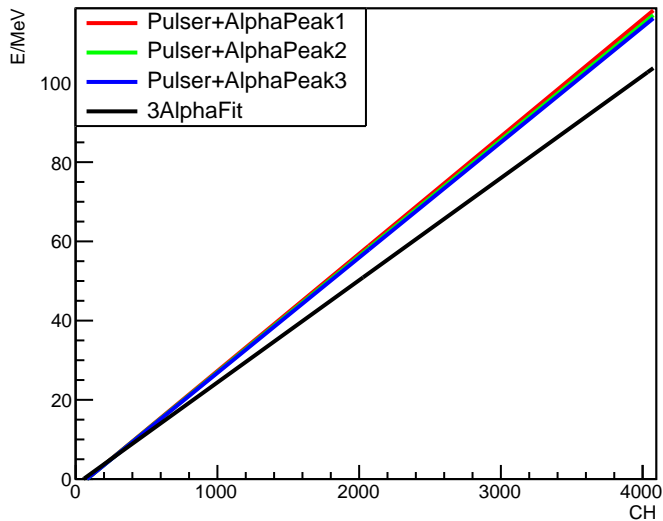


SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH07

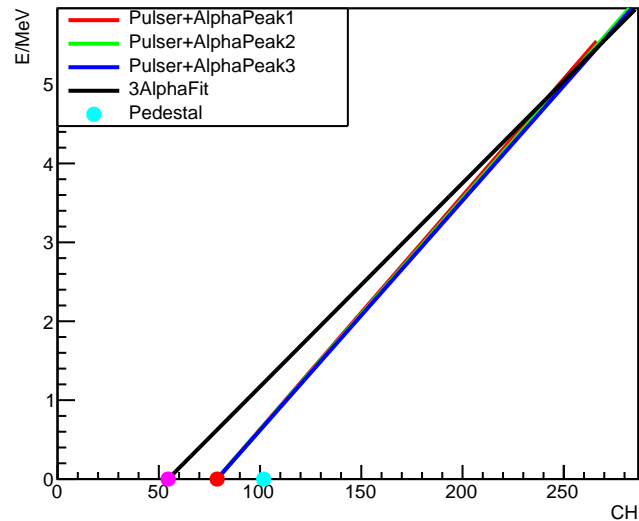




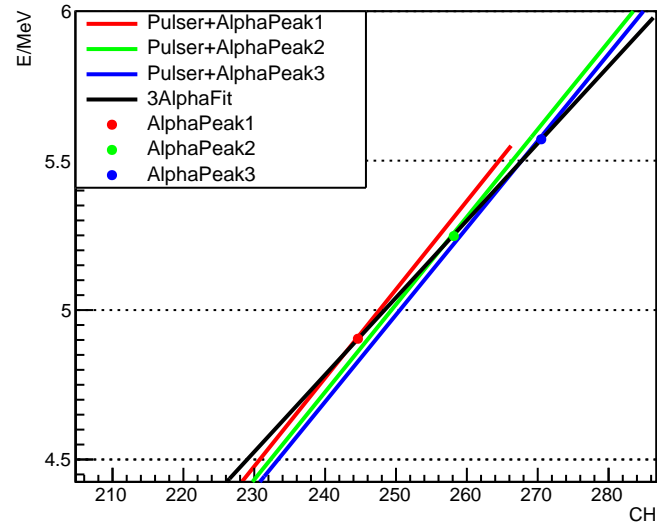
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH08



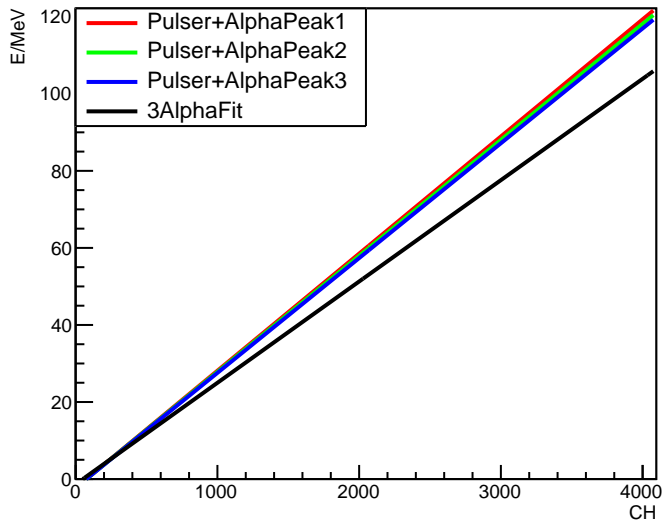
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH08



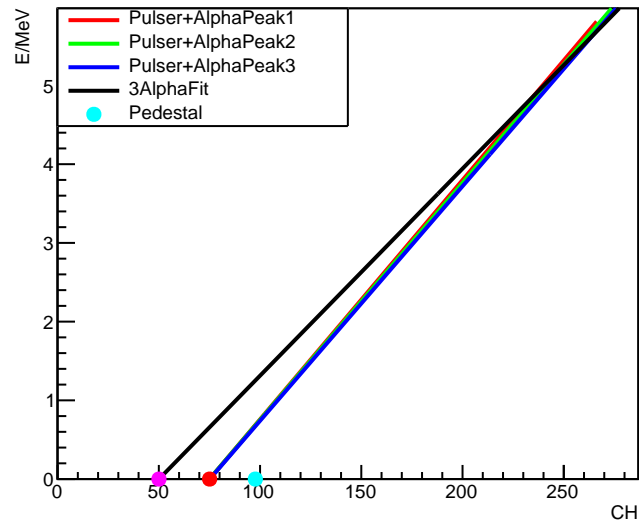
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH08



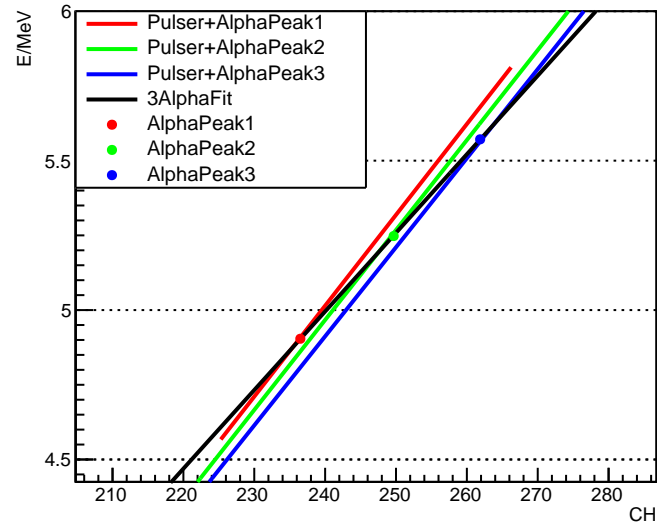
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH09



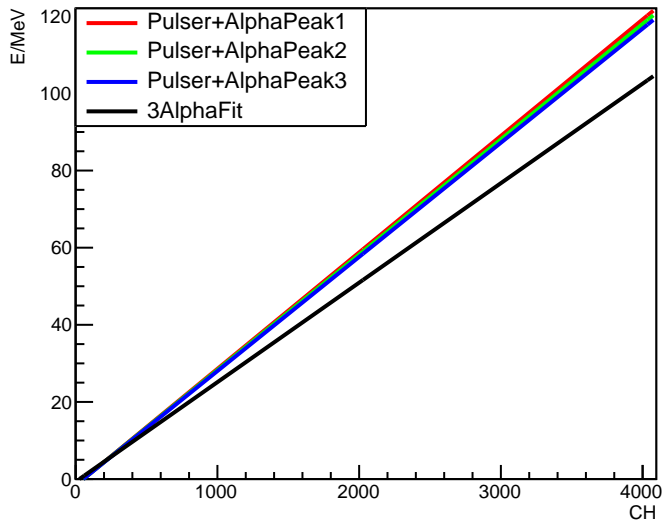
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH09



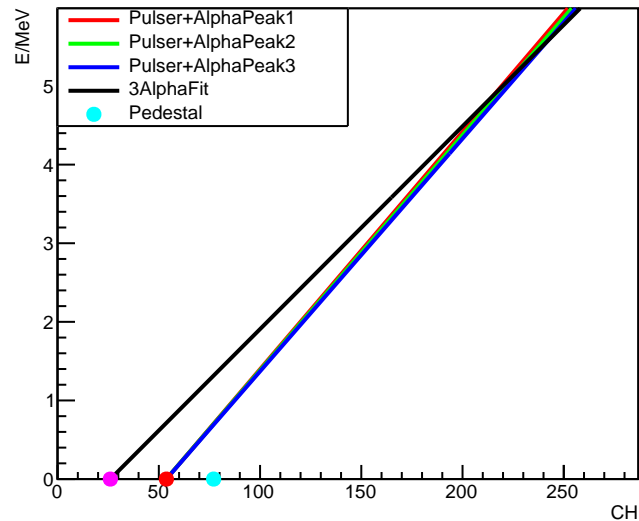
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH09



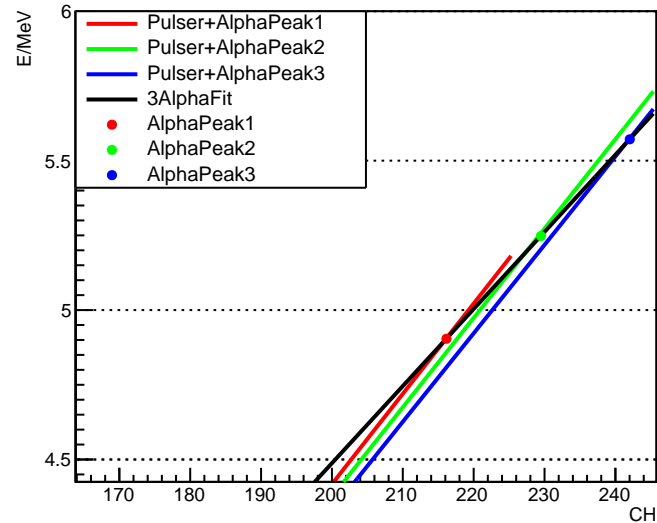
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH10



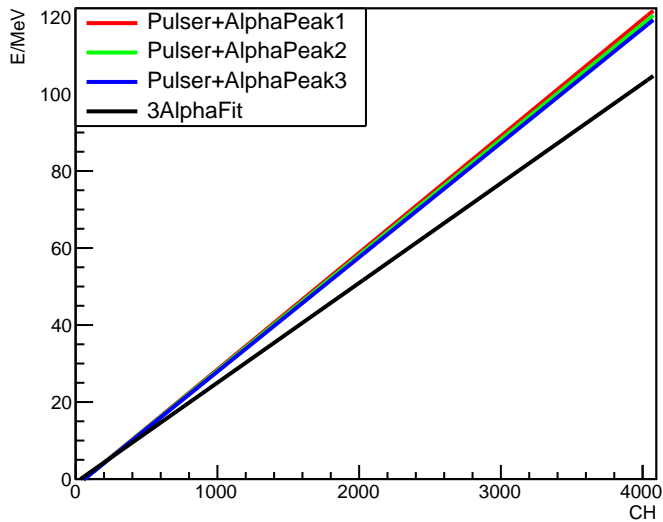
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH10



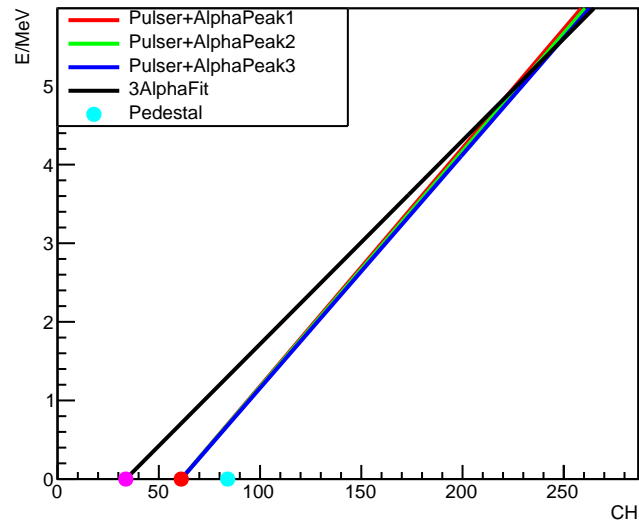
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH10



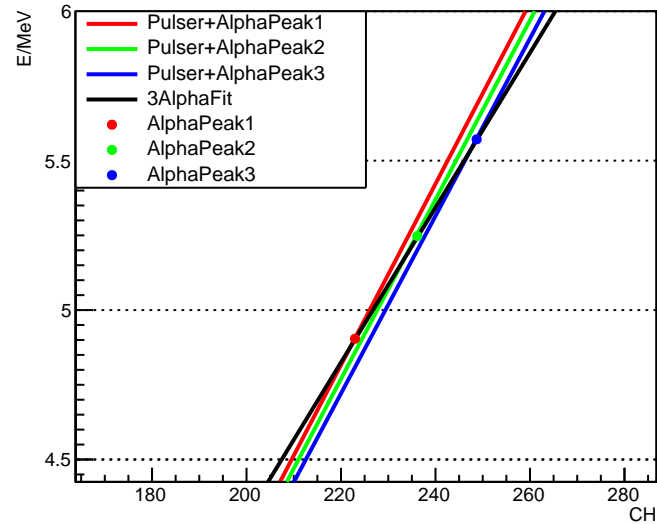
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH11



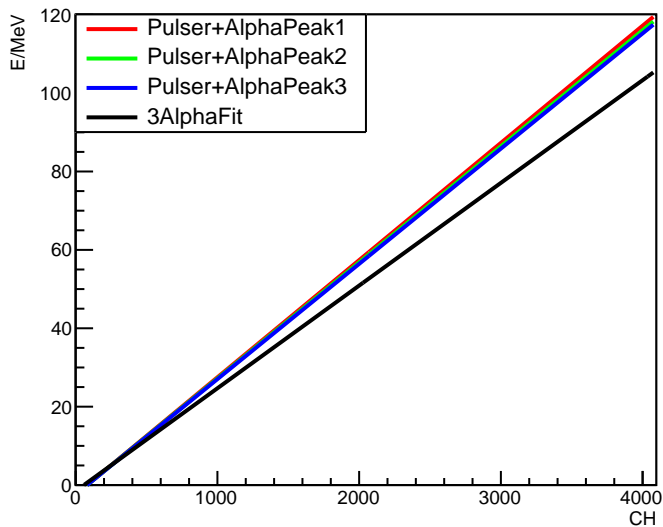
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH11



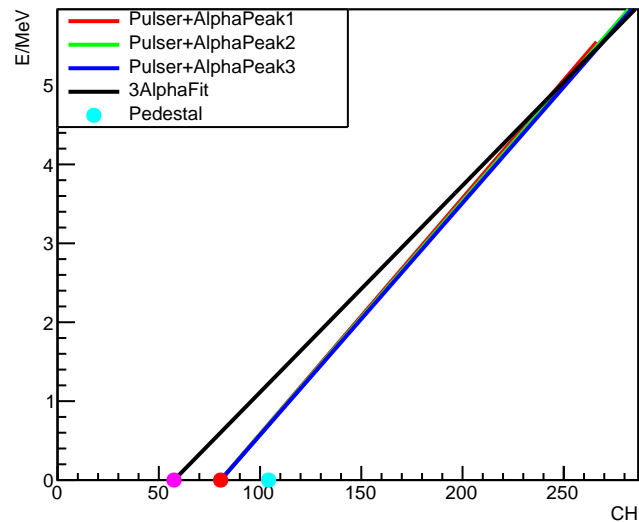
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH11



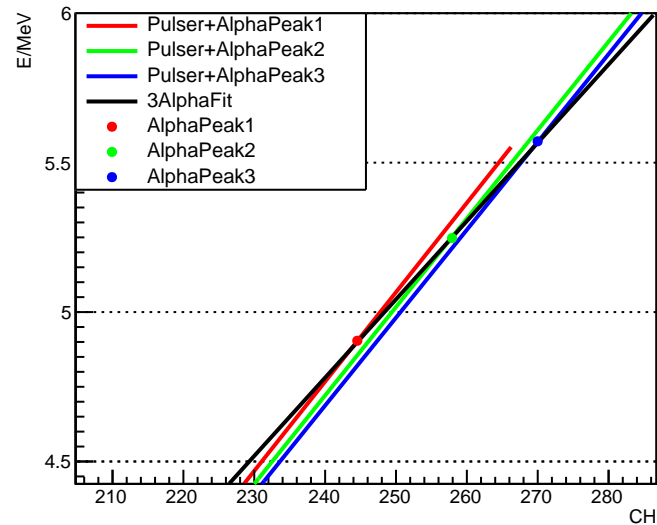
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH12



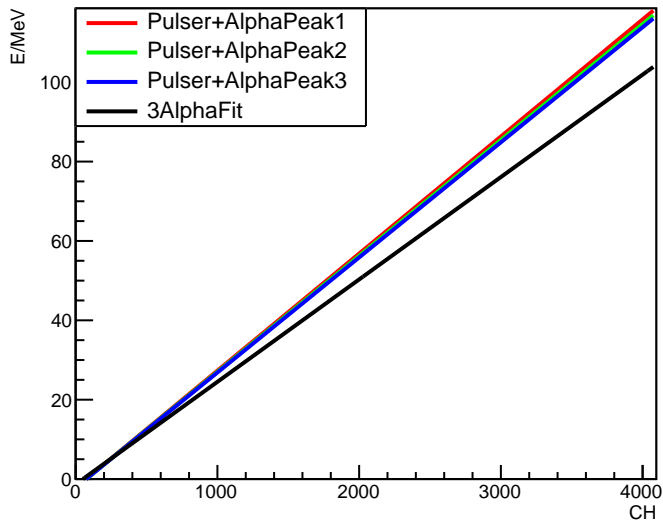
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH12



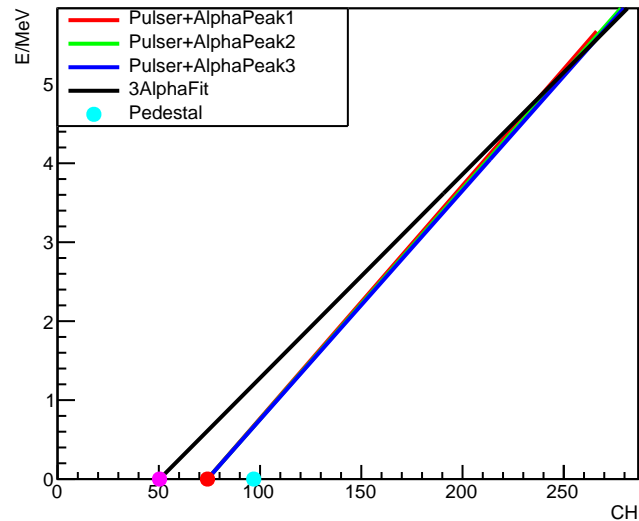
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH12



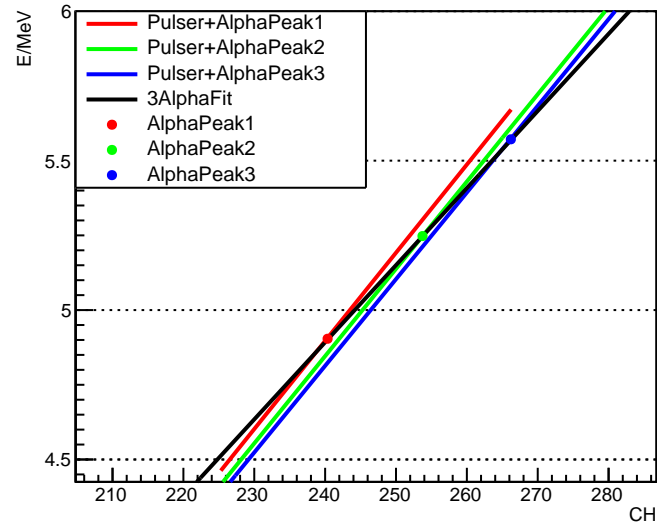
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH13



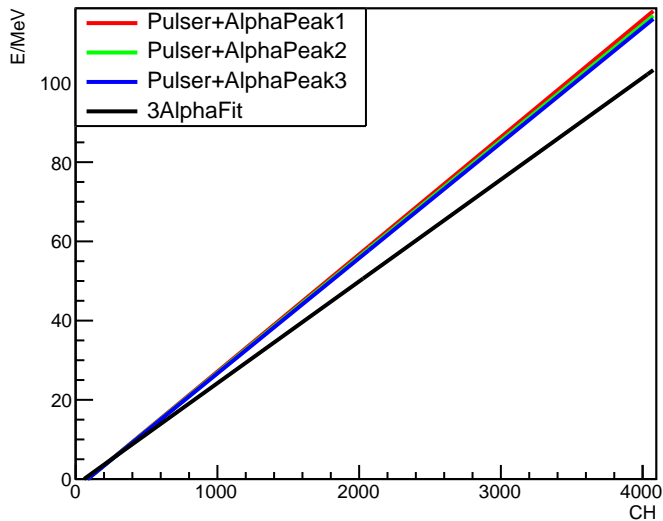
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH13



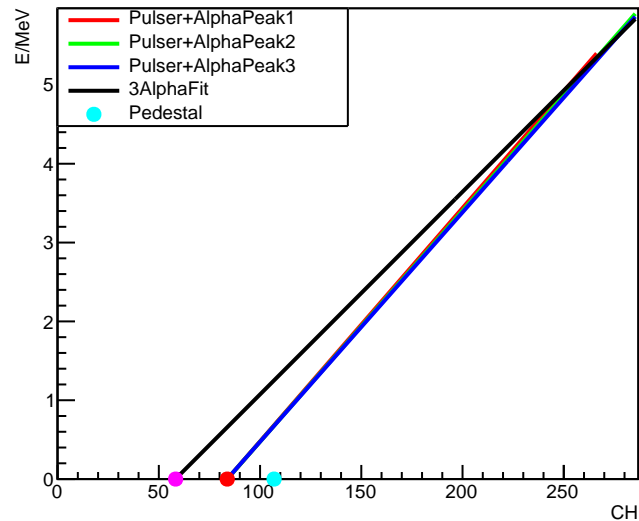
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH13



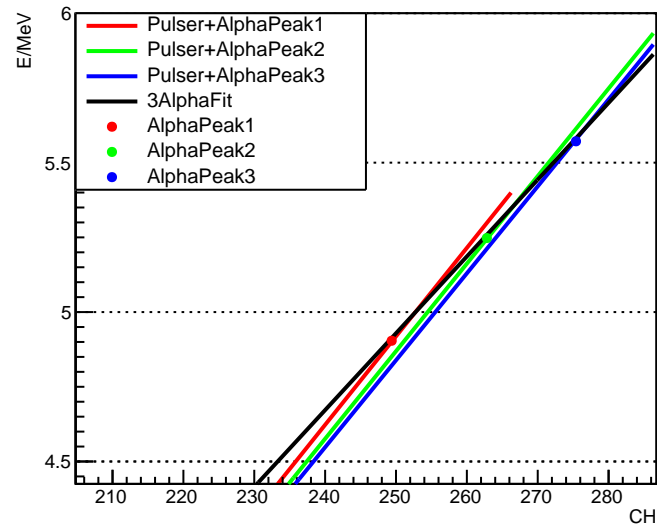
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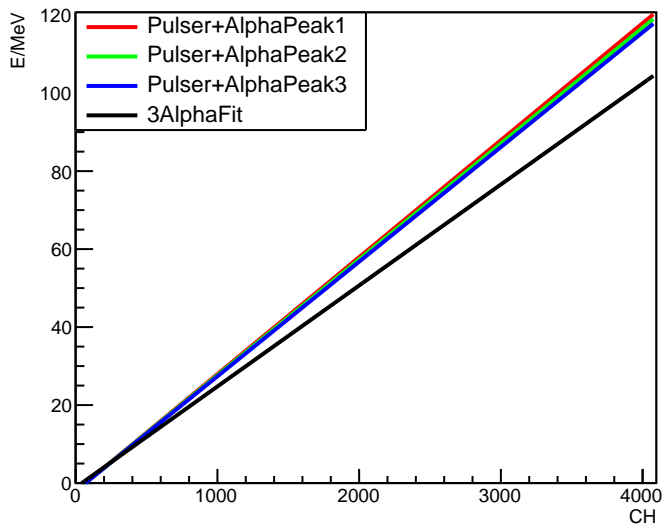
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH14



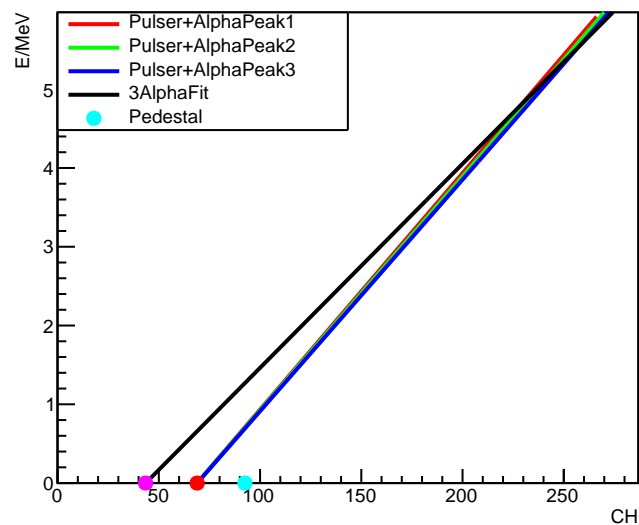
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH14



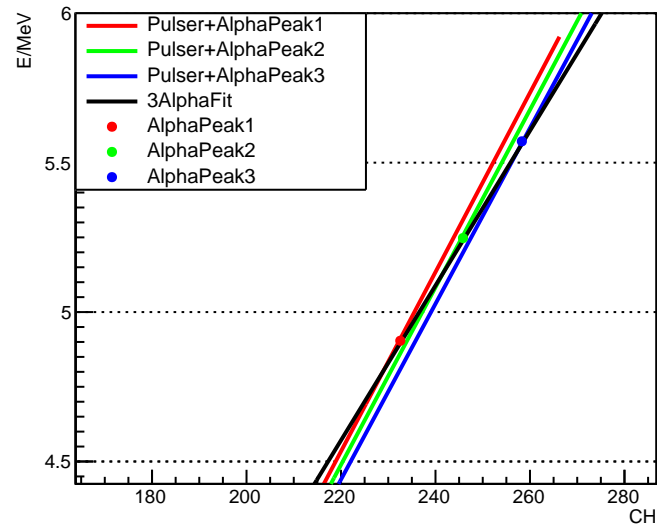
SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH15



SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH15

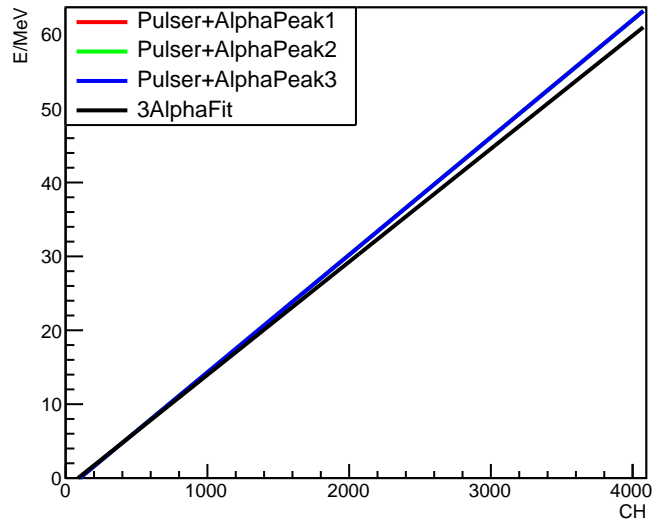


SSD3\_L2B\_Switch\_AlphaCali33\_48\_CH15

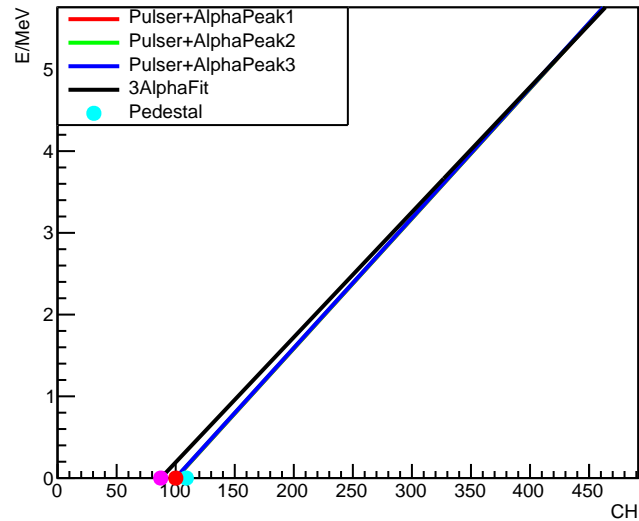




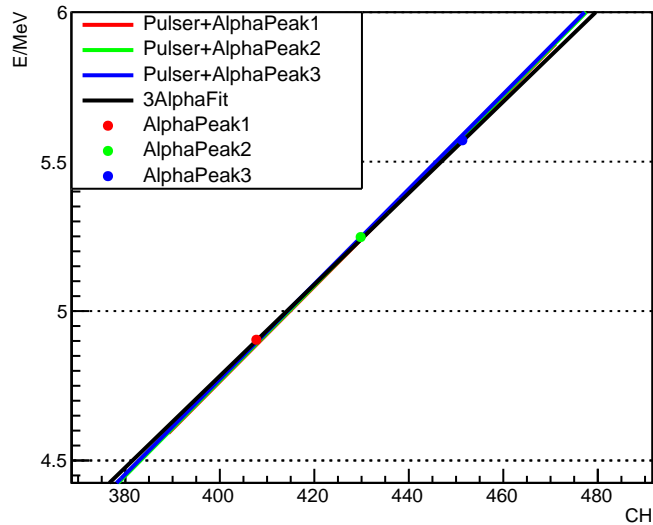
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH00



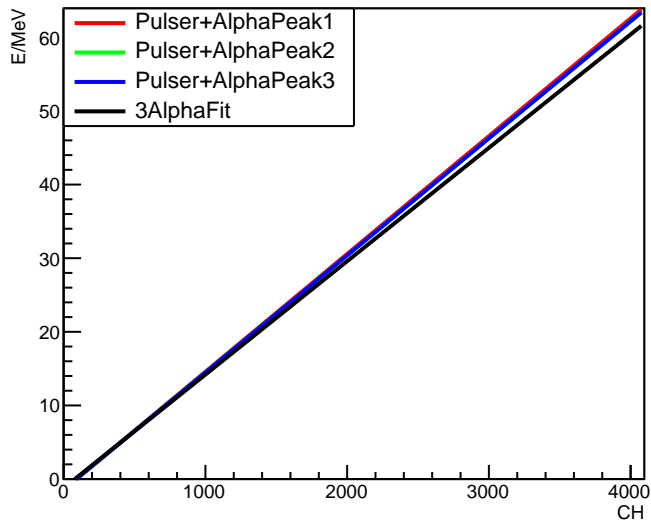
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH00



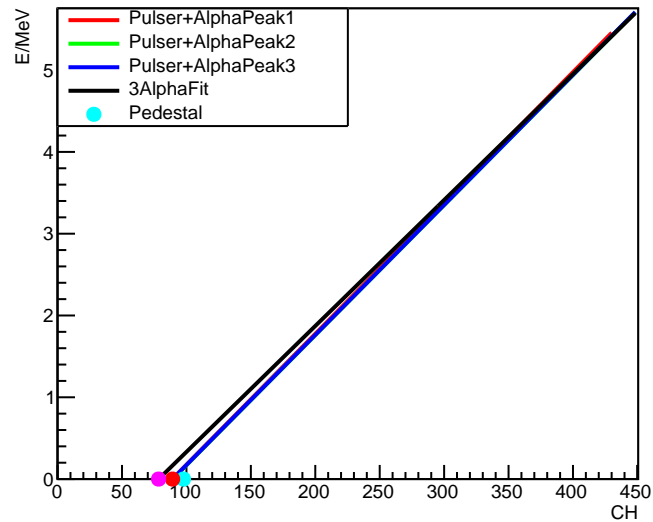
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH00



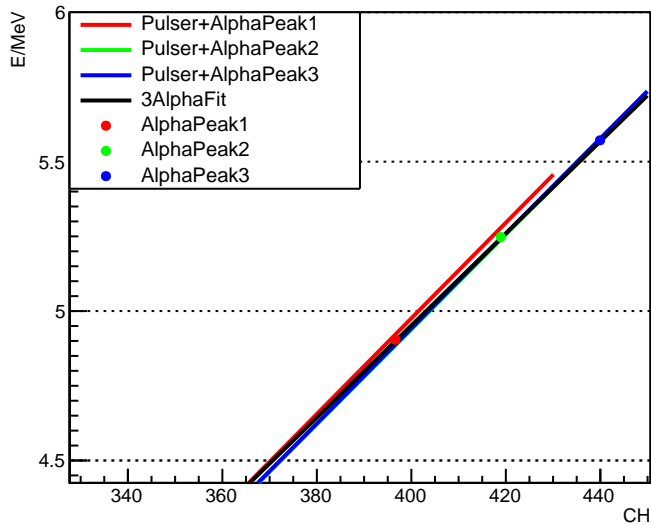
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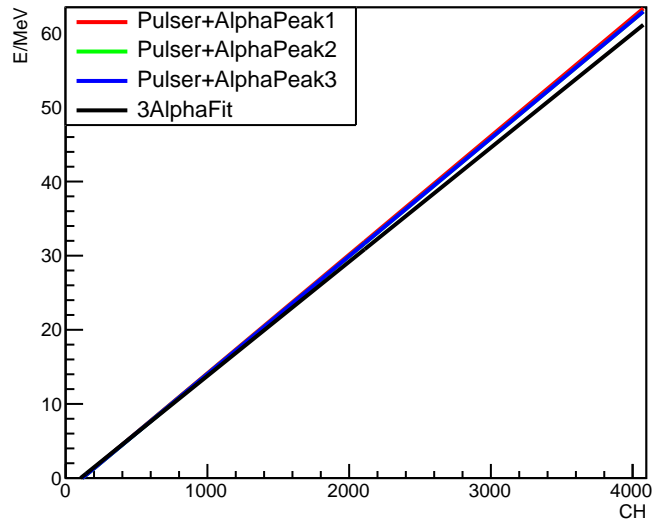
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH01



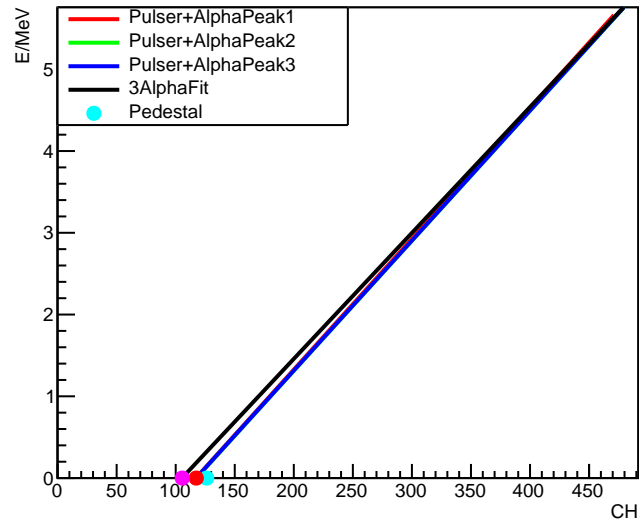
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH01



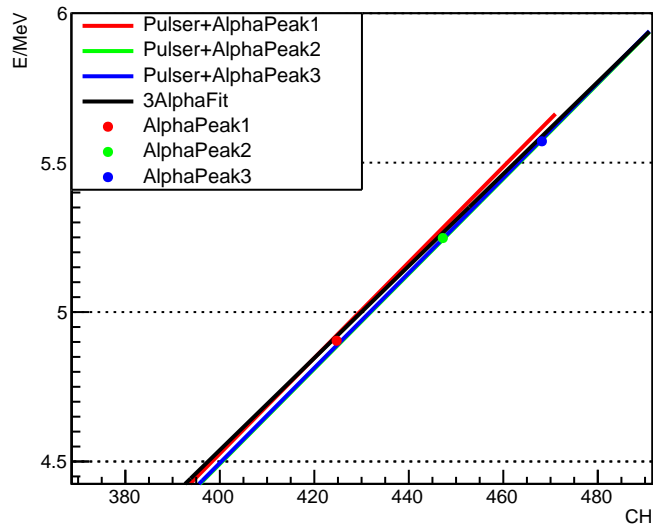
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH02



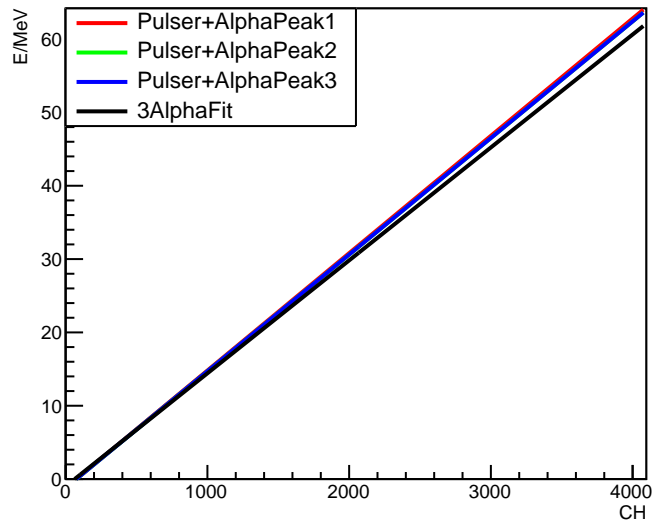
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH02



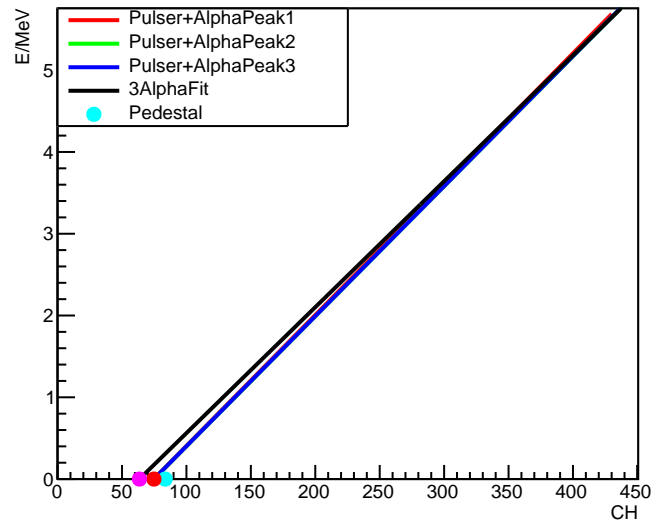
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH02



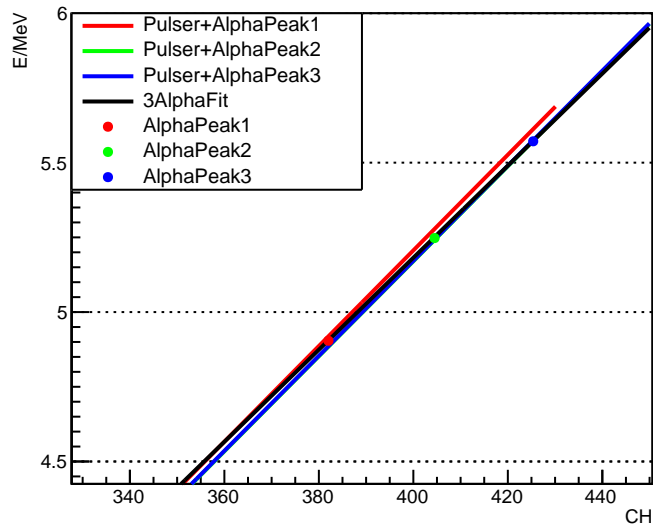
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH03



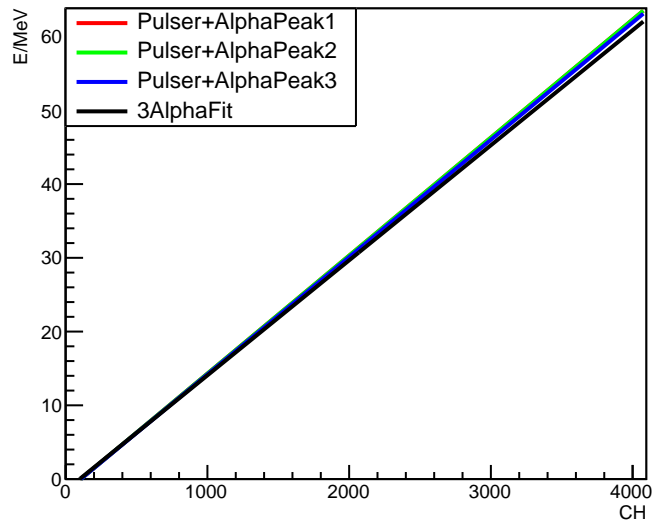
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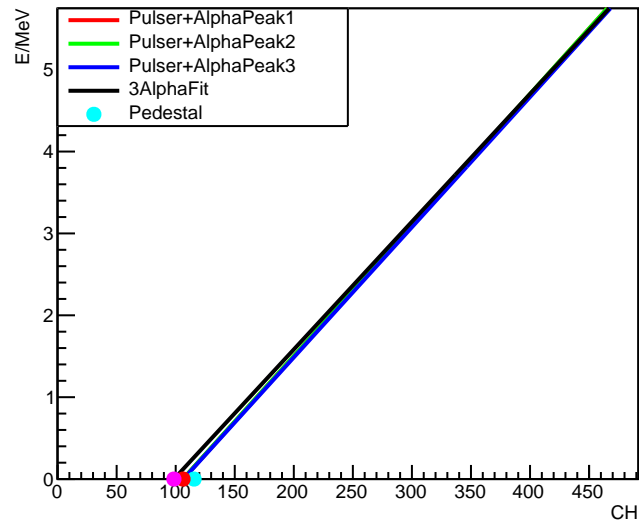
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH03



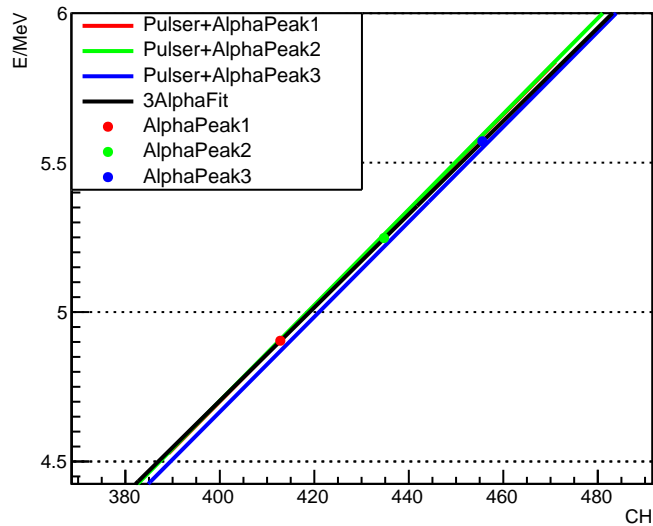
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH04



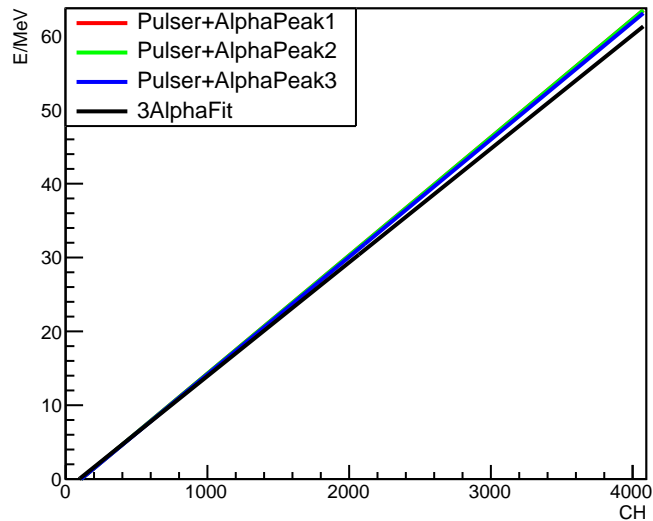
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH04



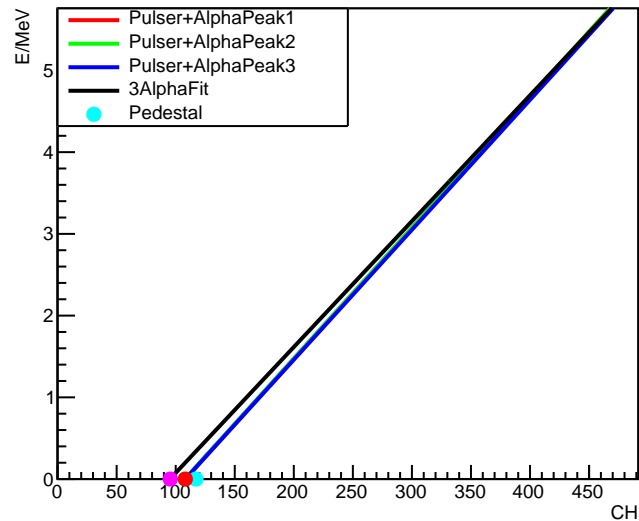
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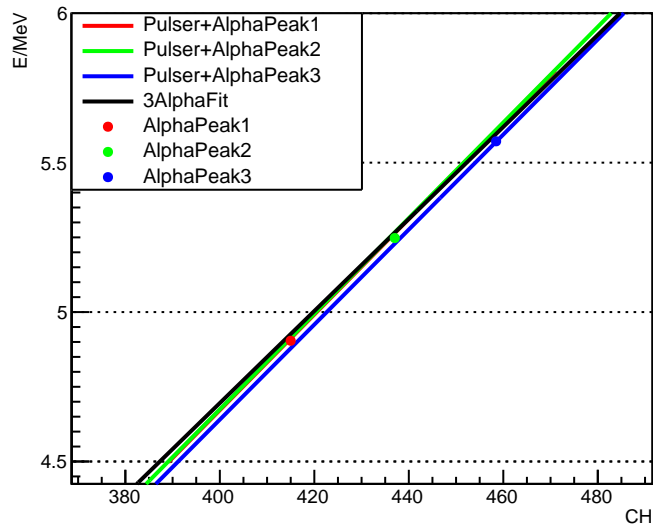
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH05



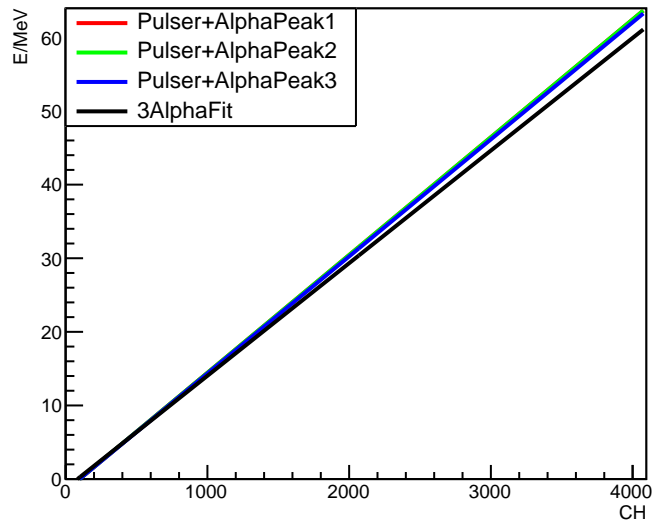
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH05



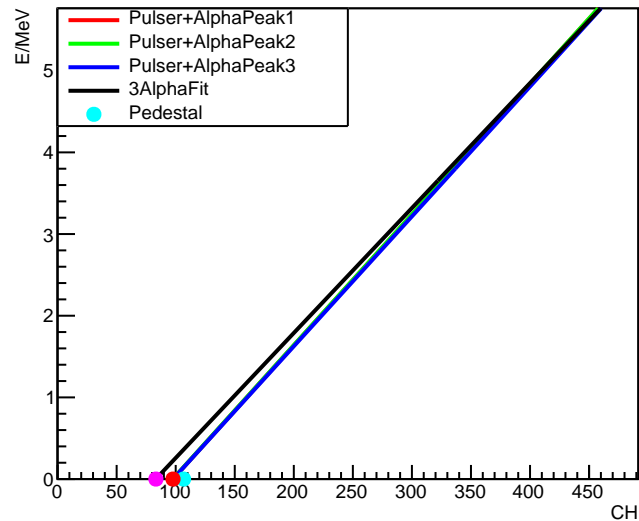
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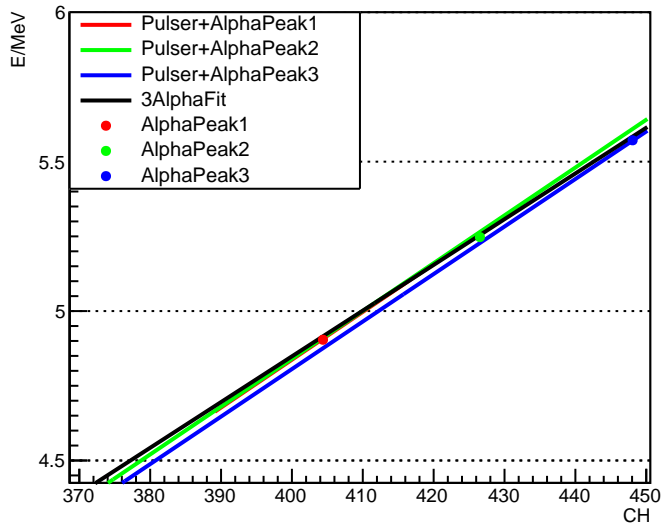
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH06



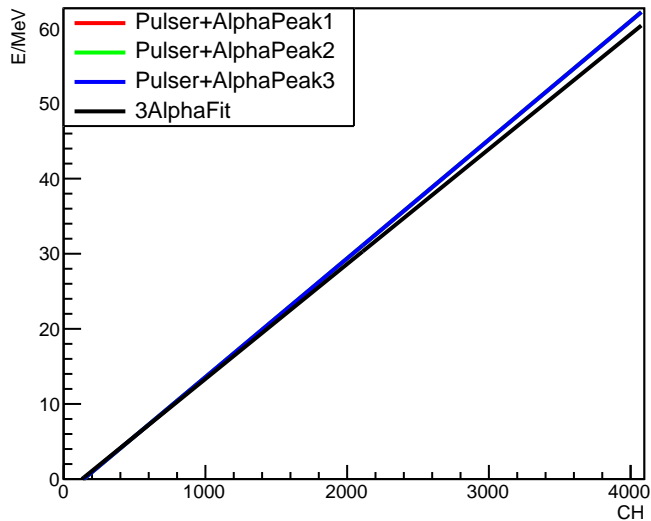
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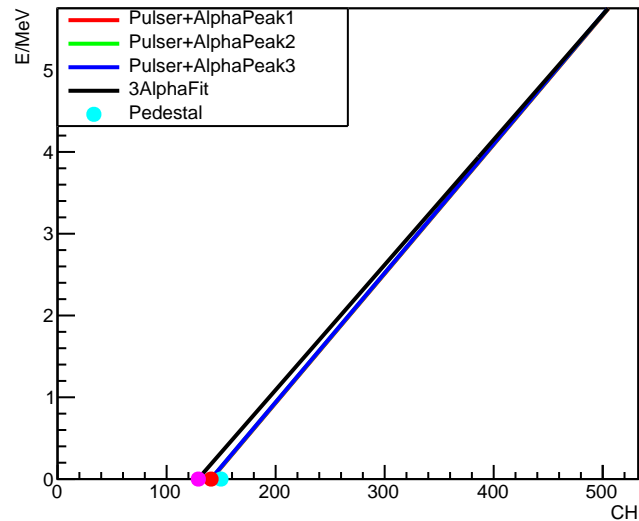
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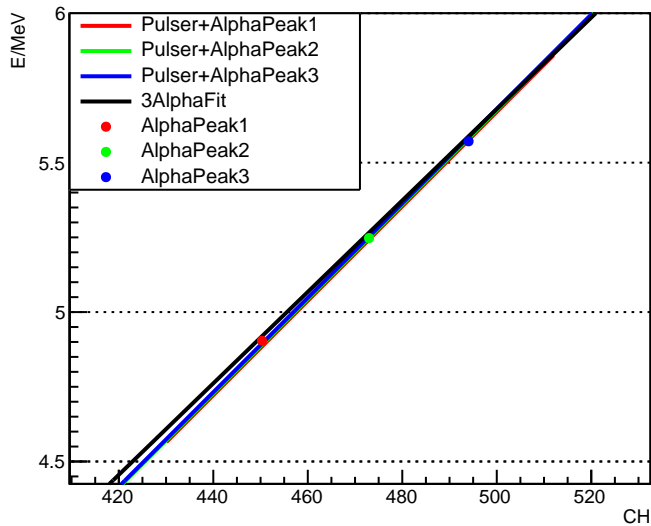
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH07



SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH07

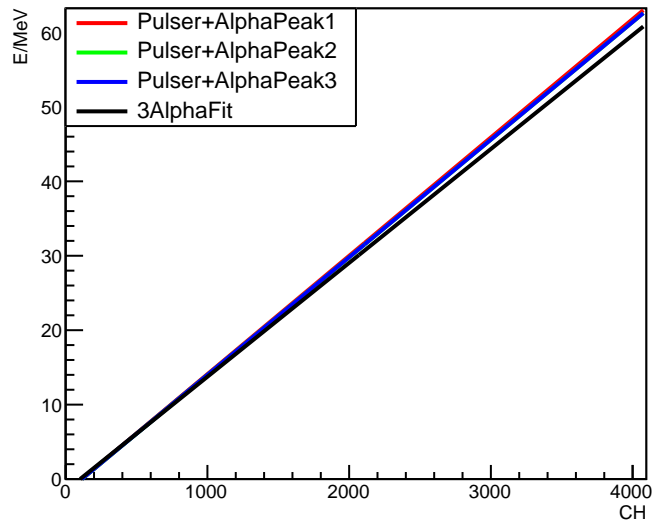


SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH07

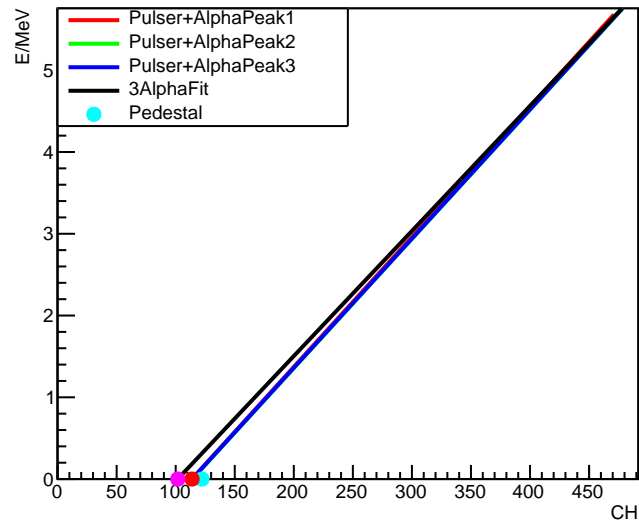




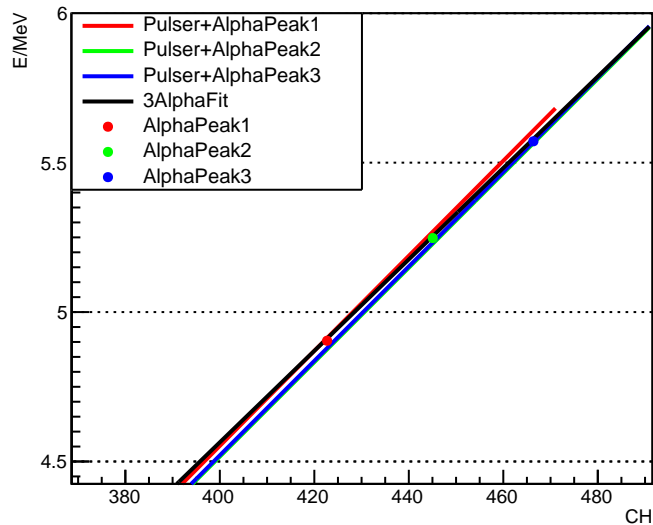
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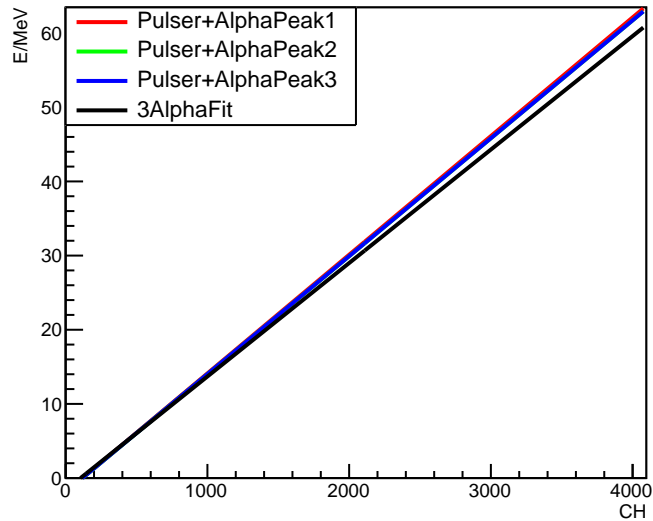
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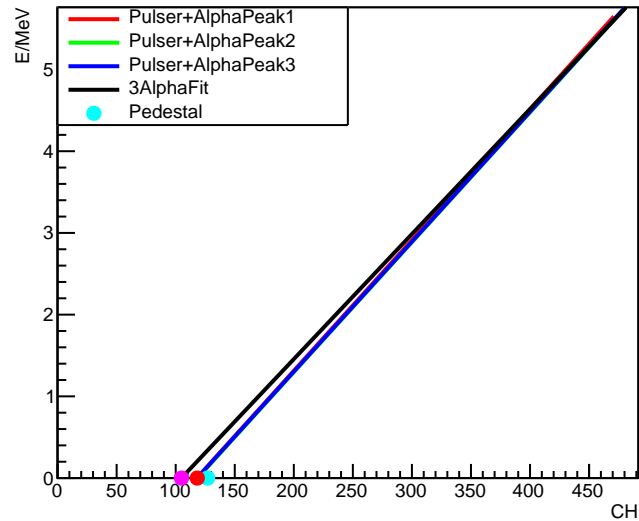
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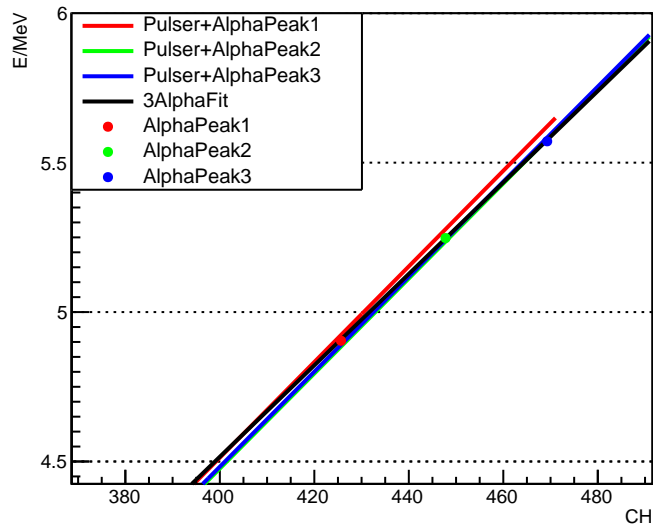
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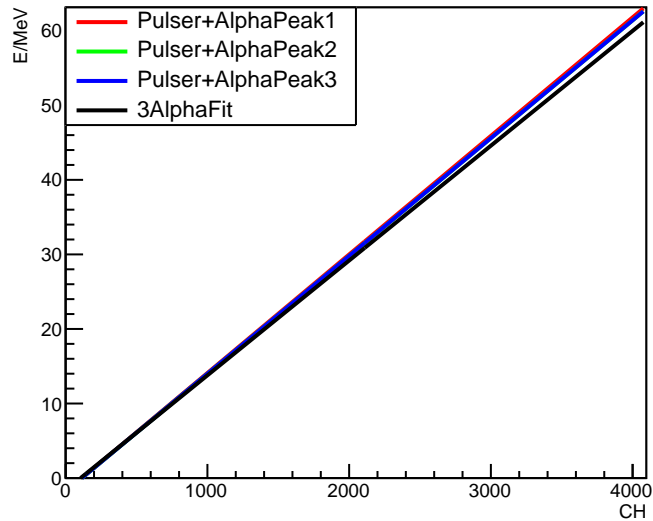
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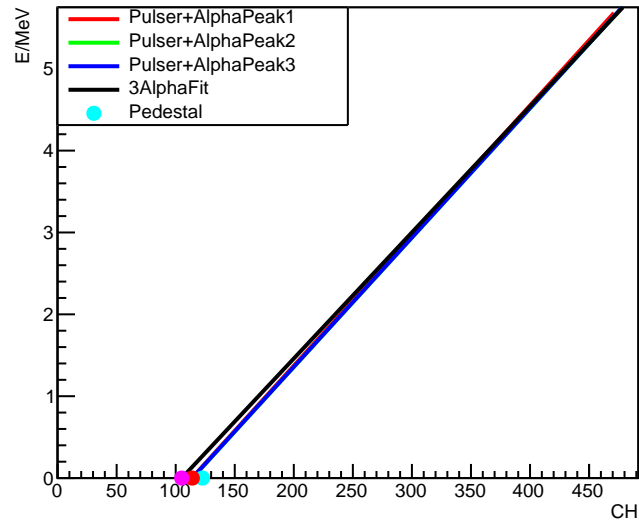
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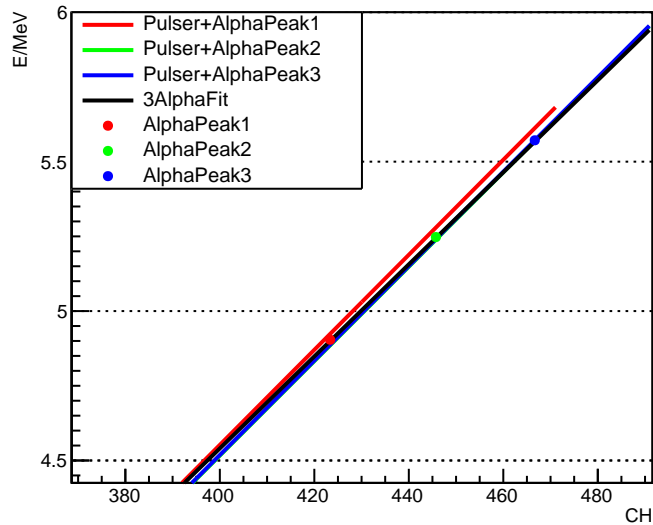
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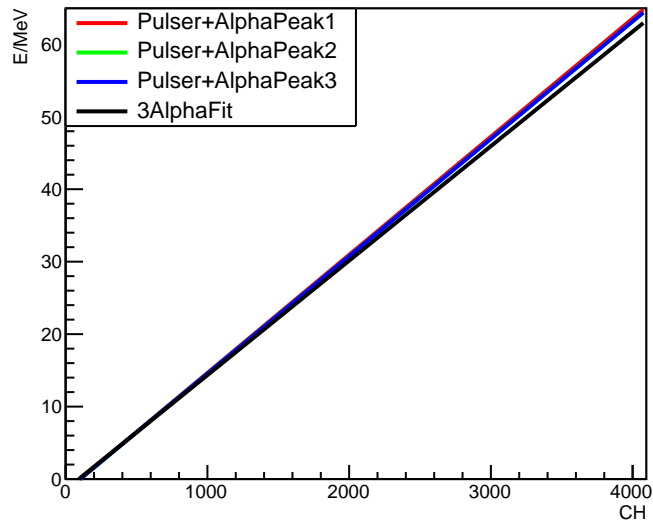
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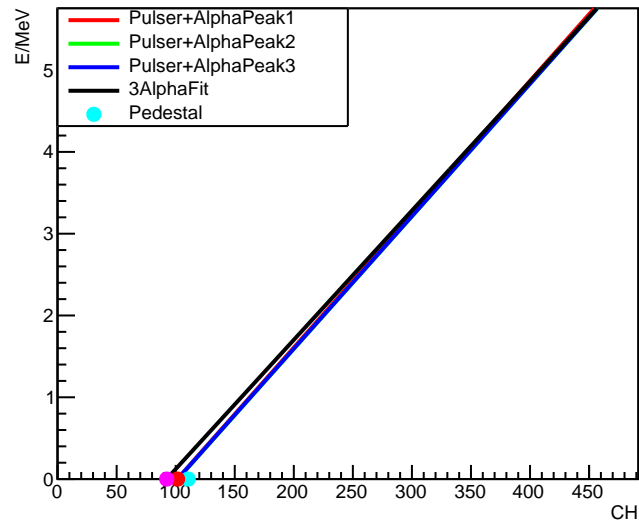
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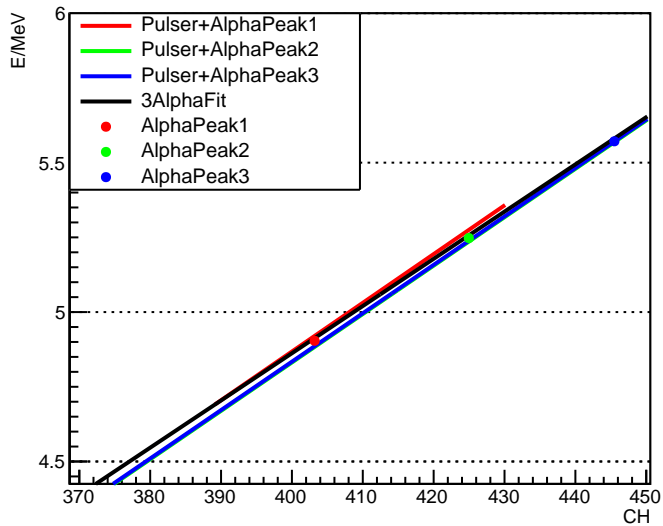
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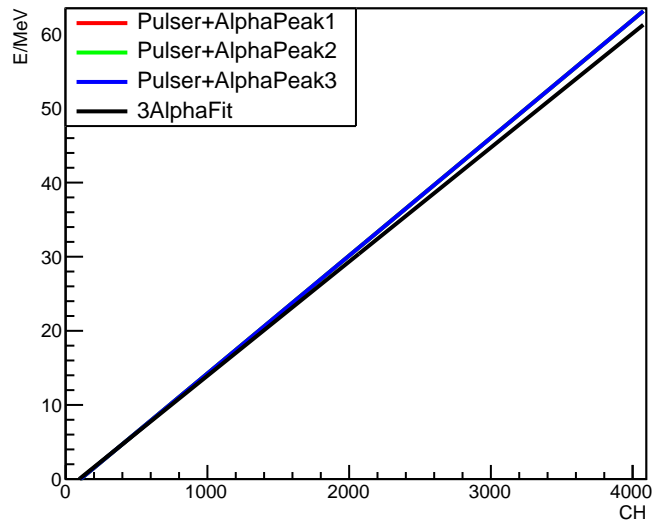
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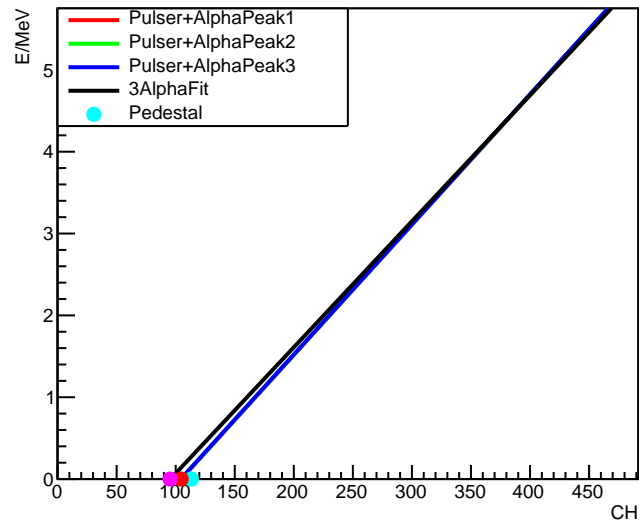
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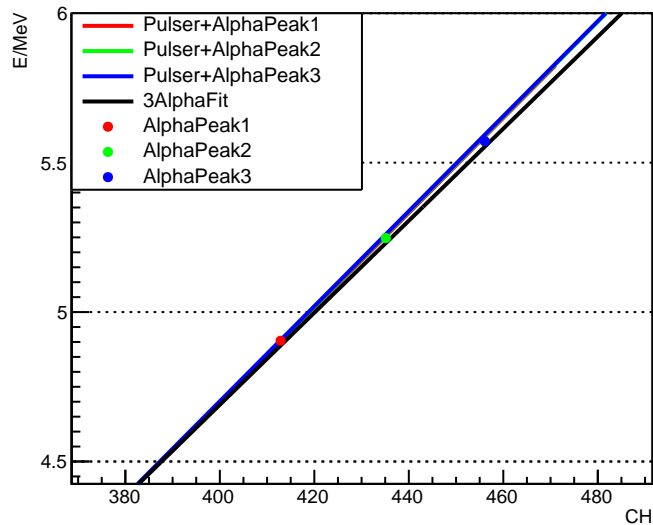
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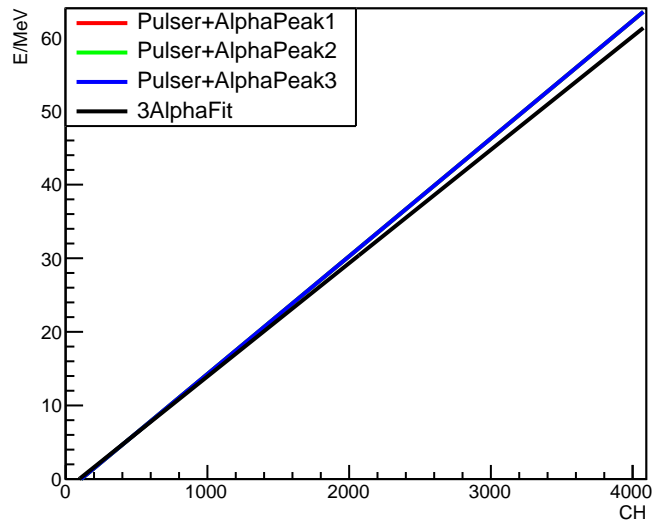
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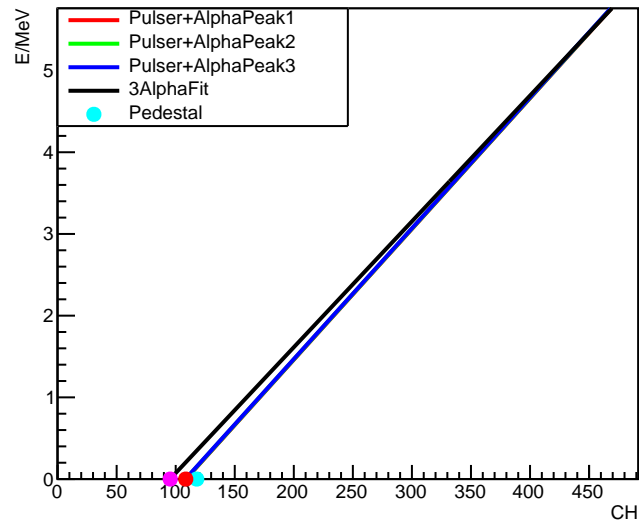
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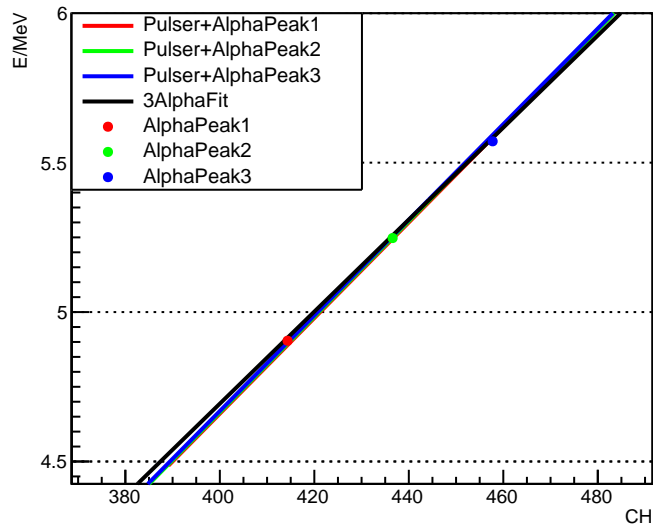
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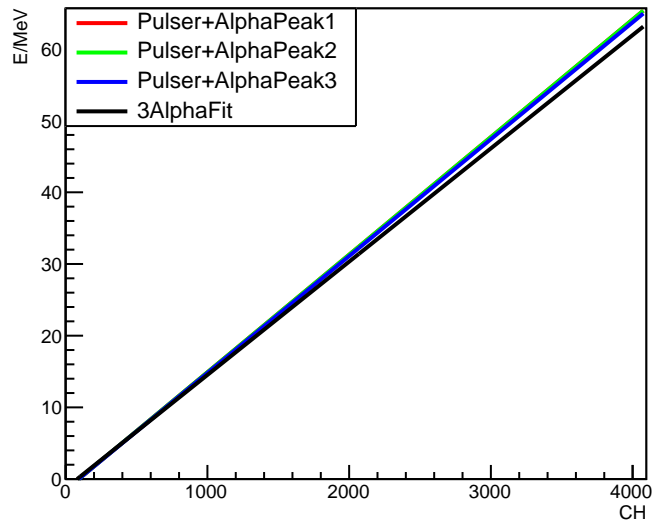
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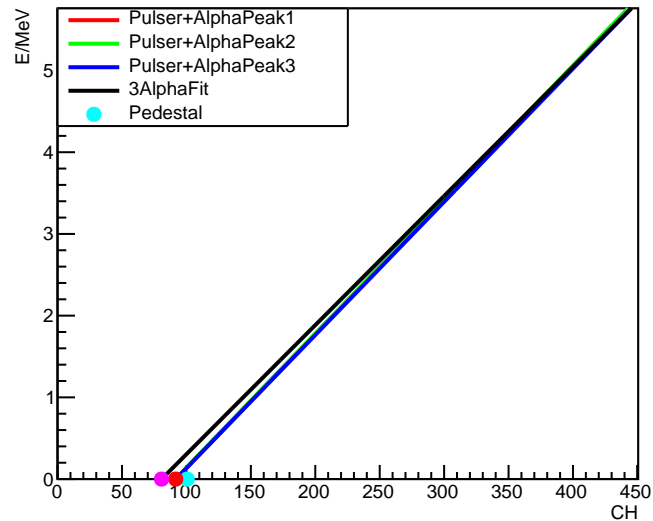
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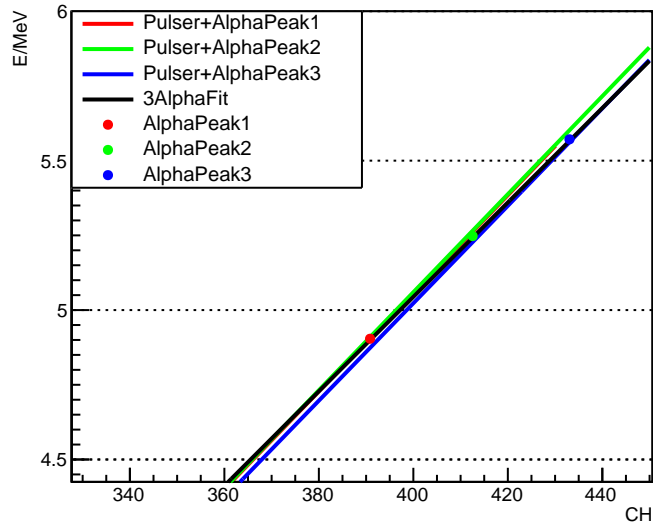
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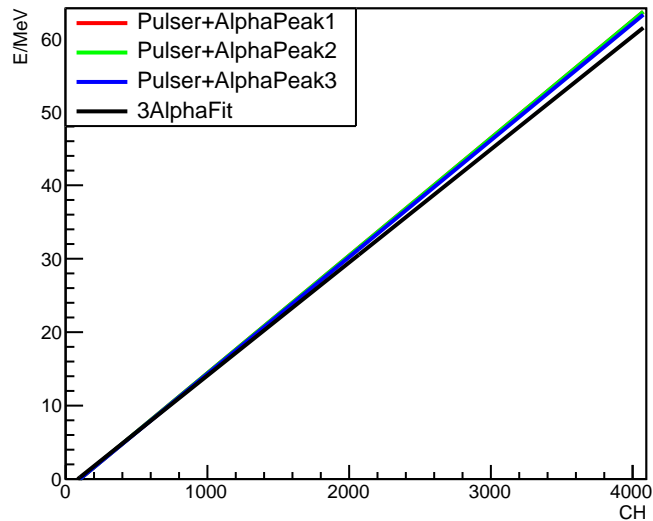
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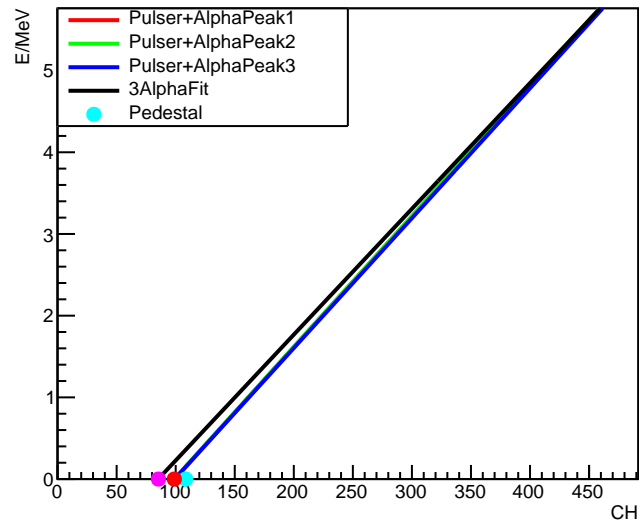
SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH14



SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH15



SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH15



SSD4\_L2B\_Switch\_AlphaCali33\_48\_CH15

