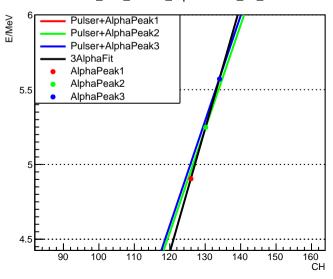
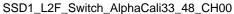
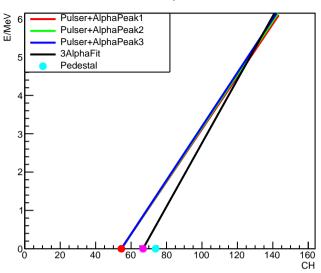
SSD1_L2F_Switch_AlphaCali33_48_CH00

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

SSD1_L2F_Switch_AlphaCali33_48_CH00







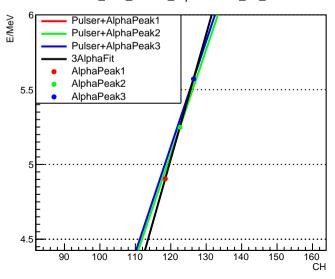
SSD1_L2F_Switch_AlphaCali33_48_CH01

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

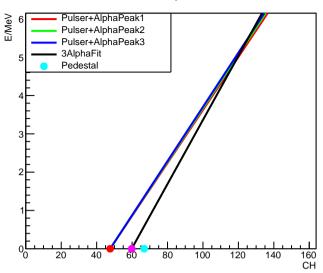
SSD1_L2F_Switch_AlphaCali33_48_CH01

3000

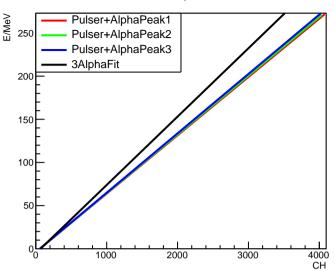
1000



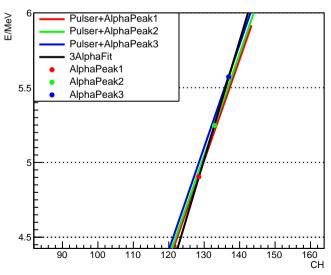


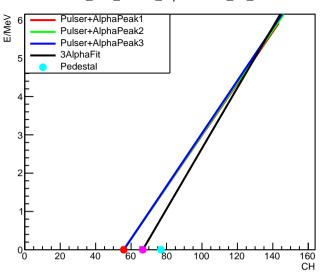


SSD1_L2F_Switch_AlphaCali33_48_CH02

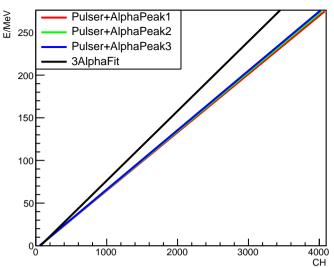


SSD1_L2F_Switch_AlphaCali33_48_CH02

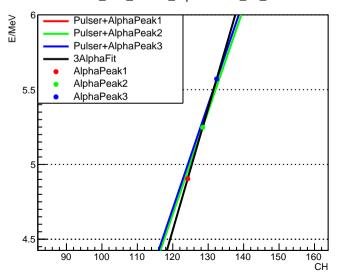




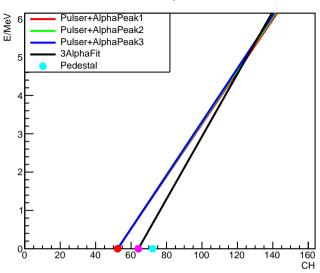
SSD1_L2F_Switch_AlphaCali33_48_CH03



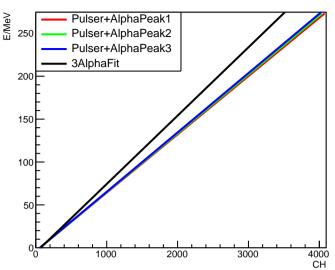
SSD1_L2F_Switch_AlphaCali33_48_CH03



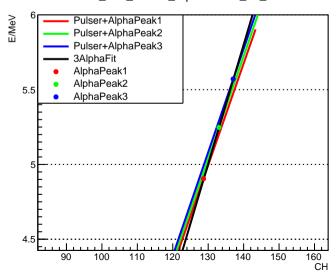


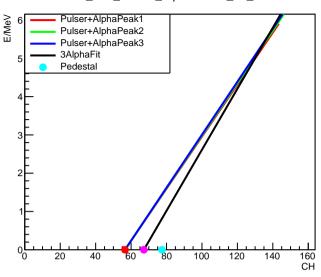


SSD1_L2F_Switch_AlphaCali33_48_CH04

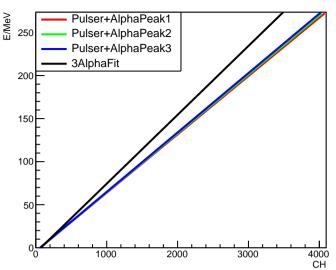


SSD1_L2F_Switch_AlphaCali33_48_CH04

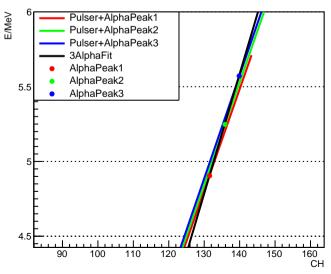


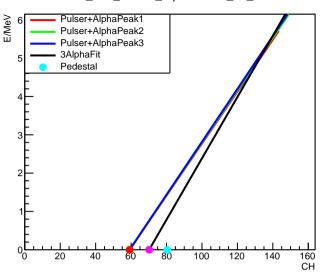


SSD1_L2F_Switch_AlphaCali33_48_CH05

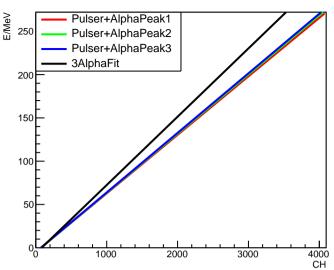


SSD1_L2F_Switch_AlphaCali33_48_CH05

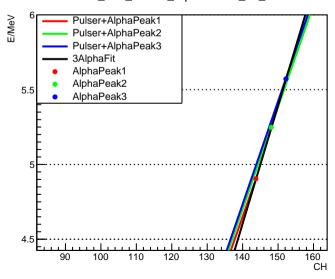


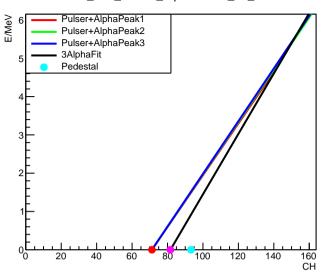


SSD1_L2F_Switch_AlphaCali33_48_CH06

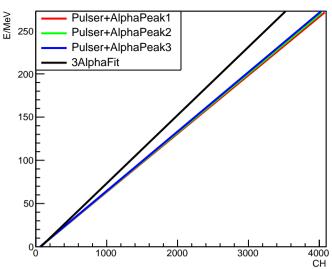


SSD1_L2F_Switch_AlphaCali33_48_CH06

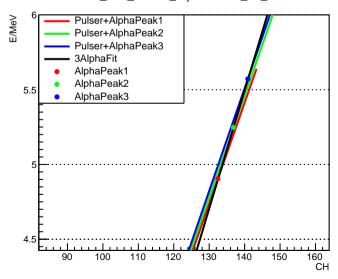




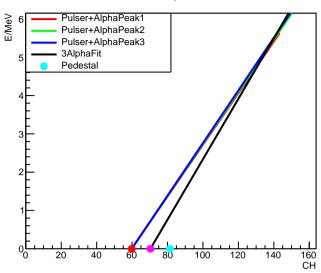
SSD1_L2F_Switch_AlphaCali33_48_CH07



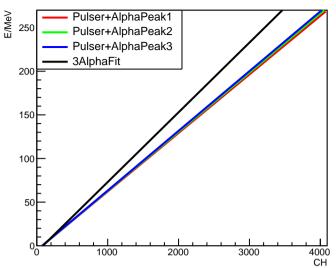
SSD1_L2F_Switch_AlphaCali33_48_CH07



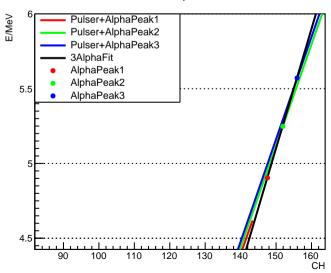




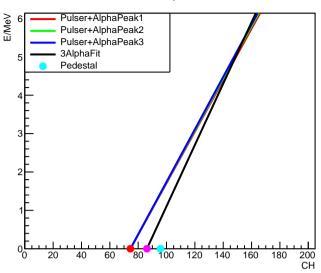
SSD1_L2F_Switch_AlphaCali33_48_CH08



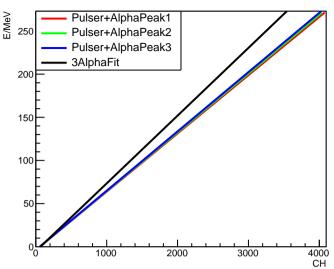
SSD1_L2F_Switch_AlphaCali33_48_CH08



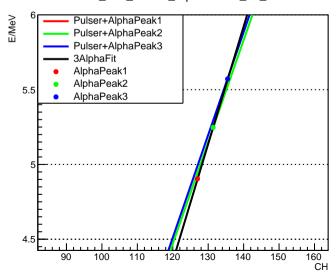


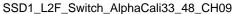


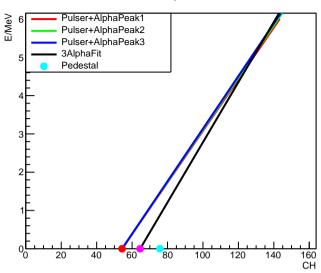
SSD1_L2F_Switch_AlphaCali33_48_CH09



SSD1_L2F_Switch_AlphaCali33_48_CH09

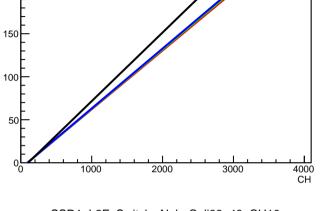




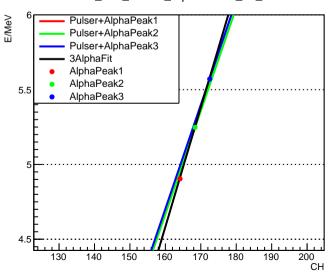


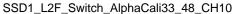
SSD1_L2F_Switch_AlphaCali33_48_CH10

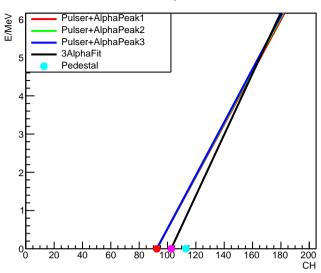
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit



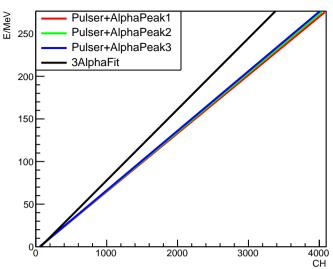
SSD1_L2F_Switch_AlphaCali33_48_CH10



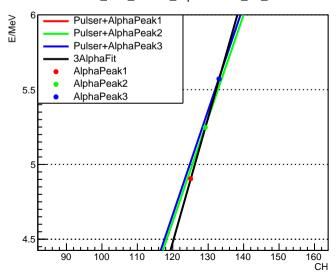




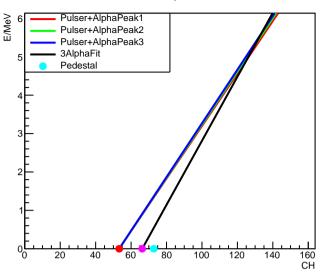
SSD1_L2F_Switch_AlphaCali33_48_CH11

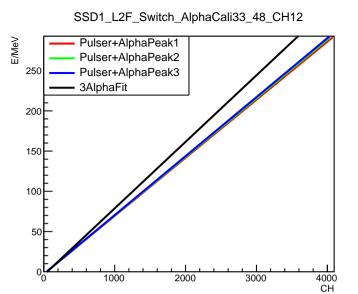


SSD1_L2F_Switch_AlphaCali33_48_CH11

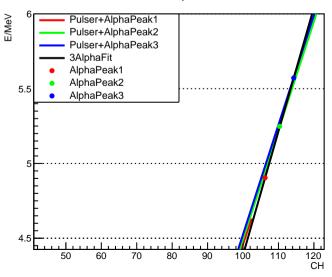


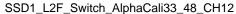


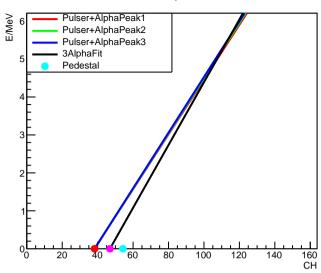




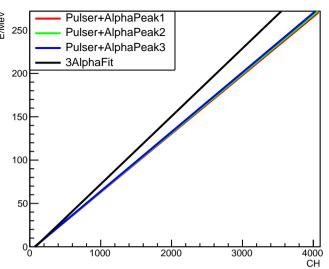




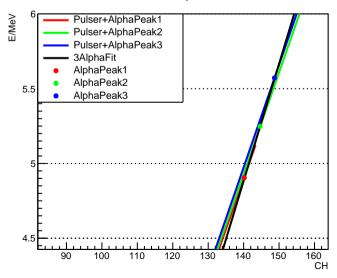




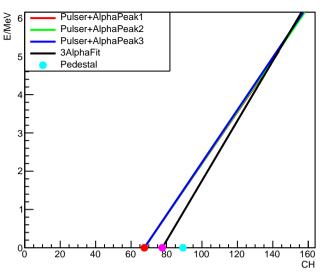
SSD1_L2F_Switch_AlphaCali33_48_CH13 E/MeV Pulser+AlphaPeak1



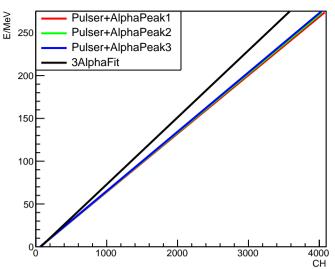
SSD1_L2F_Switch_AlphaCali33_48_CH13



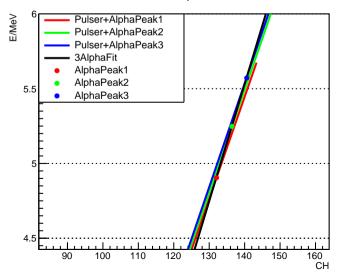


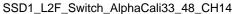


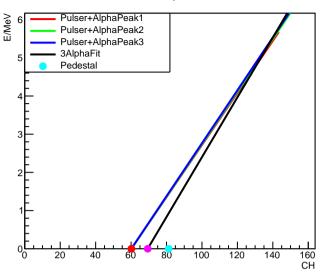
SSD1_L2F_Switch_AlphaCali33_48_CH14



SSD1_L2F_Switch_AlphaCali33_48_CH14



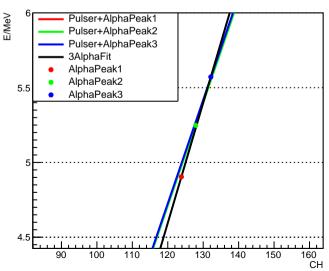




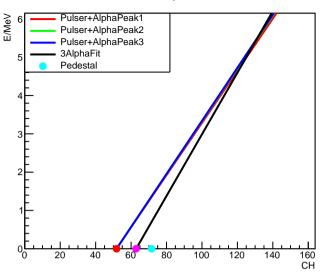
SSD1_L2F_Switch_AlphaCali33_48_CH15

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

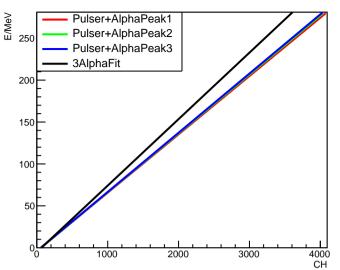




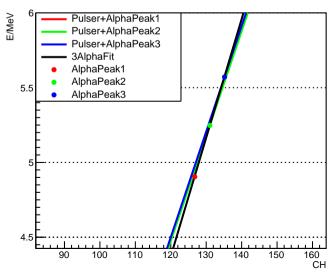


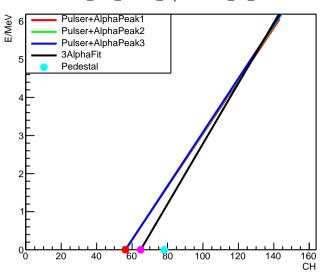


SSD2_L2F_Switch_AlphaCali33_48_CH00

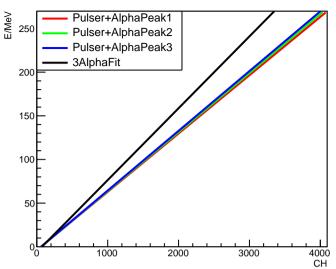


SSD2_L2F_Switch_AlphaCali33_48_CH00

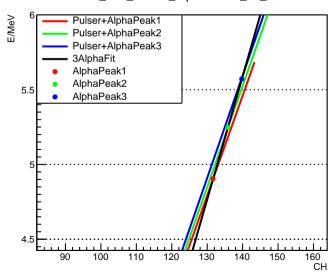




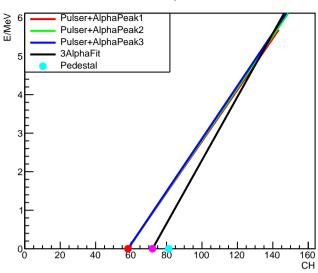
SSD2_L2F_Switch_AlphaCali33_48_CH01



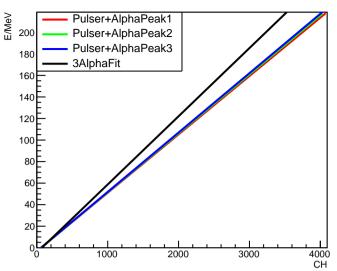
SSD2_L2F_Switch_AlphaCali33_48_CH01



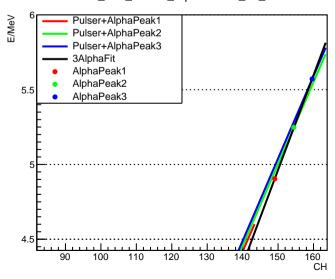


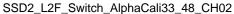


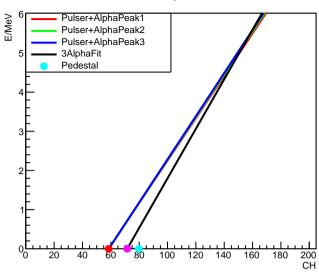
SSD2_L2F_Switch_AlphaCali33_48_CH02



SSD2_L2F_Switch_AlphaCali33_48_CH02





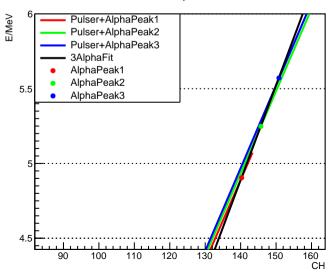


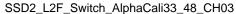
SSD2_L2F_Switch_AlphaCali33_48_CH03

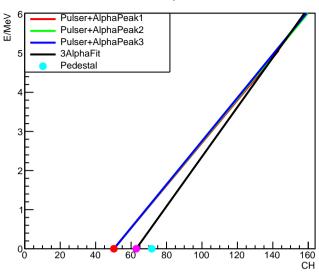
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
AlphaFit

160
140
120
100

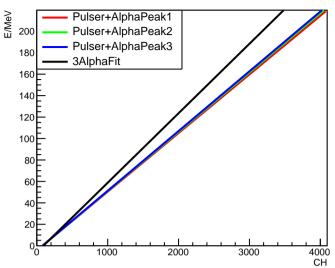




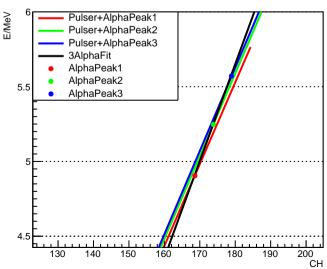


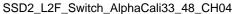


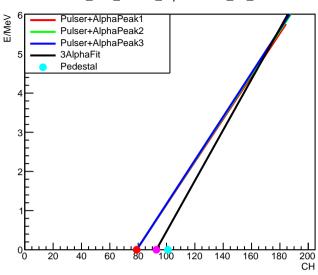
SSD2_L2F_Switch_AlphaCali33_48_CH04



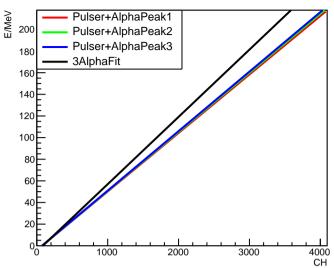
SSD2_L2F_Switch_AlphaCali33_48_CH04



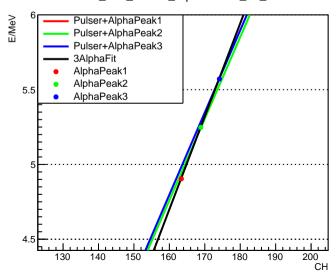




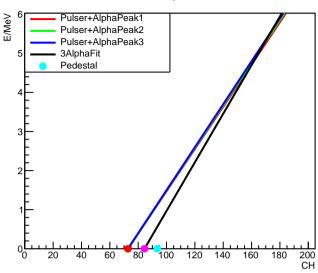
SSD2_L2F_Switch_AlphaCali33_48_CH05



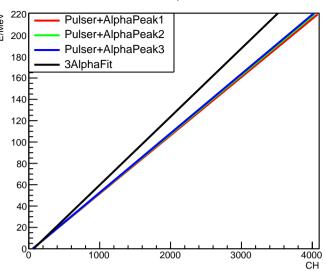
SSD2_L2F_Switch_AlphaCali33_48_CH05



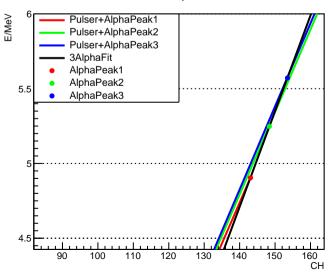


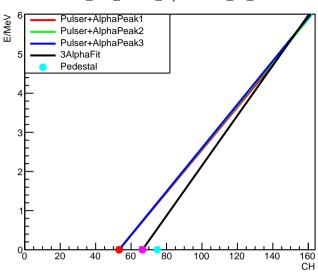


SSD2_L2F_Switch_AlphaCali33_48_CH06 Pulser+AlphaPeak1



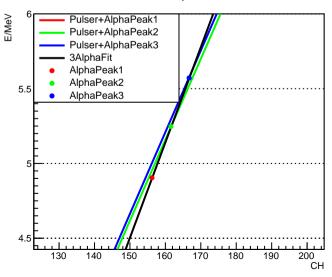
SSD2_L2F_Switch_AlphaCali33_48_CH06



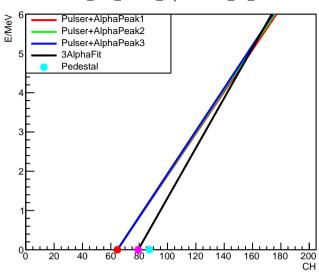


SSD2_L2F_Switch_AlphaCali33_48_CH07 E/MeV Pulser+AlphaPeak1 Pulser+AlphaPeak2 Pulser+AlphaPeak3 3AlphaFit

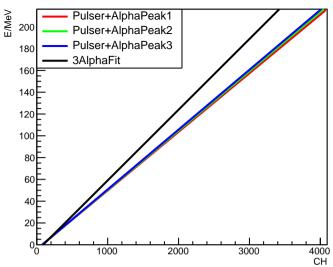




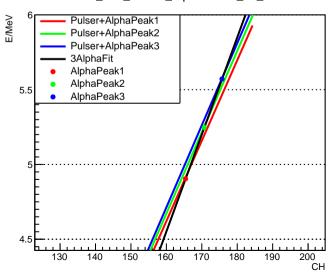


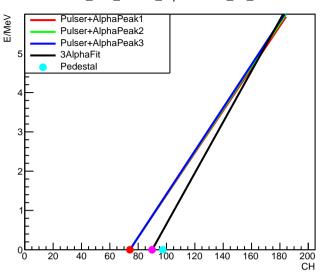


SSD2_L2F_Switch_AlphaCali33_48_CH08

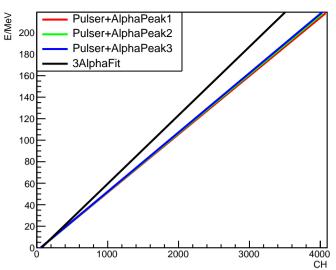


SSD2_L2F_Switch_AlphaCali33_48_CH08

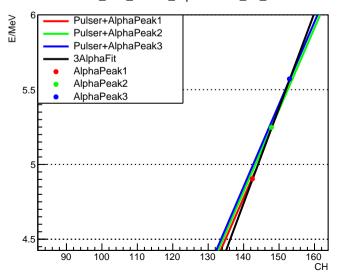


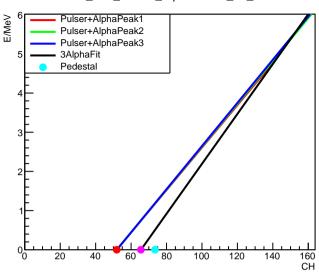


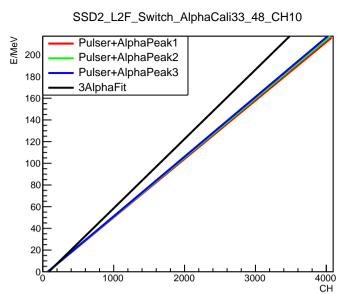
SSD2_L2F_Switch_AlphaCali33_48_CH09



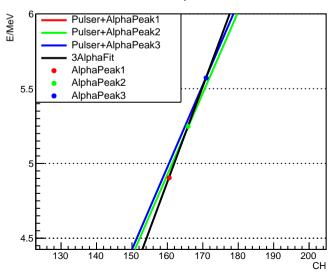
SSD2_L2F_Switch_AlphaCali33_48_CH09



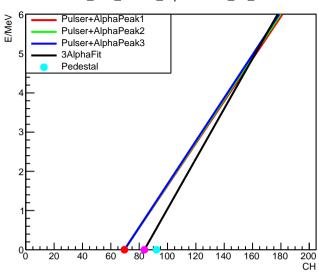




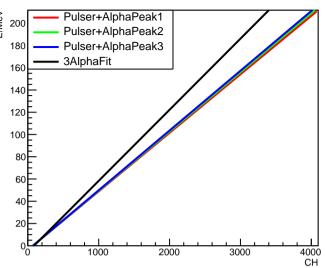




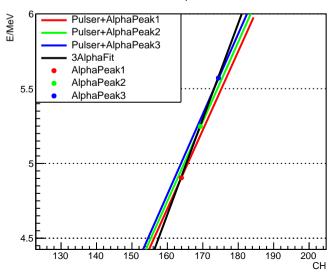


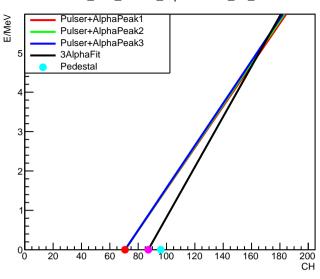


SSD2_L2F_Switch_AlphaCali33_48_CH11



SSD2_L2F_Switch_AlphaCali33_48_CH11





SSD2_L2F_Switch_AlphaCali33_48_CH12

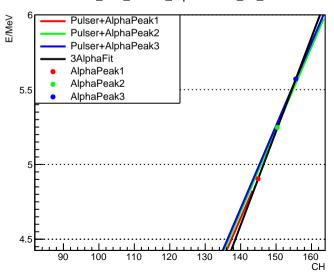
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

160
140
120
100
80
40
20

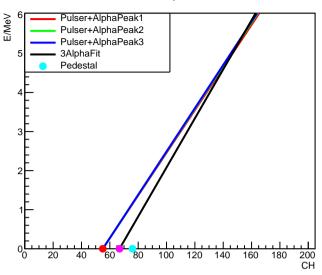
SSD2_L2F_Switch_AlphaCali33_48_CH12

3000

1000







SSD2_L2F_Switch_AlphaCali33_48_CH13

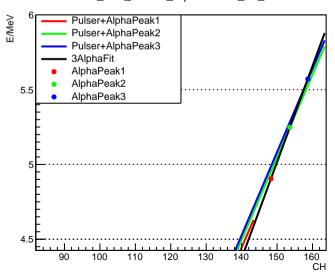
220
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

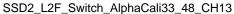
160
140
120
100
80
40
20

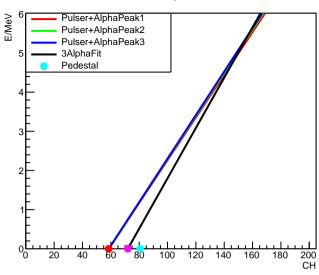
SSD2_L2F_Switch_AlphaCali33_48_CH13

3000

1000







SSD2_L2F_Switch_AlphaCali33_48_CH14

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

140
120
100
80
60
40

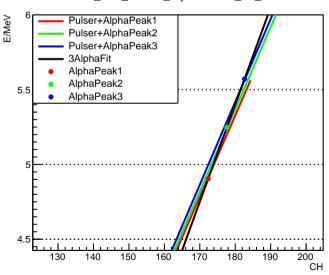
SSD2_L2F_Switch_AlphaCali33_48_CH14

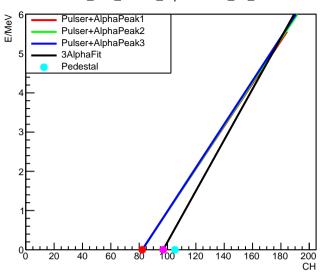
3000

4000 CH

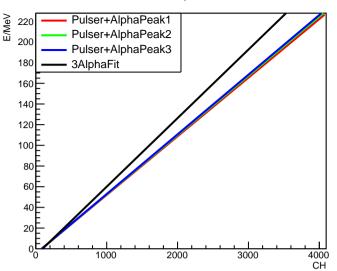
20

1000

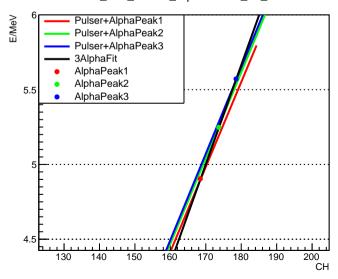


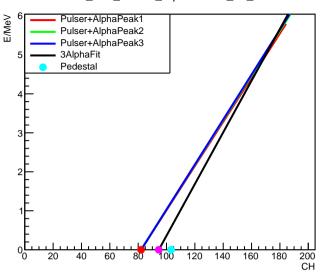


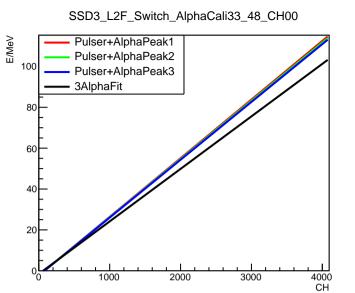
SSD2_L2F_Switch_AlphaCali33_48_CH15



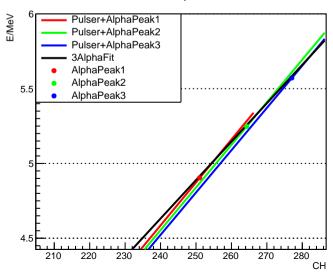
SSD2_L2F_Switch_AlphaCali33_48_CH15

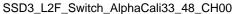


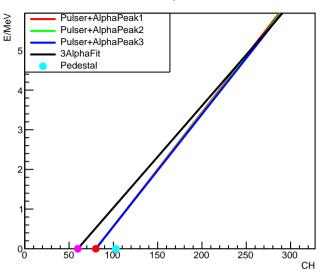




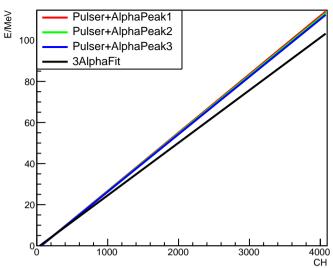




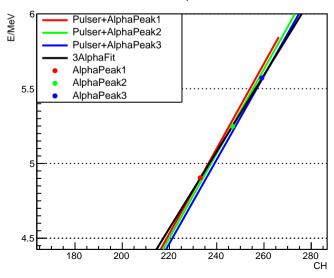




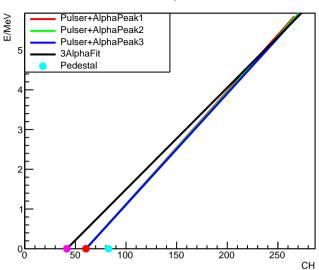
SSD3_L2F_Switch_AlphaCali33_48_CH01

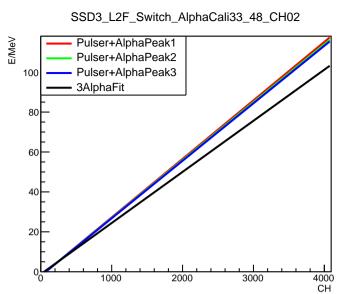


SSD3_L2F_Switch_AlphaCali33_48_CH01

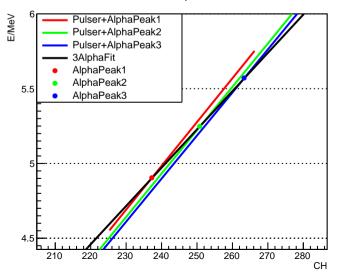


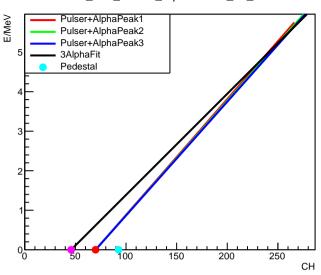




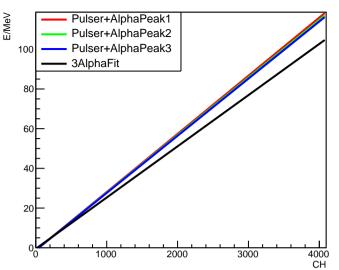




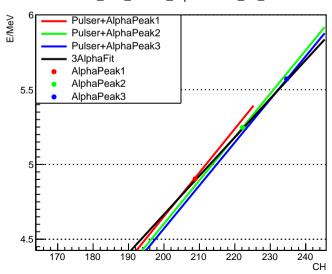


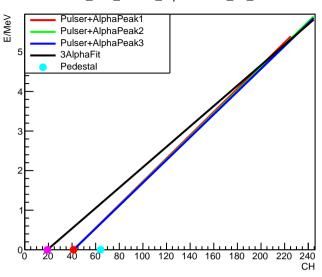


SSD3_L2F_Switch_AlphaCali33_48_CH03



SSD3_L2F_Switch_AlphaCali33_48_CH03



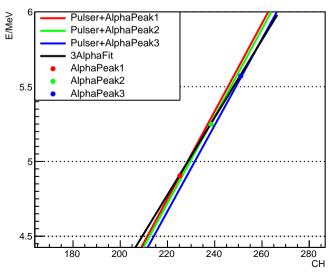


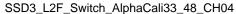
SSD3_L2F_Switch_AlphaCali33_48_CH04

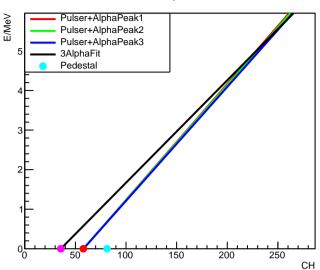
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

SSD3_L2F_Switch_AlphaCali33_48_CH04

CH





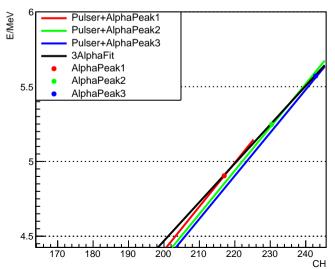


SSD3_L2F_Switch_AlphaCali33_48_CH05

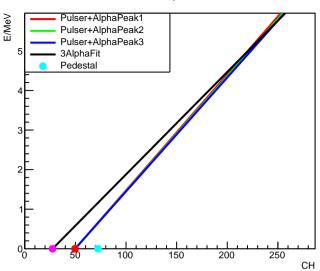
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

SSD3_L2F_Switch_AlphaCali33_48_CH05

CH







SSD3_L2F_Switch_AlphaCali33_48_CH06

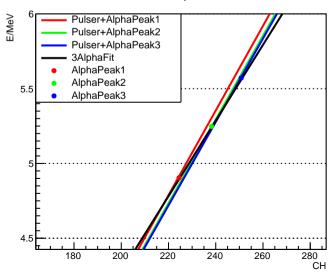
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

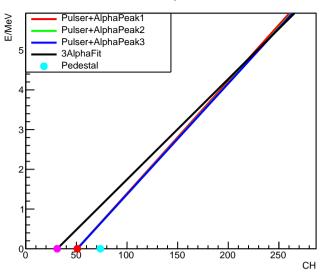


3000

1000

4000 CH





SSD3_L2F_Switch_AlphaCali33_48_CH07

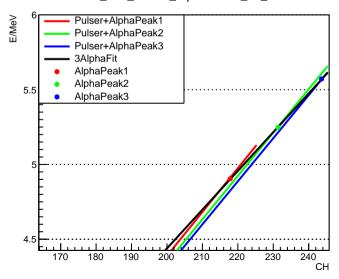
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

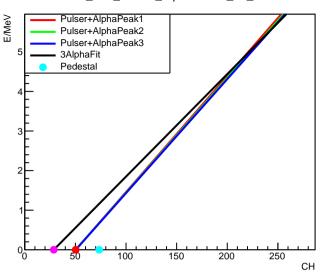
SSD3_L2F_Switch_AlphaCali33_48_CH07

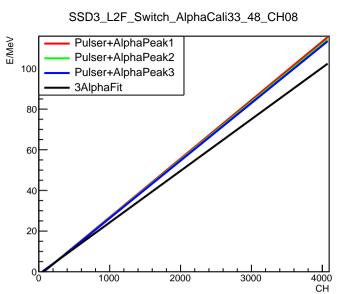
3000

1000

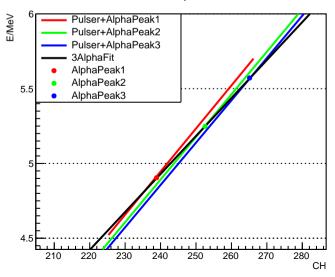
4000 CH

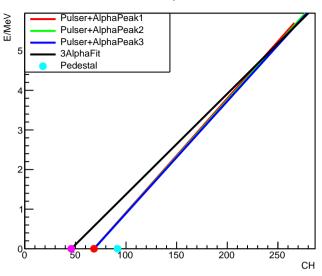


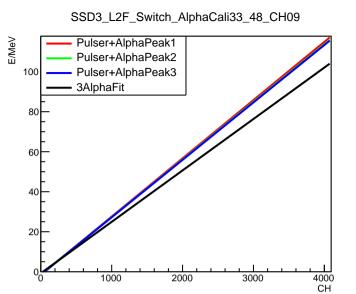




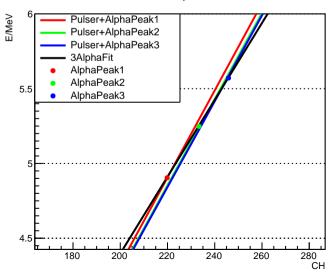


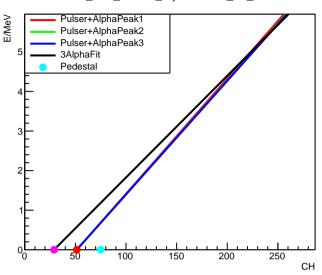


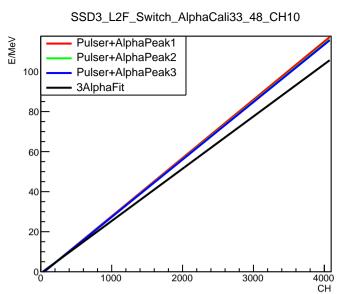




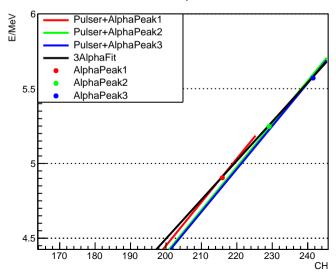




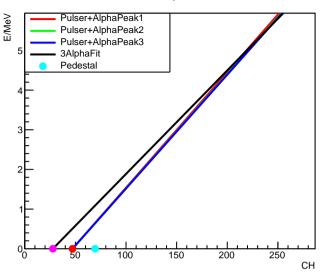


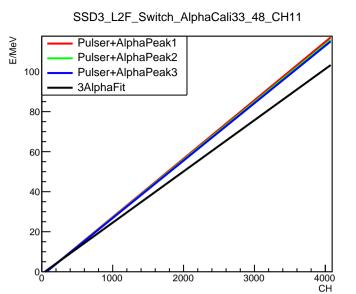




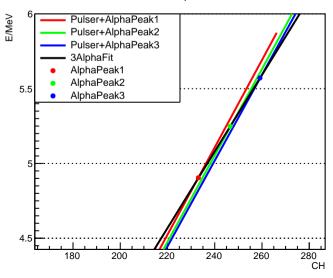




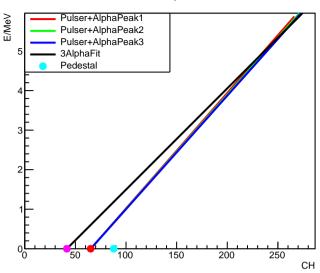


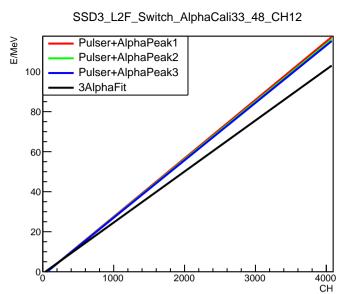




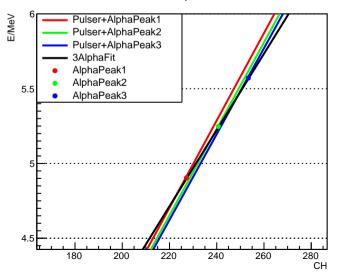




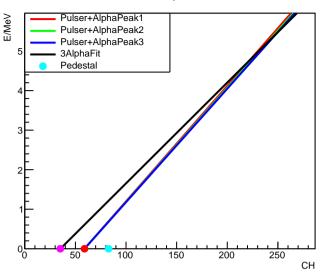


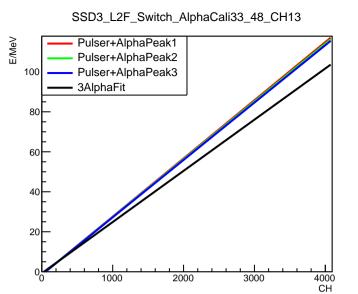




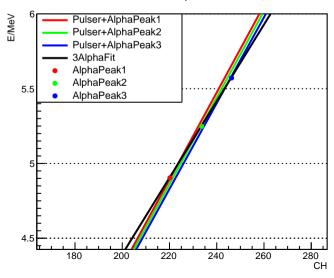




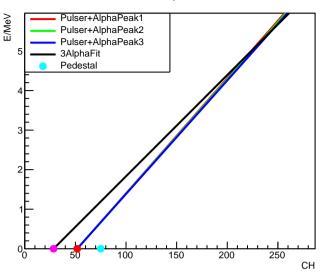




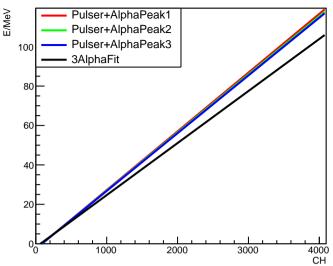




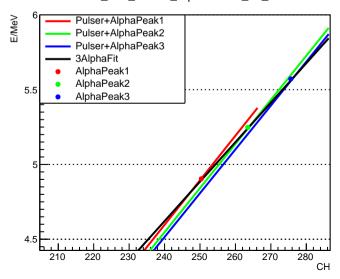


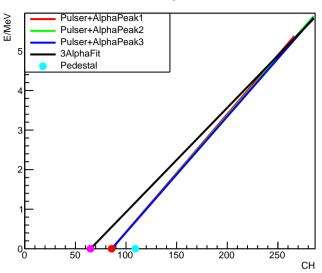


SSD3_L2F_Switch_AlphaCali33_48_CH14

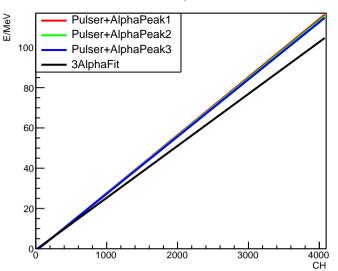


SSD3_L2F_Switch_AlphaCali33_48_CH14

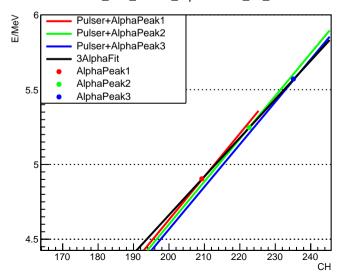


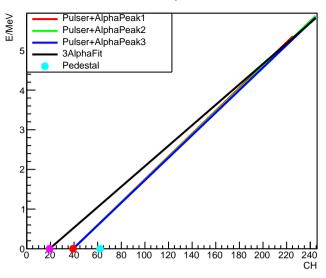


SSD3_L2F_Switch_AlphaCali33_48_CH15

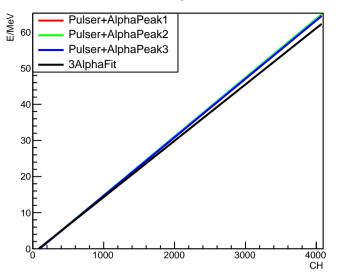


SSD3_L2F_Switch_AlphaCali33_48_CH15

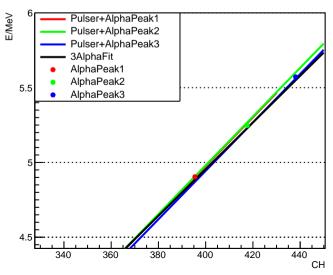




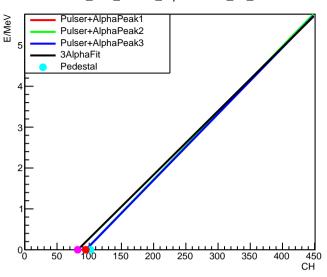
SSD4_L2F_Switch_AlphaCali33_48_CH00



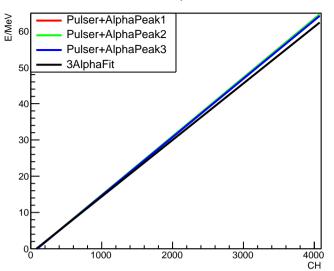
SSD4_L2F_Switch_AlphaCali33_48_CH00



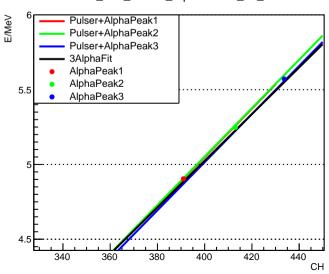
SSD4_L2F_Switch_AlphaCali33_48_CH00



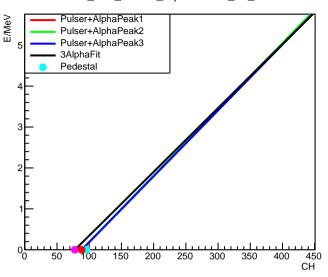
SSD4_L2F_Switch_AlphaCali33_48_CH01



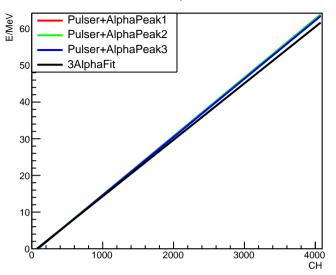
SSD4_L2F_Switch_AlphaCali33_48_CH01



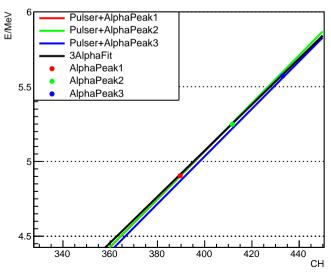
SSD4_L2F_Switch_AlphaCali33_48_CH01



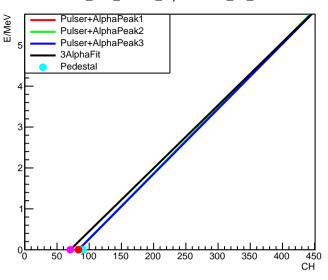
SSD4_L2F_Switch_AlphaCali33_48_CH02



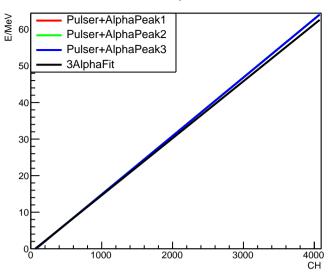
SSD4_L2F_Switch_AlphaCali33_48_CH02



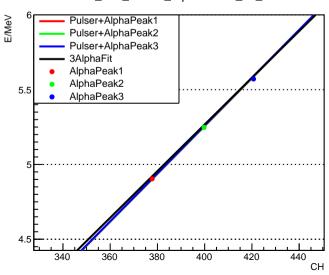
SSD4_L2F_Switch_AlphaCali33_48_CH02



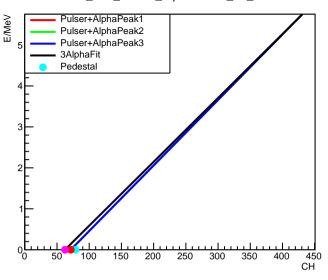
SSD4_L2F_Switch_AlphaCali33_48_CH03



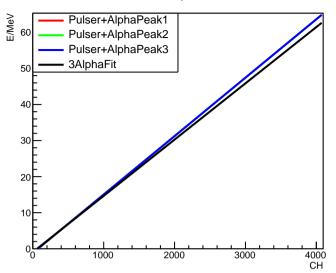
SSD4_L2F_Switch_AlphaCali33_48_CH03



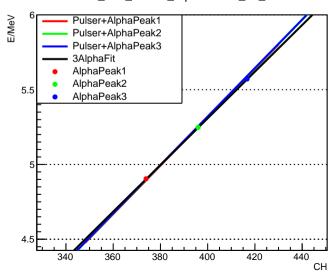
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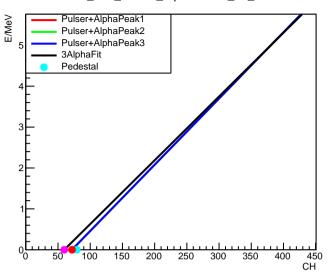
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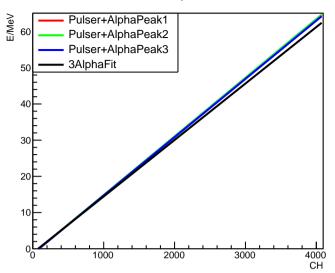
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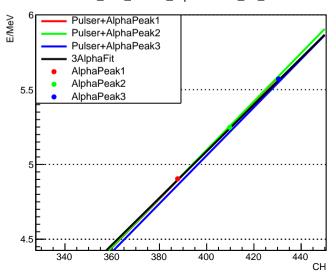
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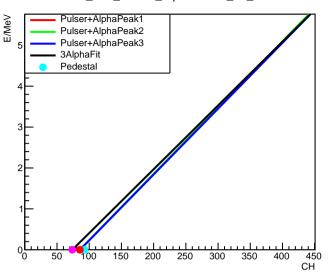
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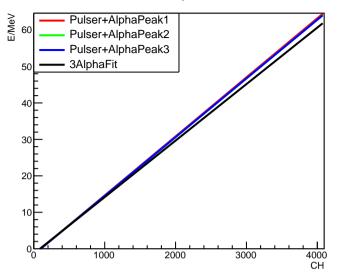
SSD4_L2F_Switch_AlphaCali33_48_CH05



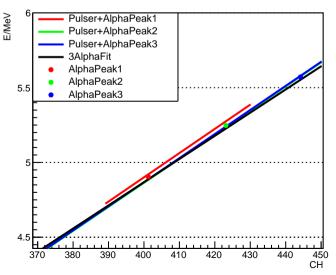
SSD4_L2F_Switch_AlphaCali33_48_CH05



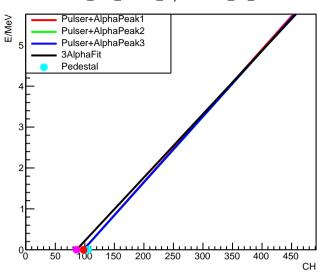
SSD4_L2F_Switch_AlphaCali33_48_CH06



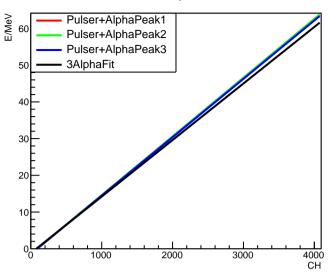
SSD4_L2F_Switch_AlphaCali33_48_CH06



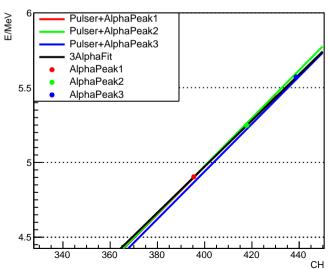
SSD4_L2F_Switch_AlphaCali33_48_CH06



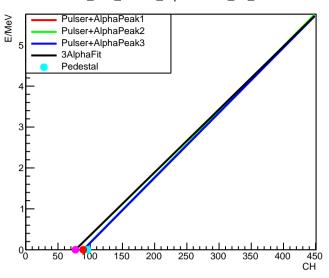
SSD4_L2F_Switch_AlphaCali33_48_CH07



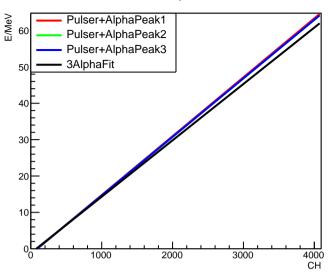
SSD4_L2F_Switch_AlphaCali33_48_CH07



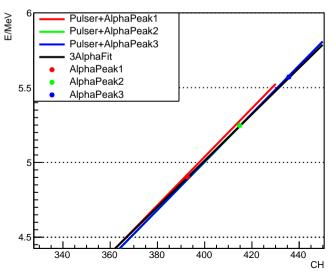
SSD4_L2F_Switch_AlphaCali33_48_CH07

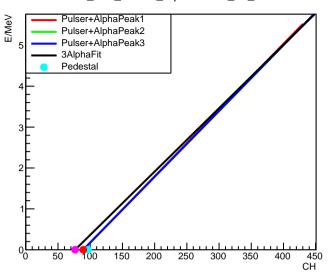


SSD4_L2F_Switch_AlphaCali33_48_CH08

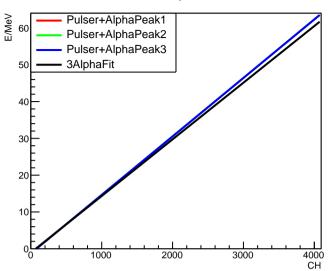


SSD4_L2F_Switch_AlphaCali33_48_CH08

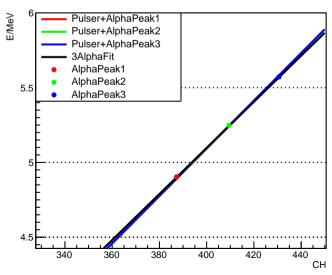


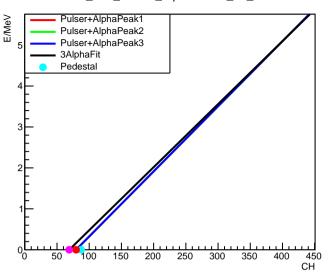


SSD4_L2F_Switch_AlphaCali33_48_CH09

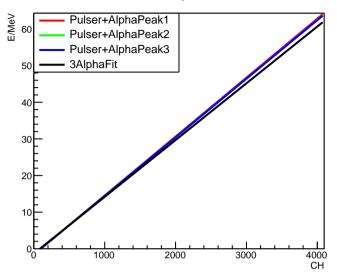


SSD4_L2F_Switch_AlphaCali33_48_CH09

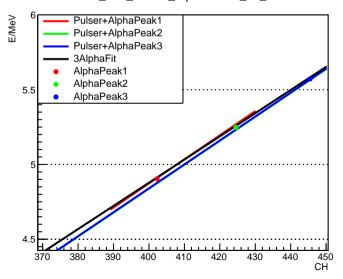




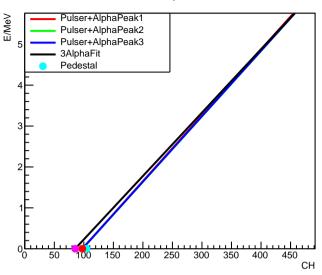
SSD4_L2F_Switch_AlphaCali33_48_CH10



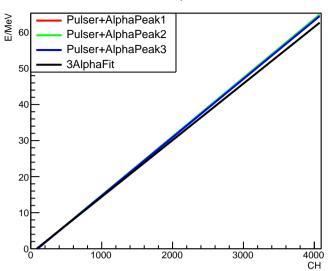
SSD4_L2F_Switch_AlphaCali33_48_CH10



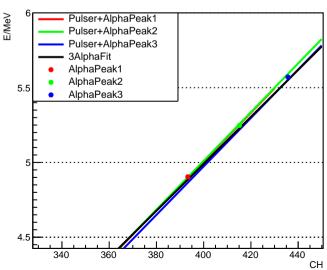
SSD4_L2F_Switch_AlphaCali33_48_CH10



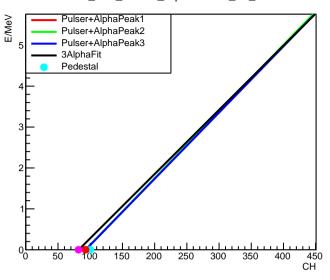
SSD4_L2F_Switch_AlphaCali33_48_CH11



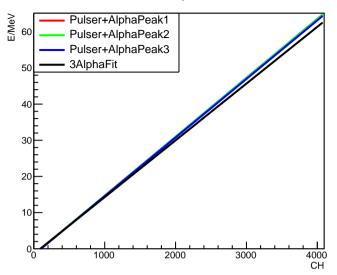
SSD4_L2F_Switch_AlphaCali33_48_CH11



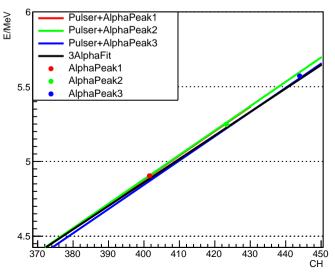
SSD4_L2F_Switch_AlphaCali33_48_CH11



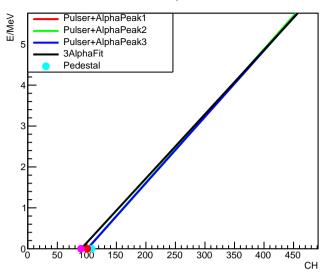
SSD4_L2F_Switch_AlphaCali33_48_CH12



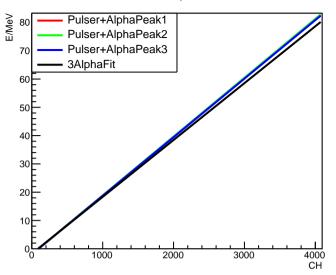
SSD4_L2F_Switch_AlphaCali33_48_CH12



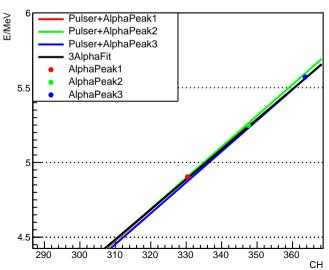
SSD4_L2F_Switch_AlphaCali33_48_CH12



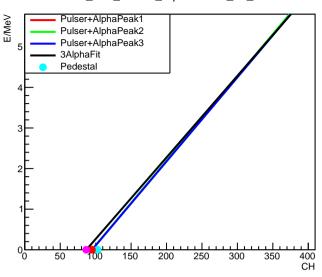
SSD4_L2F_Switch_AlphaCali33_48_CH13



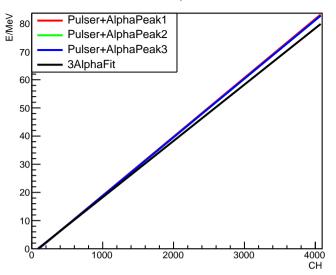
SSD4_L2F_Switch_AlphaCali33_48_CH13



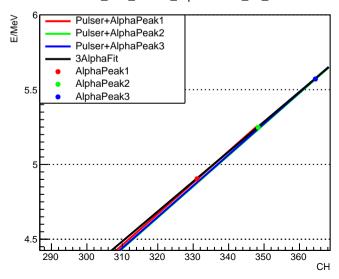
SSD4_L2F_Switch_AlphaCali33_48_CH13

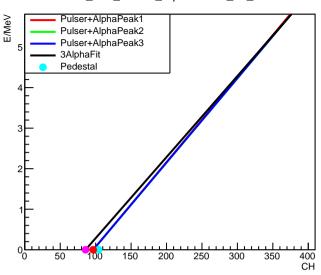


SSD4_L2F_Switch_AlphaCali33_48_CH14

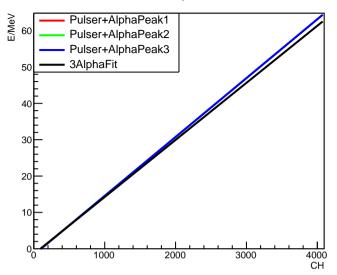


SSD4_L2F_Switch_AlphaCali33_48_CH14

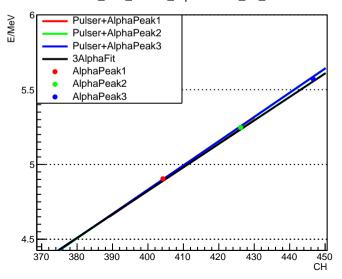




SSD4_L2F_Switch_AlphaCali33_48_CH15



SSD4_L2F_Switch_AlphaCali33_48_CH15



SSD4_L2F_Switch_AlphaCali33_48_CH15

