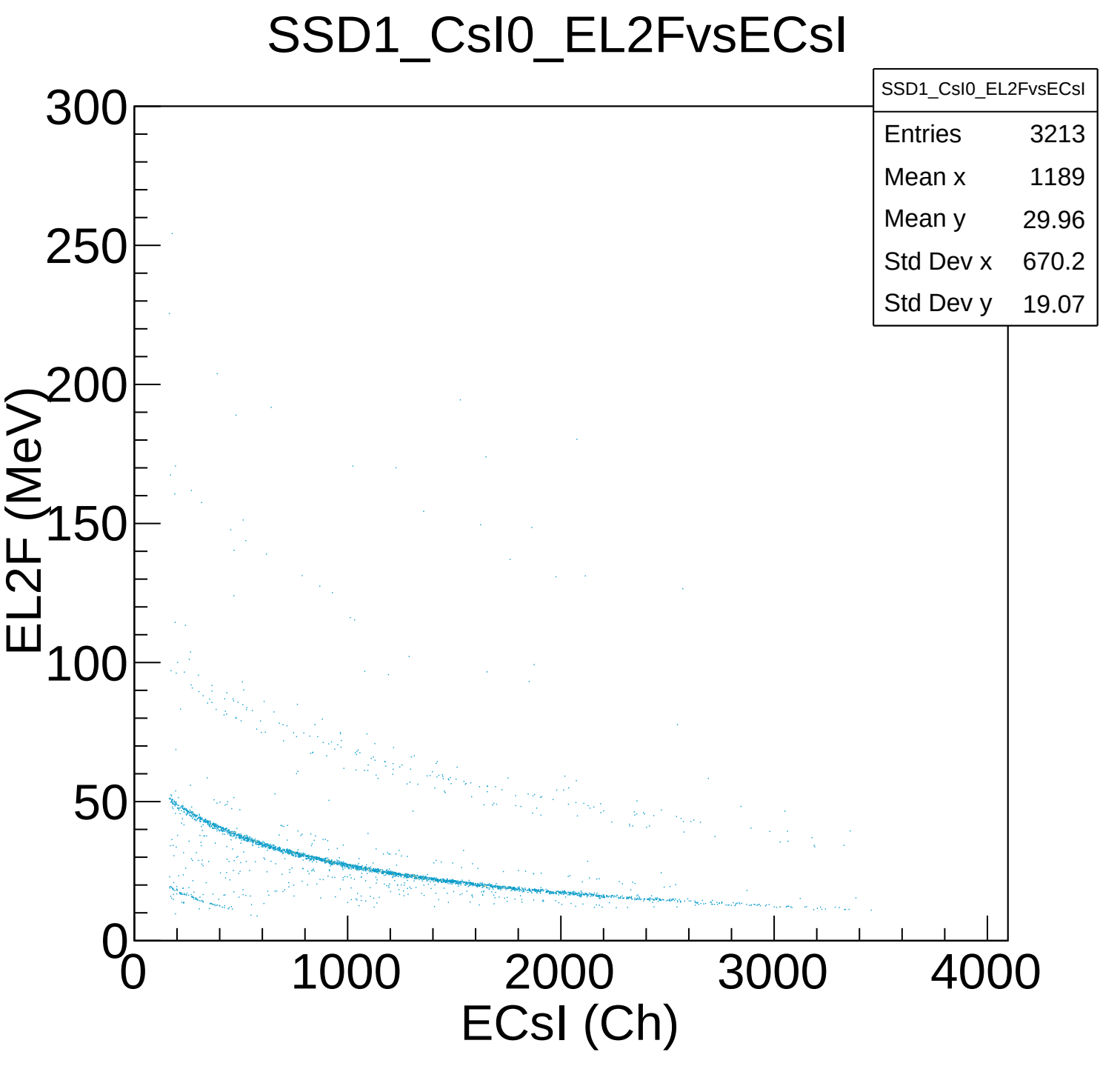
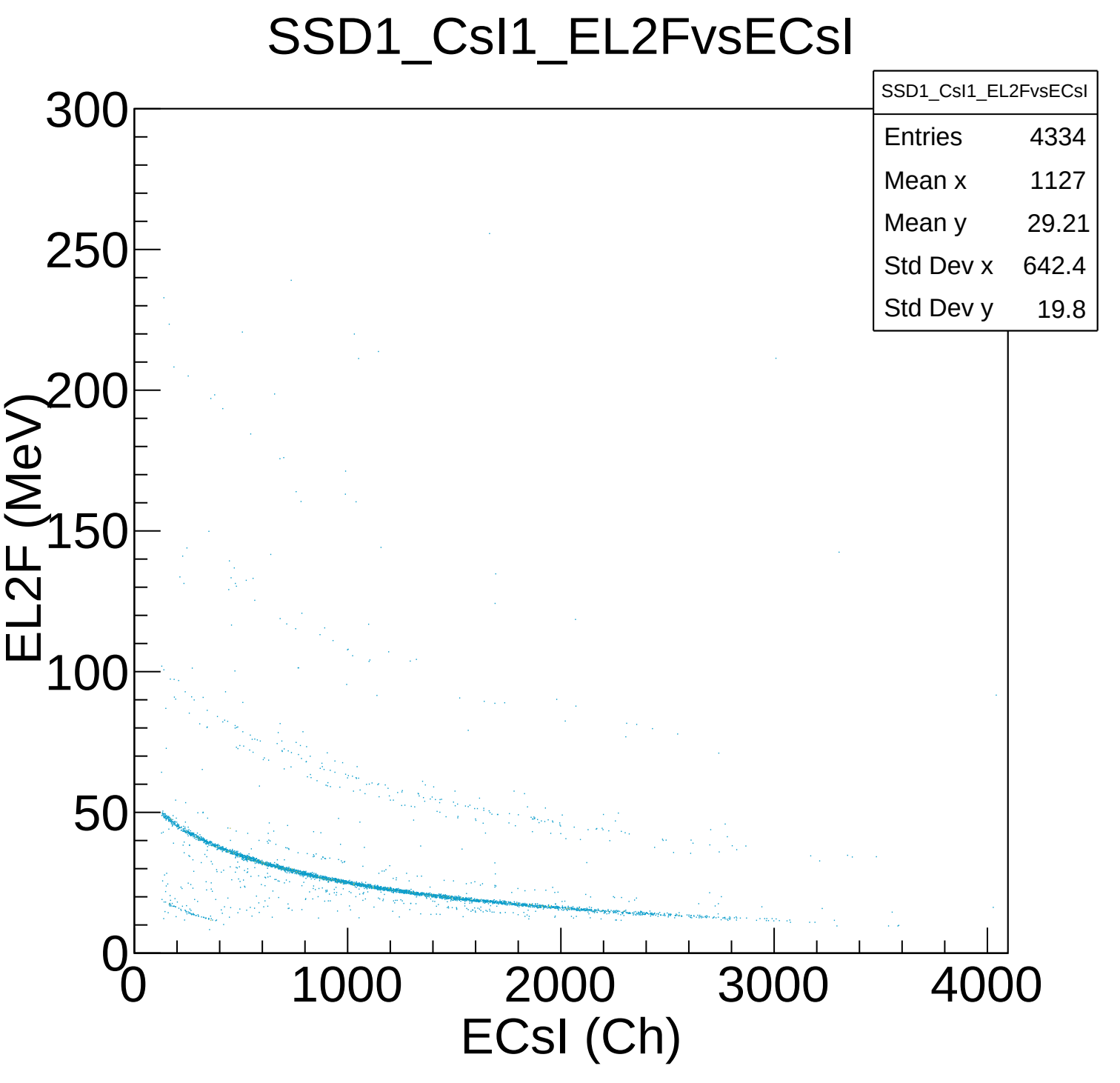
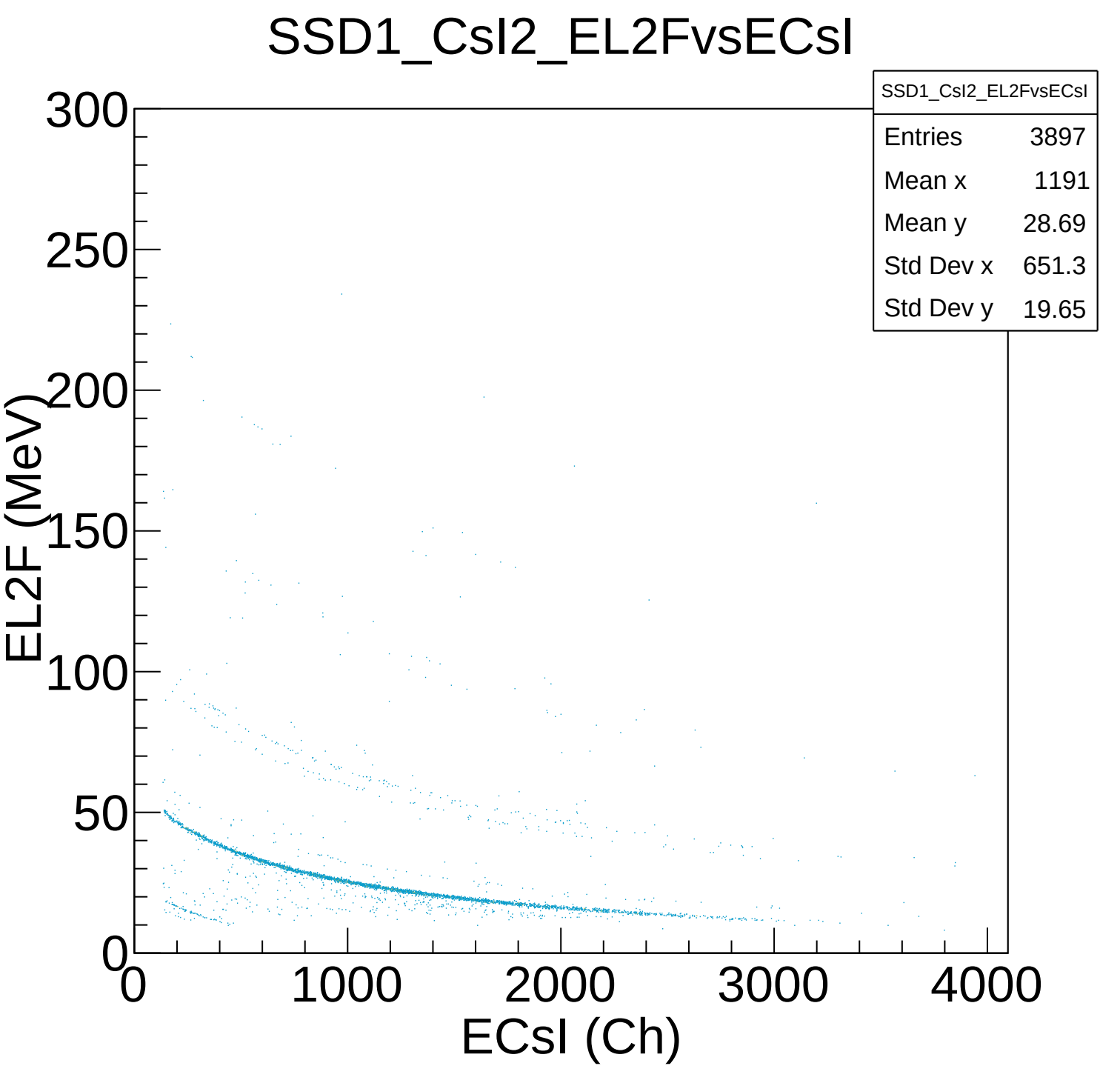
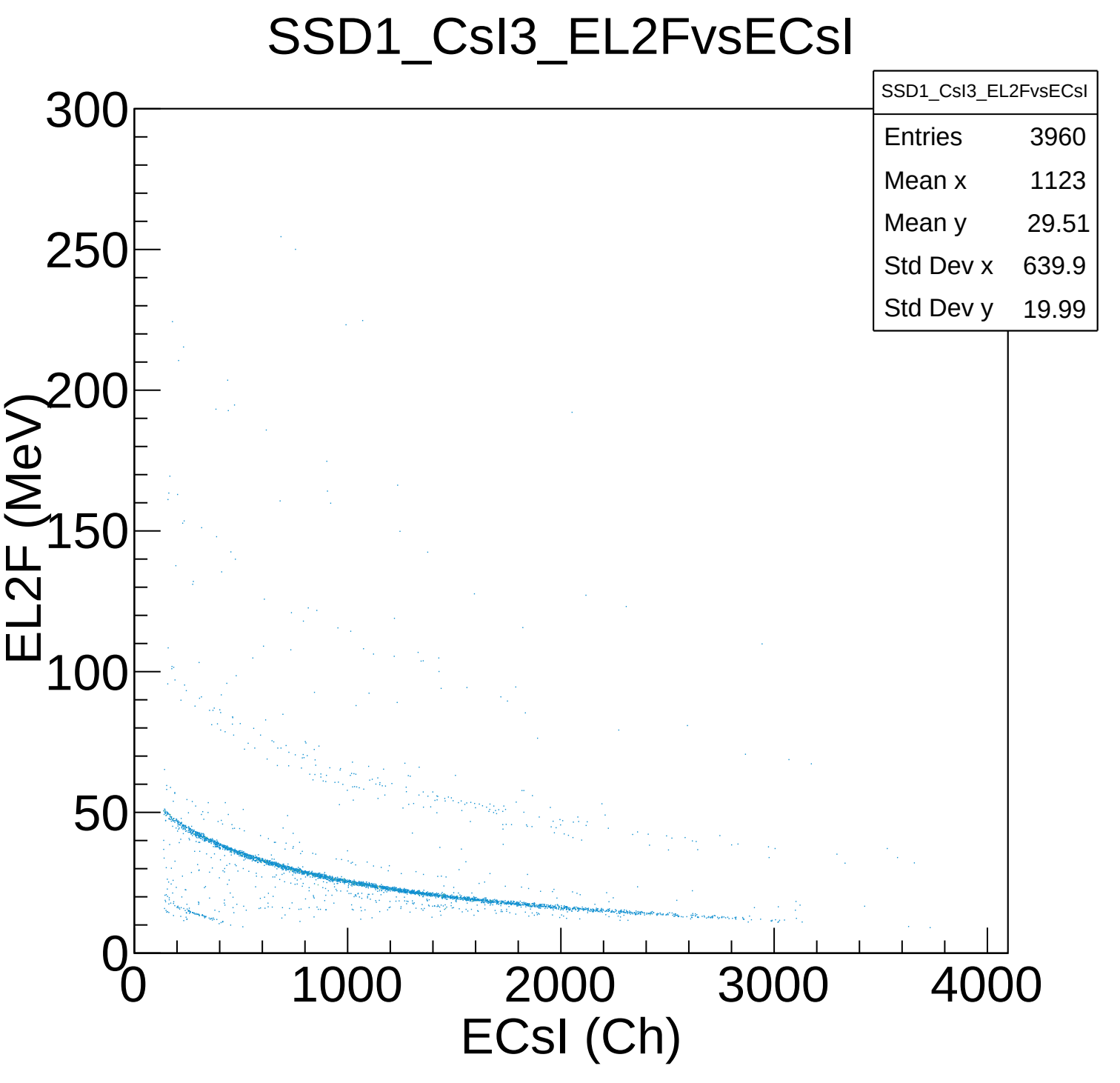
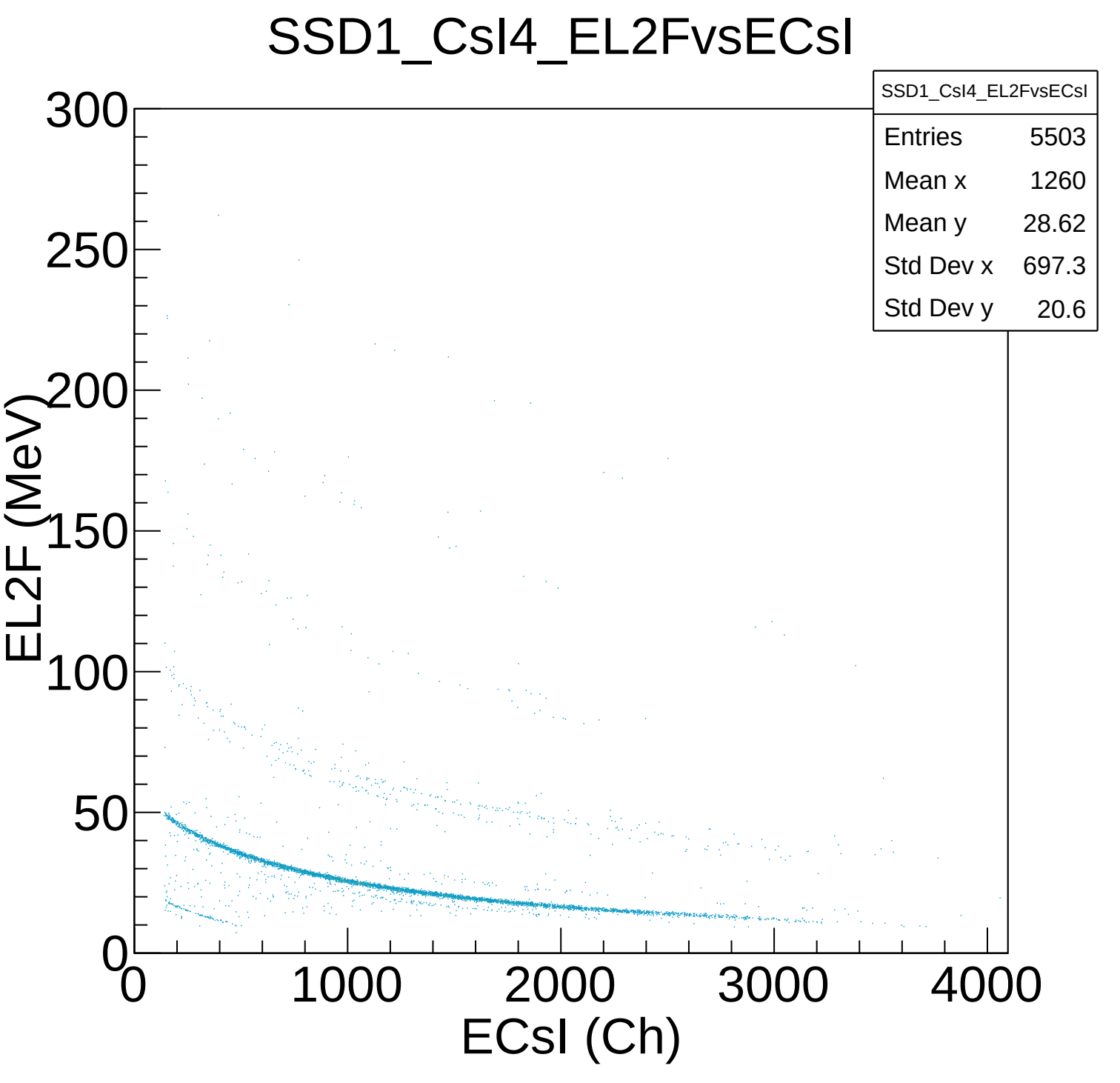

SSD1_CsI0_EL2FvsECsI
SSD1_CsI0_EL2FvsECsI
Entries 3213
Mean x 1189
Mean y 29.96
Std Dev x 670.2
Std Dev y 19.07
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
300
0
1000
2000
3000
4000


SSD1_CsI1_EL2FvsECsI
SSD1_CsI1_EL2FvsECsI
Entries 4334
Mean x 1127
Mean y 29.21
Std Dev x 642.4
Std Dev y 19.8
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
300
0
1000
2000
3000
4000


SSD1_CsI2_EL2FvsECsI
SSD1_CsI2_EL2FvsECsI
Entries 3897
Mean x 1191
Mean y 28.69
Std Dev x 651.3
Std Dev y 19.65
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
300
0
1000
2000
3000
4000


SSD1_CsI3_EL2FvsECsI
SSD1_CsI3_EL2FvsECsI
Entries 3960
Mean x 1123
Mean y 29.51
Std Dev x 639.9
Std Dev y 19.99
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
300
0
1000
2000
3000
4000


SSD1_CsI4_EL2FvsECsI
SSD1_CsI4_EL2FvsECsI
Entries 5503
Mean x 1260
Mean y 28.62
Std Dev x 697.3
Std Dev y 20.6
EL2F (MeV)
ECsI (Ch)
0
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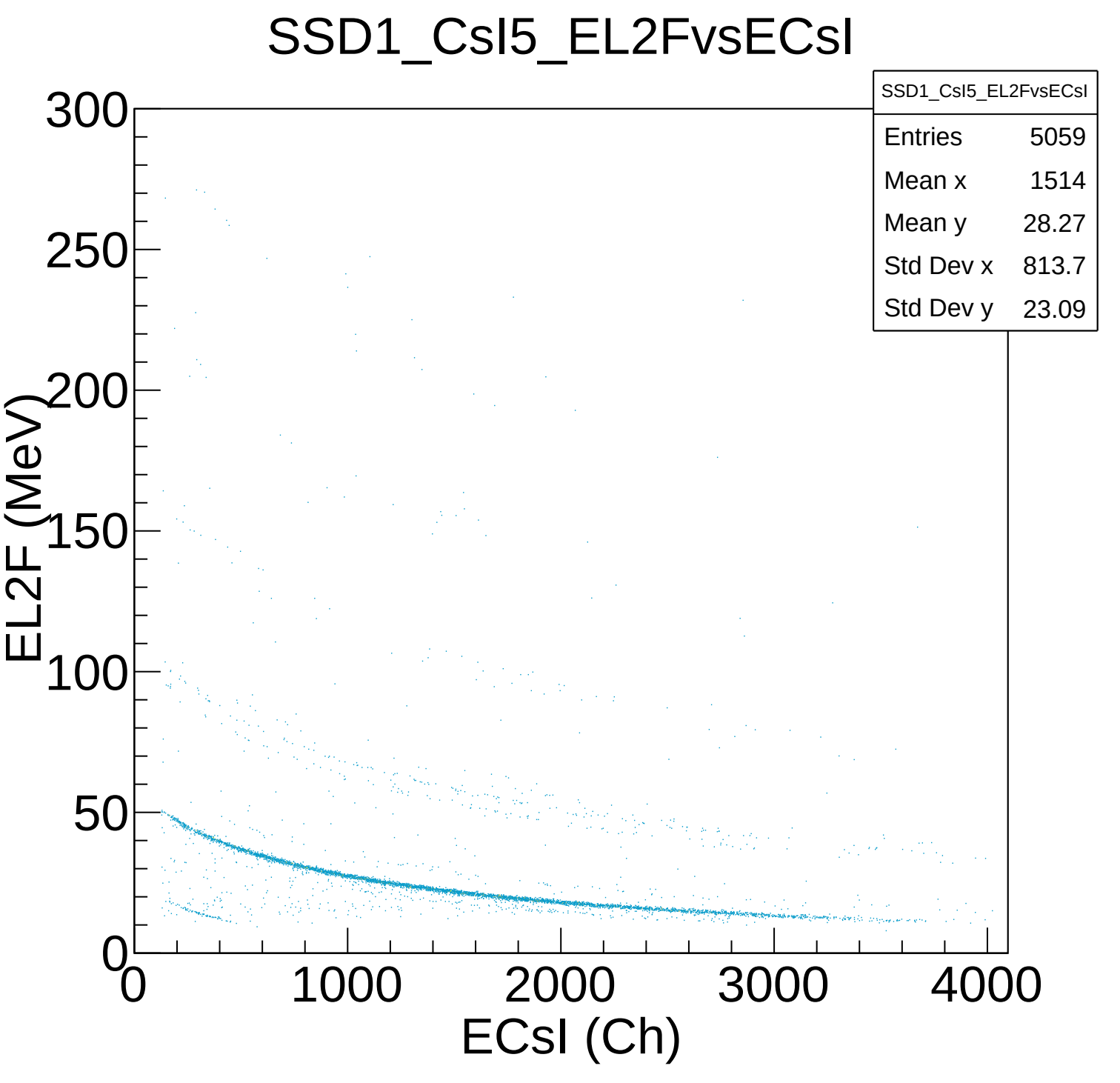
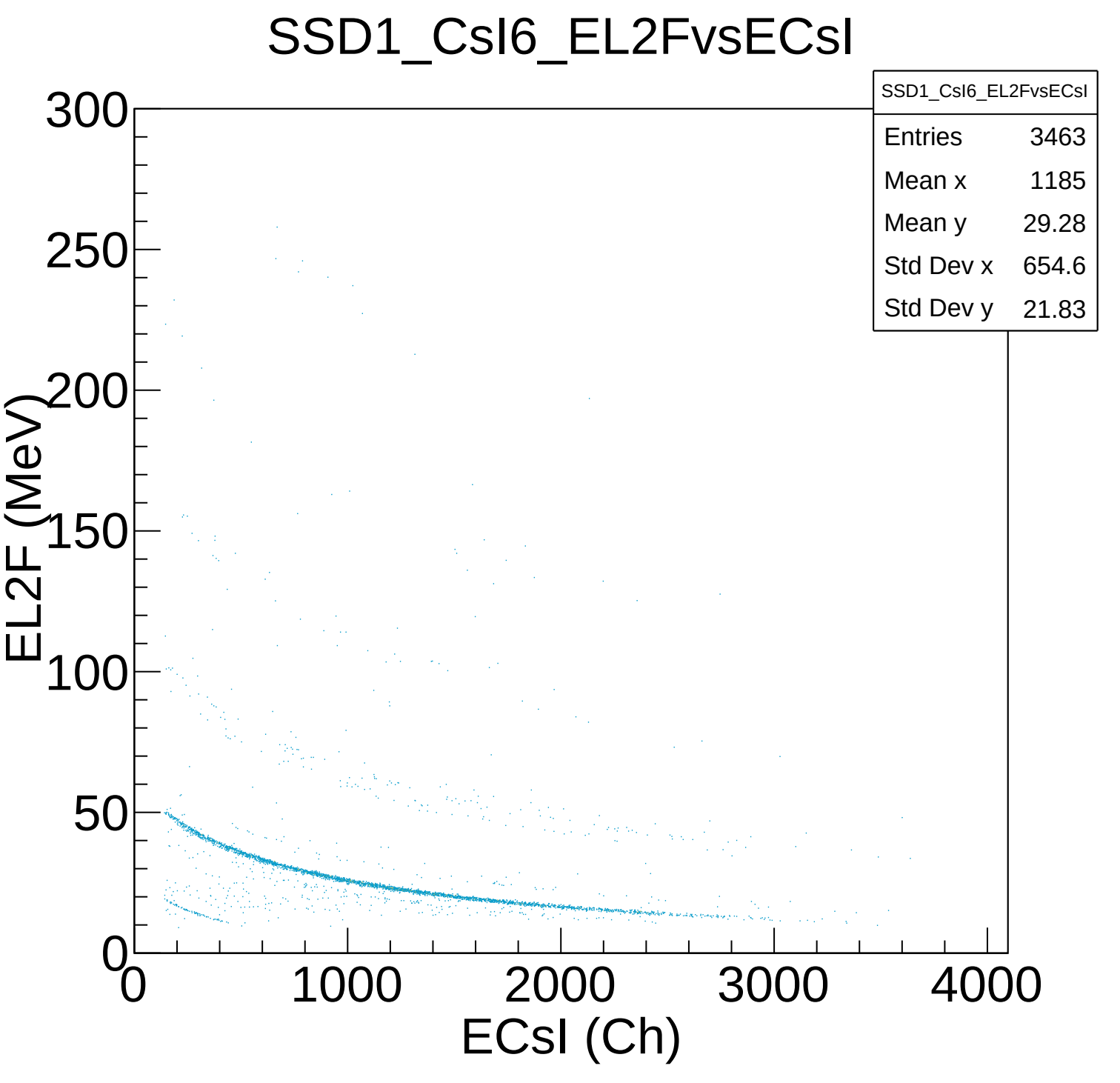
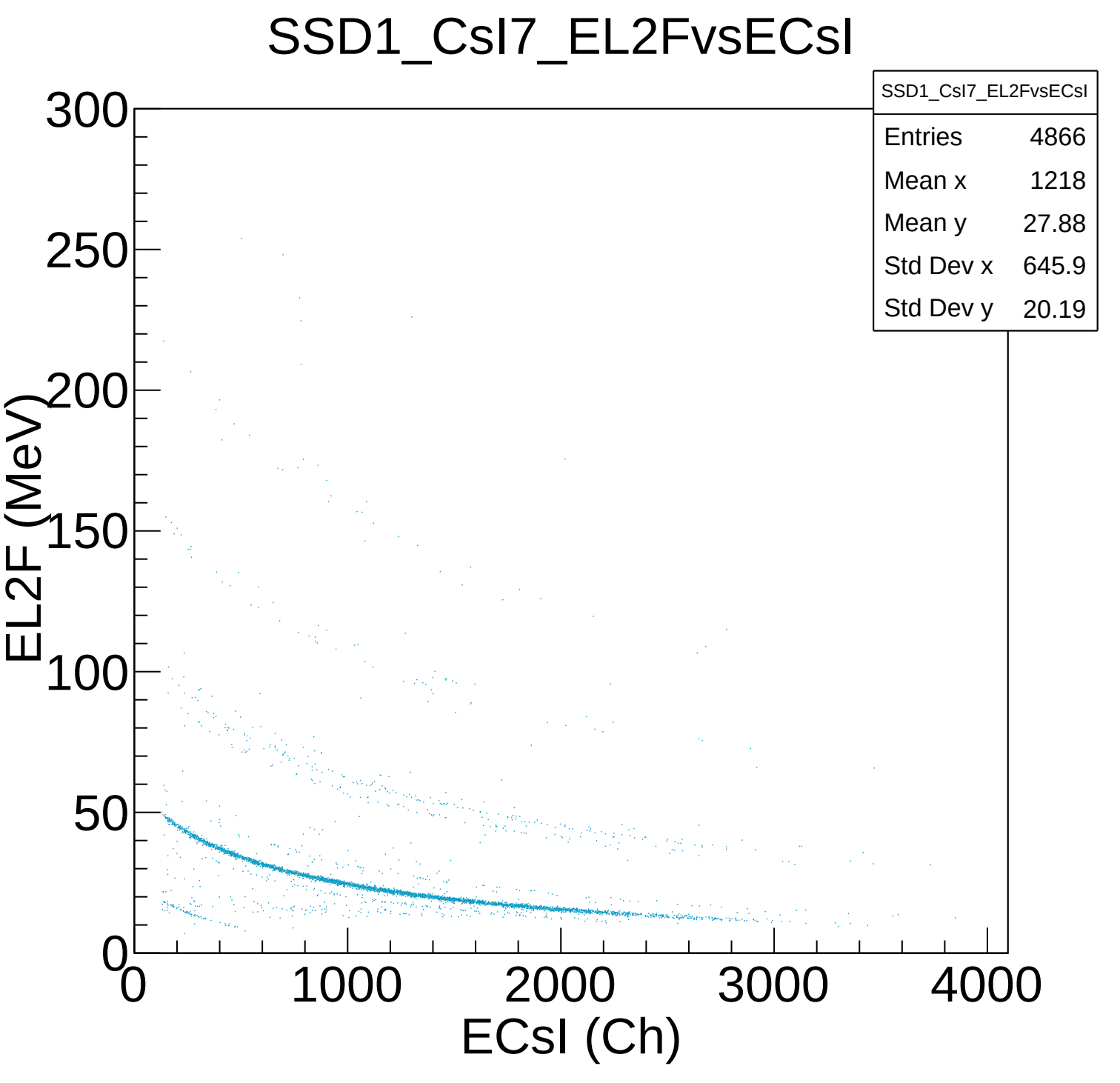
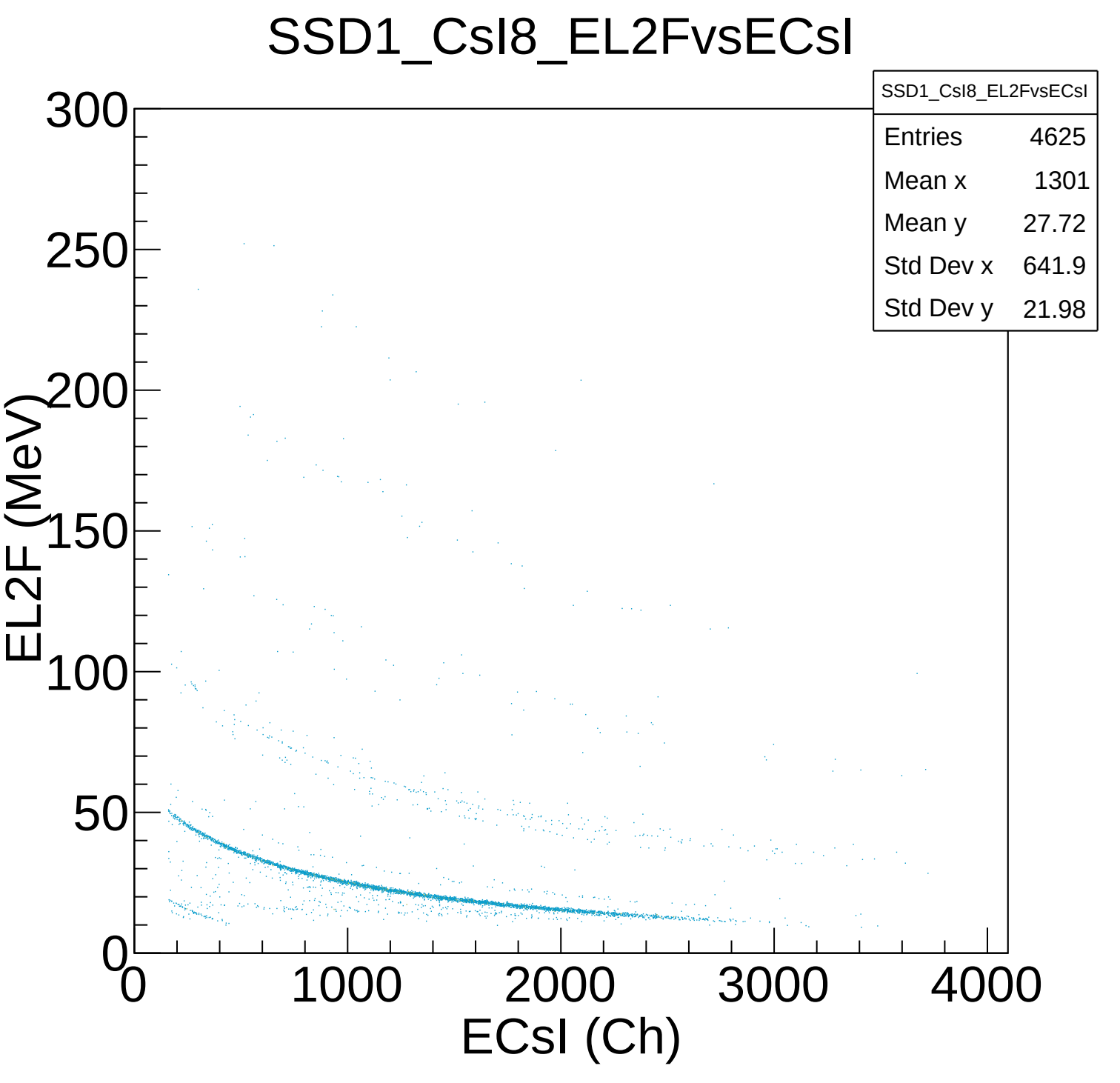
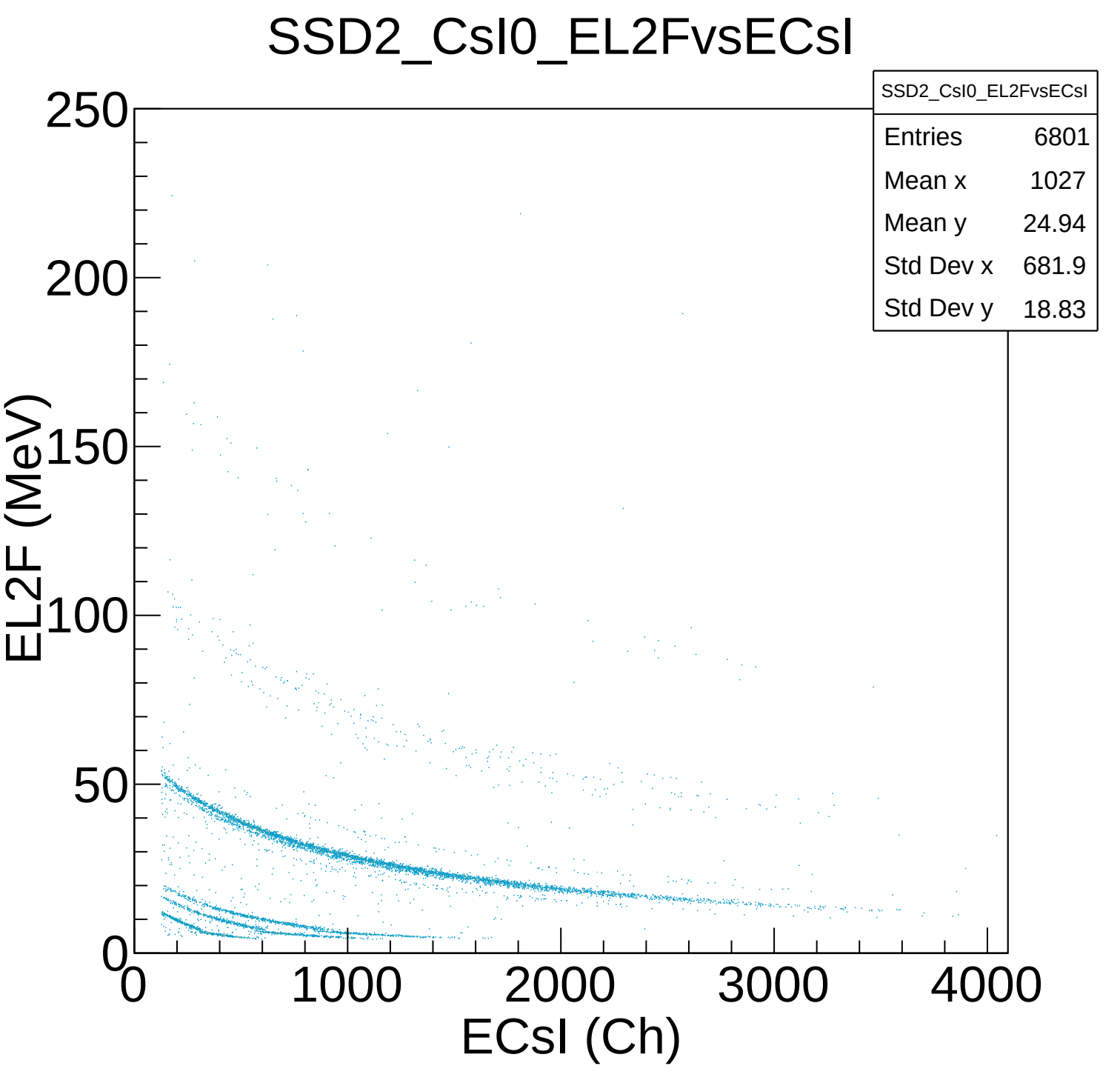
100
150
200
250
300
0
1000
2000
3000
4000

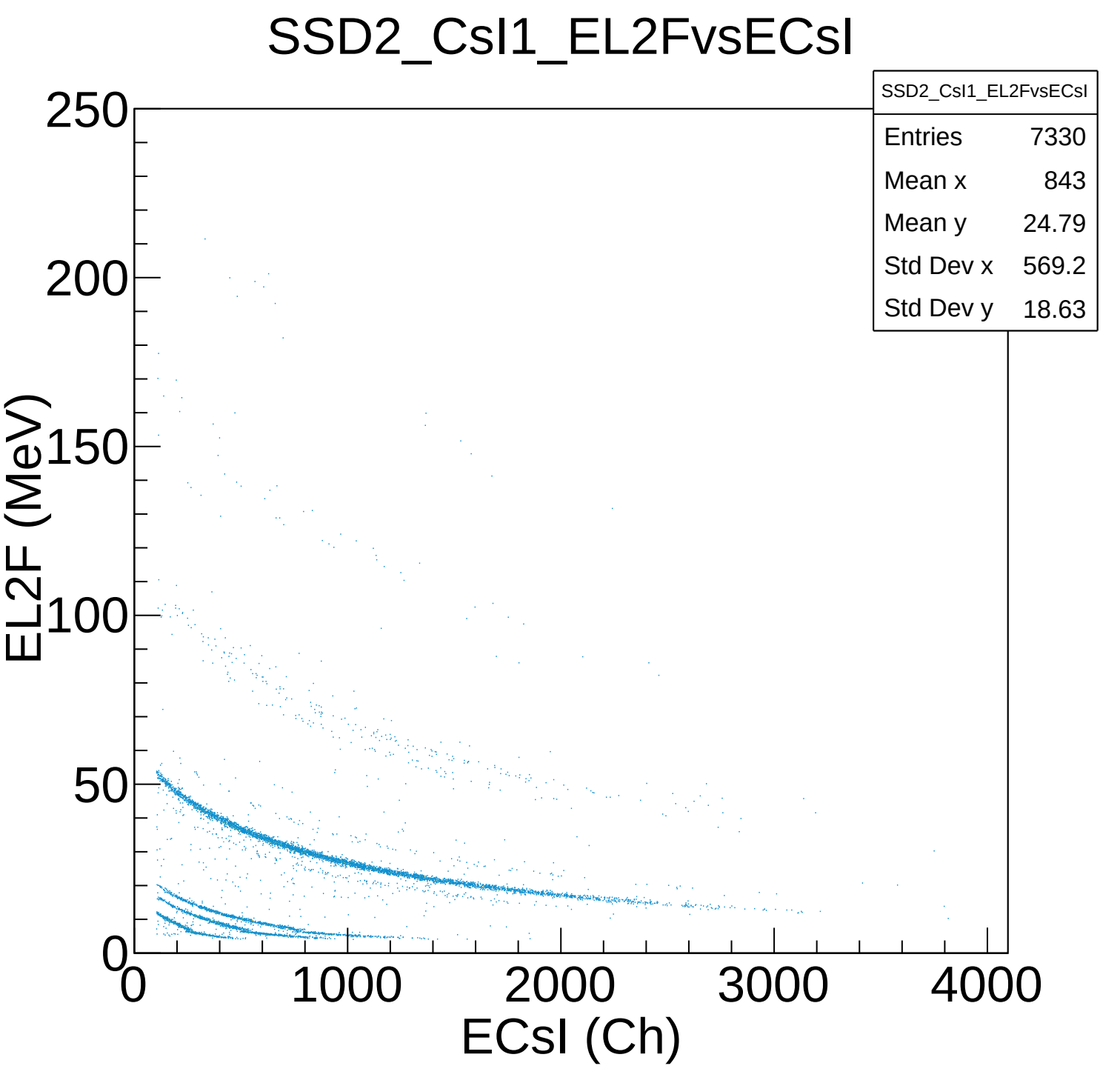

SSD1_CsI5_EL2FvsECsI
SSD1_CsI5_EL2FvsECsI
Entries 5059
Mean x 1514
Mean y 28.27
Std Dev x 813.7
Std Dev y 23.09
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
300
0
1000
2000
3000
4000

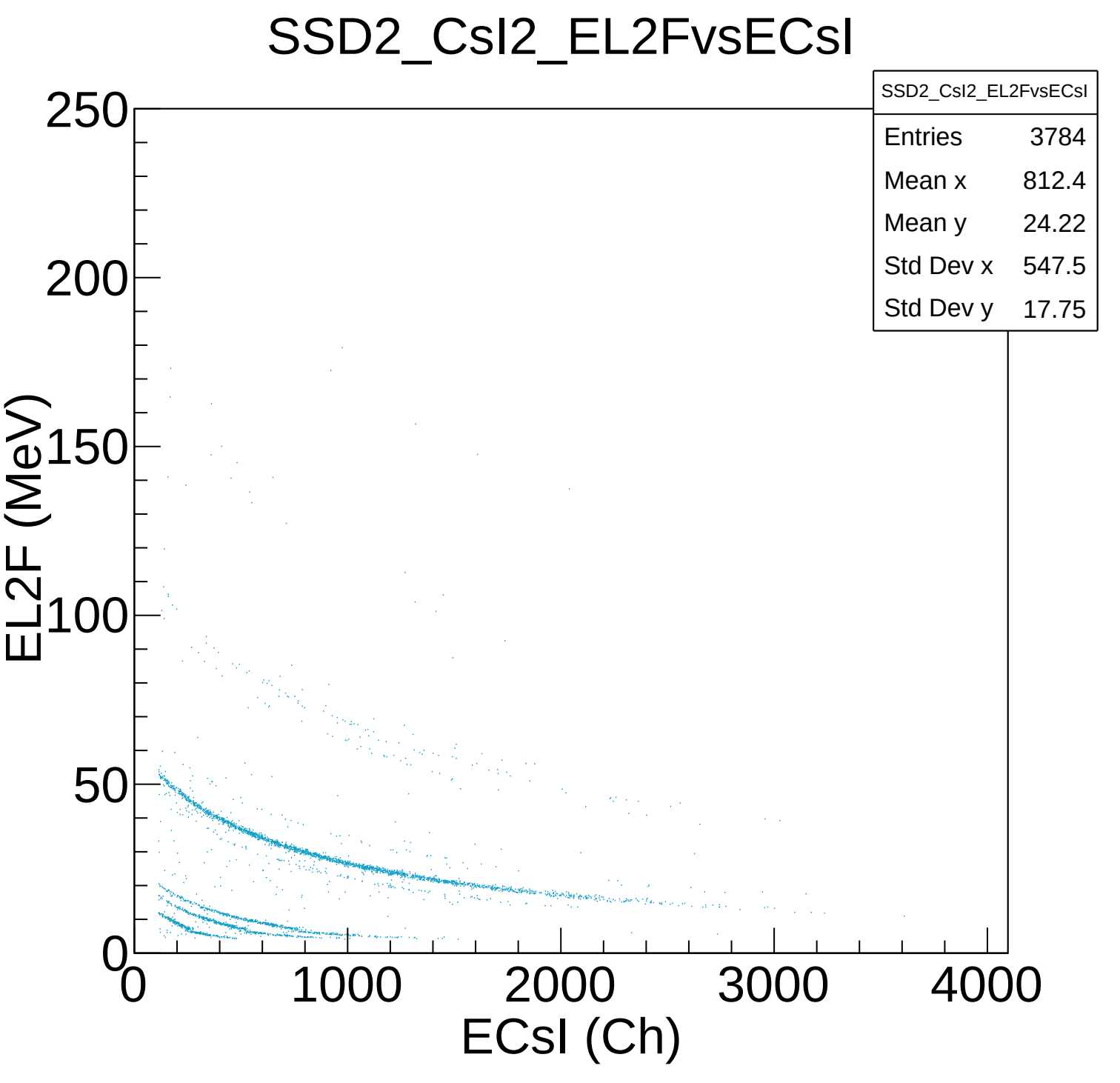

SSD1_CsI6_EL2FvsECsI
SSD1_CsI6_EL2FvsECsI
Entries 3463
Mean x 1185
Mean y 29.28
Std Dev x 654.6
Std Dev y 21.83
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
300
0
1000
2000
3000
4000

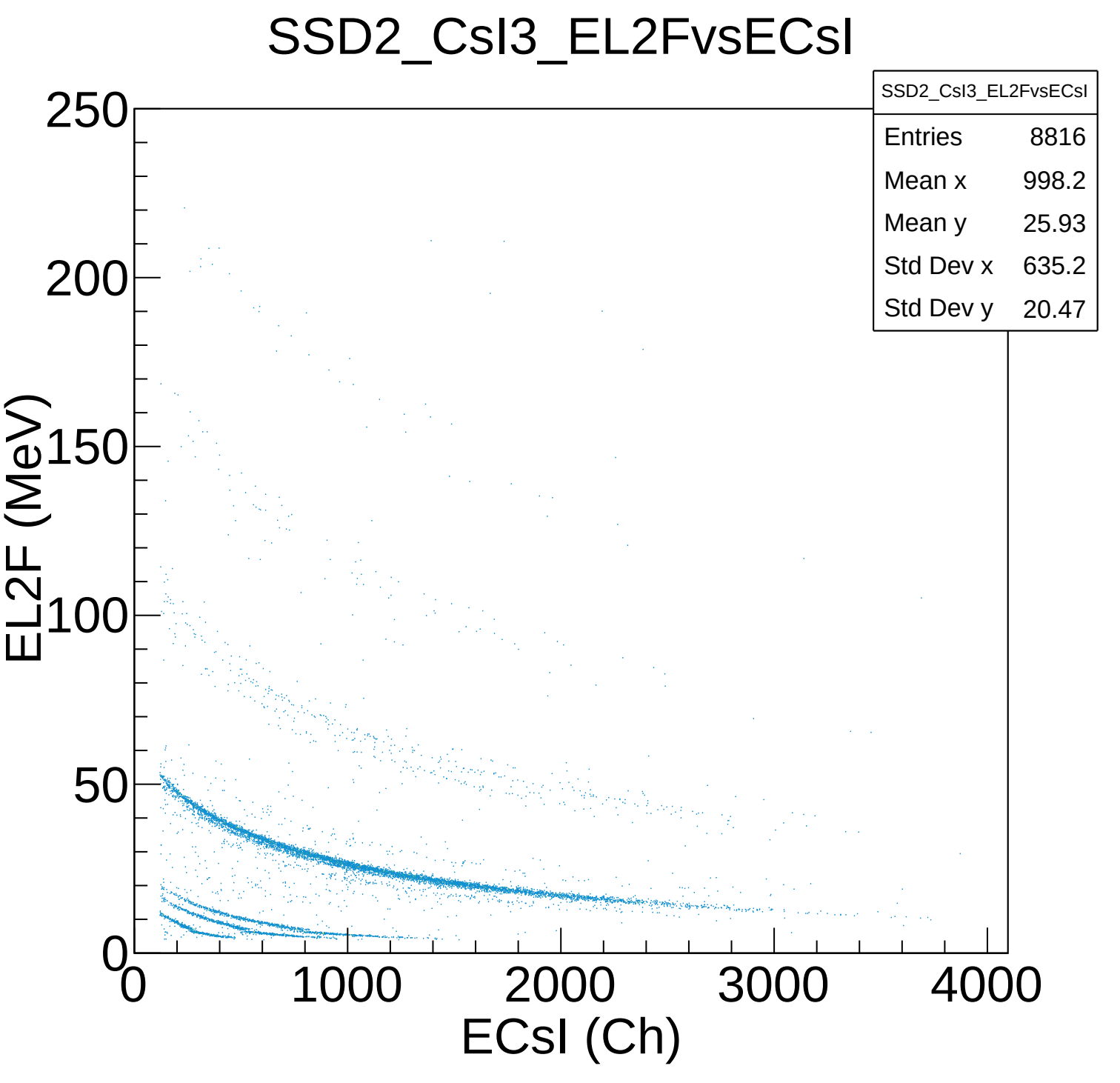

SSD1_CsI7_EL2FvsECsI
SSD1_CsI7_EL2FvsECsI
Entries 4866
Mean x 1218
Mean y 27.88
Std Dev x 645.9
Std Dev y 20.19
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
300
0
1000
2000
3000
4000

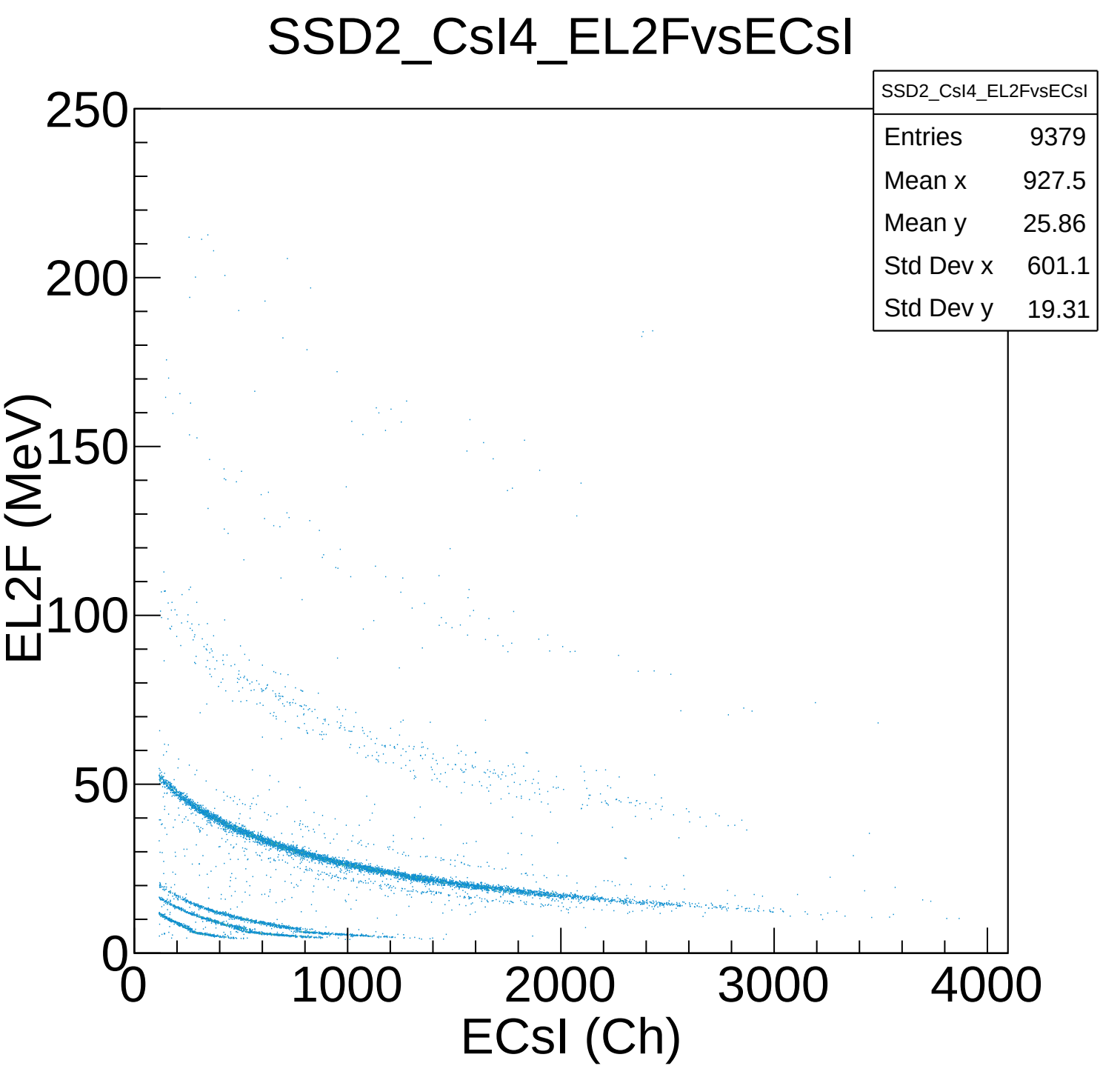

SSD1_CsI8_EL2FvsECsI
SSD1_CsI8_EL2FvsECsI
Entries 4625
Mean x 1301
Mean y 27.72
Std Dev x 641.9
Std Dev y 21.98
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
300
0
1000
2000
3000
4000

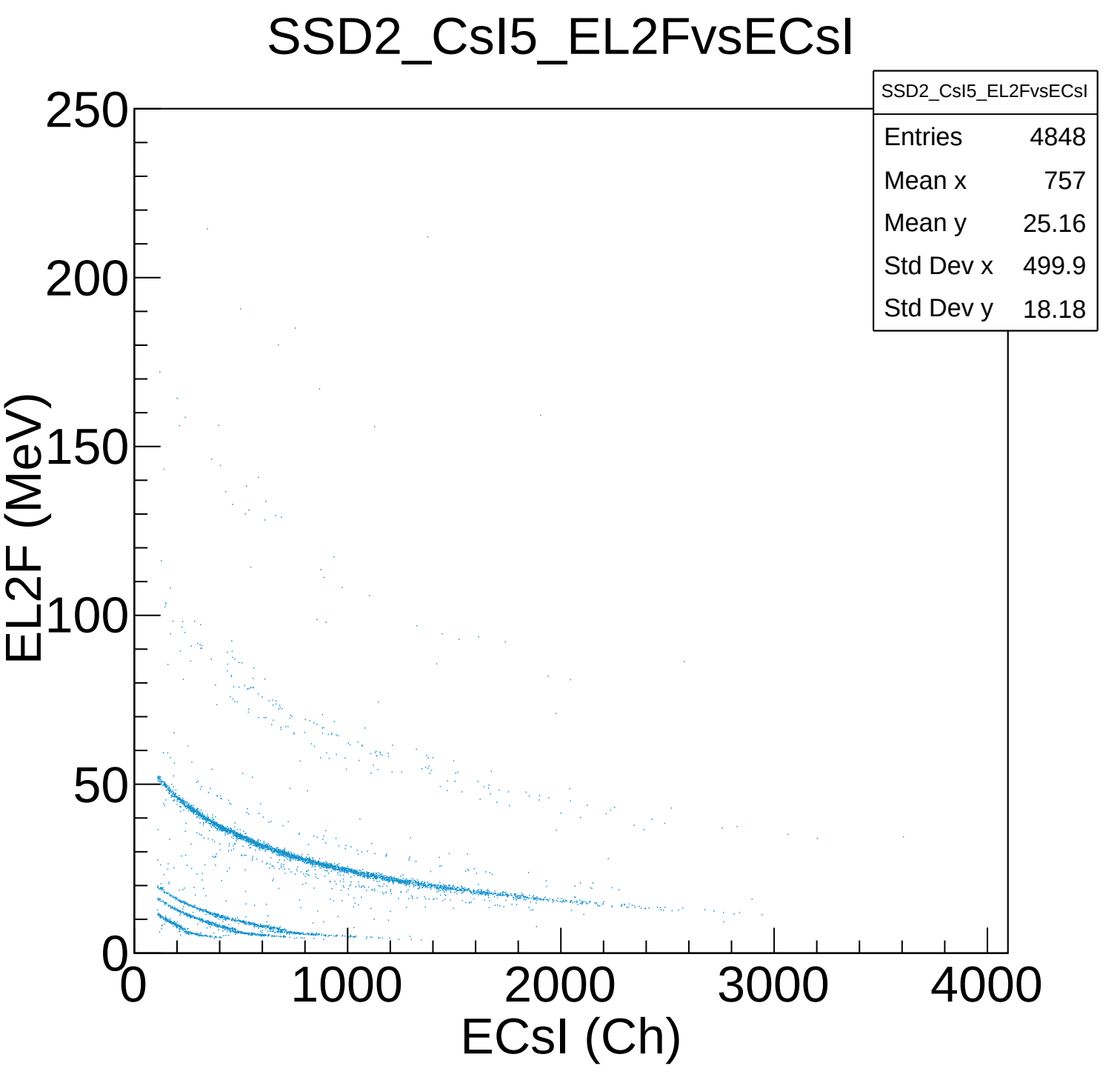

SSD2_CsI0_EL2FvsECsI
SSD2_CsI0_EL2FvsECsI
Entries 6801
Mean x 1027
Mean y 24.94
Std Dev x 681.9
Std Dev y 18.83
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
0
1000
2000
3000
4000

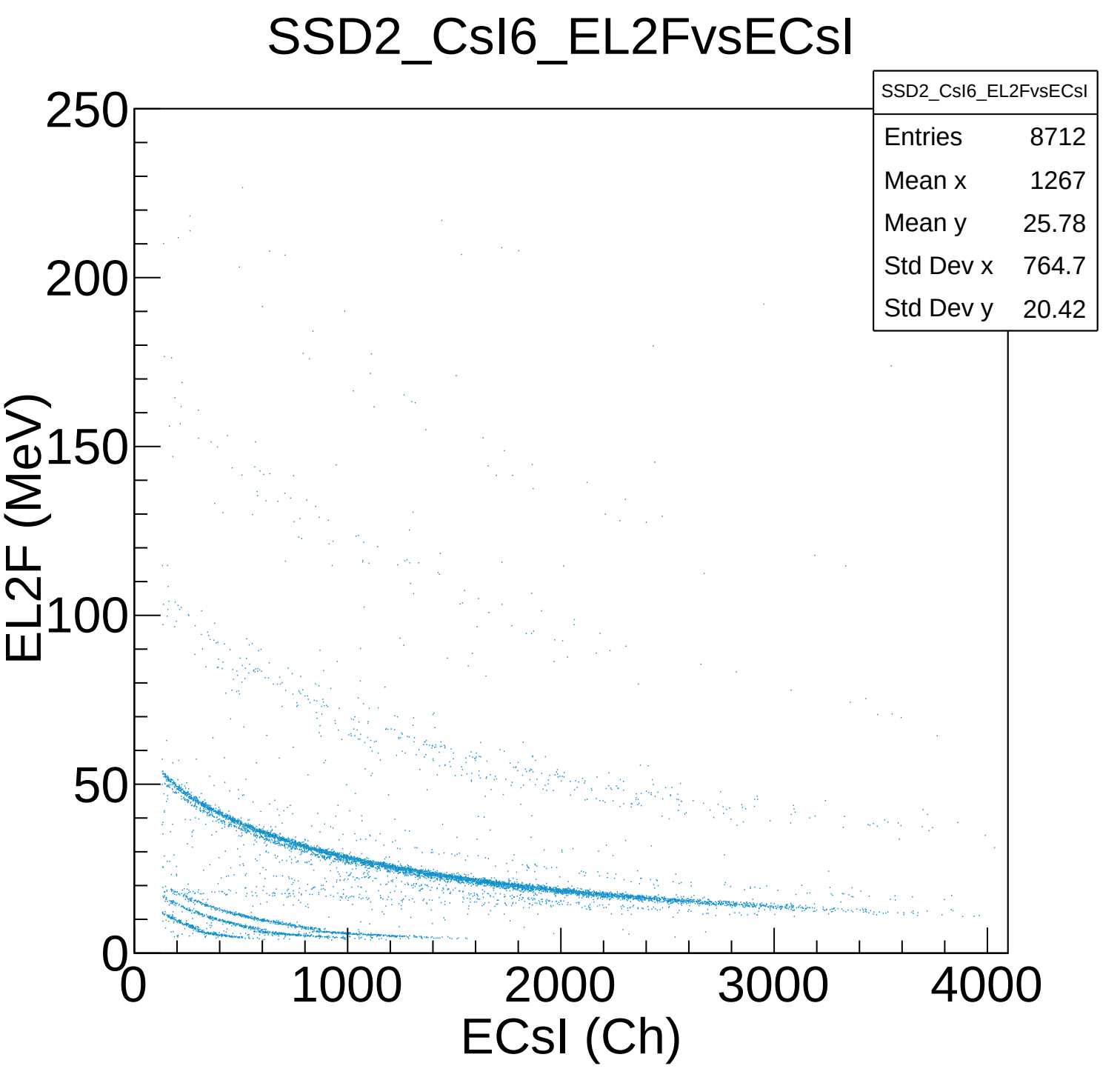

SSD2_CsI1_EL2FvsECsI
SSD2_CsI1_EL2FvsECsI
Entries 7330
Mean x 843
Mean y 24.79
Std Dev x 569.2
Std Dev y 18.63
250
200
150
100
50
0
EL2F (MeV)
0
1000
2000
3000
4000
ECsI (Ch)

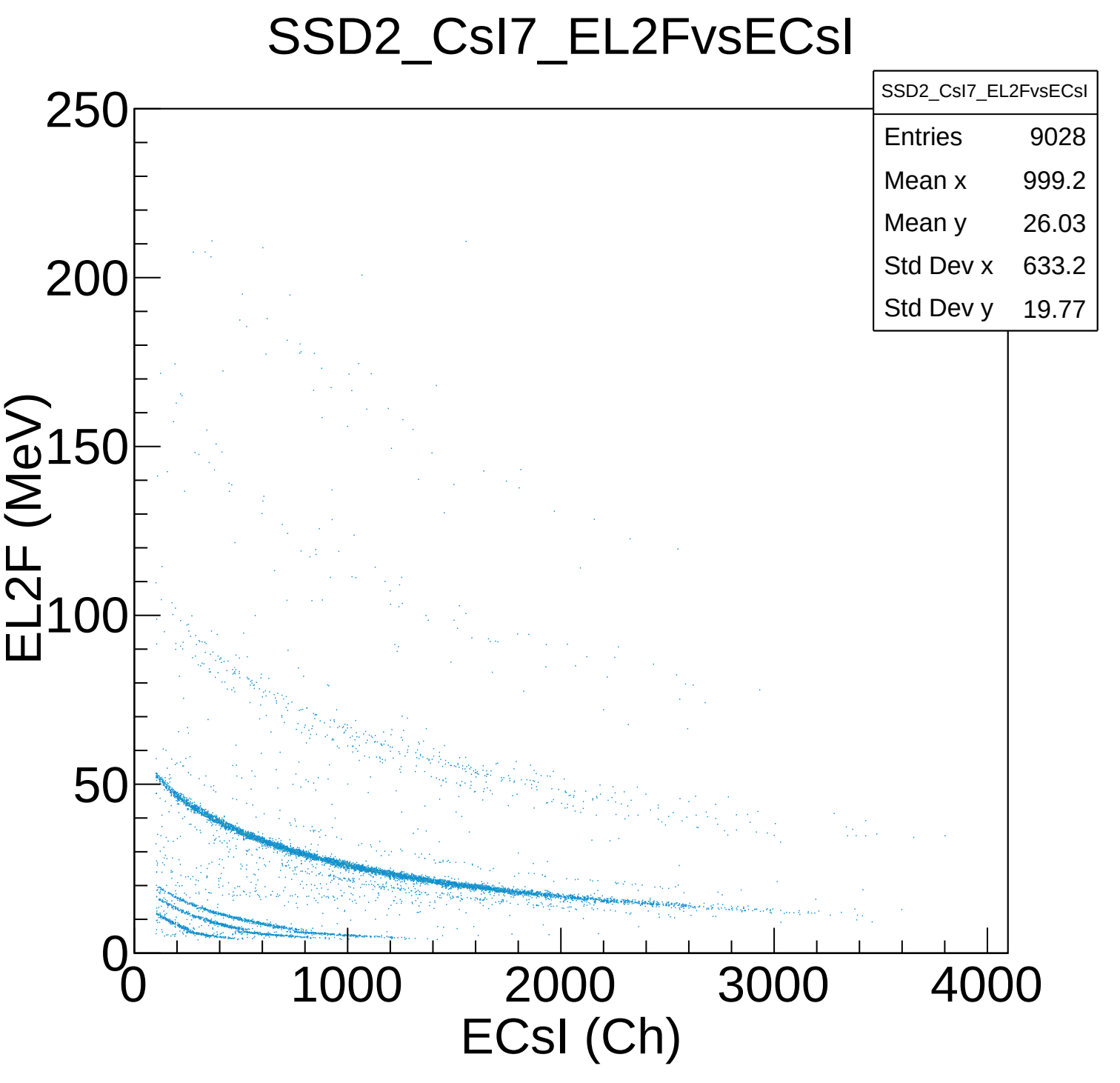

SSD2_CsI2_EL2FvsECsI
SSD2_CsI2_EL2FvsECsI
Entries 3784
Mean x 812.4
Mean y 24.22
Std Dev x 547.5
Std Dev y 17.75
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
0
1000
2000
3000
4000


SSD2_CsI3_EL2FvsECsI
SSD2_CsI3_EL2FvsECsI
Entries 8816
Mean x 998.2
Mean y 25.93
Std Dev x 635.2
Std Dev y 20.47
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
0
1000
2000
3000
4000


SSD2_CsI4_EL2FvsECsI
SSD2_CsI4_EL2FvsECsI
Entries 9379
Mean x 927.5
Mean y 25.86
Std Dev x 601.1
Std Dev y 19.31
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
0
1000
2000
3000
4000


SSD2_CsI5_EL2FvsECsI
SSD2_CsI5_EL2FvsECsI
Entries 4848
Mean x 757
Mean y 25.16
Std Dev x 499.9
Std Dev y 18.18
250
200
150
100
50
0
EL2F (MeV)
0
1000
2000
3000
4000
ECsI (Ch)


SSD2_CsI6_EL2FvsECsI
SSD2_CsI6_EL2FvsECsI
Entries 8712
Mean x 1267
Mean y 25.78
Std Dev x 764.7
Std Dev y 20.42
EL2F (MeV)
ECsI (Ch)
0
50
100
150
200
250
0
1000
2000
3000
4000


SSD2_CsI7_EL2FvsECsI
SSD2_CsI7_EL2FvsECsI
Entries 9028
Mean x 999.2
Mean y 26.03
Std Dev x 633.2
Std Dev y 19.77
EL2F (MeV)
ECsI (Ch)
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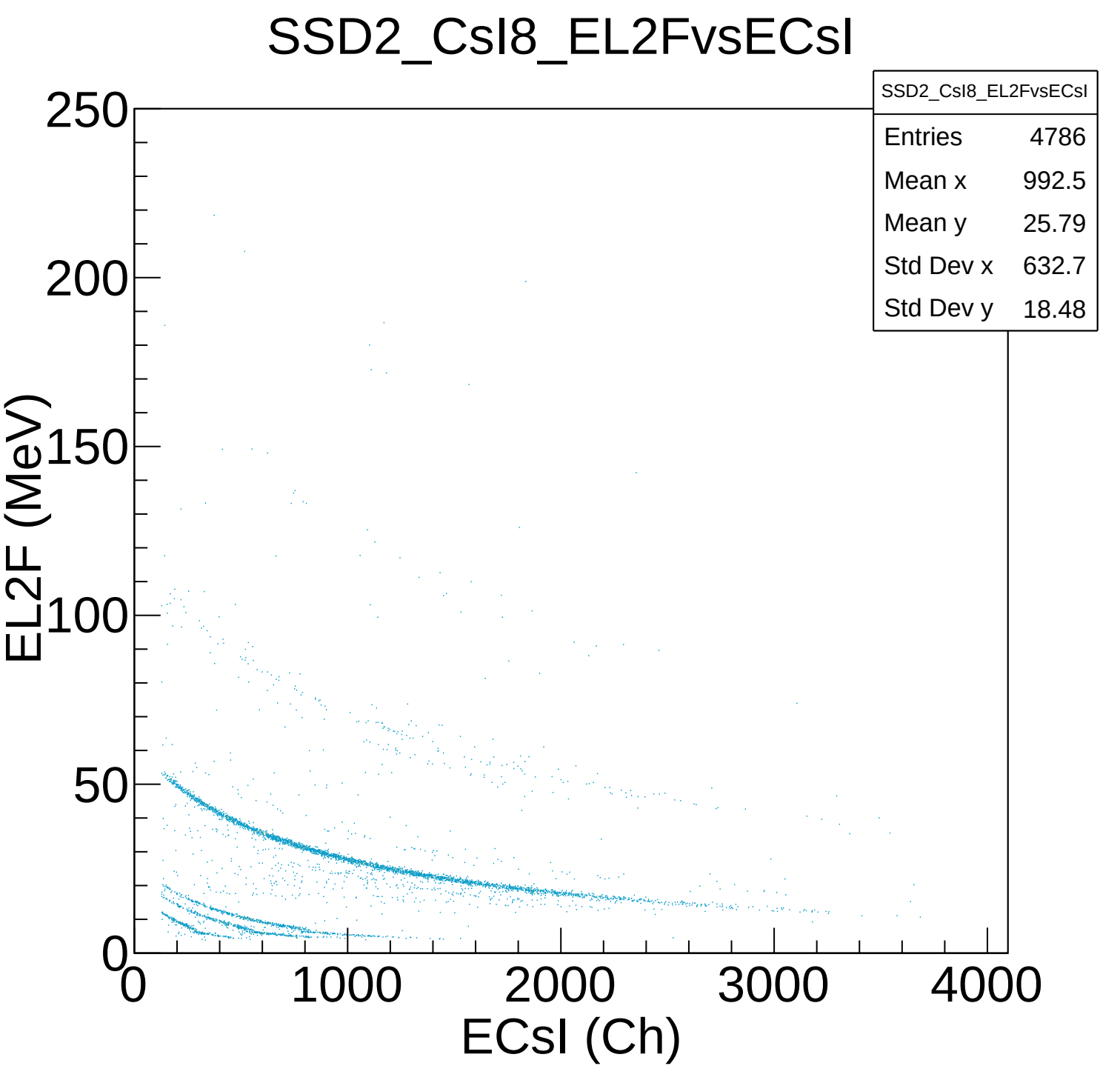
50
100
150
200
250
0
1000
2000
3000
4000


SSD2_CsI8_EL2FvsECsI
SSD2_CsI8_EL2FvsECsI
Entries 4786
Mean x 992.5
Mean y 25.79
Std Dev x 632.7
Std Dev y 18.48
EL2F (MeV)
ECsI (Ch)
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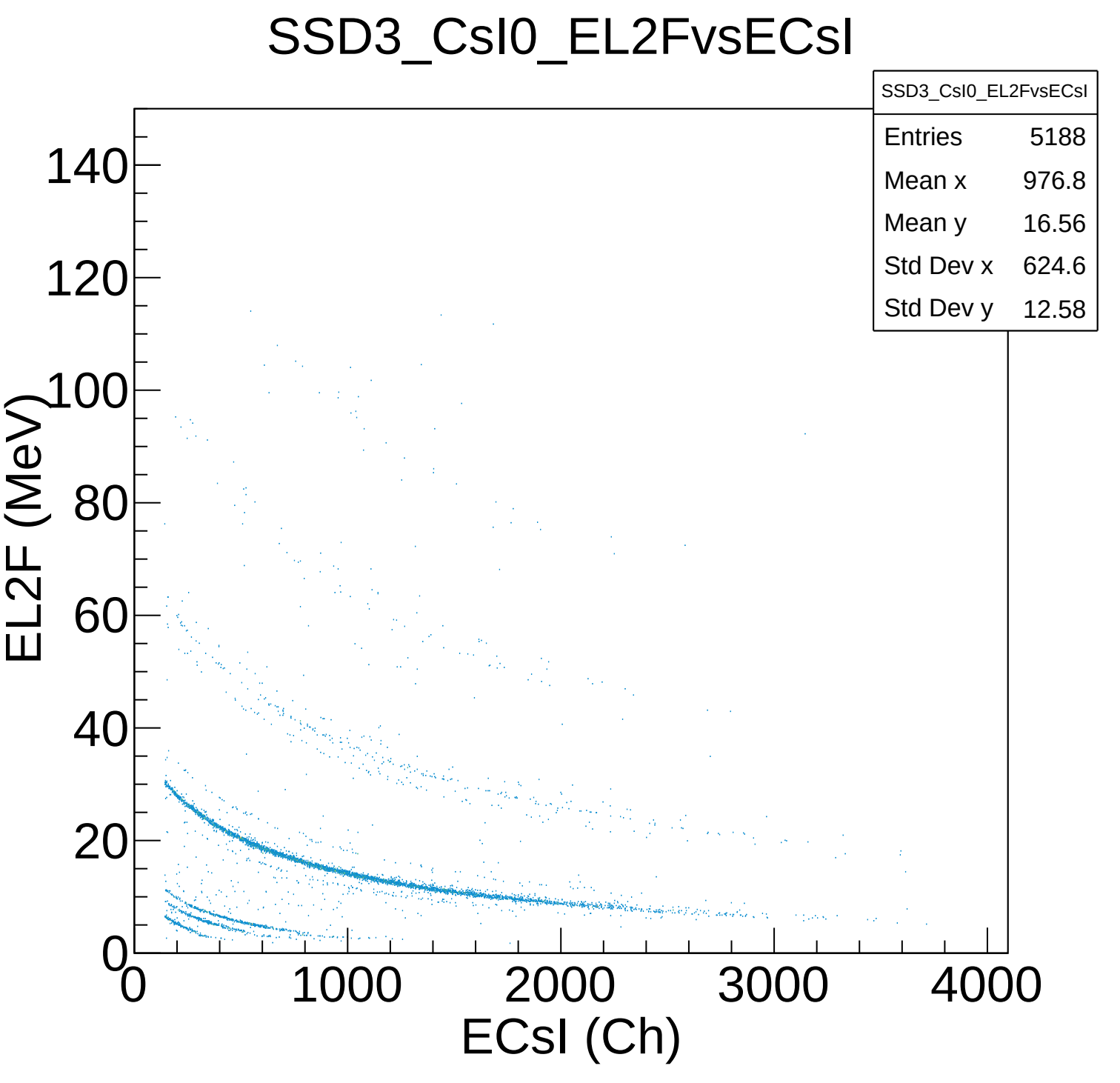
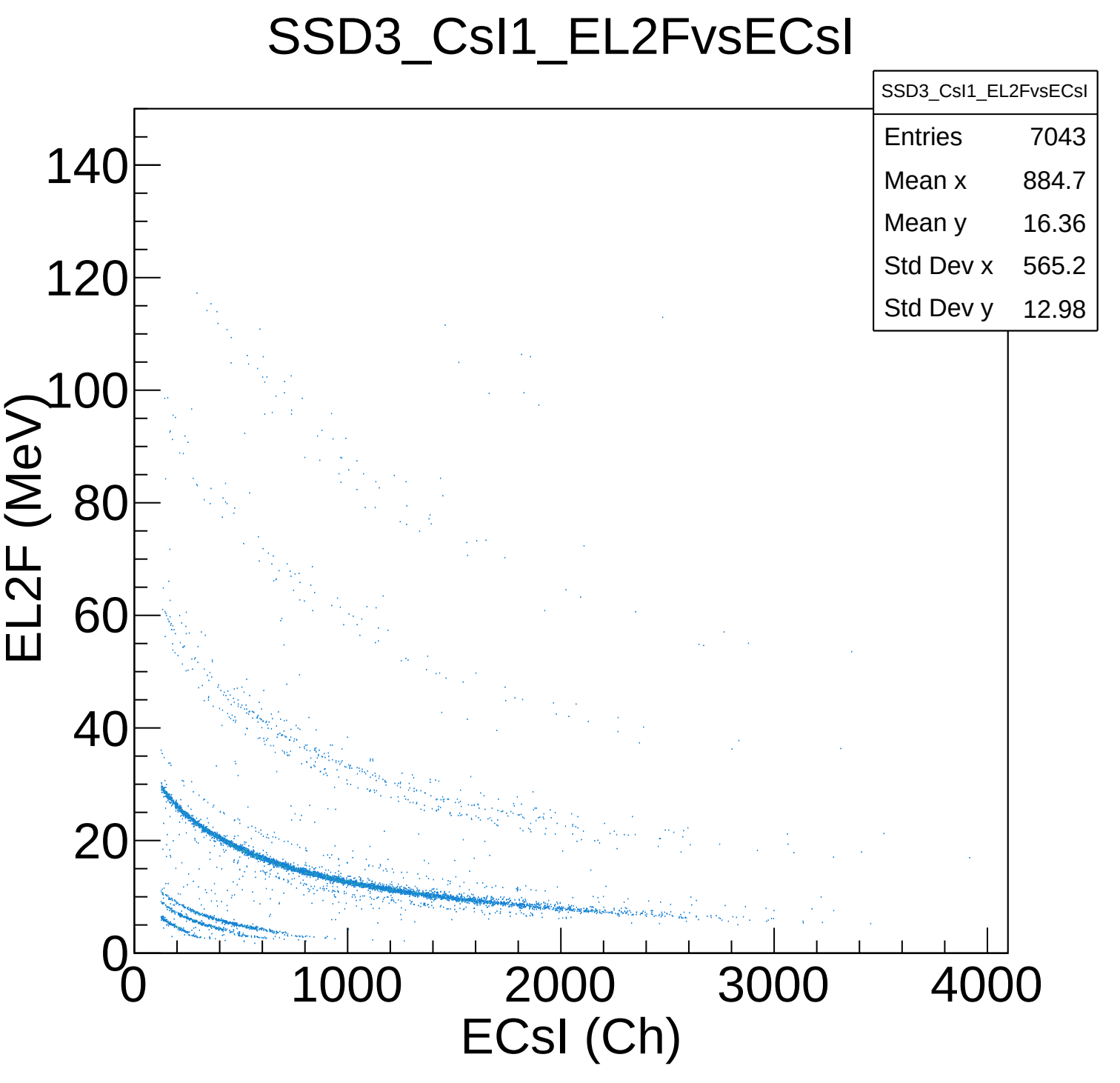
100
150
200
250
0
1000
2000
3000
4000


SSD3_CsI0_EL2FvsECsI
SSD3_CsI0_EL2FvsECsI
Entries 5188
Mean x 976.8
Mean y 16.56
Std Dev x 624.6
Std Dev y 12.58
EL2F (MeV)
ECsI (Ch)
0
20
40
60
80
100
120
140
0
1000
2000
3000
4000


SSD3_CsI1_EL2FvsECsI
SSD3_CsI1_EL2FvsECsI
Entries 7043
Mean x 884.7
Mean y 16.36
Std Dev x 565.2
Std Dev y 12.98
EL2F (MeV)
ECsI (Ch)
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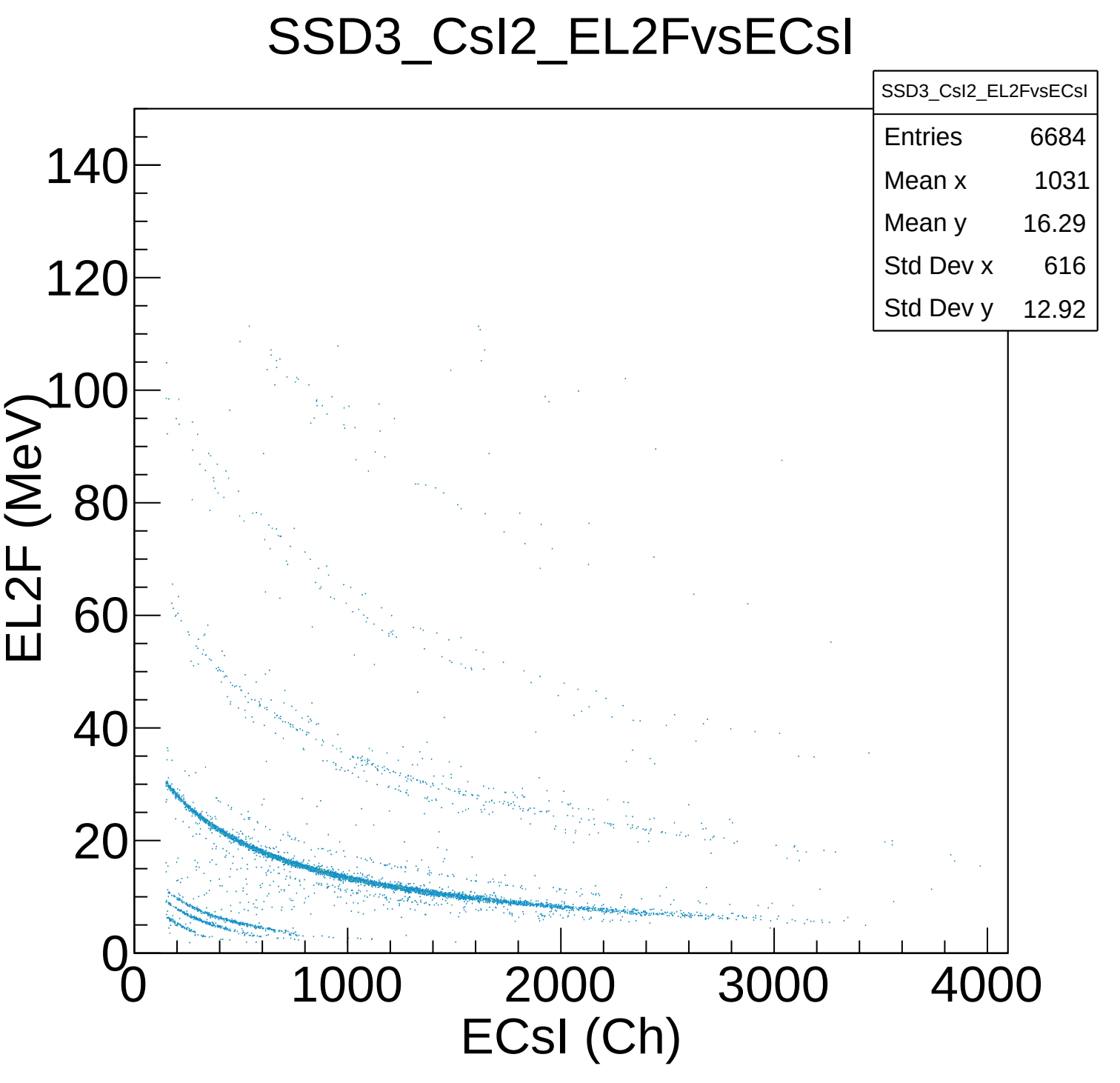
40
60
80
100
120
140
0
1000
2000
3000
4000


SSD3_CsI2_EL2FvsECsI
SSD3_CsI2_EL2FvsECsI
Entries 6684
Mean x 1031
Mean y 16.29
Std Dev x 616
Std Dev y 12.92
EL2F (MeV)
ECsI (Ch)
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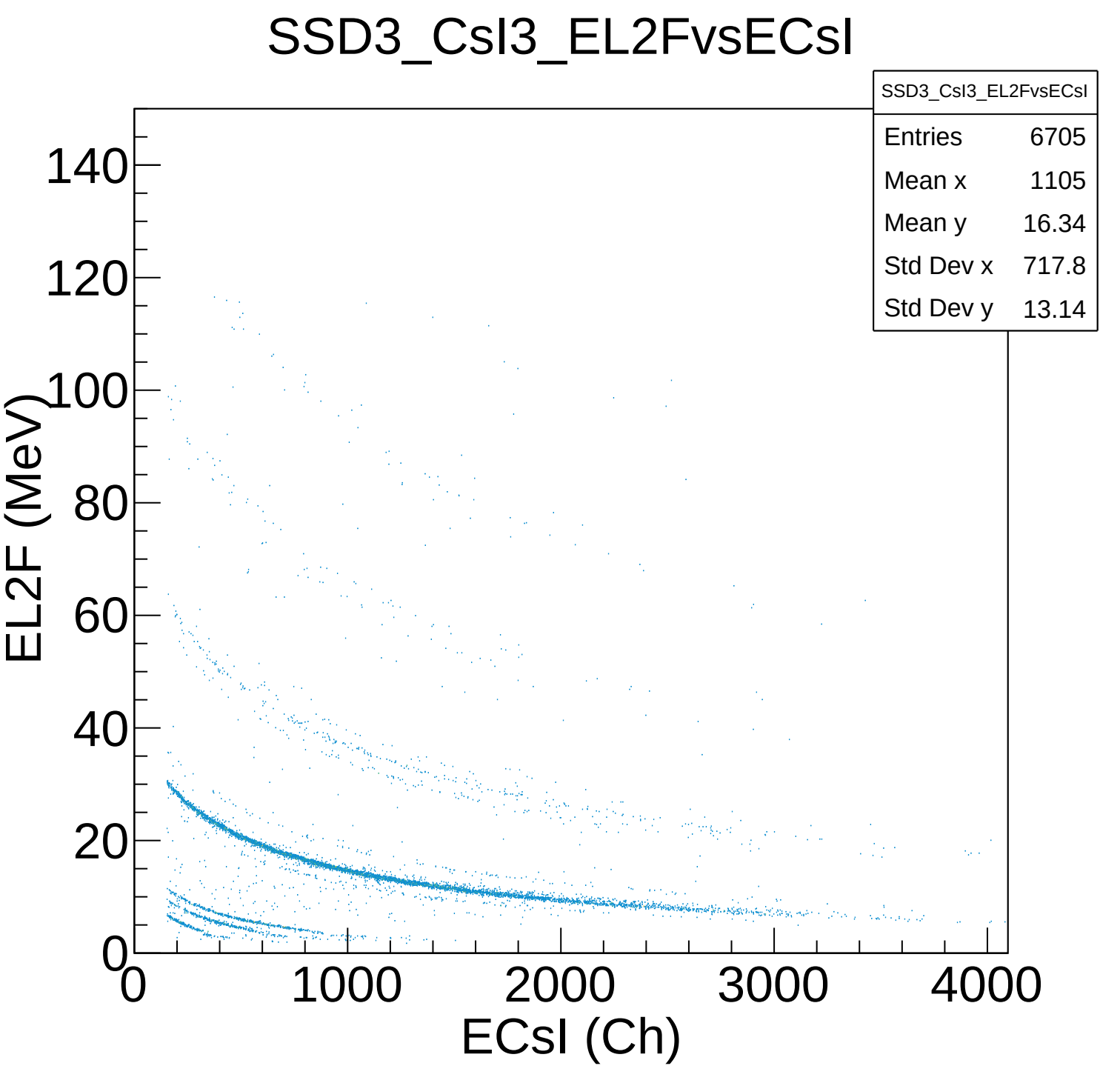
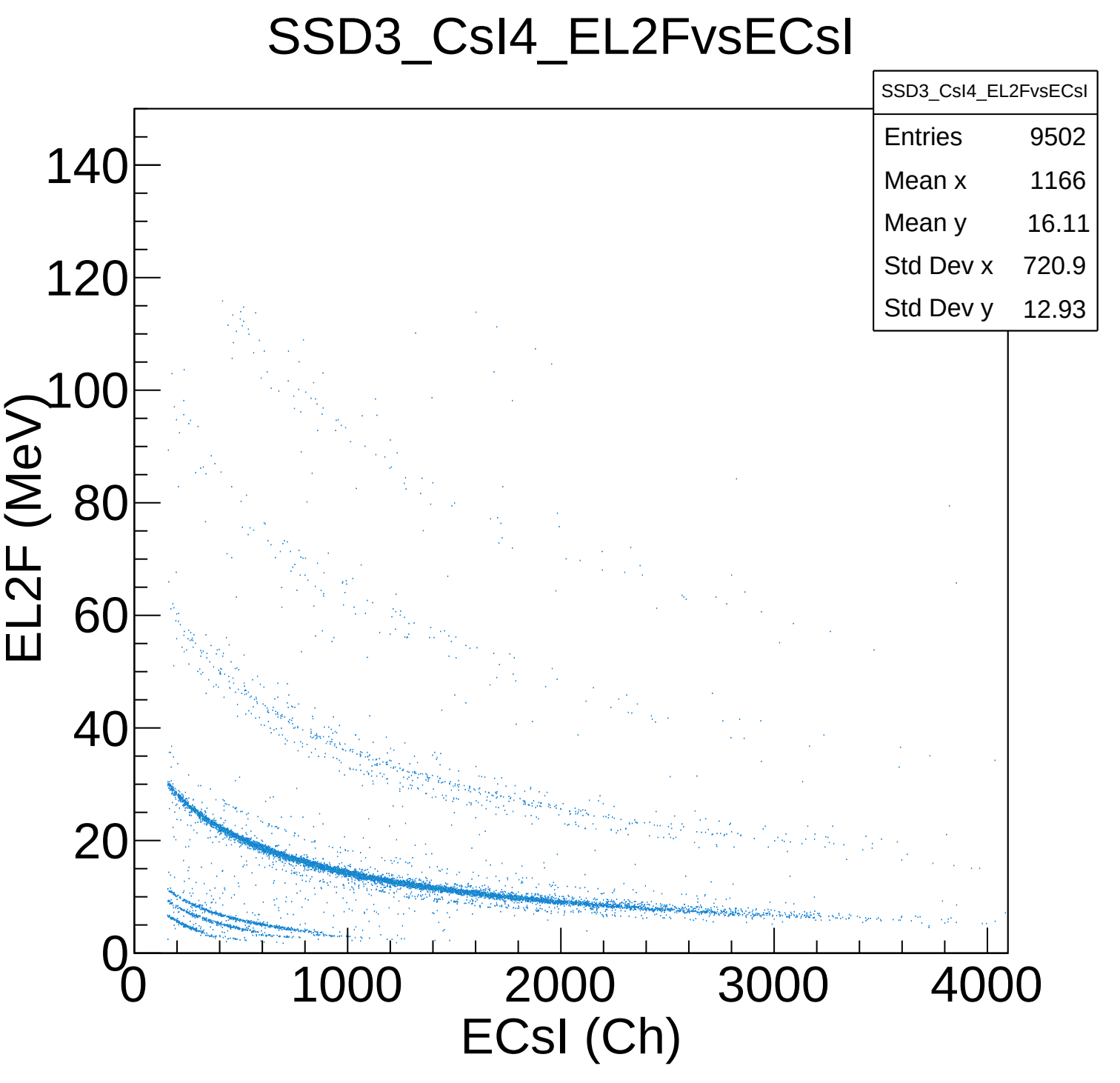
20
40
60
80
100
120
140
0
1000
2000
3000
4000


SSD3_CsI3_EL2FvsECsI
SSD3_CsI3_EL2FvsECsI
Entries 6705
Mean x 1105
Mean y 16.34
Std Dev x 717.8
Std Dev y 13.14
EL2F (MeV)
ECsI (Ch)
0
20
40
60
80
100
120
140
0
1000
2000
3000
4000


SSD3_CsI4_EL2FvsECsI
SSD3_CsI4_EL2FvsECsI
Entries 9502
Mean x 1166
Mean y 16.11
Std Dev x 720.9
Std Dev y 12.93
EL2F (MeV)
ECsI (Ch)
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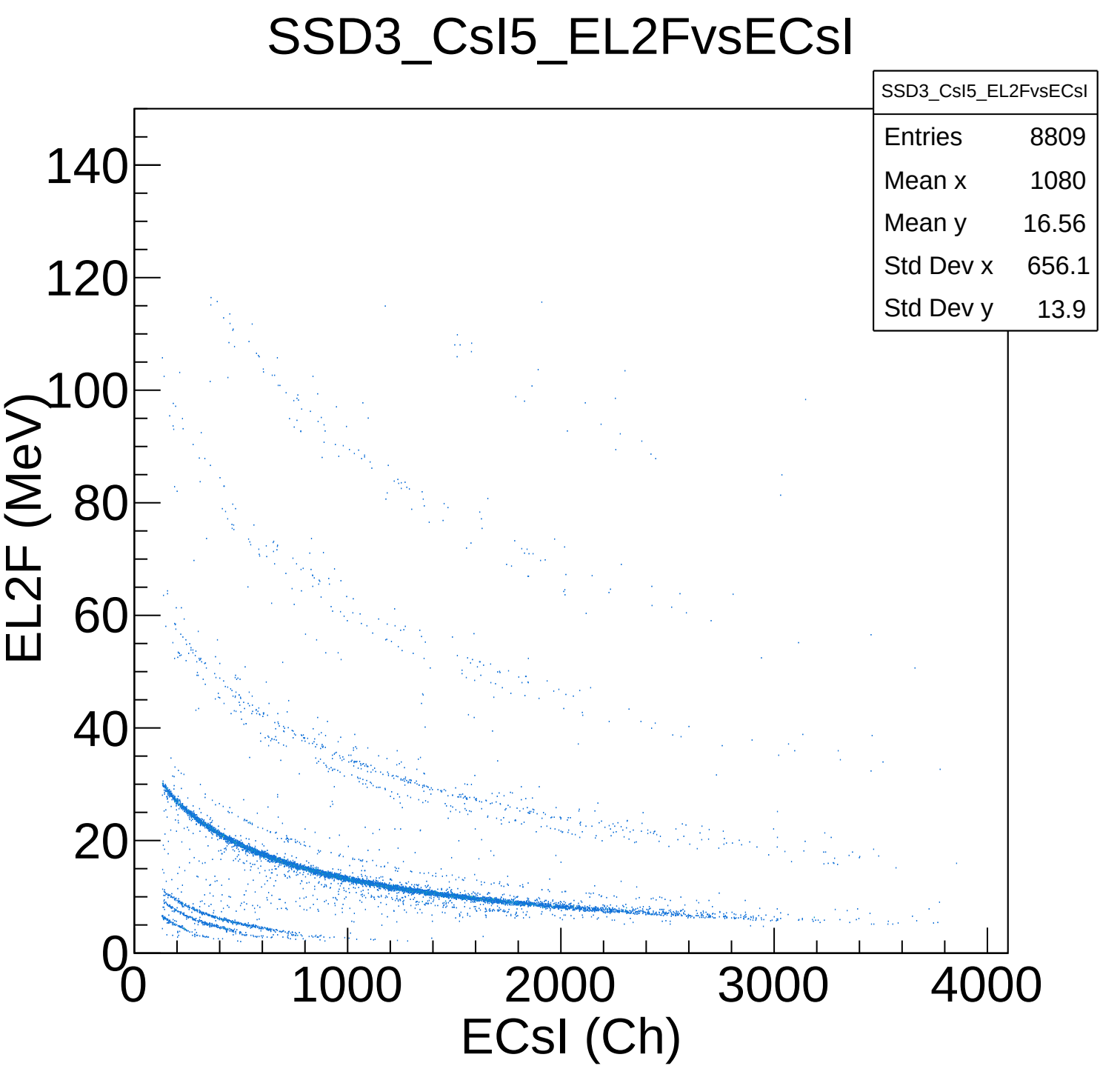
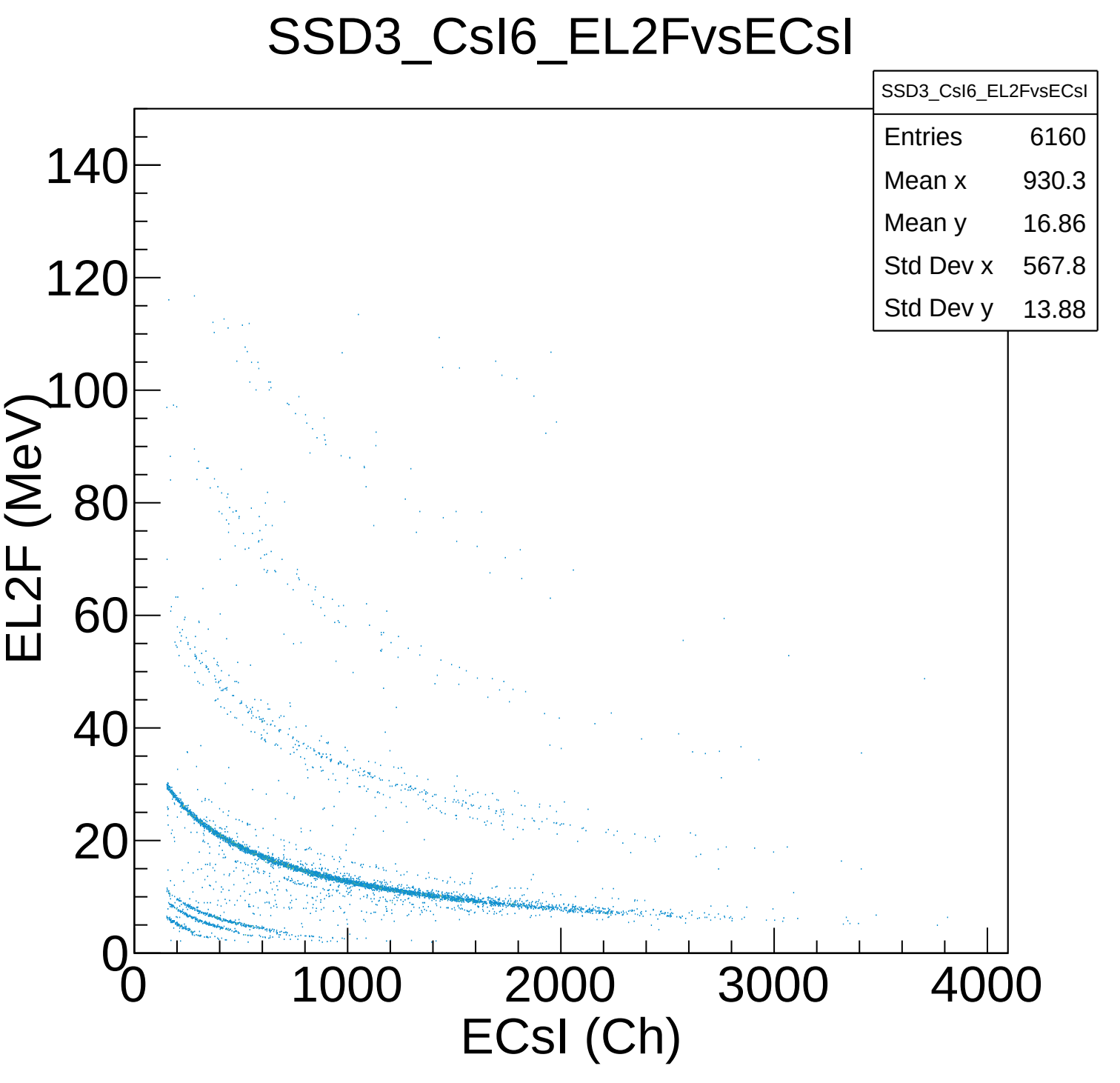
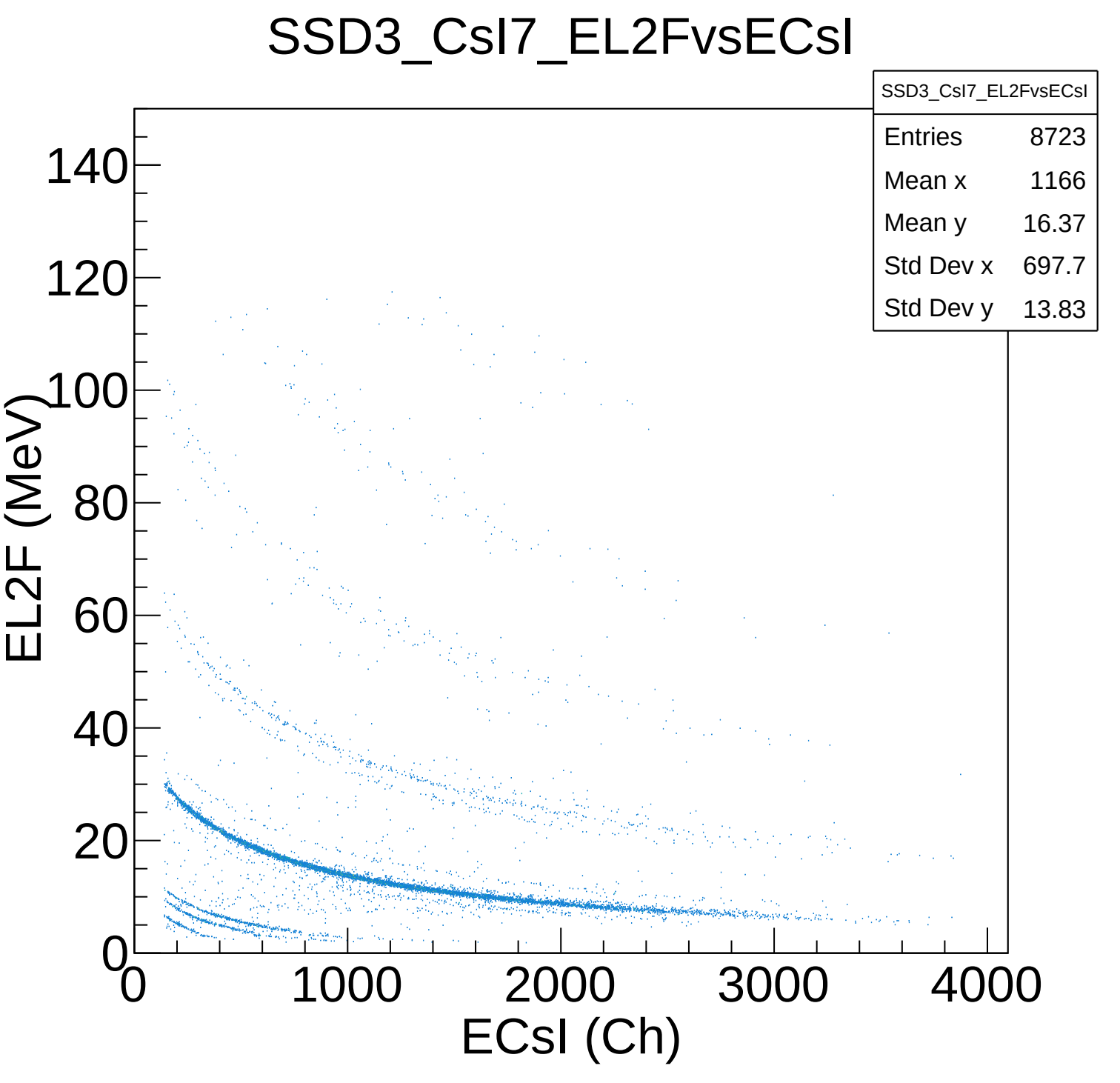
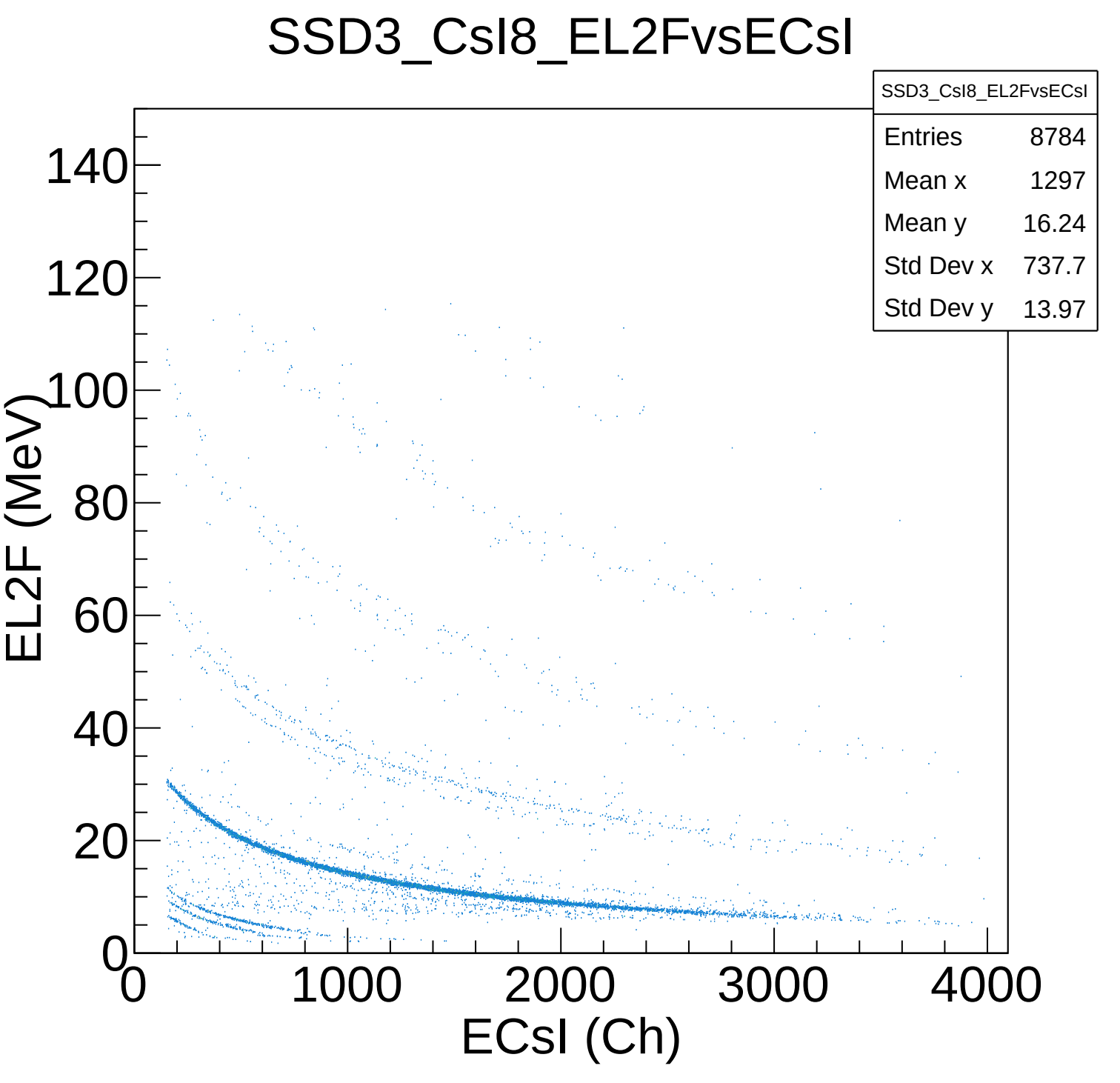
20
40
60
80
100
120
140
0
1000
2000
3000
4000


SSD3_CsI5_EL2FvsECsI
SSD3_CsI5_EL2FvsECsI
Entries 8809
Mean x 1080
Mean y 16.56
Std Dev x 656.1
Std Dev y 13.9
EL2F (MeV)
ECsI (Ch)
0
20
40
60
80
100
120
140
0
1000
2000
3000
4000


SSD3_CsI6_EL2FvsECsI
SSD3_CsI6_EL2FvsECsI
Entries 6160
Mean x 930.3
Mean y 16.86
Std Dev x 567.8
Std Dev y 13.88
EL2F (MeV)
ECsI (Ch)
0
20
40
60
80
100
120
140
0
1000
2000
3000
4000


SSD3_CsI7_EL2FvsECsI
SSD3_CsI7_EL2FvsECsI
Entries 8723
Mean x 1166
Mean y 16.37
Std Dev x 697.7
Std Dev y 13.83
EL2F (MeV)
ECsI (Ch)
0
20
40
60
80
100
120
140
0
1000
2000
3000
4000


SSD3_CsI8_EL2FvsECsI
SSD3_CsI8_EL2FvsECsI
Entries 8784
Mean x 1297
Mean y 16.24
Std Dev x 737.7
Std Dev y 13.97
EL2F (MeV)
ECsI (Ch)
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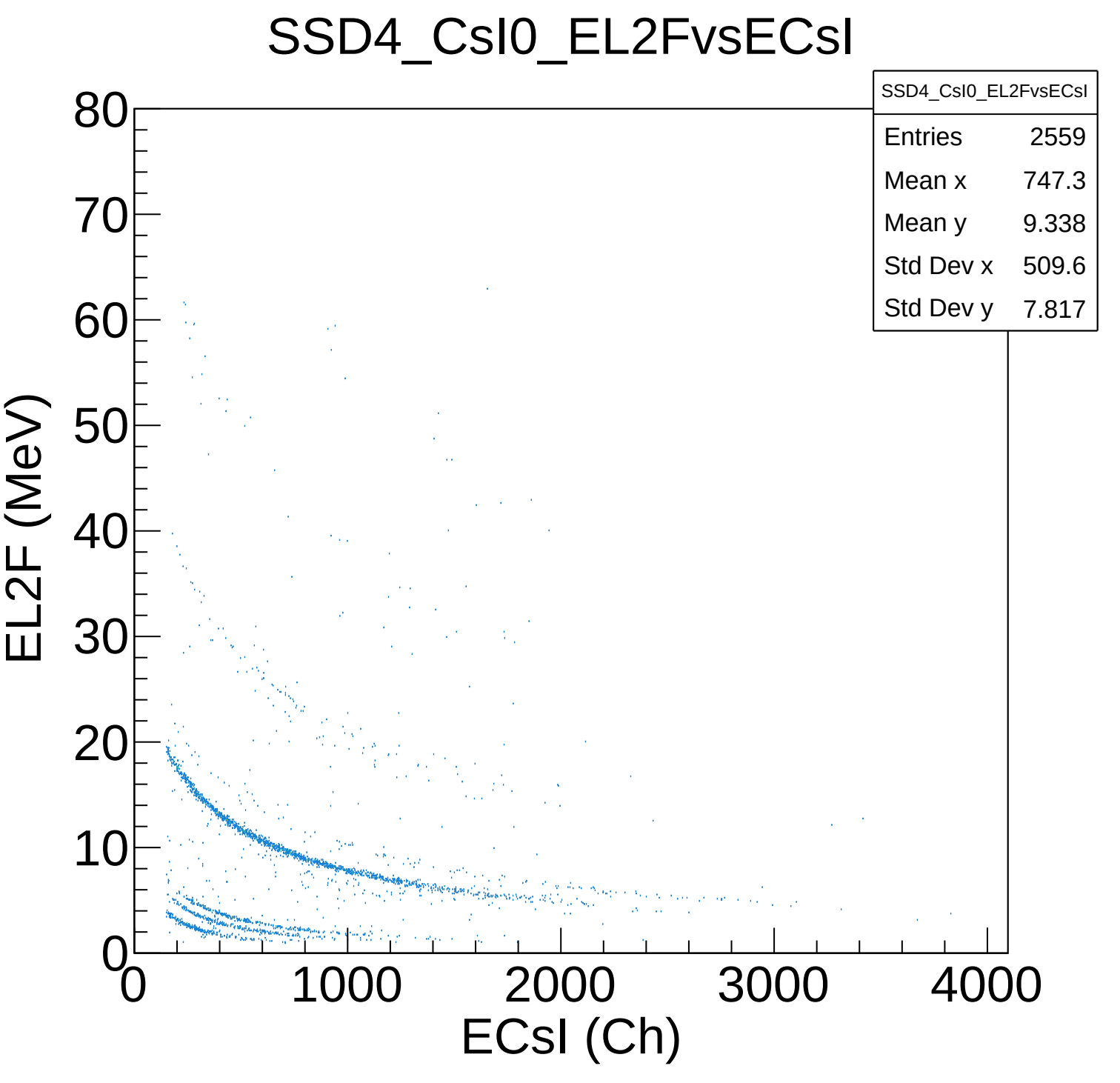
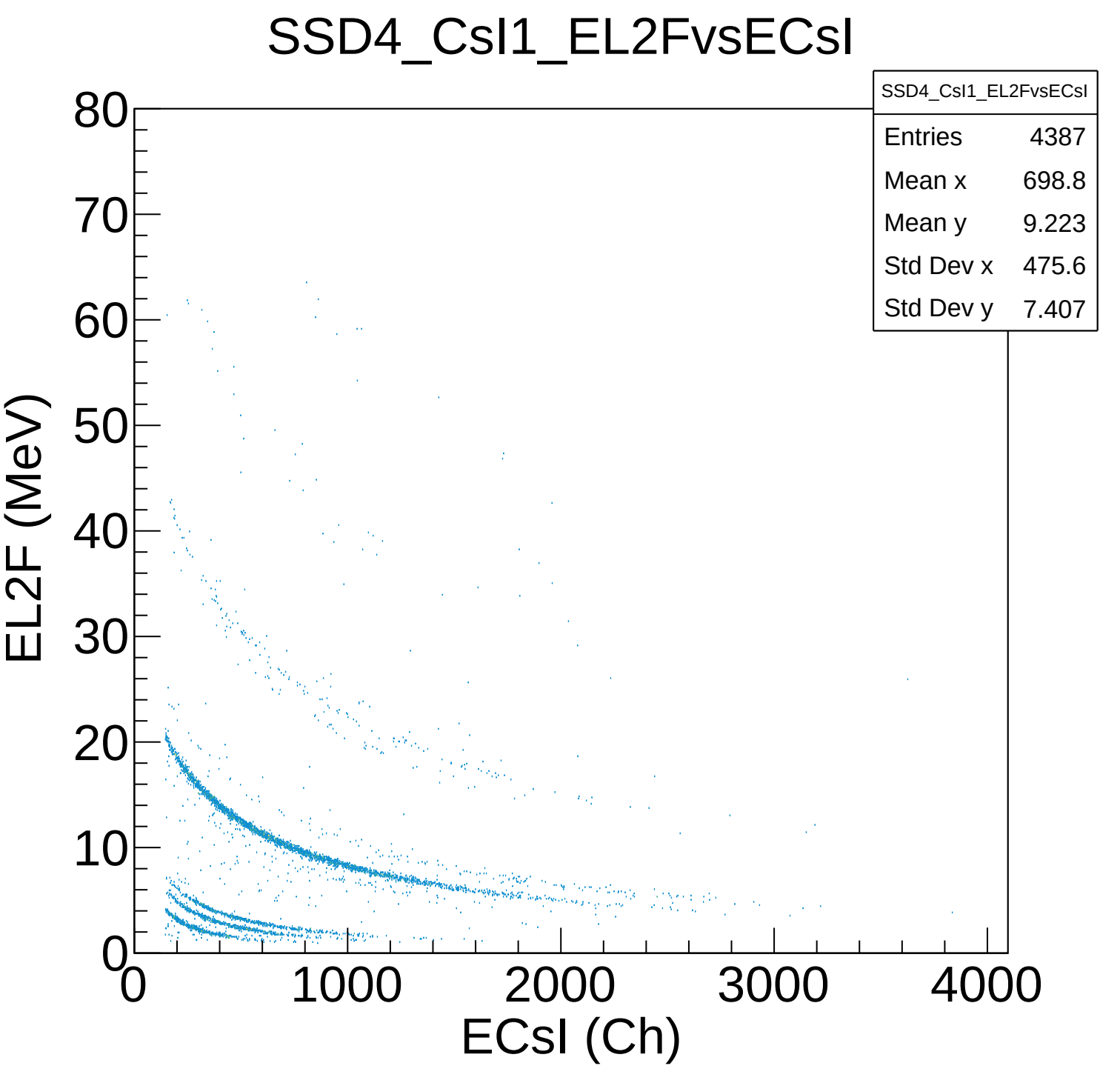
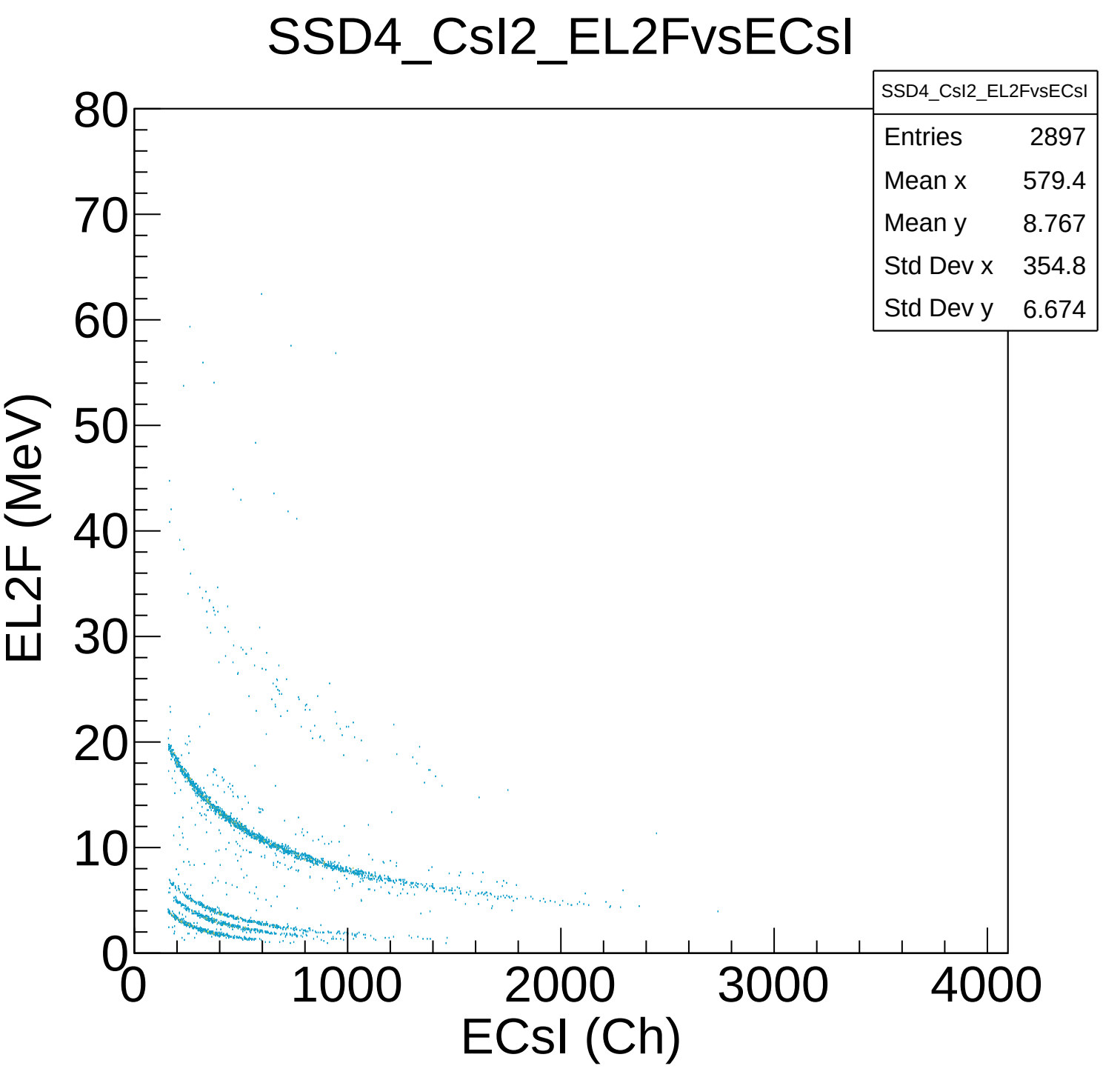
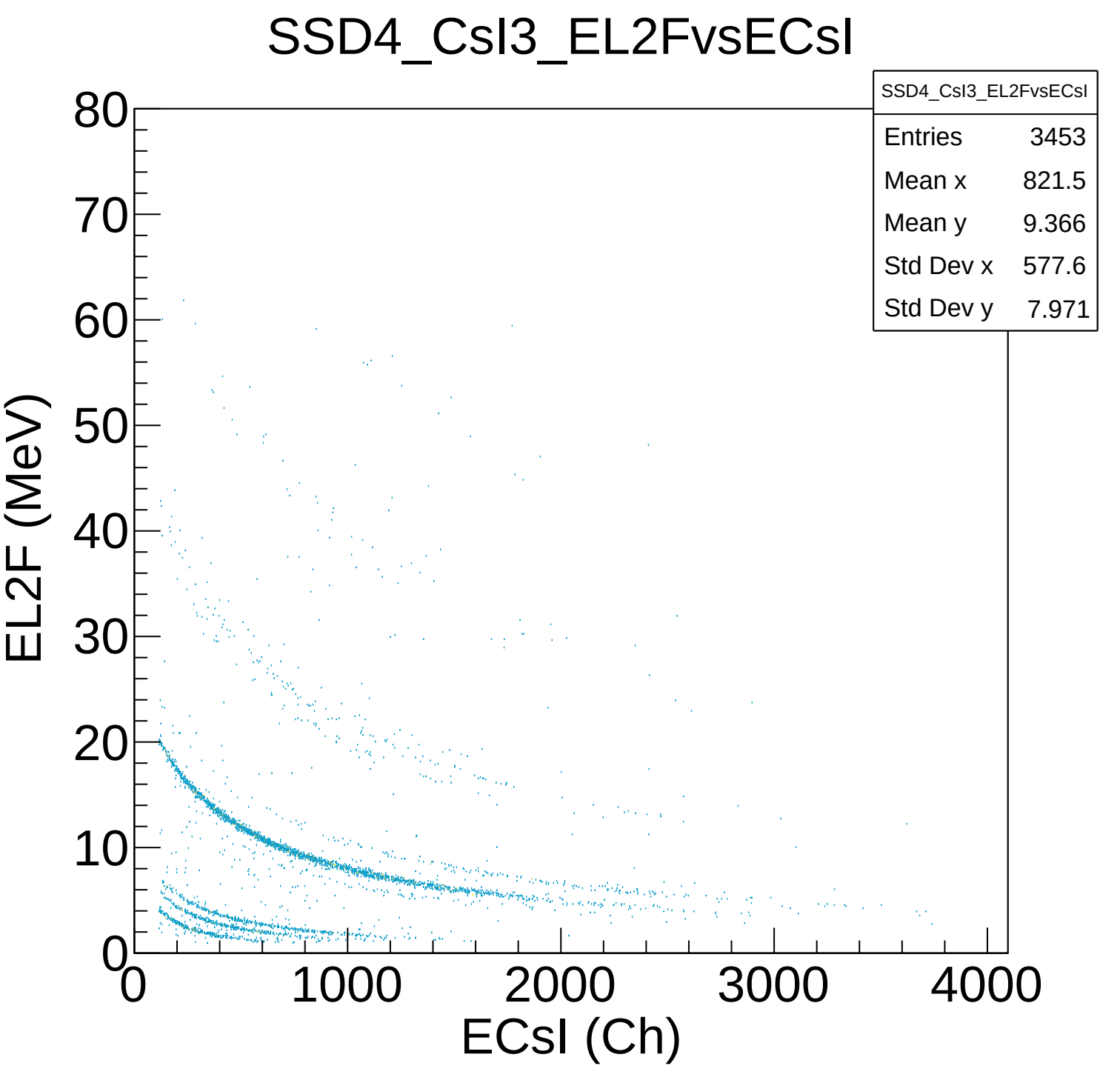
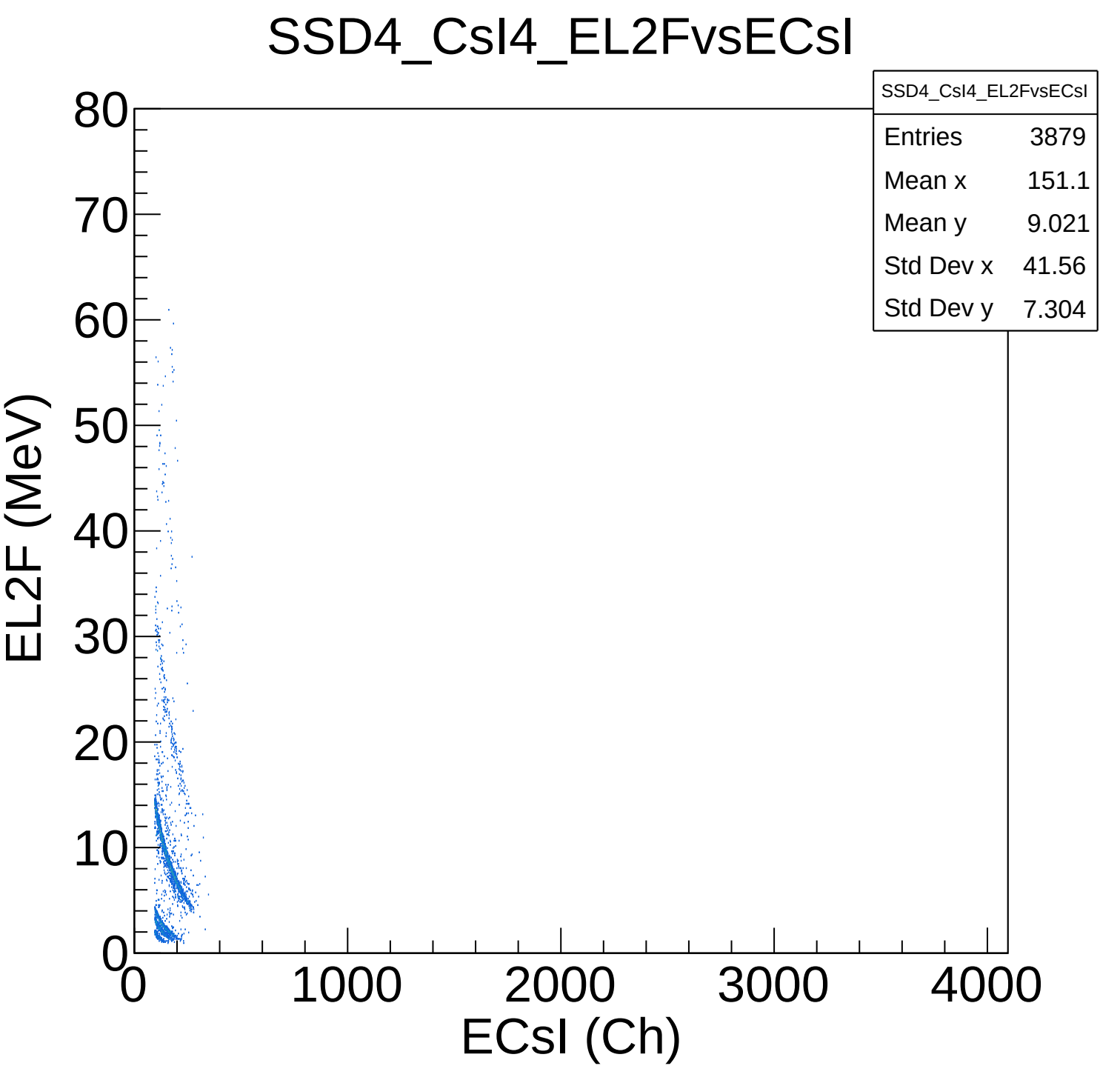
20
40
60
80
100
120
140
0
1000
2000
3000
4000

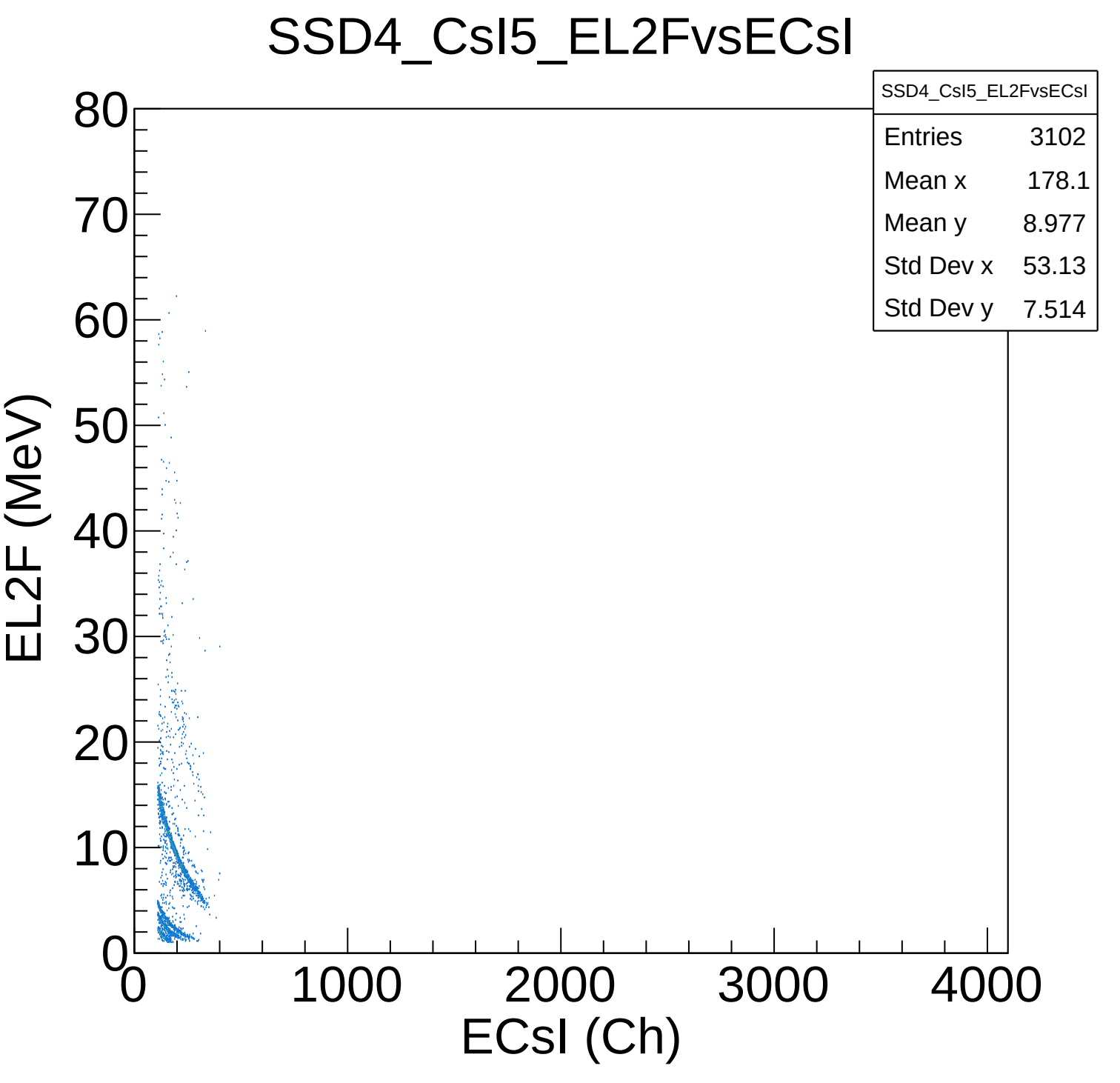

SSD4_CsI0_EL2FvsECsI
SSD4_CsI0_EL2FvsECsI
Entries 2559
Mean x 747.3
Mean y 9.338
Std Dev x 509.6
Std Dev y 7.817
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000

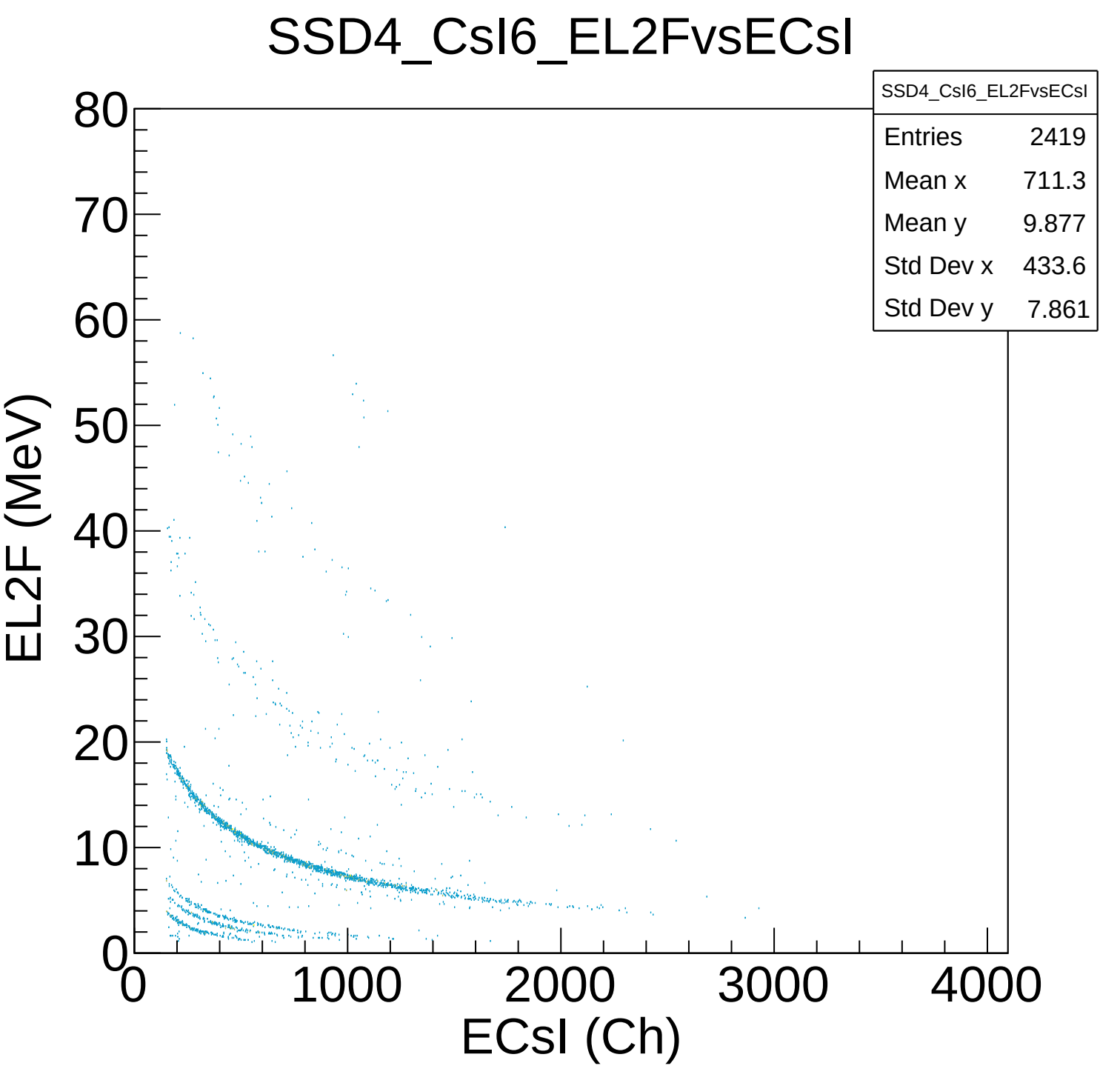

SSD4_CsI1_EL2FvsECsI
SSD4_CsI1_EL2FvsECsI
Entries 4387
Mean x 698.8
Mean y 9.223
Std Dev x 475.6
Std Dev y 7.407
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000

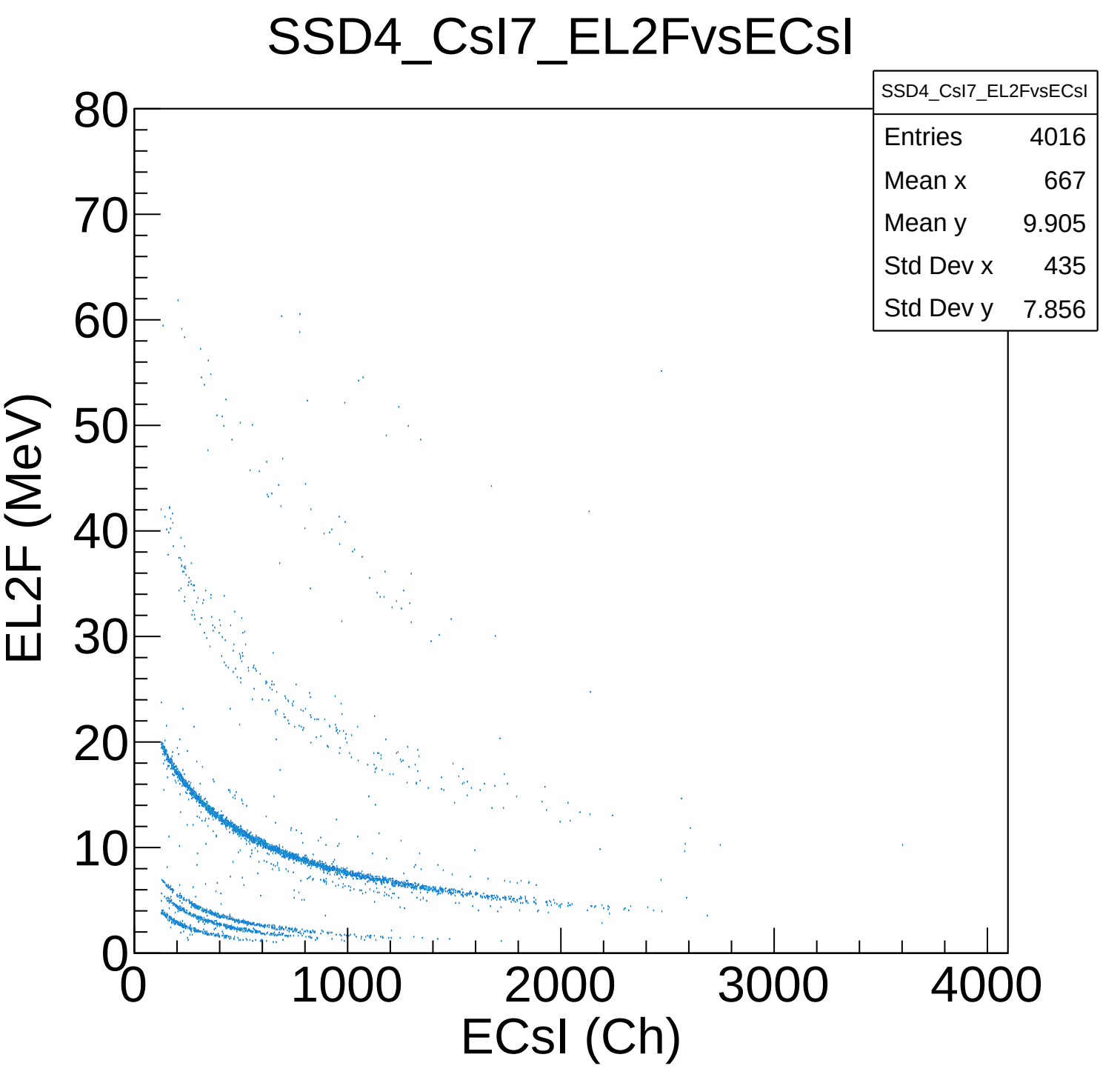

SSD4_CsI2_EL2FvsECsI
SSD4_CsI2_EL2FvsECsI
Entries 2897
Mean x 579.4
Mean y 8.767
Std Dev x 354.8
Std Dev y 6.674
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000

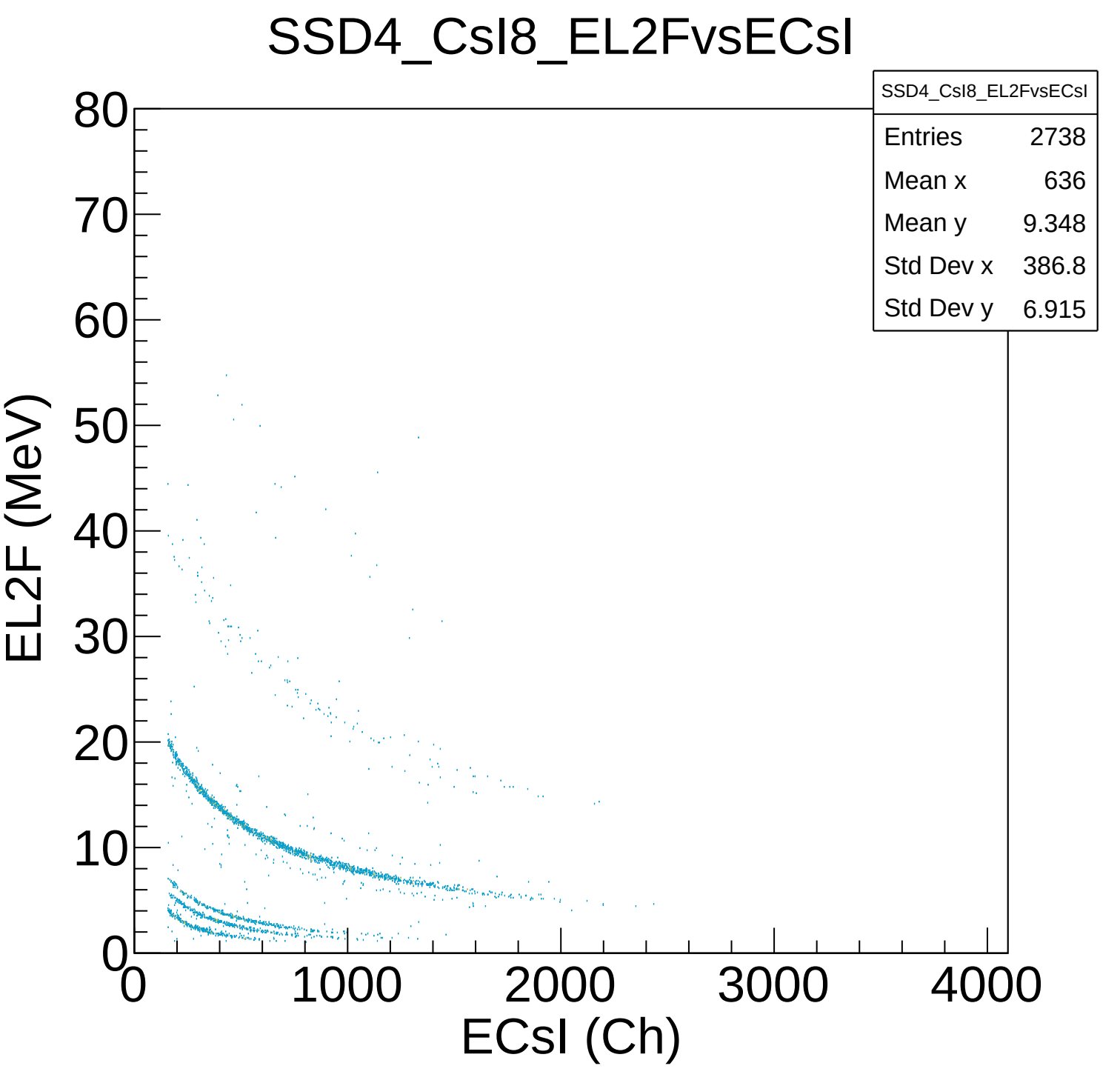

SSD4_CsI3_EL2FvsECsI
SSD4_CsI3_EL2FvsECsI
Entries 3453
Mean x 821.5
Mean y 9.366
Std Dev x 577.6
Std Dev y 7.971
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000


SSD4_CsI4_EL2FvsECsI
SSD4_CsI4_EL2FvsECsI
Entries 3879
Mean x 151.1
Mean y 9.021
Std Dev x 41.56
Std Dev y 7.304
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000


SSD4_CsI5_EL2FvsECsI
SSD4_CsI5_EL2FvsECsI
Entries 3102
Mean x 178.1
Mean y 8.977
Std Dev x 53.13
Std Dev y 7.514
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000


SSD4_CsI6_EL2FvsECsI
SSD4_CsI6_EL2FvsECsI
Entries 2419
Mean x 711.3
Mean y 9.877
Std Dev x 433.6
Std Dev y 7.861
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000


SSD4_CsI7_EL2FvsECsI
SSD4_CsI7_EL2FvsECsI
Entries 4016
Mean x 667
Mean y 9.905
Std Dev x 435
Std Dev y 7.856
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000


SSD4_CsI8_EL2FvsECsI
SSD4_CsI8_EL2FvsECsI
Entries 2738
Mean x 636
Mean y 9.348
Std Dev x 386.8
Std Dev y 6.915
EL2F (MeV)
ECsI (Ch)
0
10
20
30
40
50
60
70
80
0
1000
2000
3000
4000