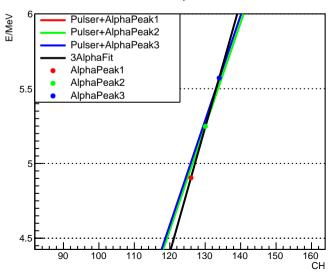
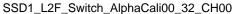
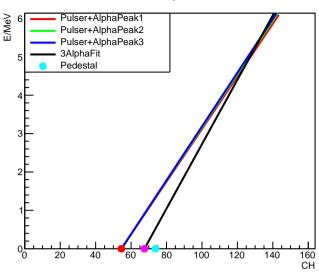
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH00

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit





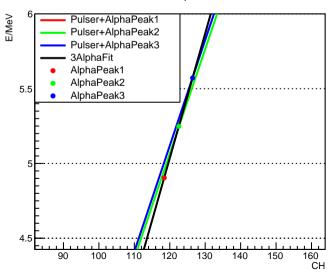




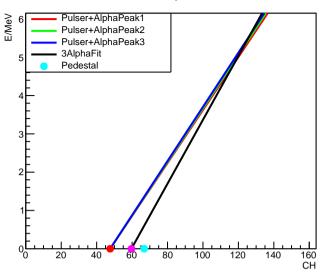
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH01

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

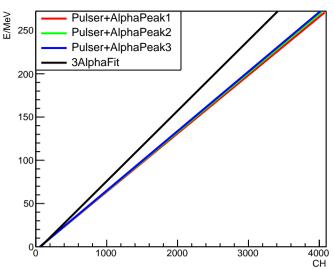




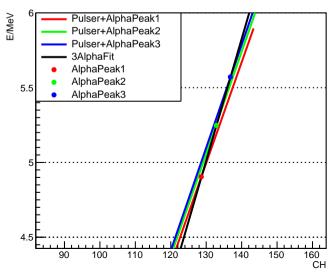


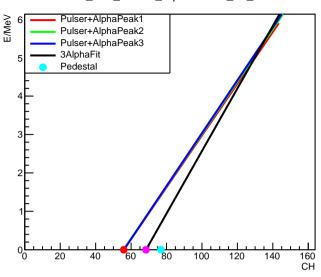


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH02

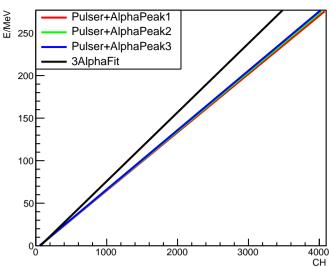


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH02

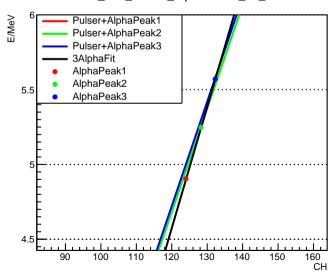




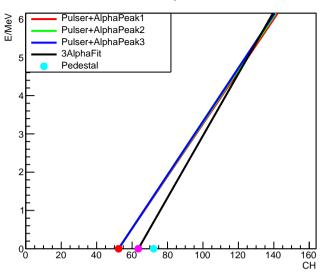
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH03



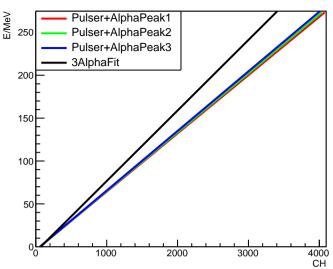
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH03



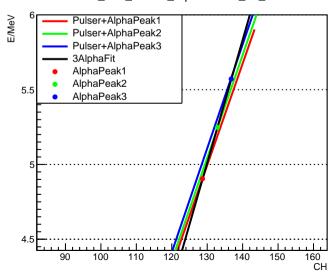


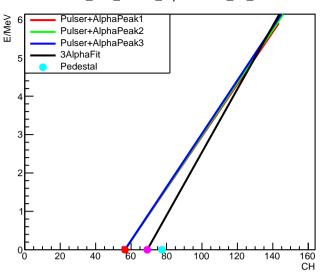


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH04

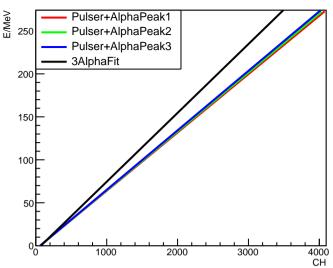


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH04

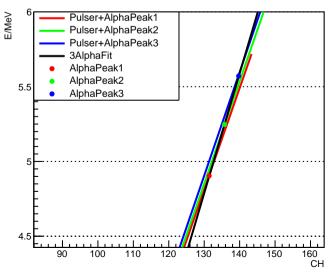


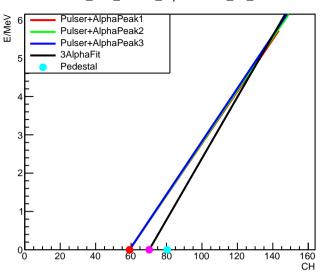


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH05

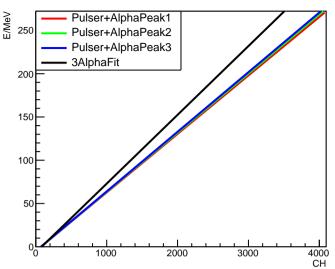


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH05

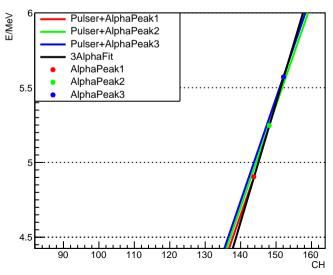


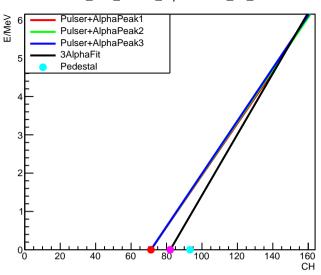


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH06

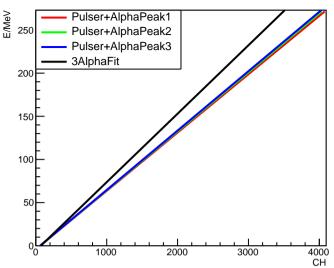


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH06

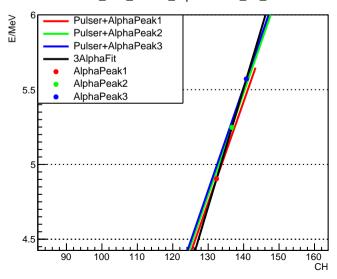




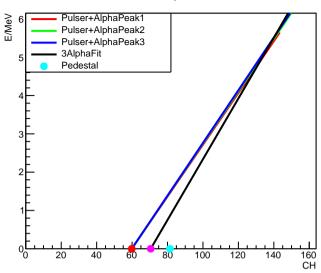
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH07



SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH07



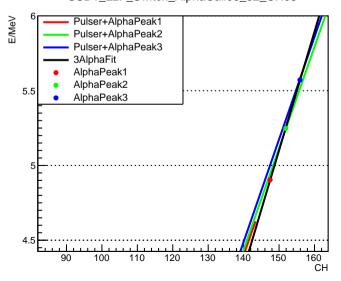


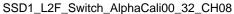


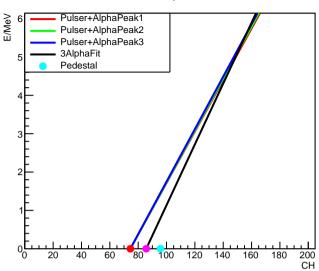
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH08

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

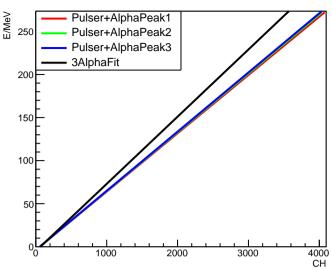




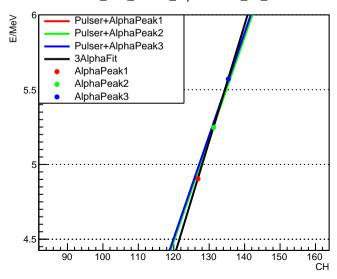




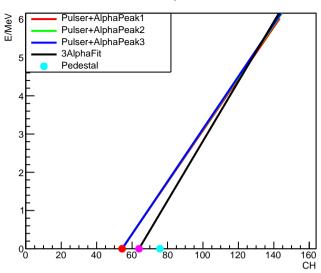
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH09



SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH09







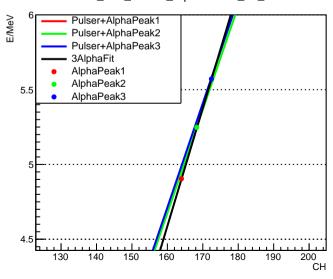
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH10

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

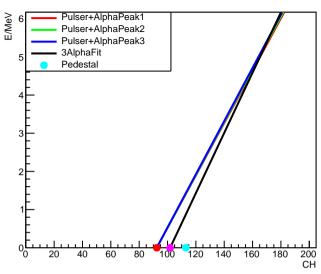
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH10

3000

1000



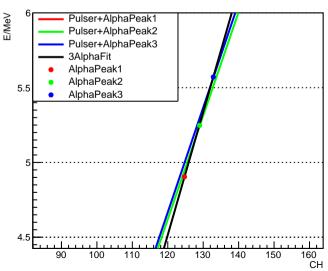




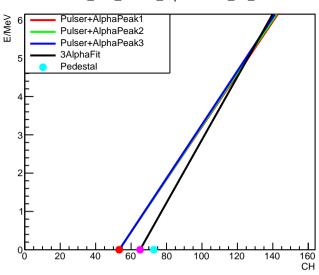
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH11

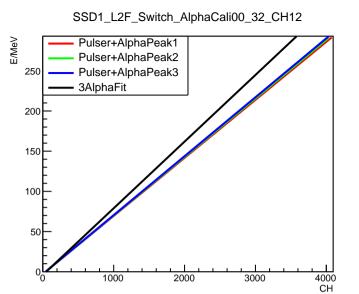
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit



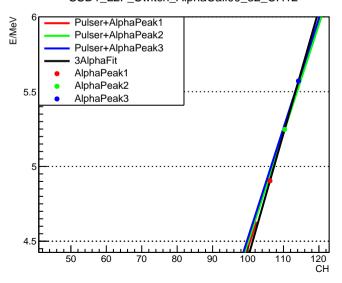




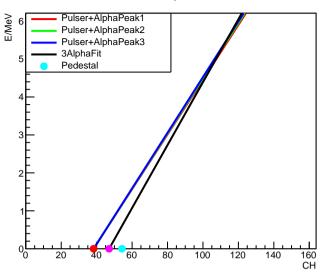






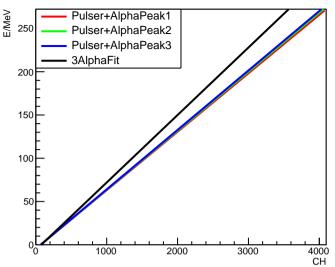




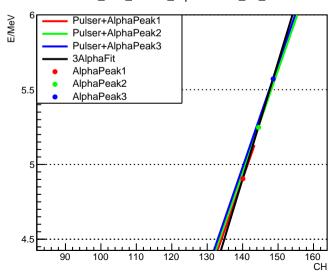


SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH13

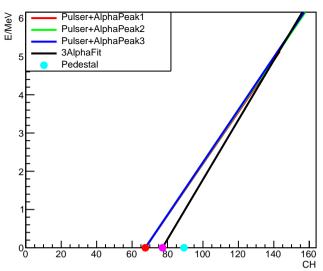
Pulser+AlphaPeak1



SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH13



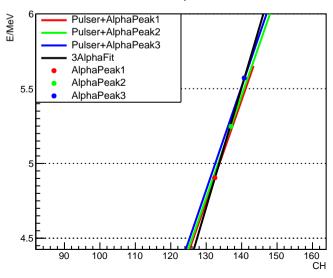




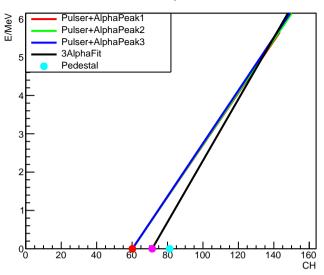
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH14

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

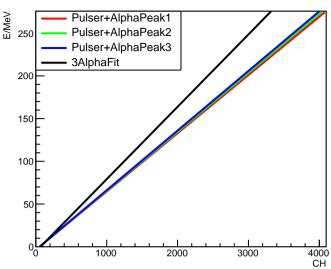




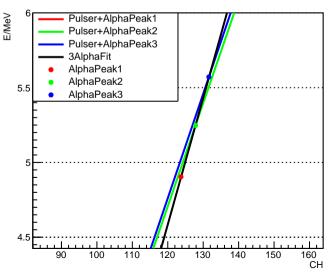




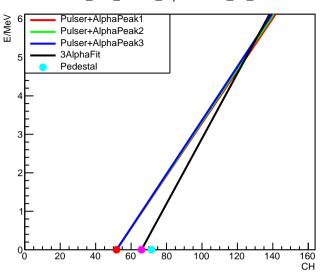
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH15



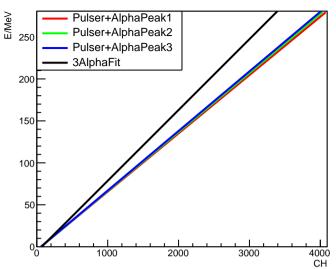
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH15



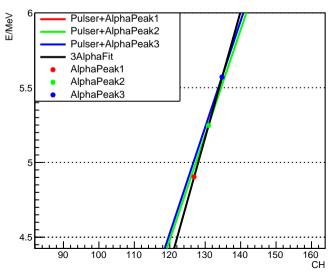
SSD1\_L2F\_Switch\_AlphaCali00\_32\_CH15

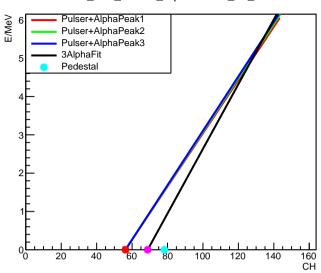


SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH00

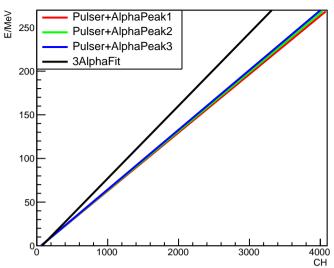


SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH00

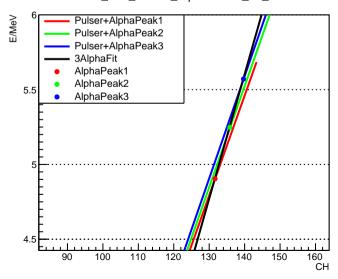




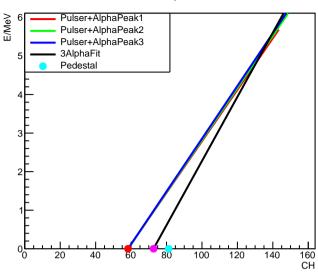
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH01



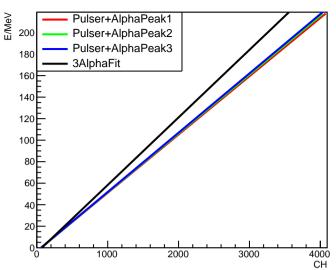
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH01



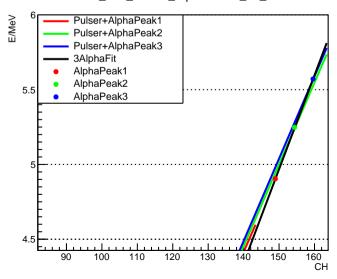


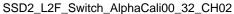


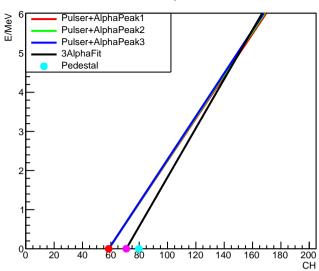
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH02



SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH02







SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH03

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

100
100
80
100
40

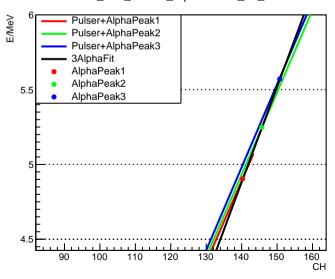
1000

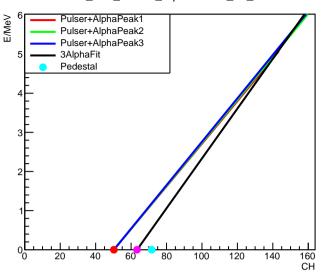
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH03

2000

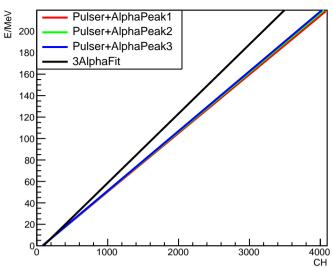
3000

4000 CH

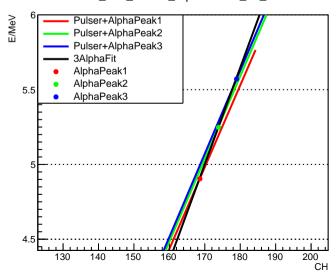


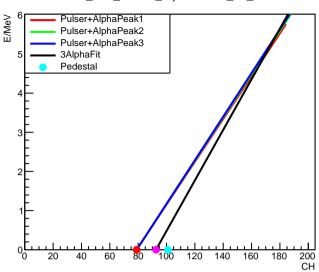


SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH04



SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH04





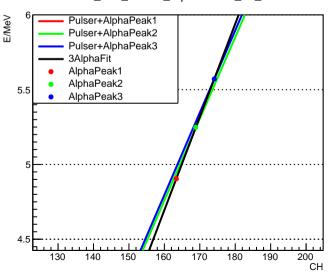
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH05

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

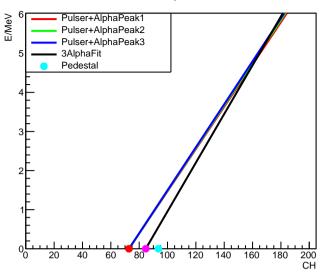
140
120
100
80
60
40

SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH05

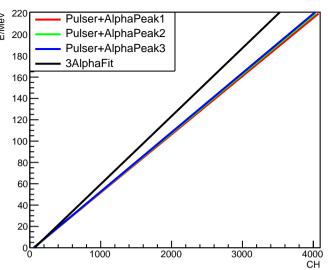
CH



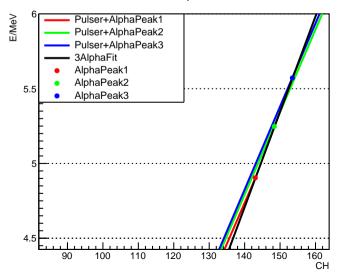


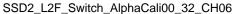


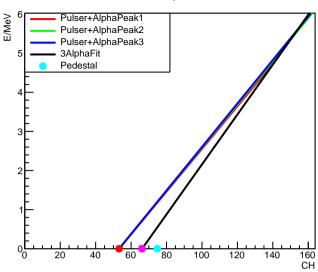
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH06 Pulser+AlphaPeak1

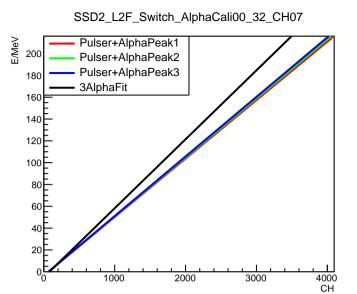


SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH06

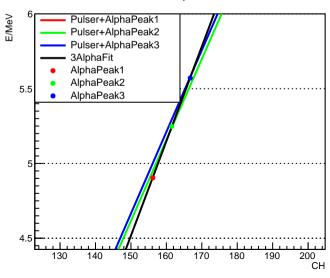




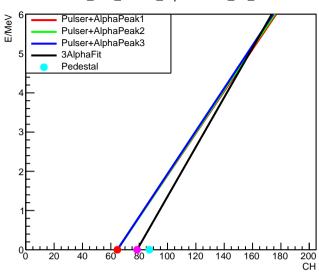










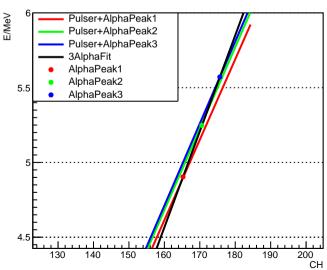


SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH08

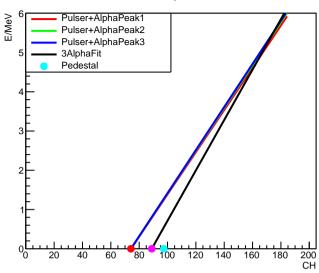
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

140
120
100
80
60
40

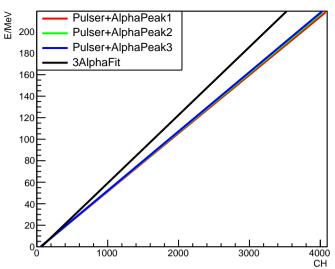
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH08



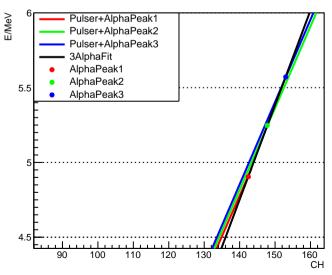


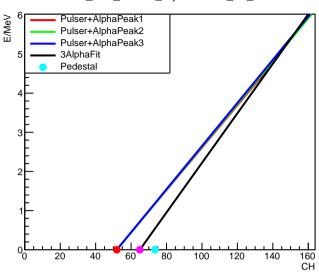


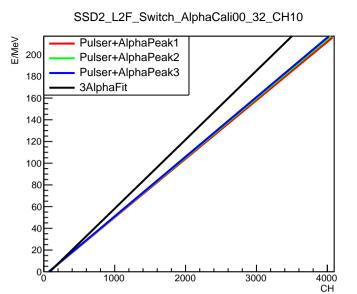
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH09



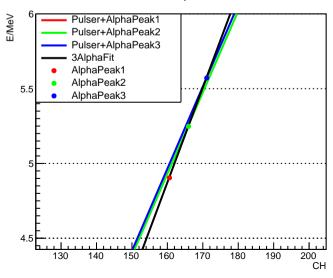
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH09



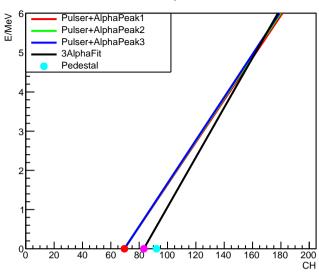










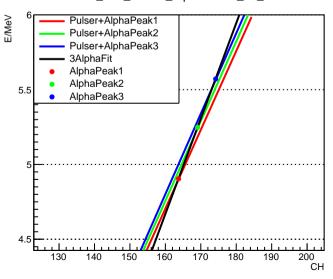


SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH11

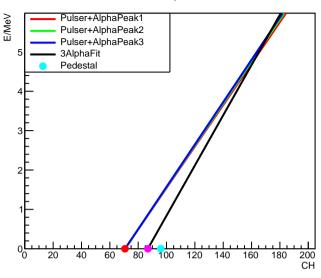
Pulser+AlphaPeak1
Pulser+AlphaPeak2
180
Pulser+AlphaPeak3
3AlphaFit

140
120
100
80
60
40

SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH11

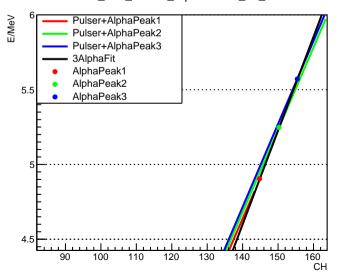




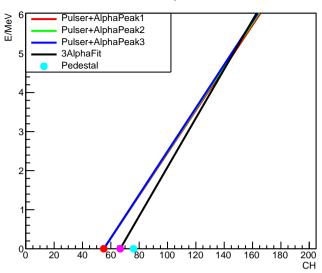


SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH12

CH







SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH13

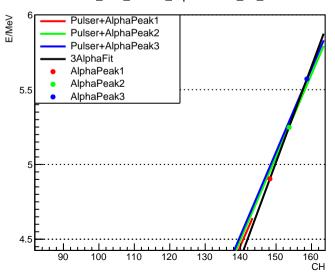
220
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

160
140
120
100
80
40
20

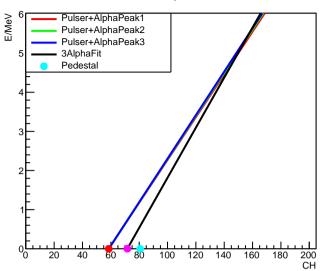
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH13

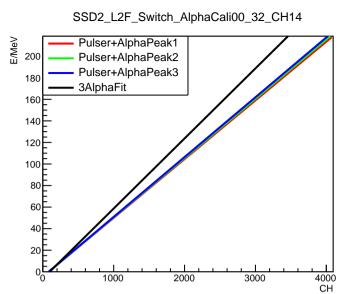
3000

1000

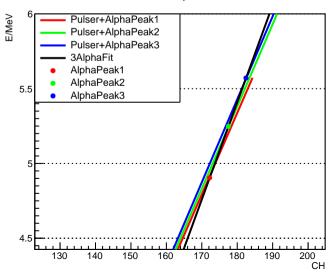


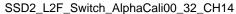


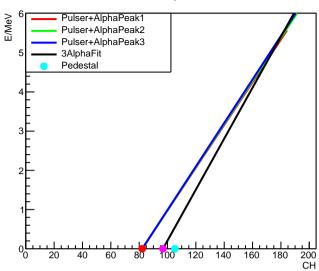




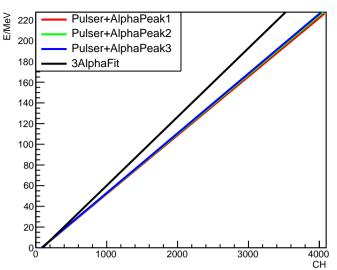




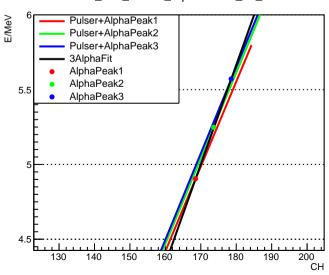


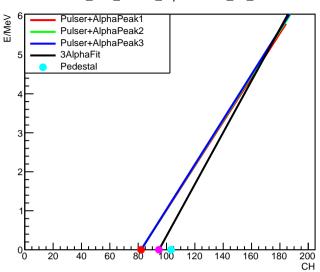


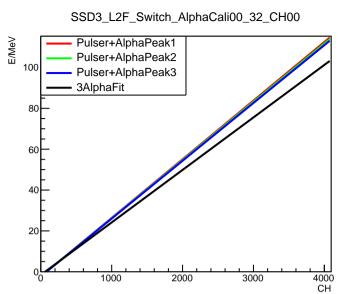
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH15



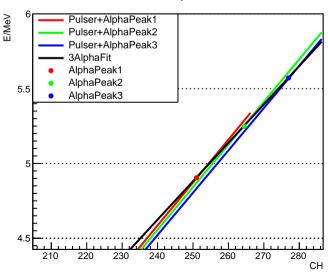
SSD2\_L2F\_Switch\_AlphaCali00\_32\_CH15

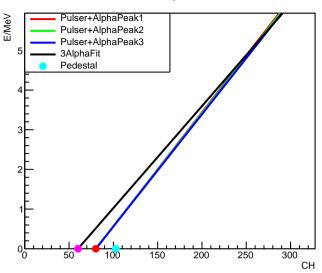








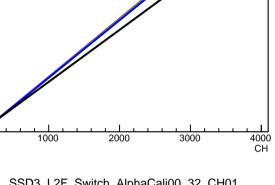




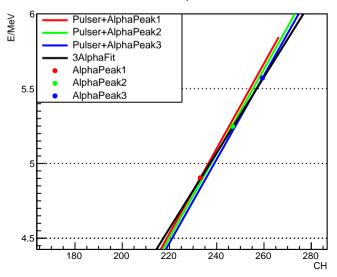
SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH01 E/MeV Pulser+AlphaPeak1 Pulser+AlphaPeak2 100 Pulser+AlphaPeak3 3AlphaFit 80

40

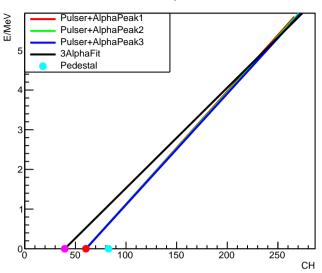
20

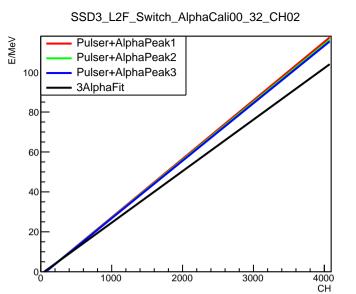


SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH01

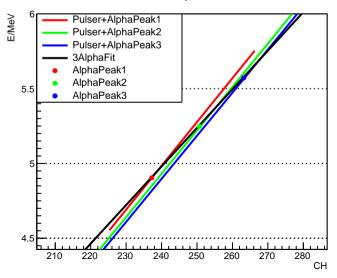


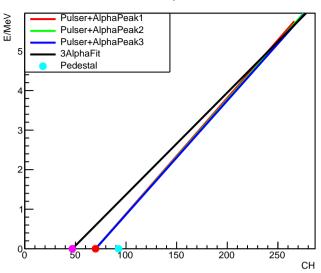




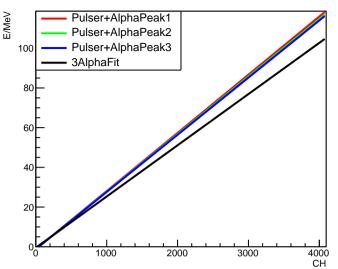




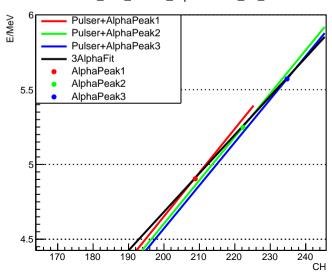


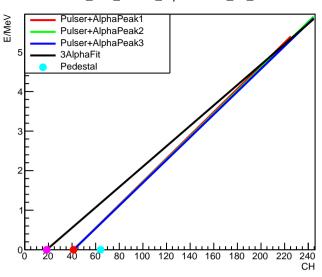


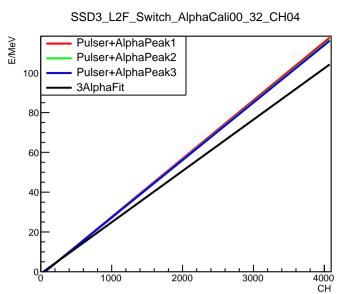
SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH03



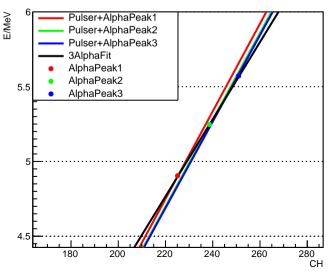
SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH03

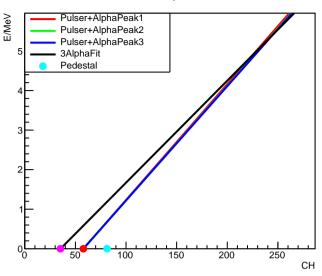












SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH05

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

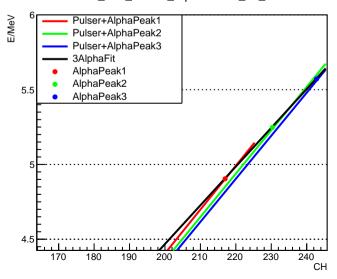
SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH05

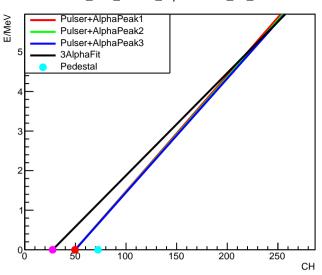
2000

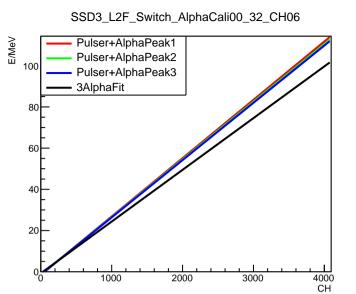
3000

1000

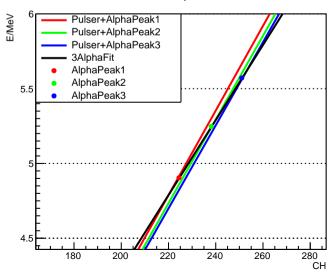
4000 CH

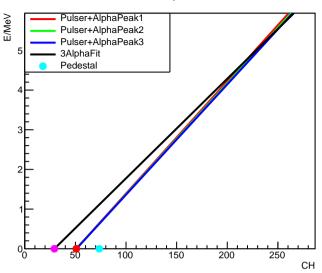












SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH07

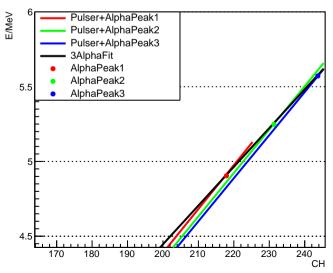
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

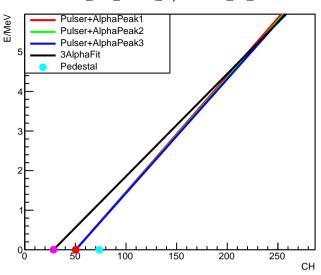
80

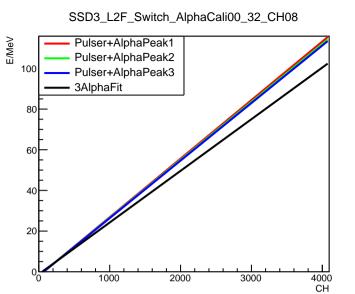
40

CH

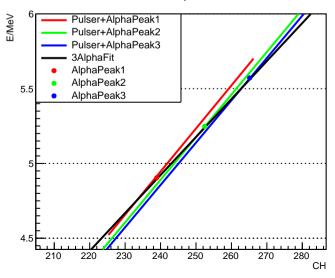


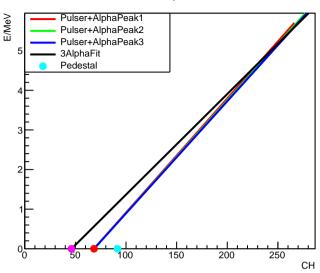


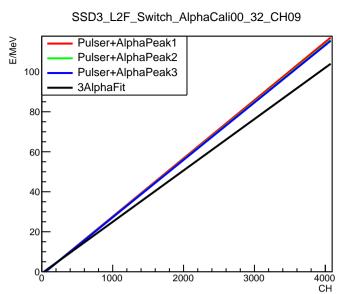




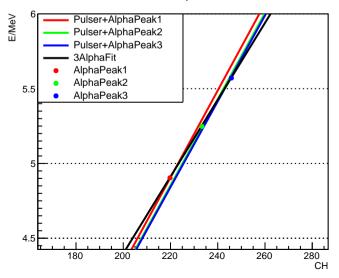




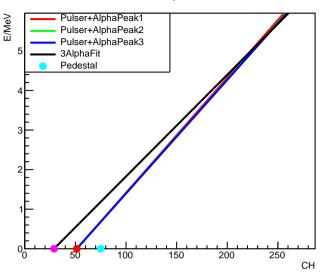


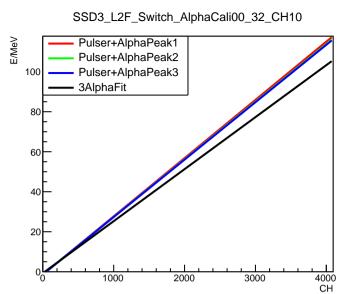




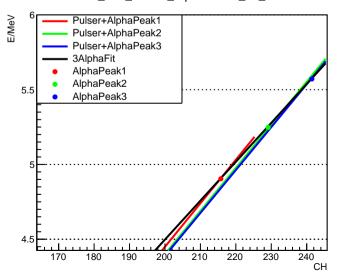




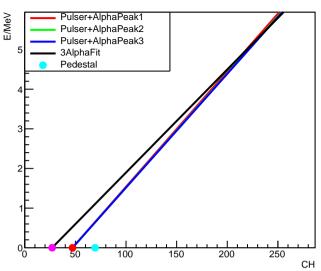


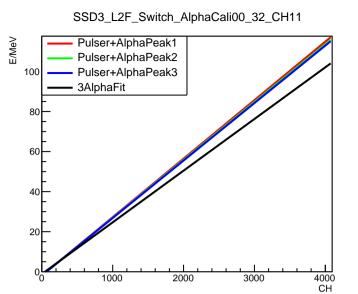


SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH10

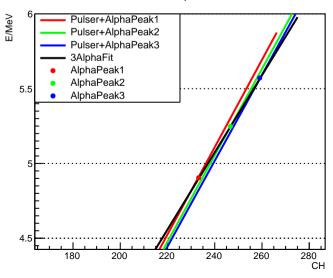




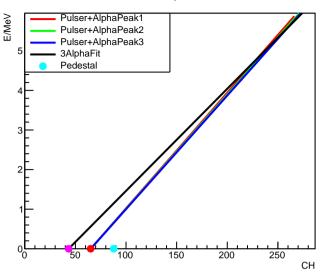


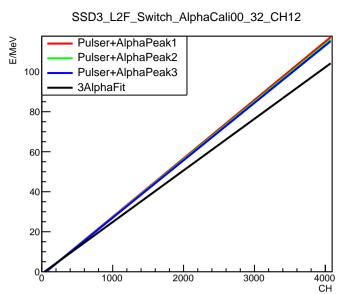




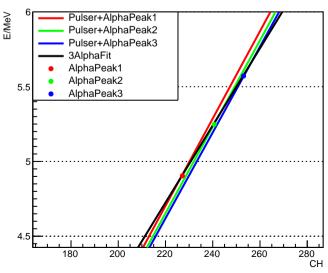


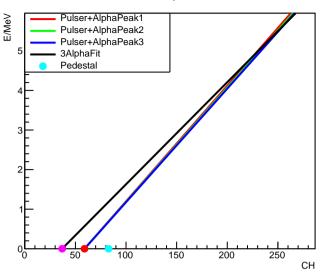


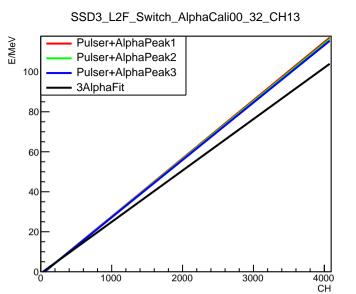




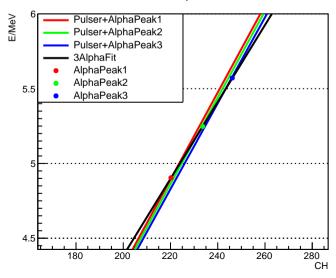




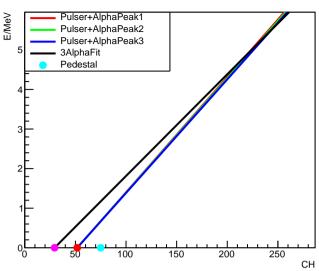












SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH14

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

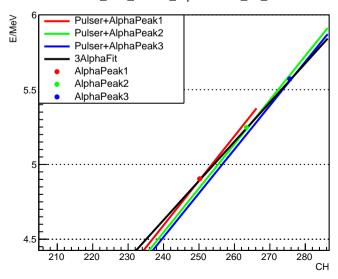
SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH14

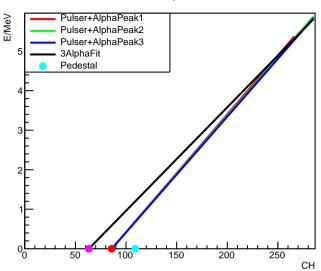
2000

3000

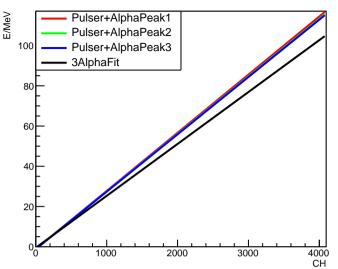
1000

4000 CH

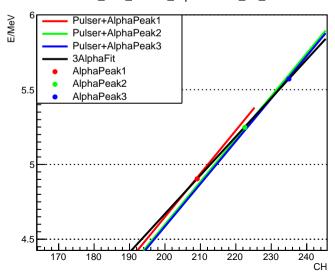


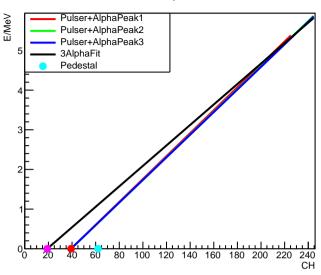


SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH15

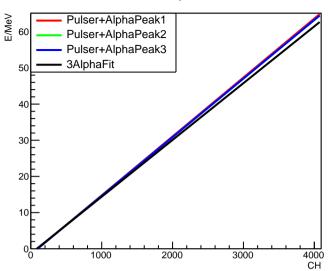


SSD3\_L2F\_Switch\_AlphaCali00\_32\_CH15

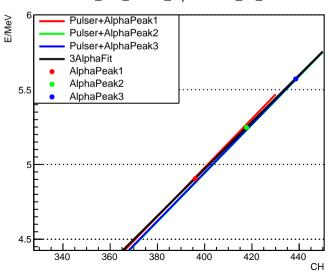


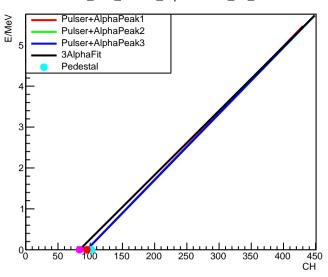


SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH00

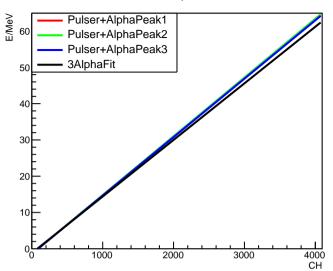


SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH00

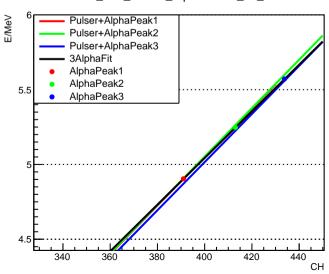


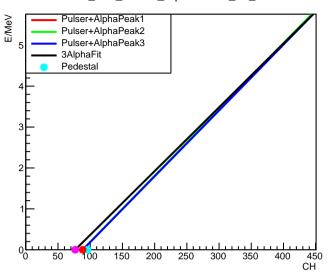


SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH01

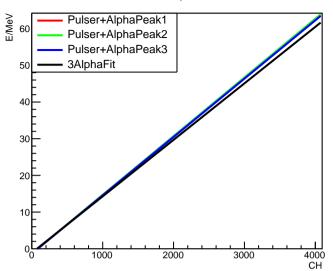


SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH01

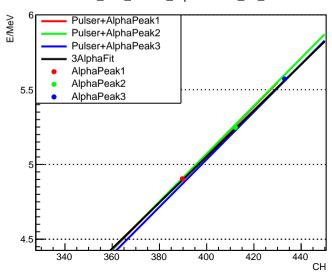




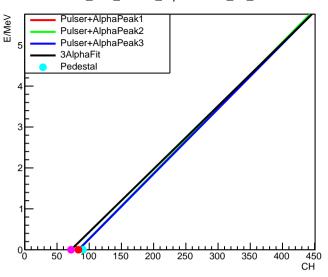
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH02



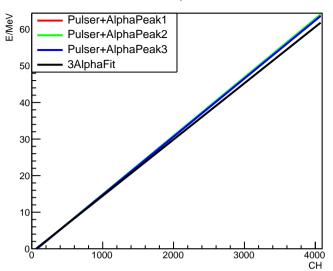
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH02



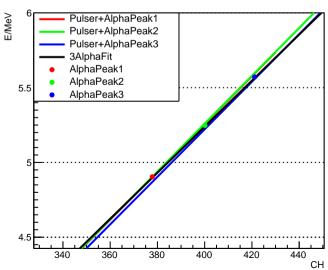
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH02



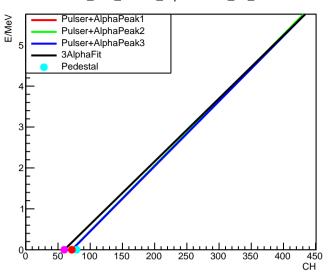
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH03



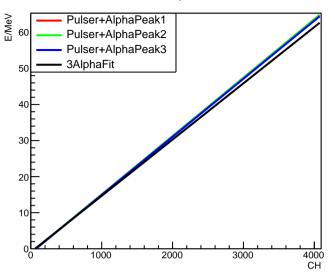
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH03



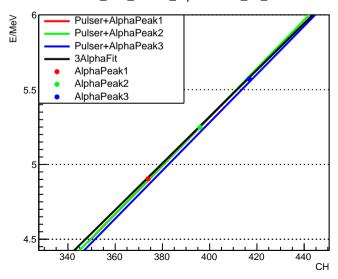
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH03



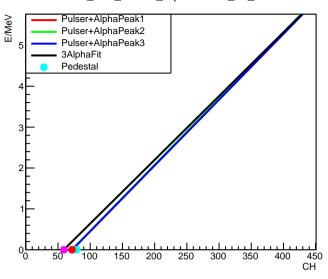
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH04



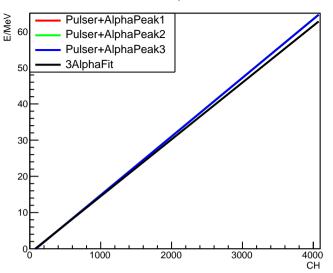
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH04



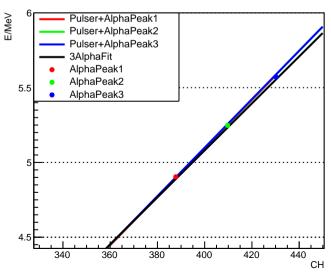
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH04



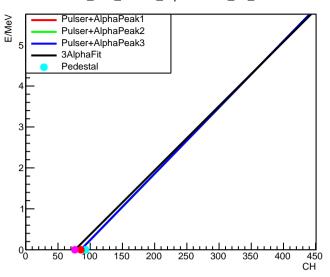
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH05



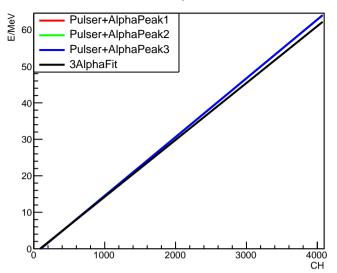
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH05



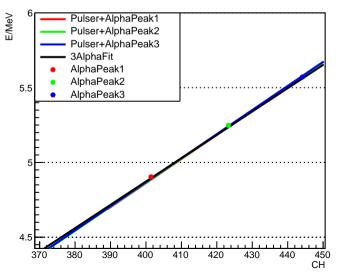
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH05



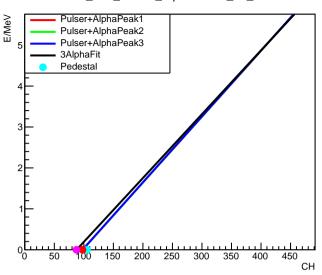
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH06



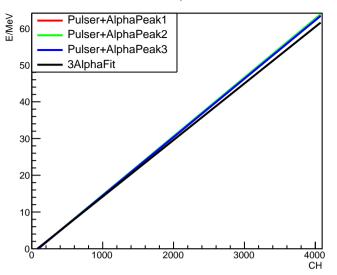
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH06



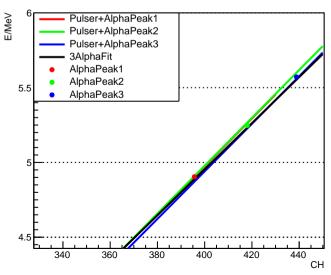
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH06



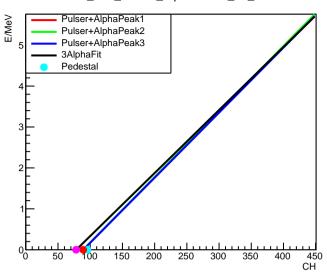
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH07



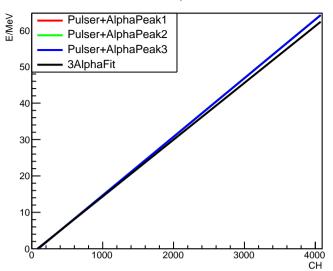
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH07



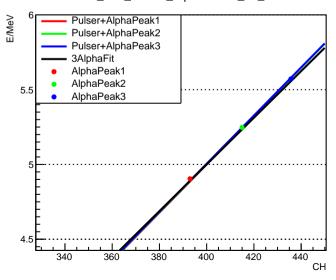
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH07

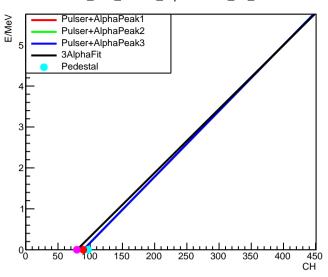


SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH08

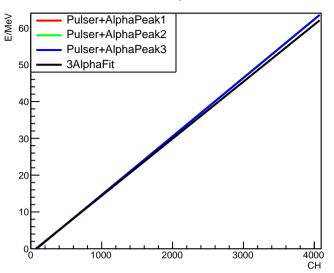


SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH08

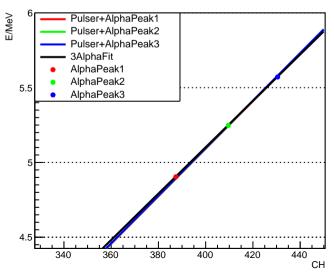




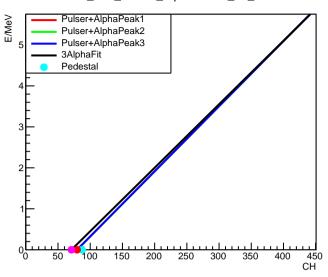
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH09



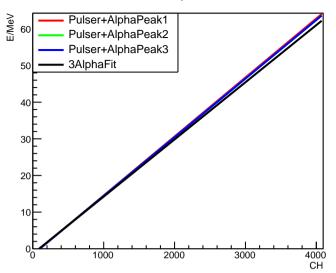
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH09



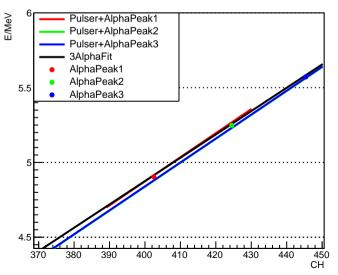
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH09



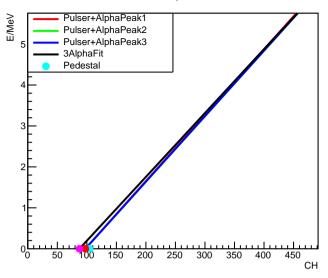
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH10



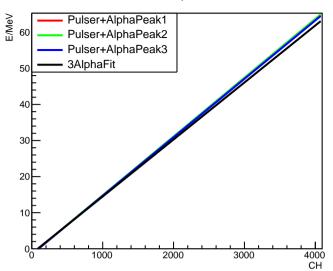
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH10



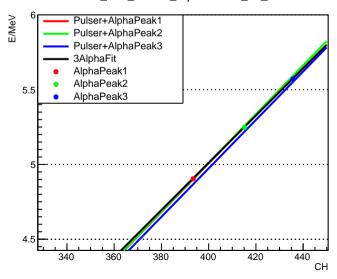
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH10



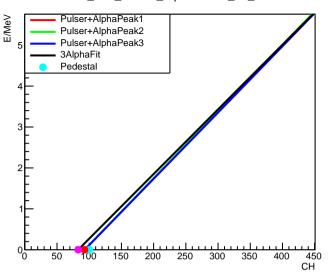
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH11



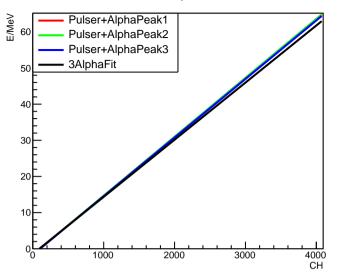
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH11



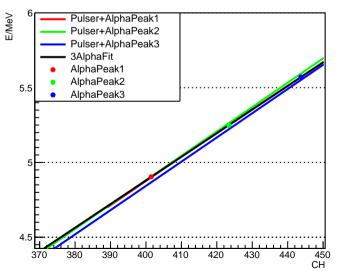
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH11



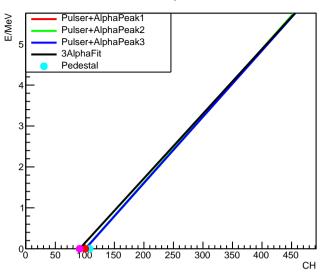
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH12



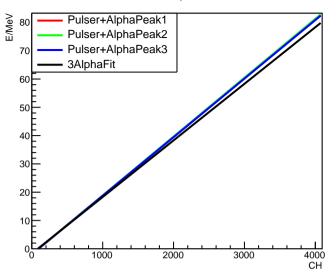
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH12



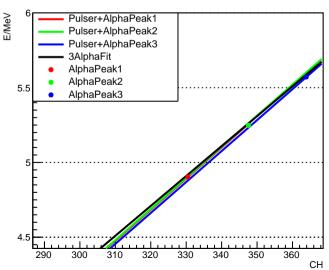
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH12

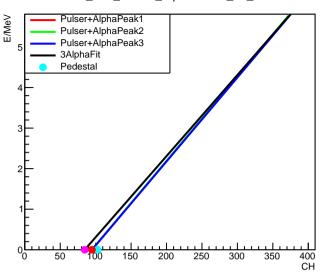


SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH13

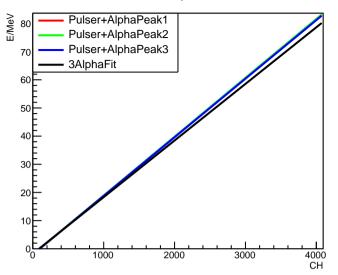


SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH13

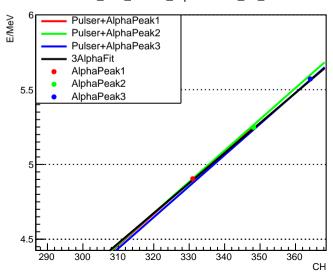




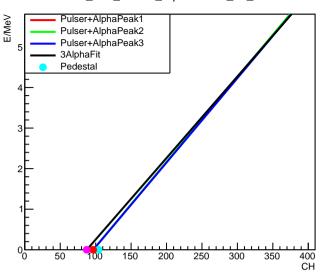
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH14



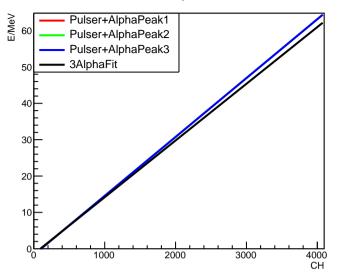
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH14



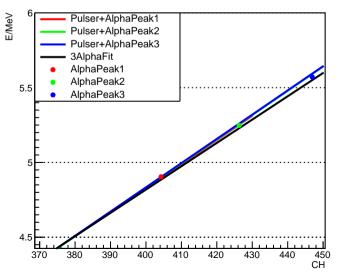
SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH14



SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH15



SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH15



SSD4\_L2F\_Switch\_AlphaCali00\_32\_CH15

