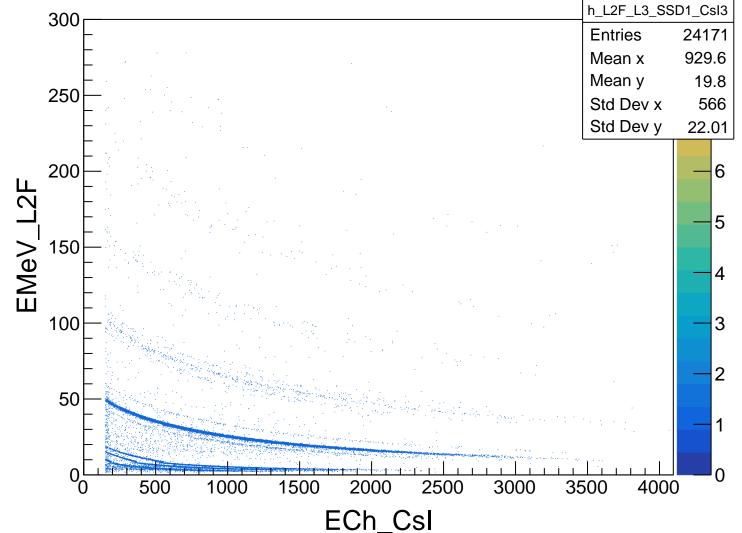
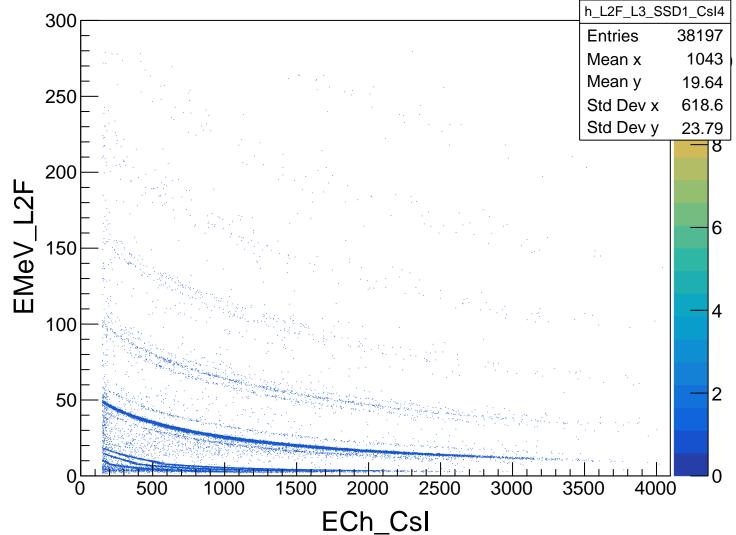
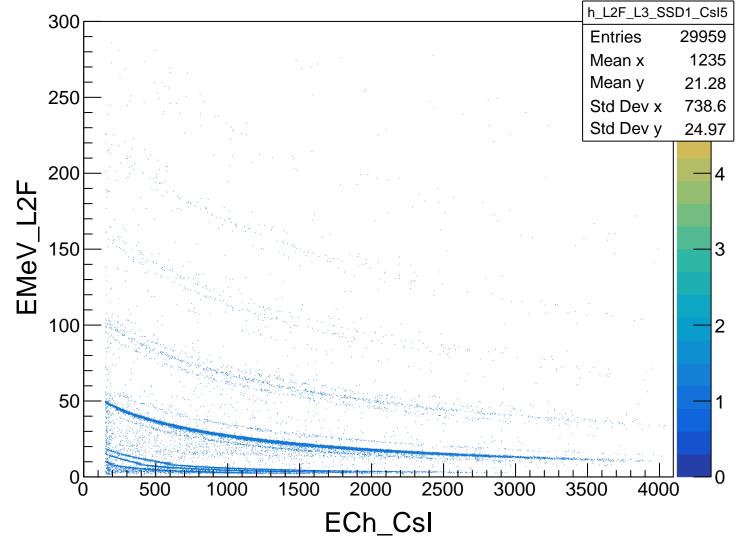


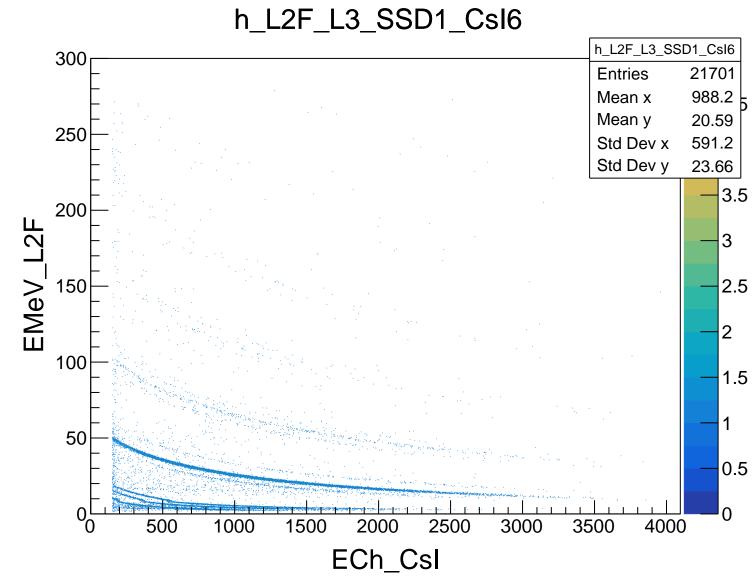
h\_L2F\_L3\_SSD1\_Csl3

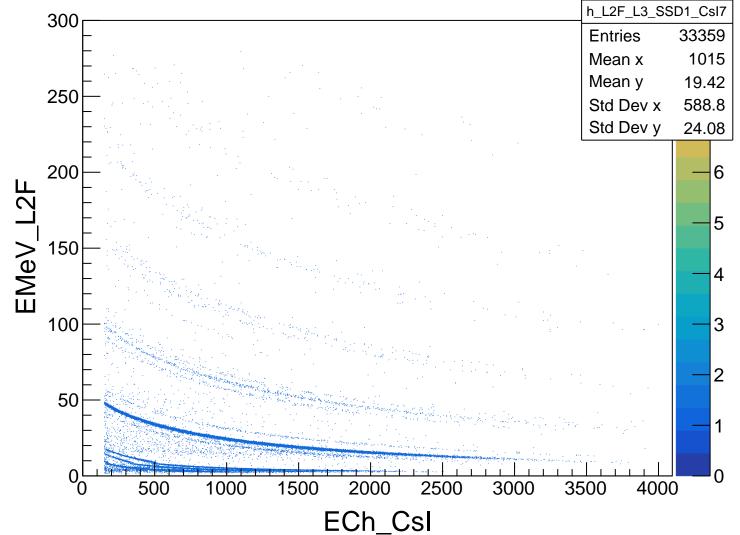


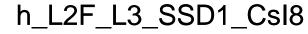
h\_L2F\_L3\_SSD1\_CsI4

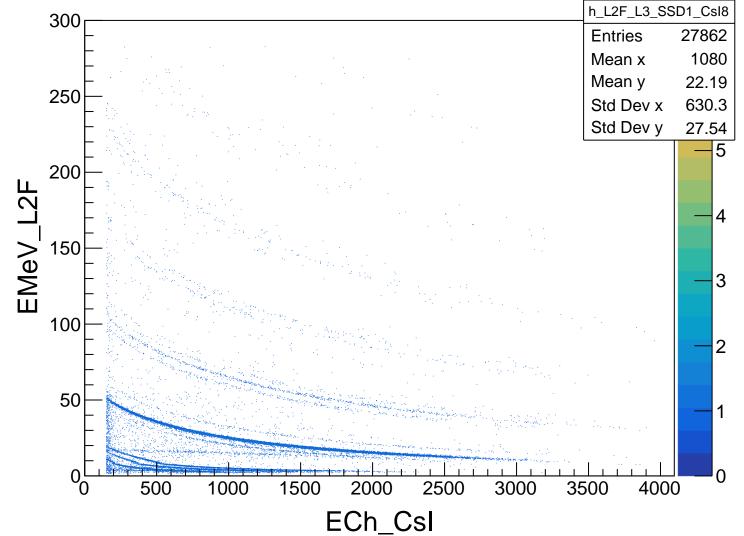


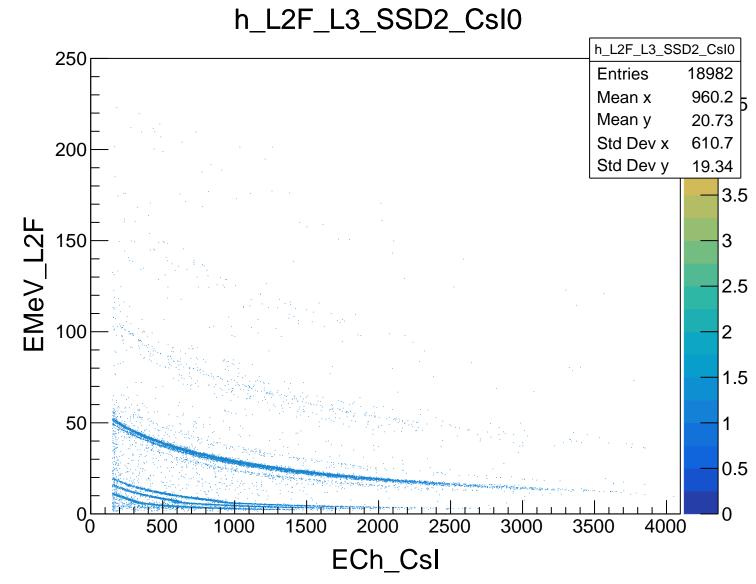




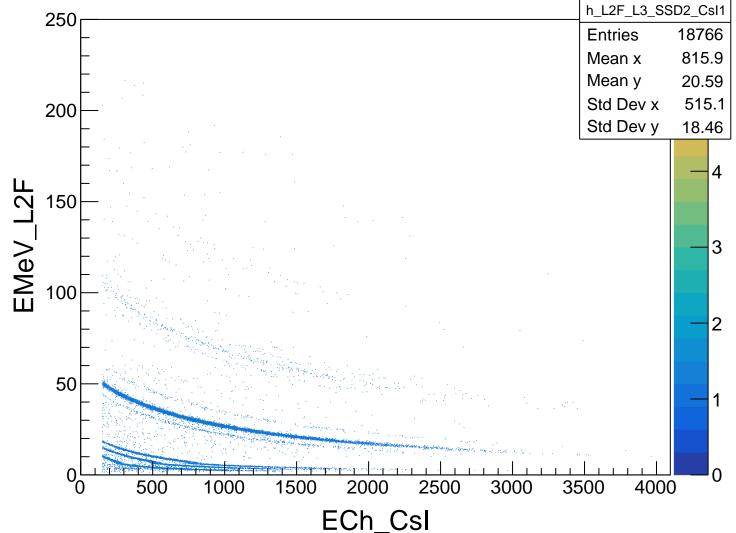


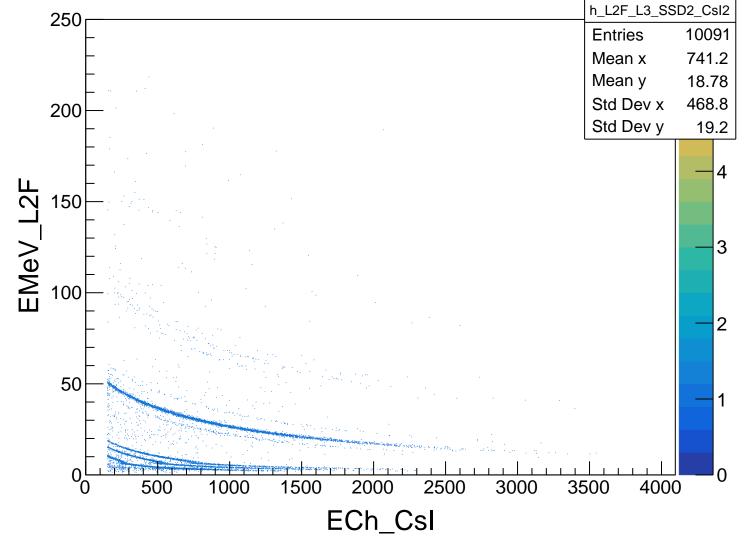


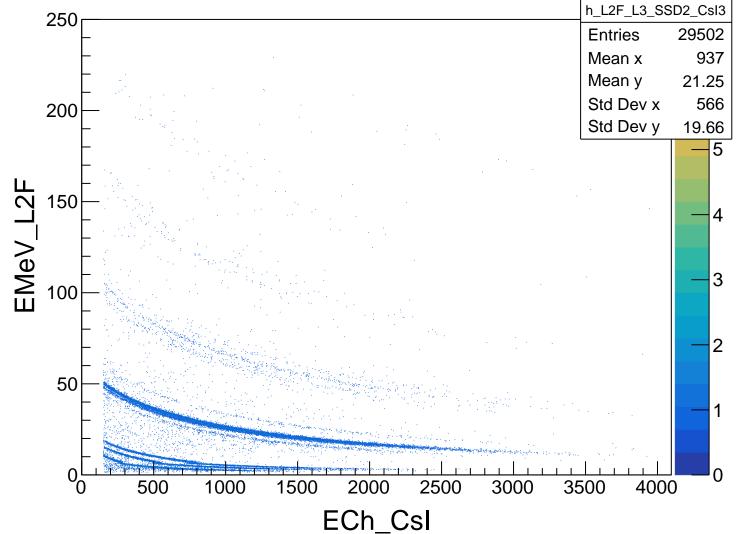




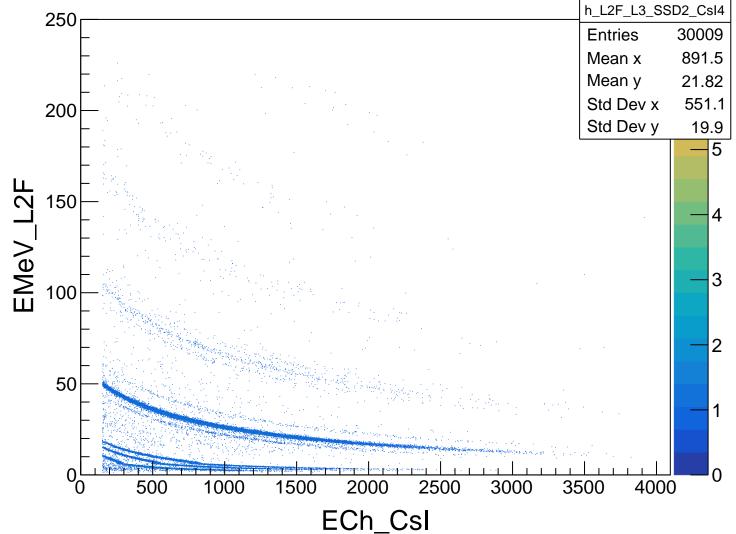
### h\_L2F\_L3\_SSD2\_CsI1

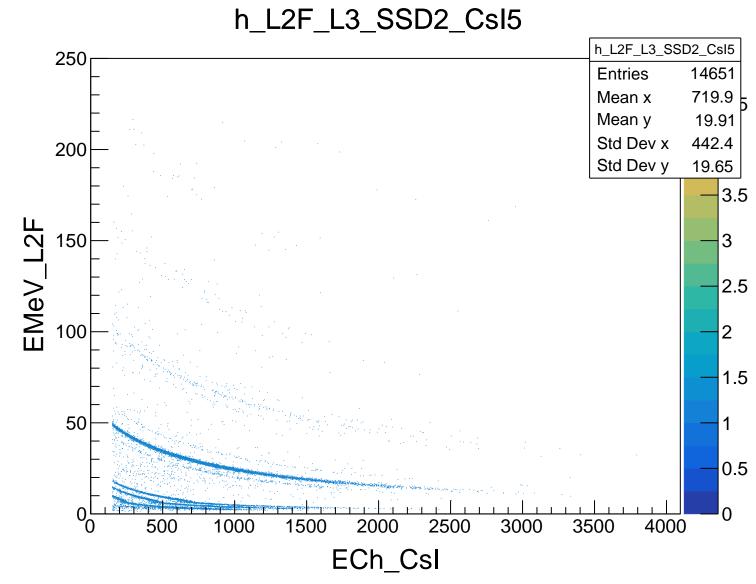




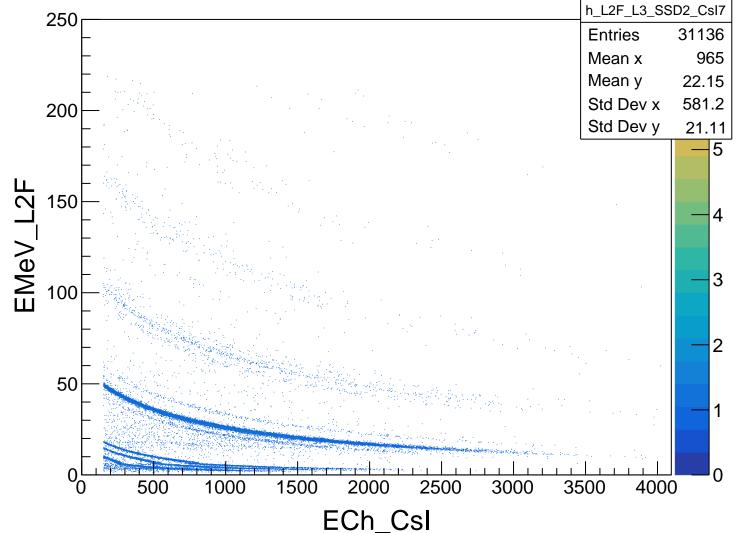


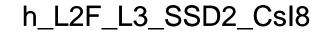
h\_L2F\_L3\_SSD2\_CsI4

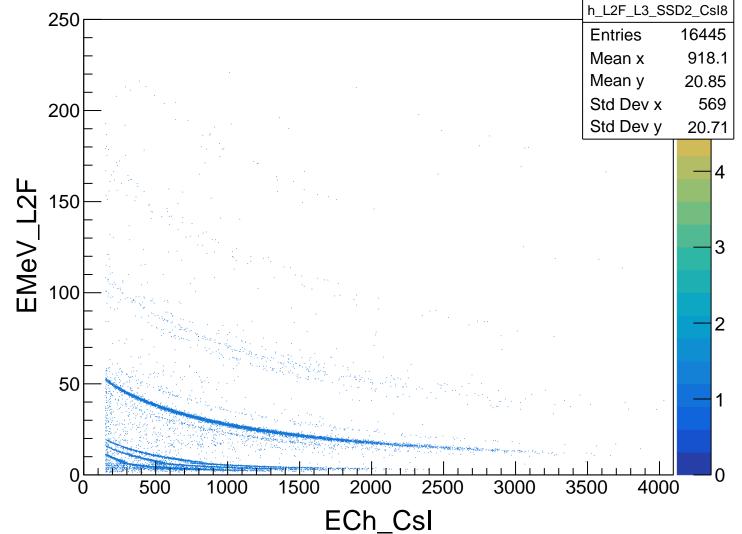


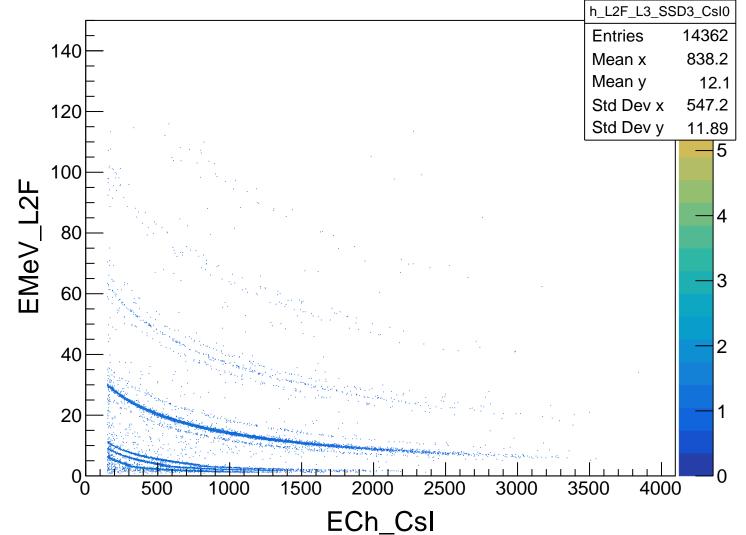


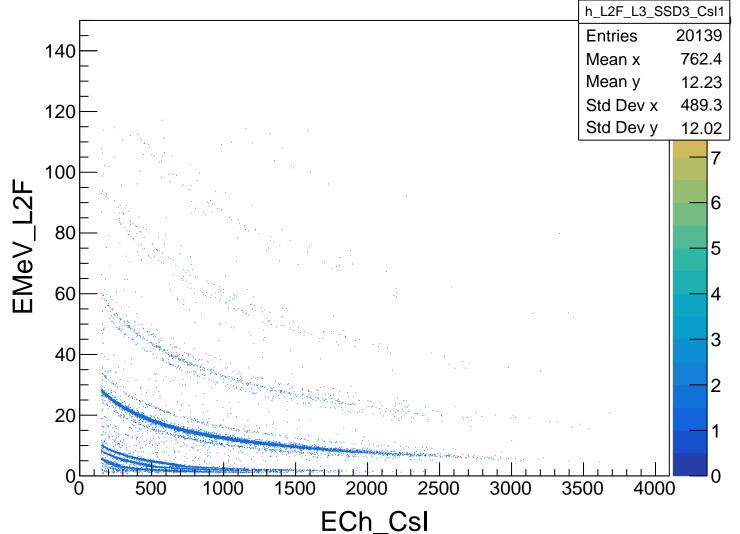
h\_L2F\_L3\_SSD2\_Csl6 h\_L2F\_L3\_SSD2\_Csl6 250 31453 **Entries** Mean x 1168 Mean y 21.87 Std Dev x 696.5 200 Std Dev y 21.65 EMeV\_L2F 150 100 50 3000 4000 500 1500 2000 2500 3500 1000 ECh\_CsI

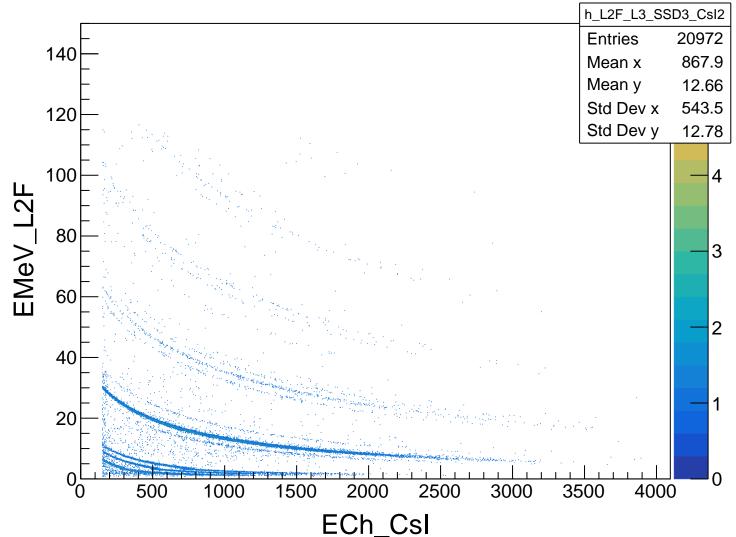


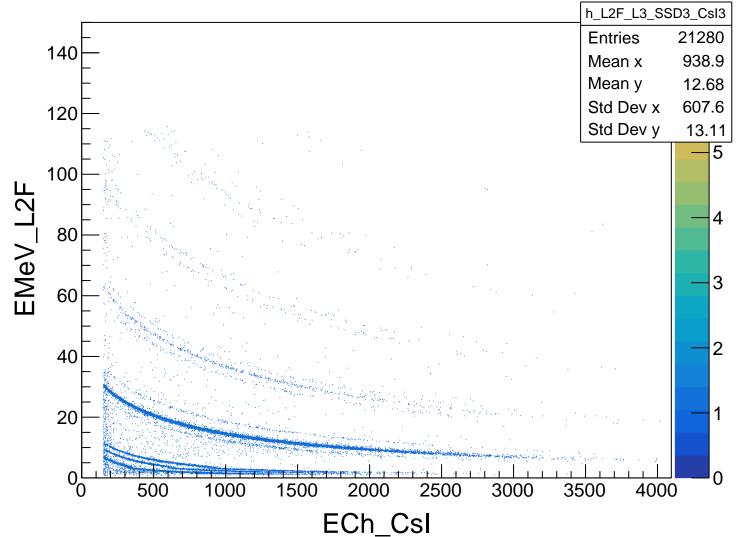


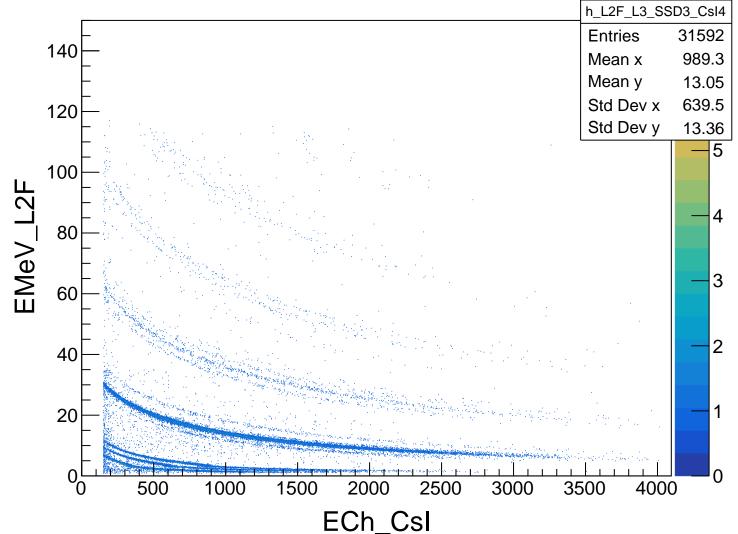




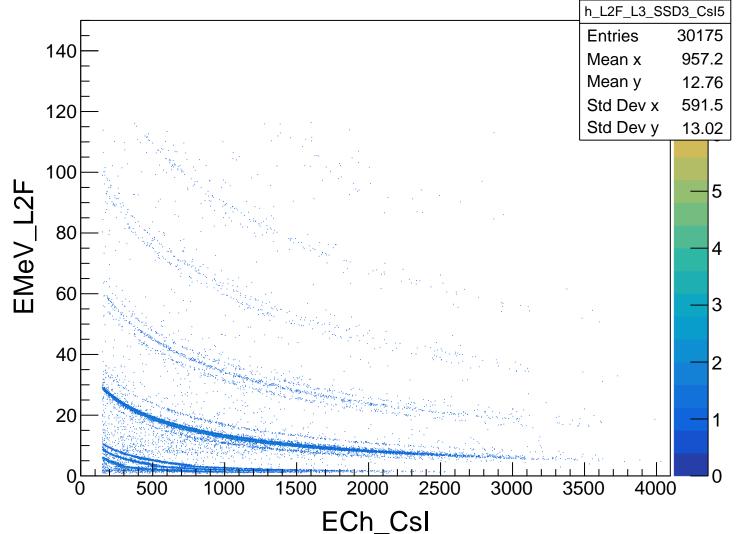




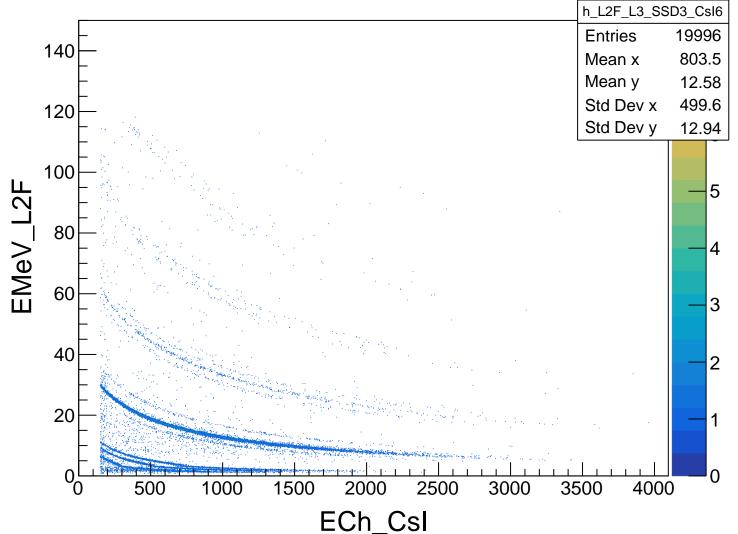


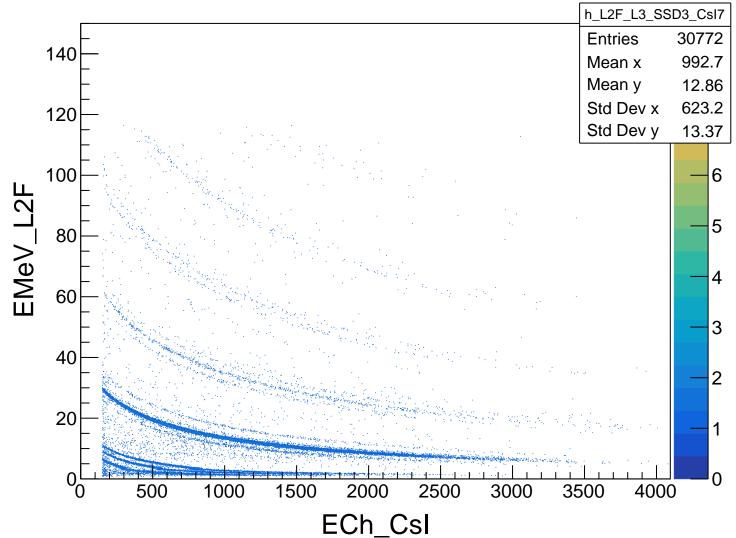


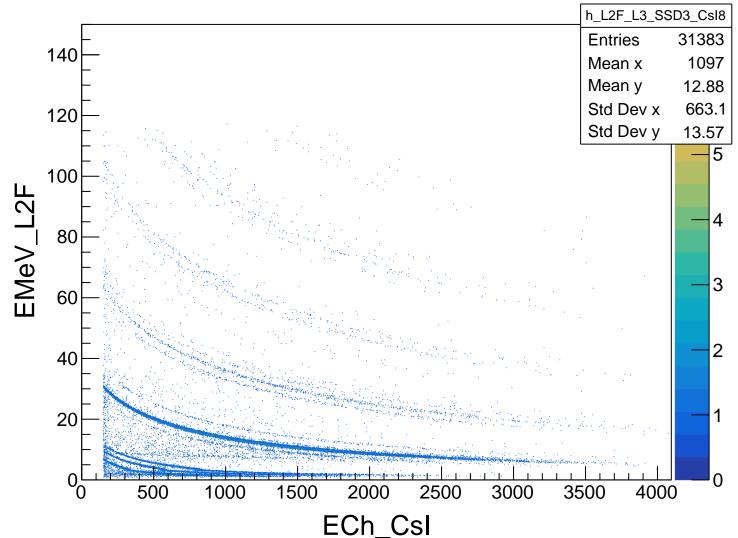
h\_L2F\_L3\_SSD3\_Csl5



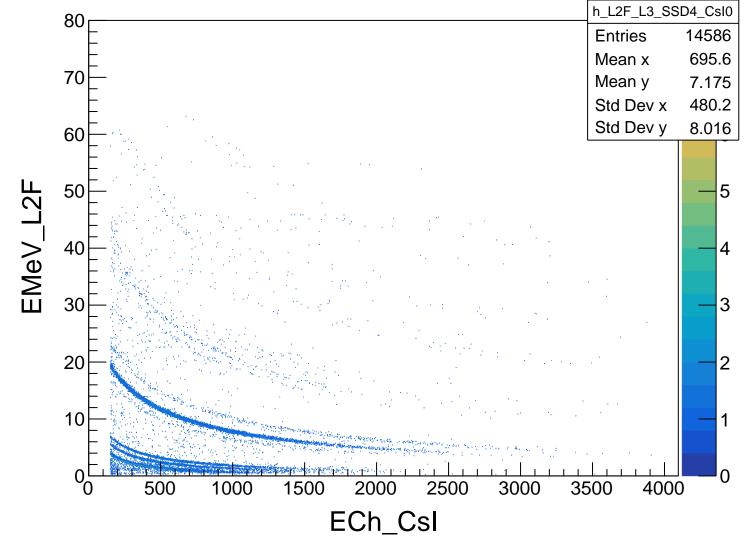
h\_L2F\_L3\_SSD3\_Csl6



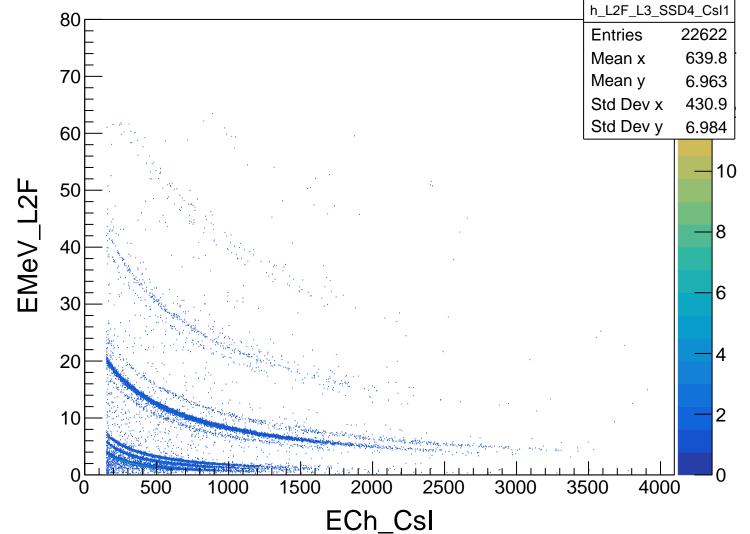


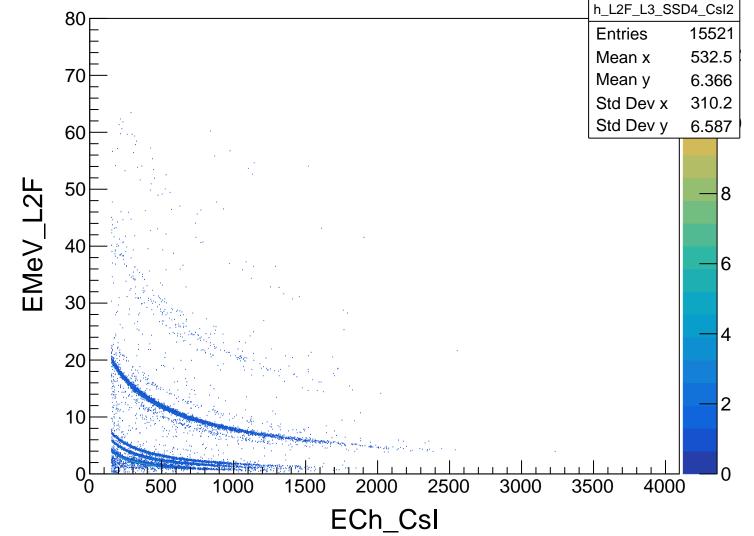


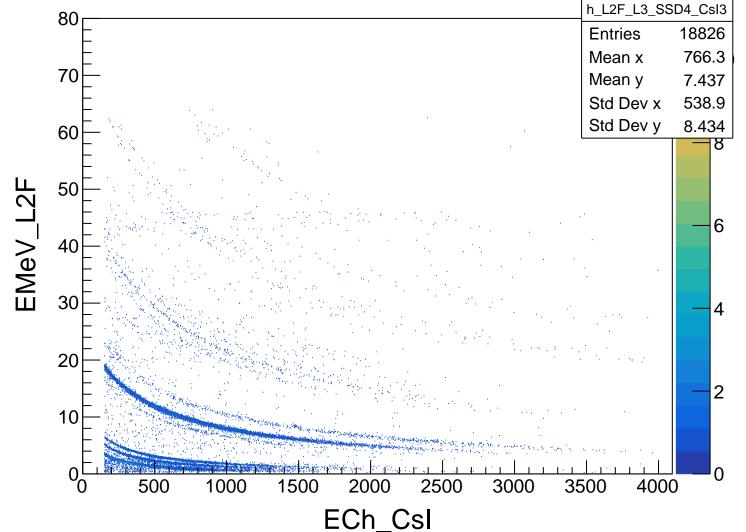
#### h\_L2F\_L3\_SSD4\_CsI0



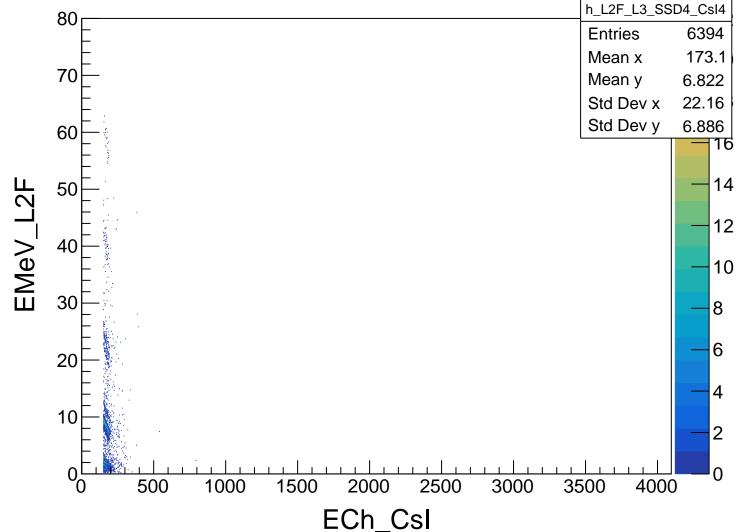
#### h\_L2F\_L3\_SSD4\_CsI1



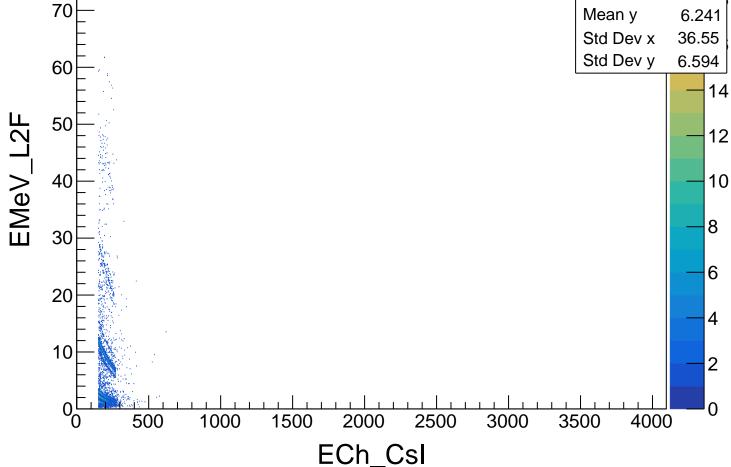




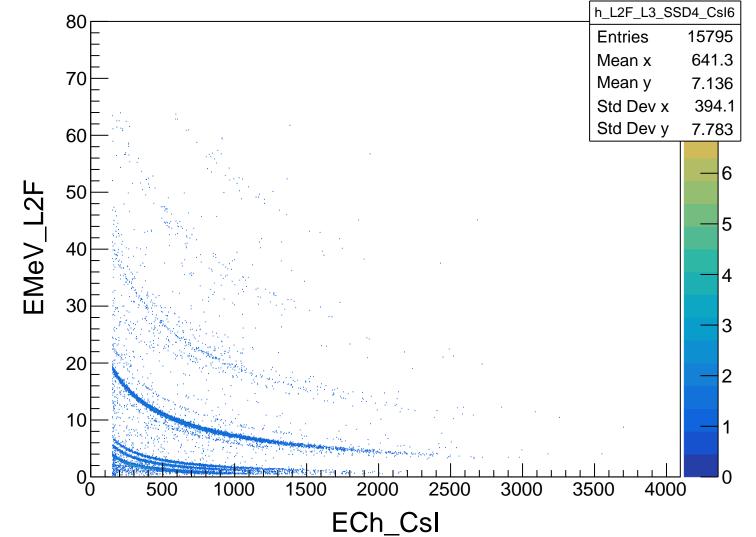
# h\_L2F\_L3\_SSD4\_CsI4



#### h\_L2F\_L3\_SSD4\_Csl5 h\_L2F\_L3\_SSD4\_Csl5 9449 **Entries** 195.4 Mean x Mean y Std Dev x Std Dev y



80



### h\_L2F\_L3\_SSD4\_CsI7

