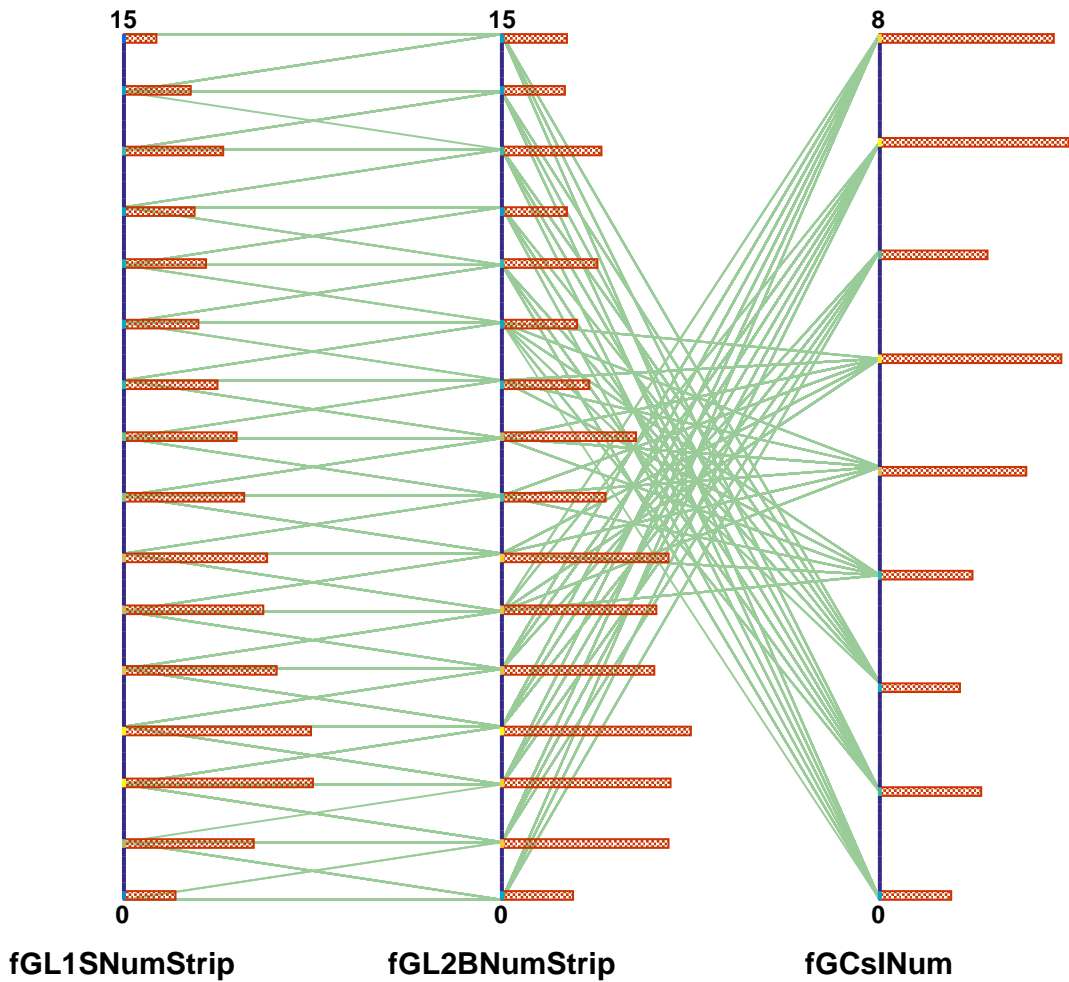
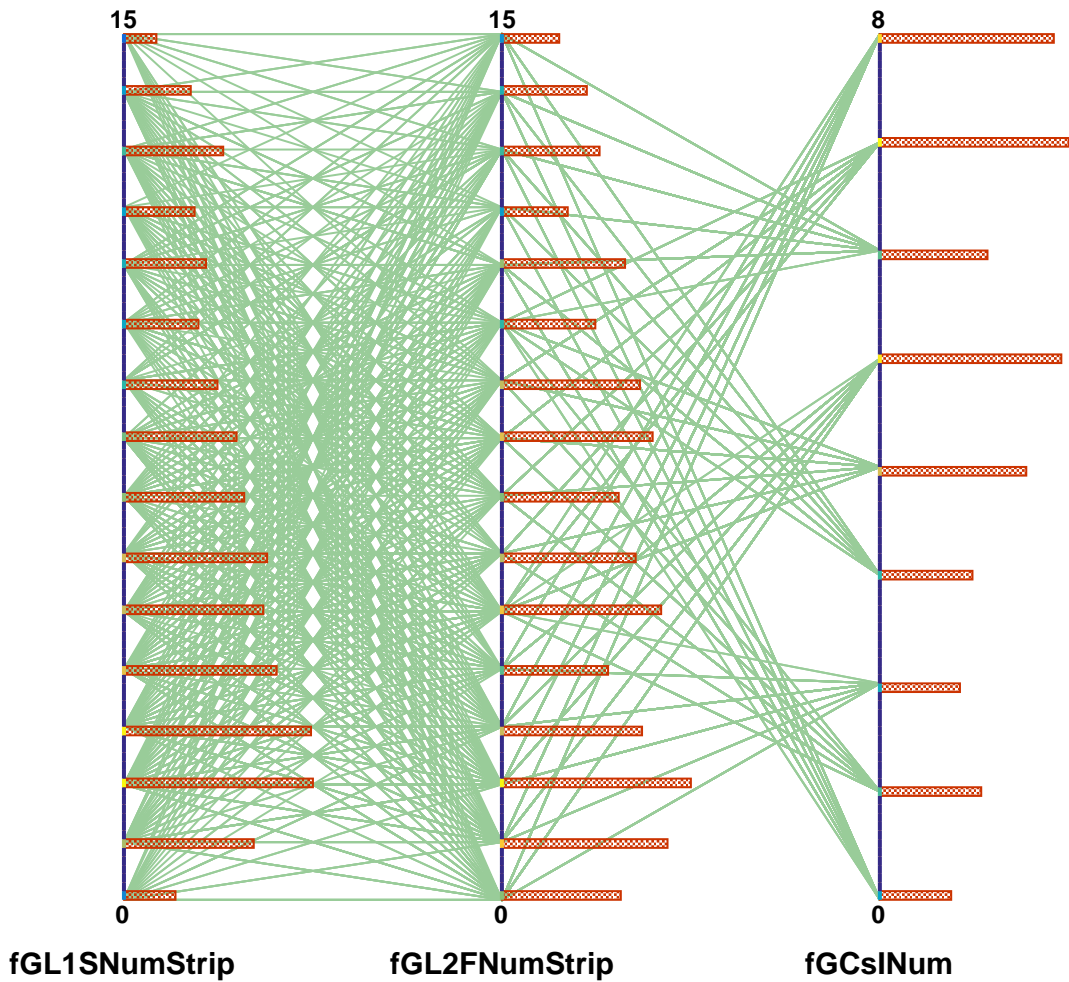


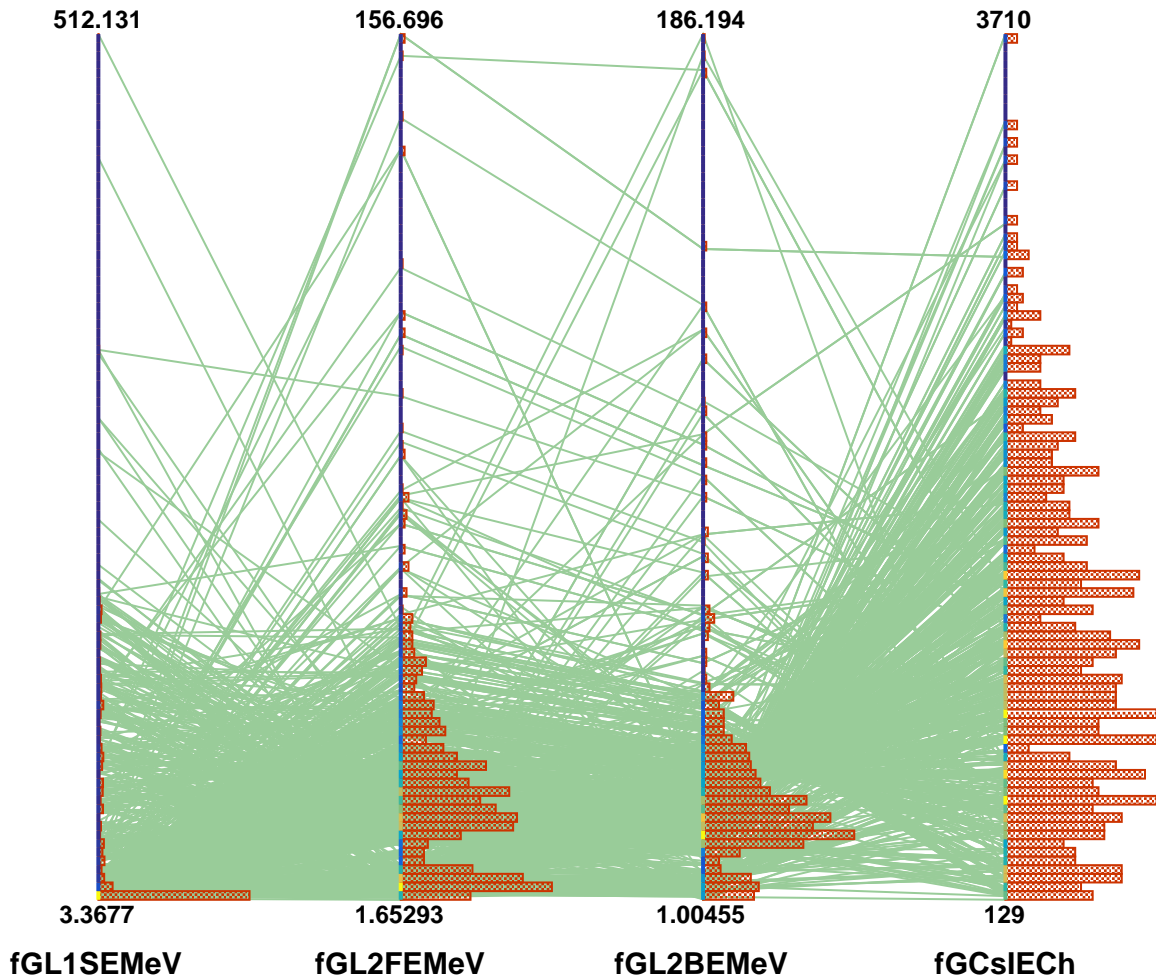
fGL1SNumStrip:fGL2BNumStrip:fGCsINum(fGSSDNum==0 && fSSDGlobalMulti[0]==2)

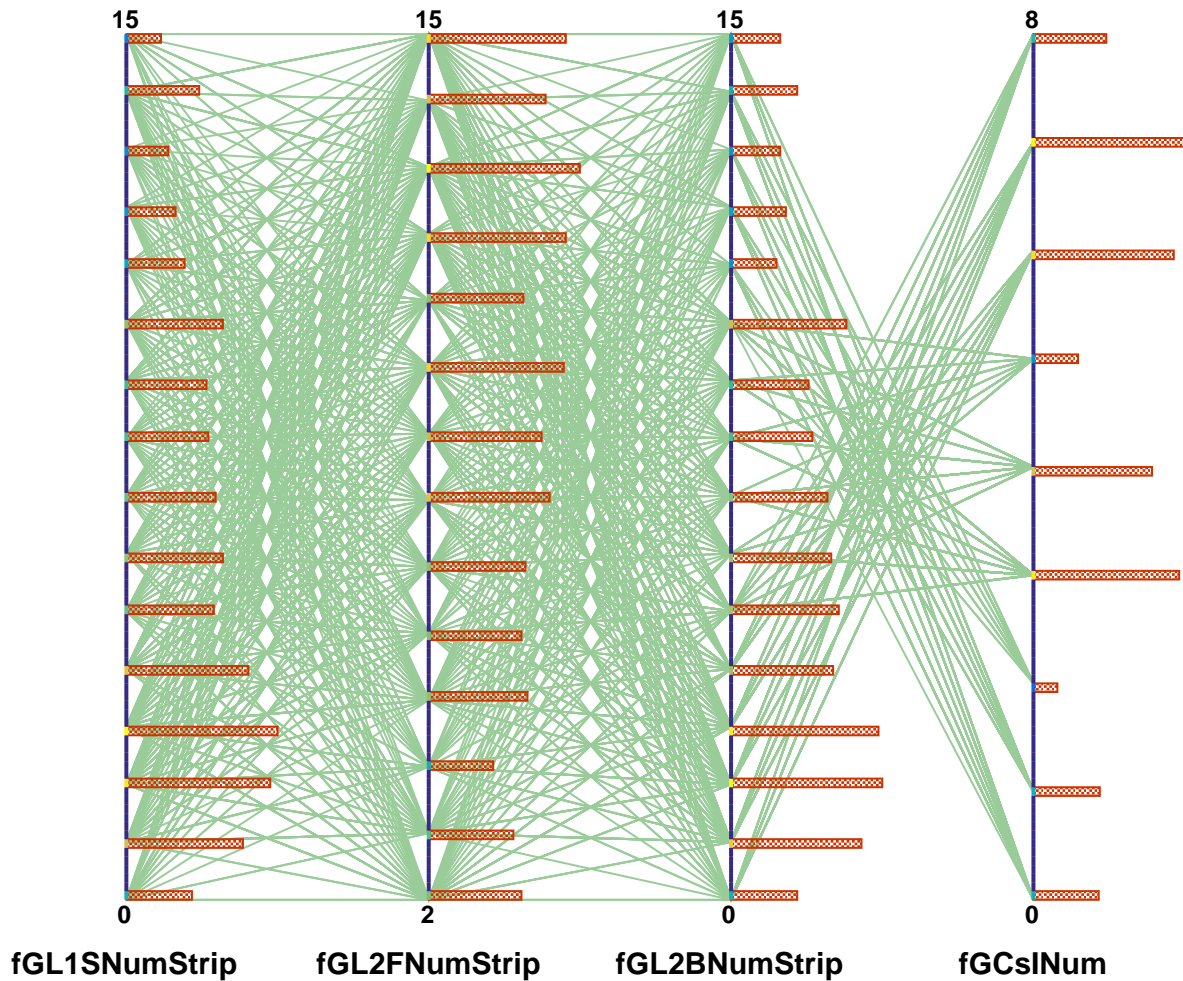


fGL1SNumStrip:fGL2FNumStrip:fGCsINum(fGSSDNum=0 && fSSDGlobalMult[0]==2)

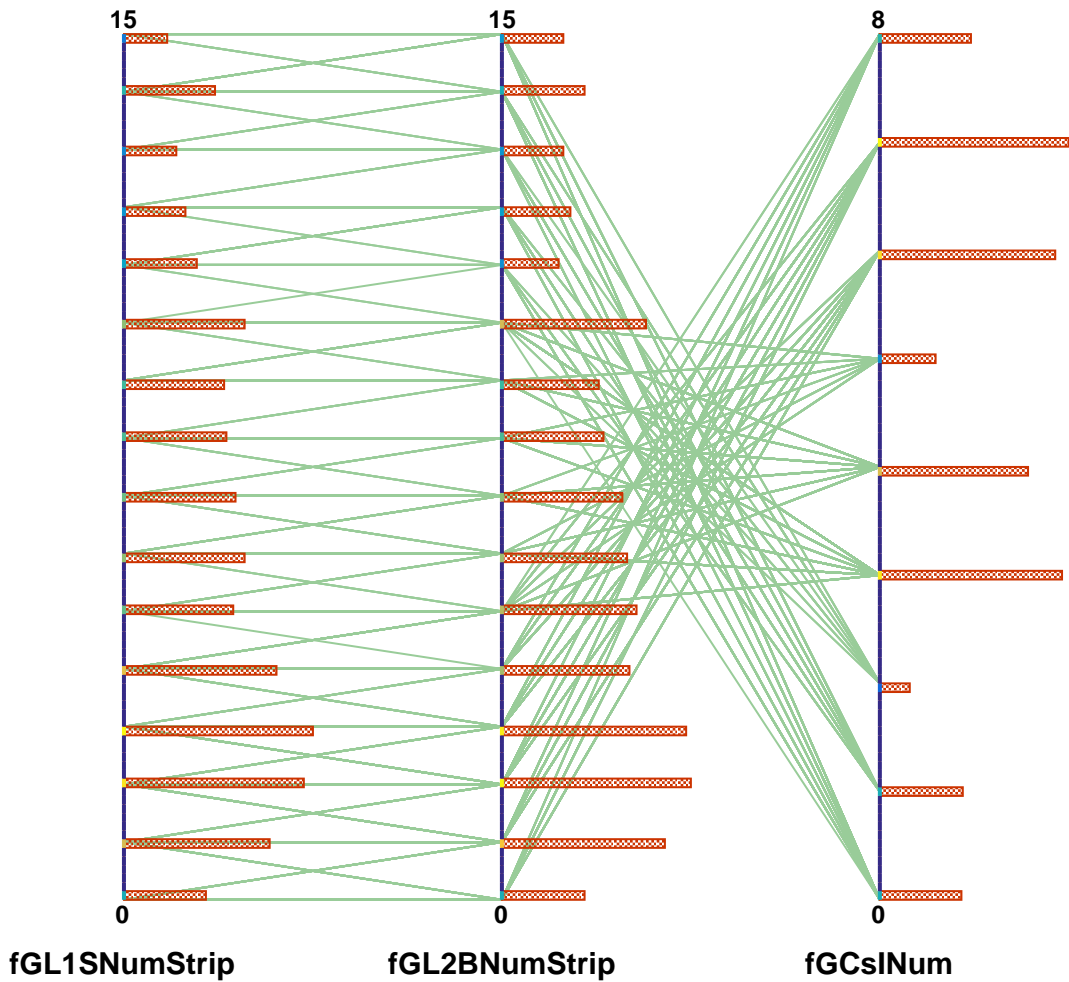


fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum==0 && fSSDGlobalMulti[0]==2)

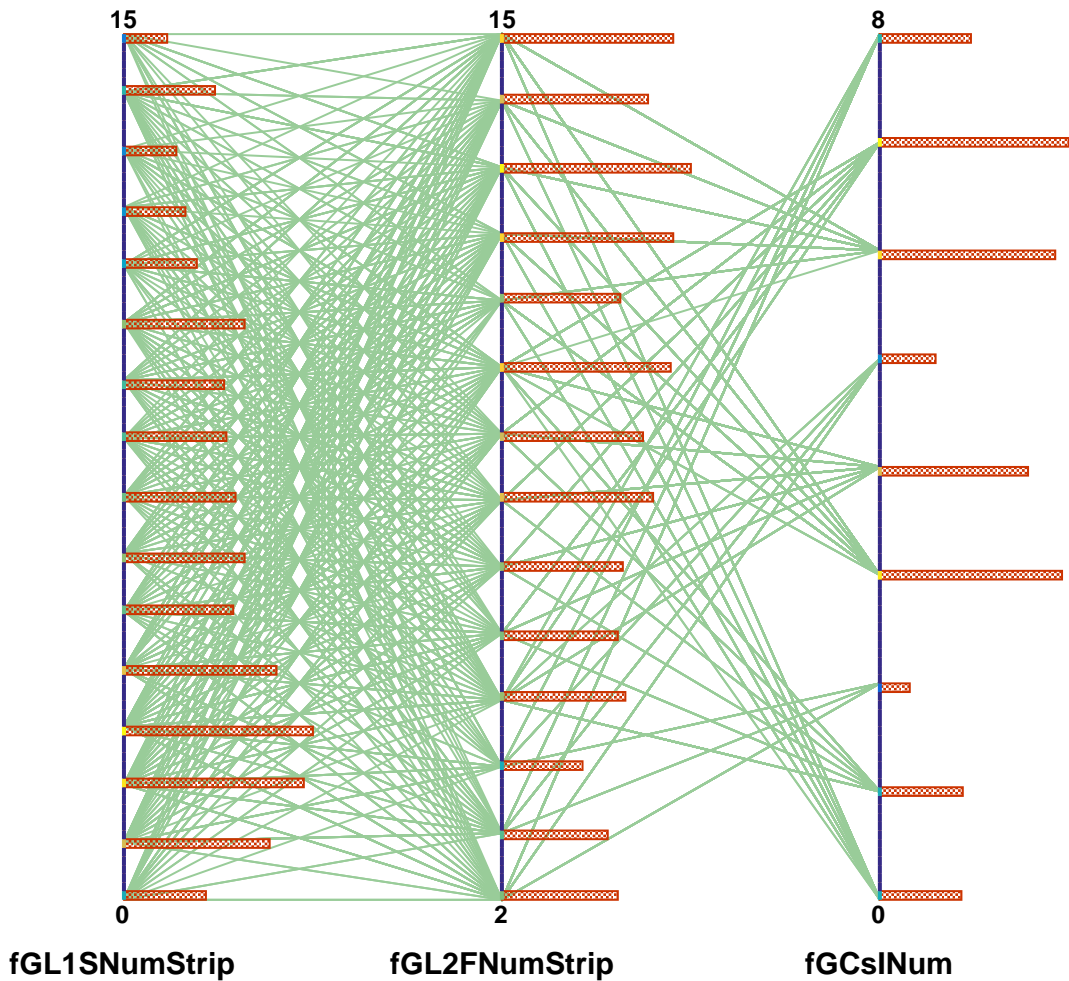




fGL1SNumStrip:fGL2BNumStrip:fGCsINum(fGSSDNum==1 && fSSDGlobalMult[1]==2)

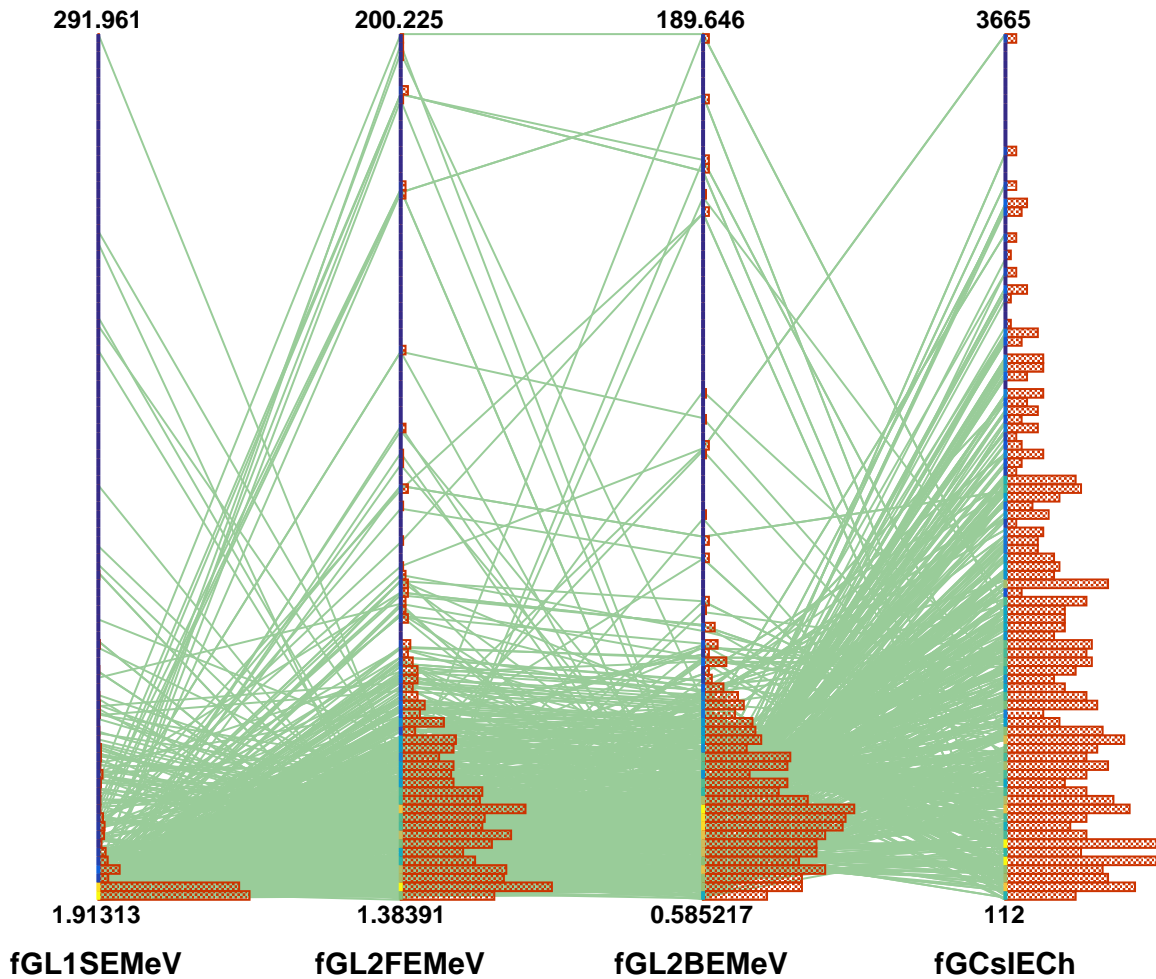


fGL1SNumStrip:fGL2FNumStrip:fGCsINum(fGSSDNum=1 && fSSDGlobalMulti[1]==2)

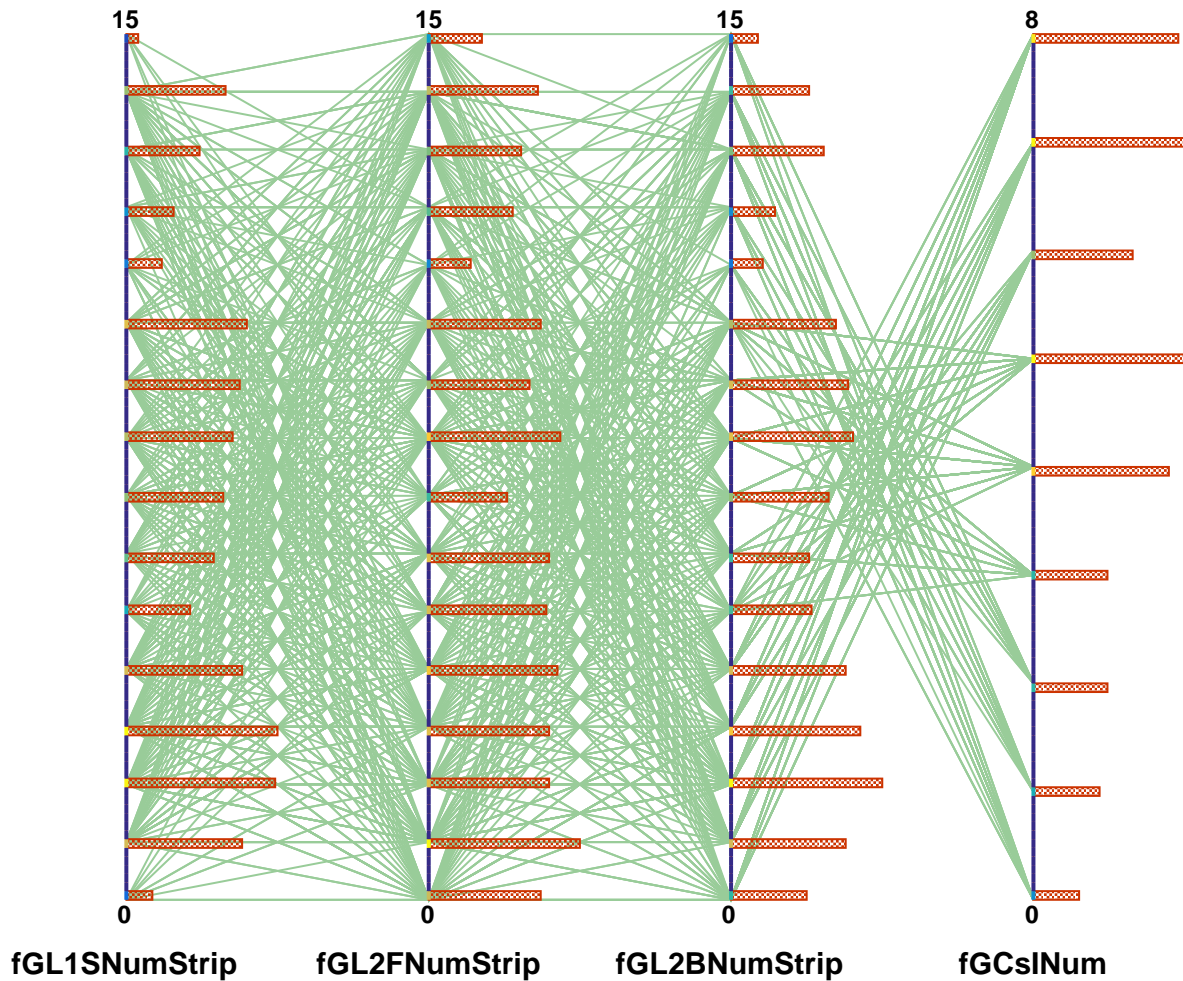




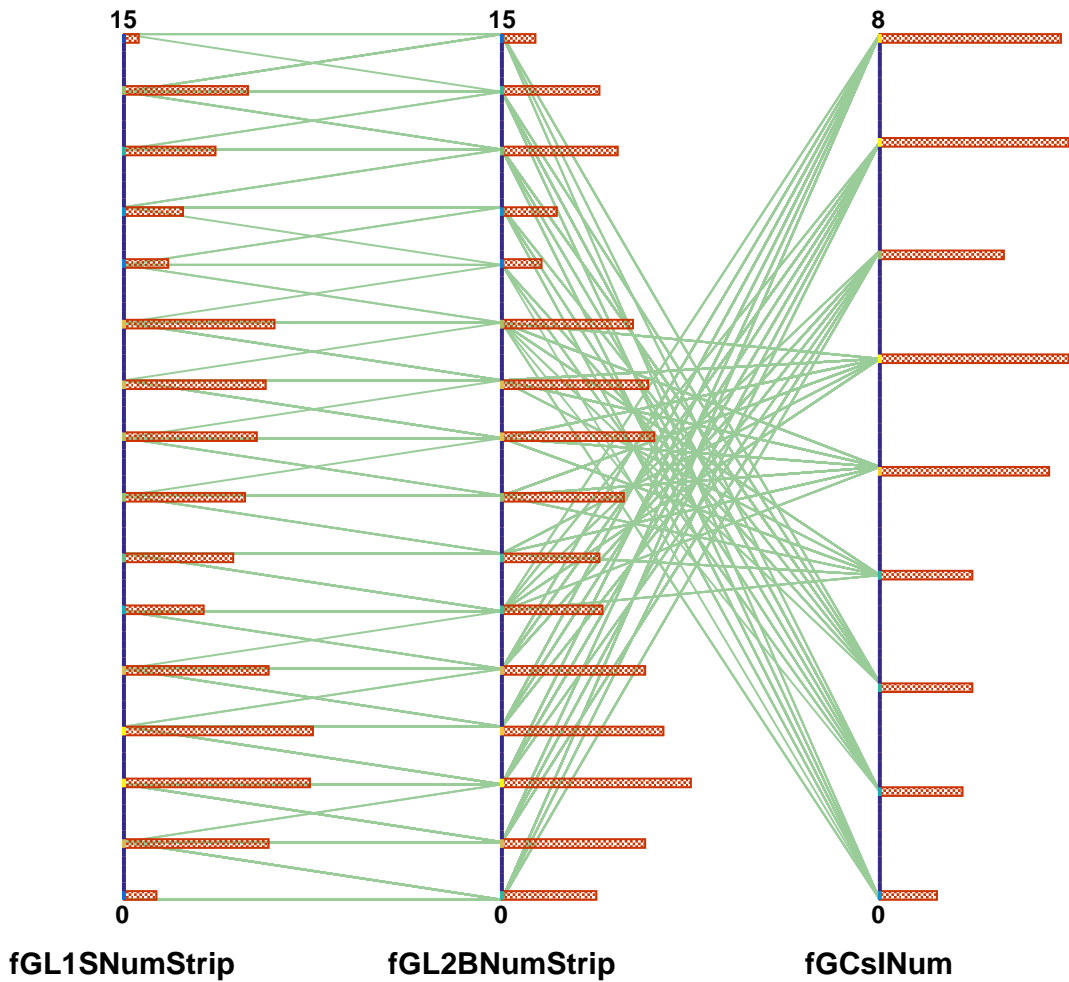
fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum=1 && fSSDGlobalMulti{1}=2)

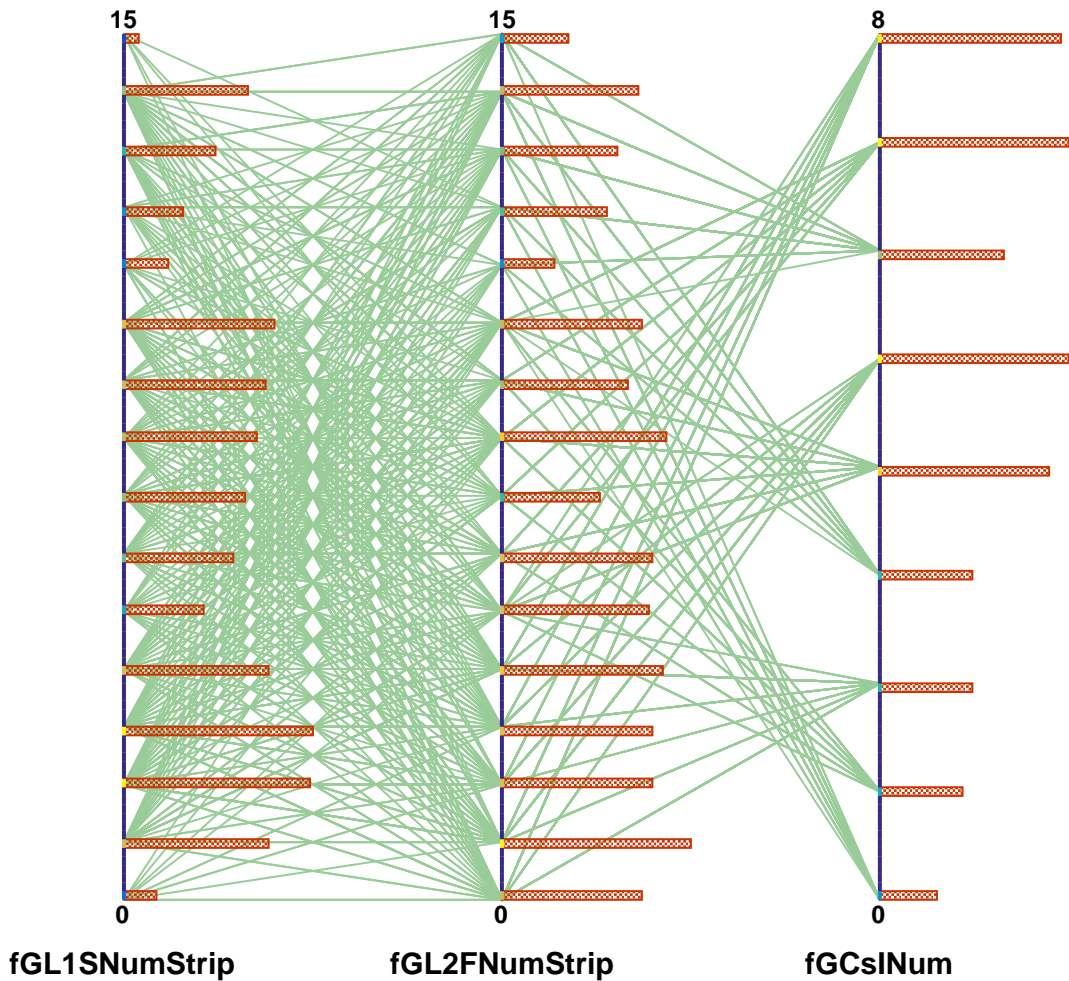




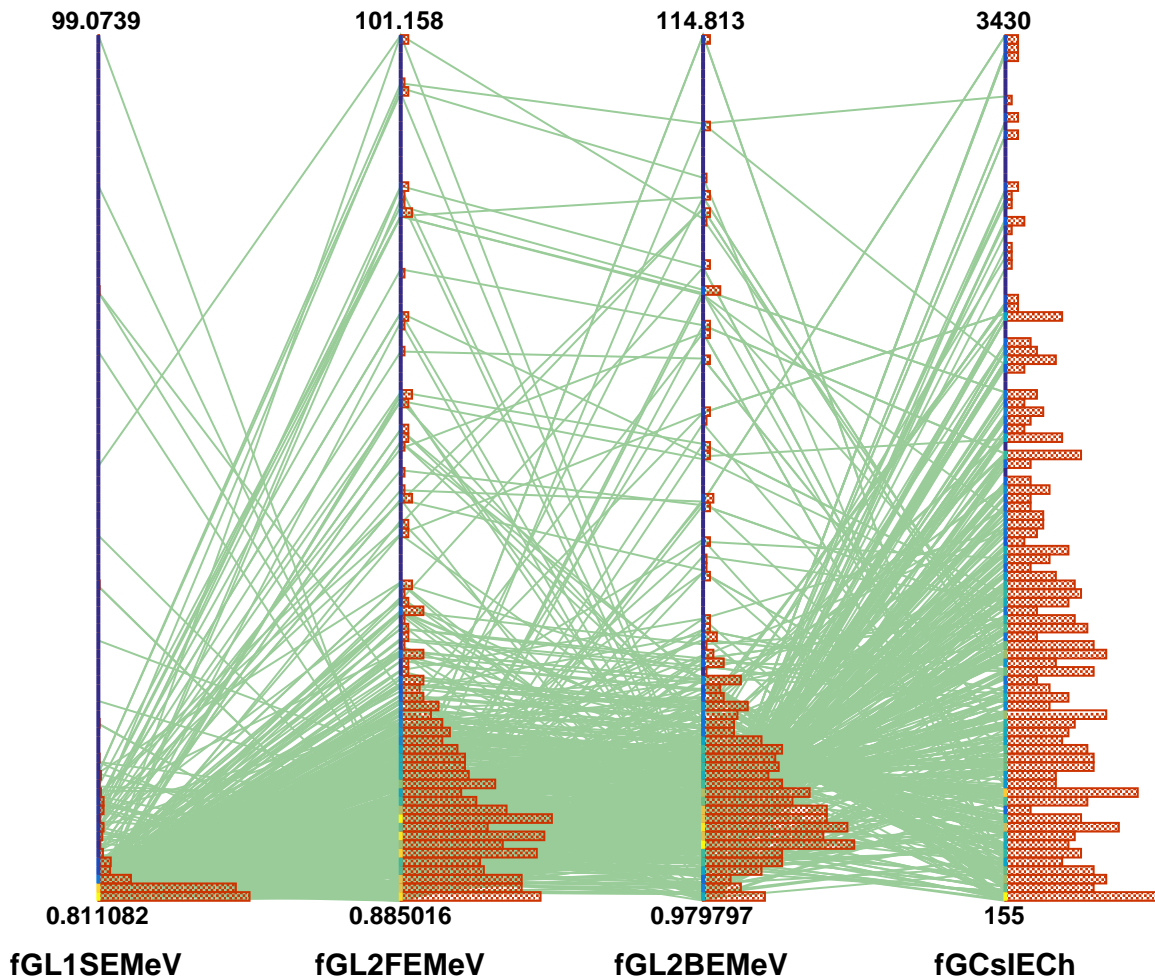


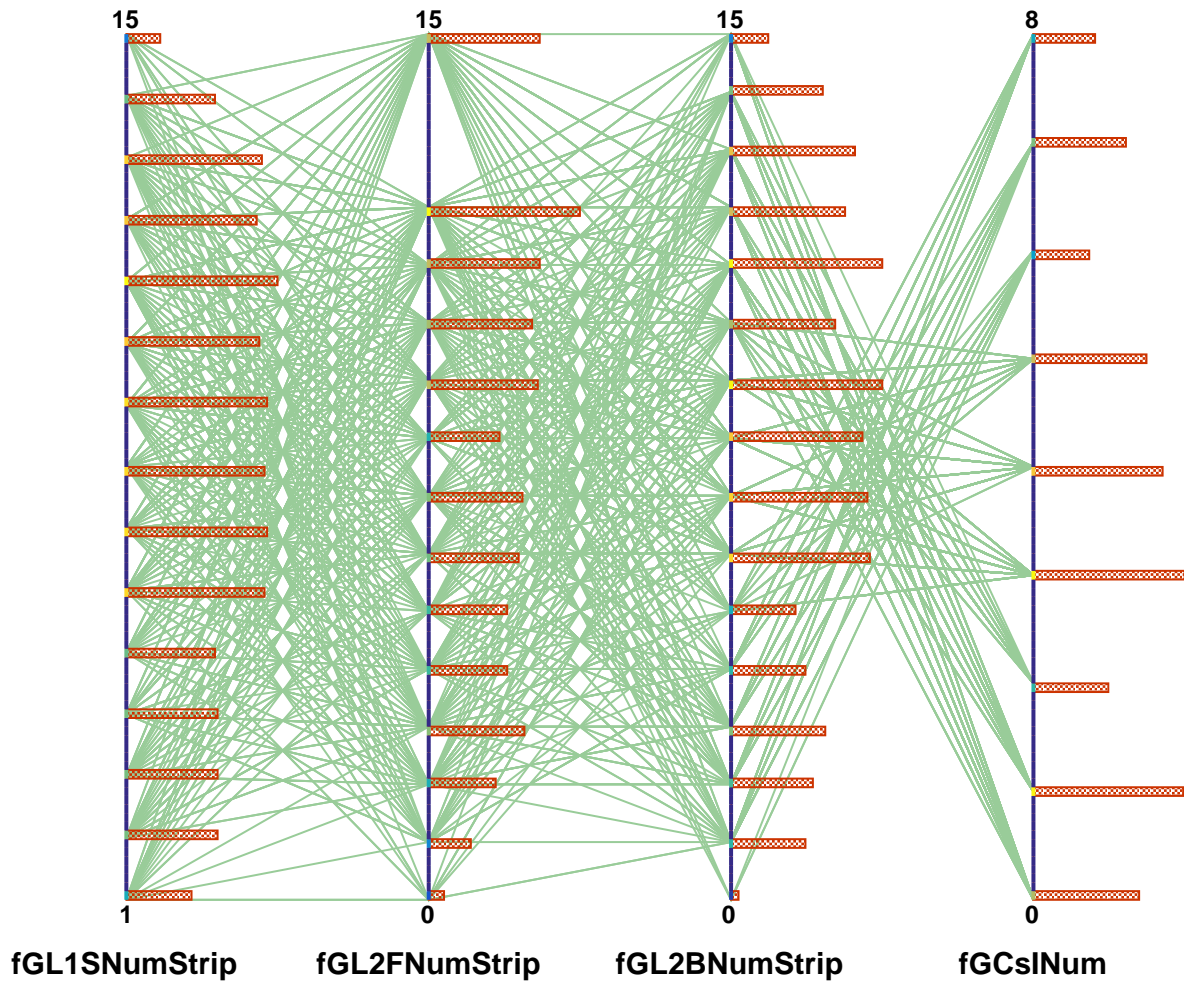
fGL1SNumStrip:fGL2BNumStrip:fGCsINum{fGSSDNum==2 && fSSDGlobalMulti[2]==2}



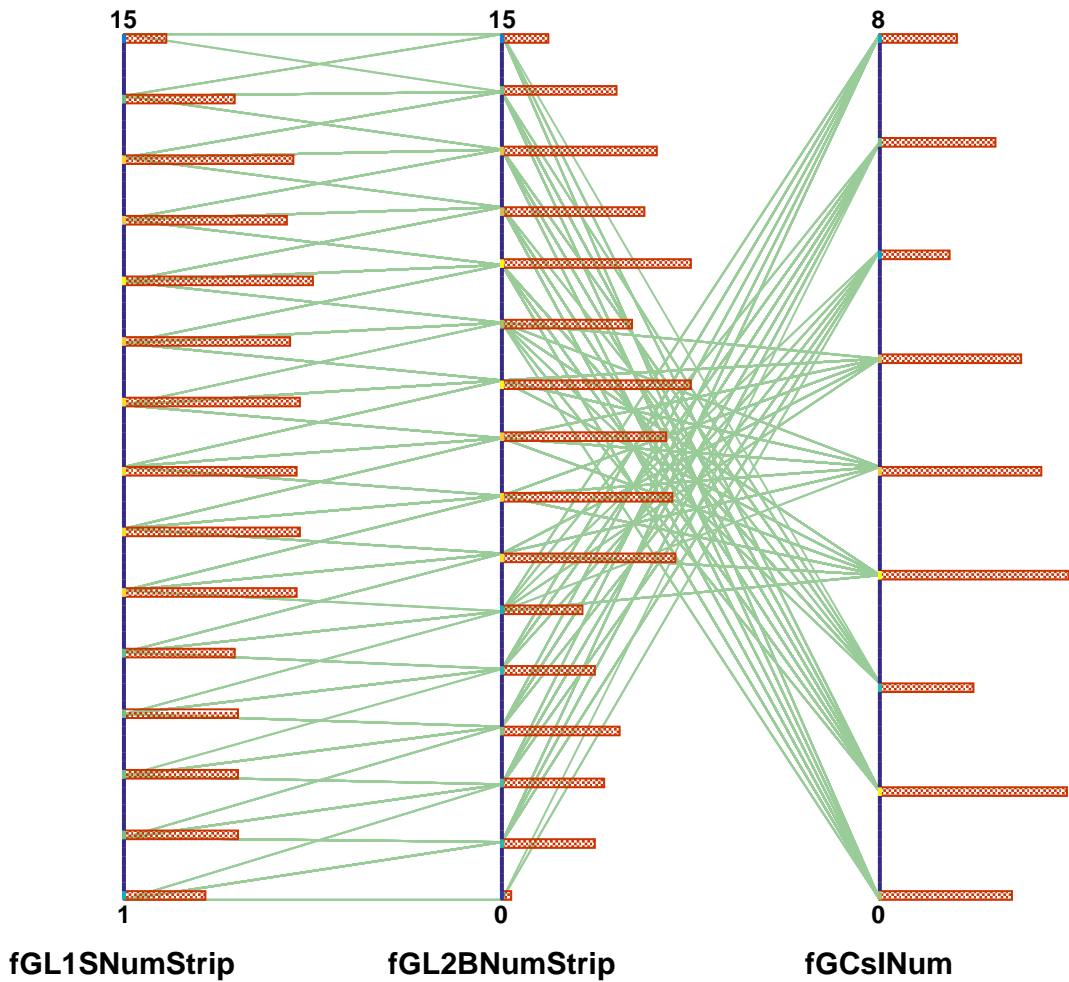


fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum==2 && fSSDGlobalMulti[2]==2)

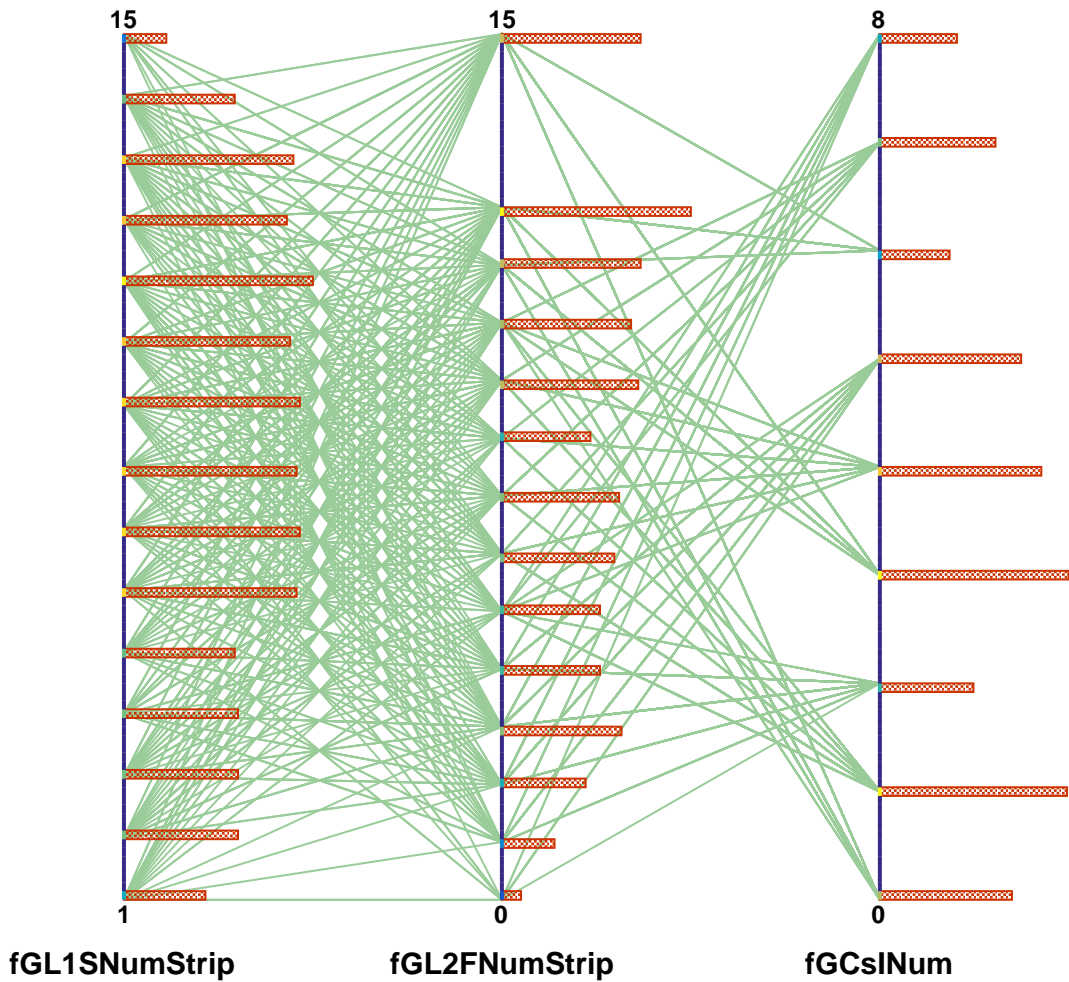




fGL1SNumStrip:fGL2BNumStrip:fGCsINum{fGSSDNum==3 && fSSDGlobalMulti[3]==2}



fGL1SNumStrip:fGL2FNumStrip:fGCsINum(fGSSDNum=3 && fSSDGlobalMulti[3]==2)





fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum==3 && fSSDGlobalMulti[3]==2)

