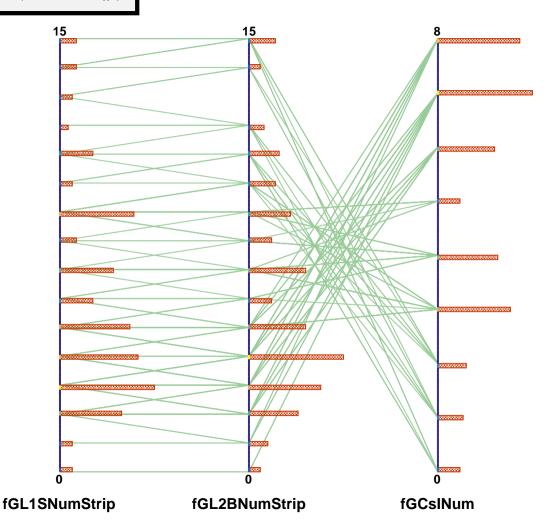
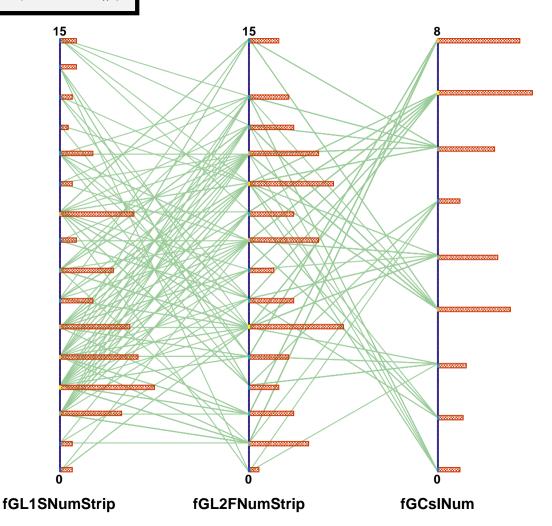


fGL1SNumStripfGL2FNumStripfGL2BNumStrip fGCslNum

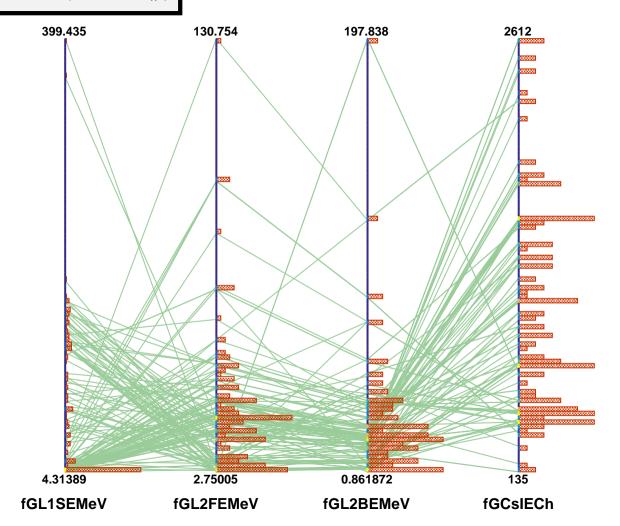
fGL1SNumStrip:fGL2BNumStrip:fGCslNum{fGSSDNum==0 && fSSDGlobalMulti[0]==3}



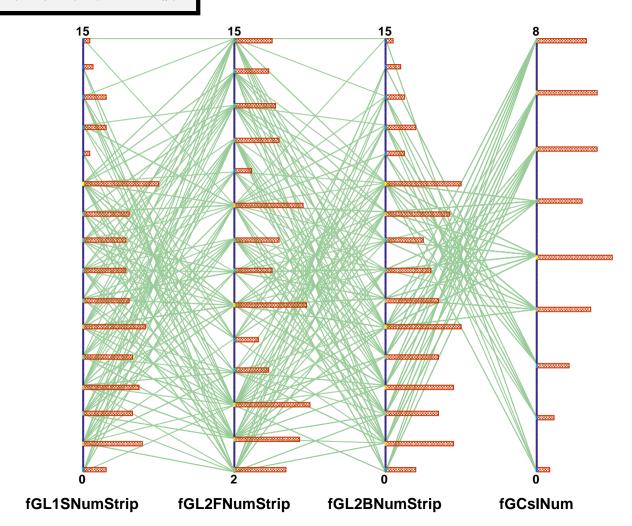
fGL1SNumStrip:fGL2FNumStrip:fGCslNum{fGSSDNum==0 && fSSDGlobalMulti[0]==3}



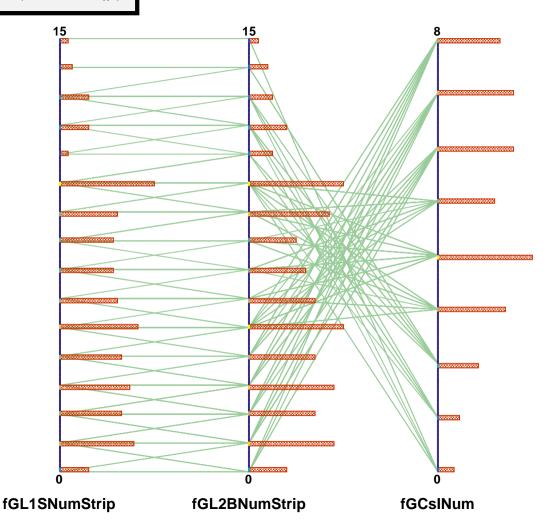
fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh(fGSSDNum==0 && fSSDGlobalMultif01==3)

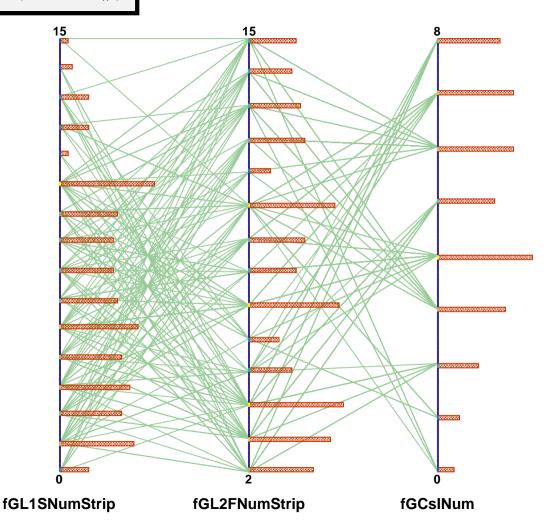


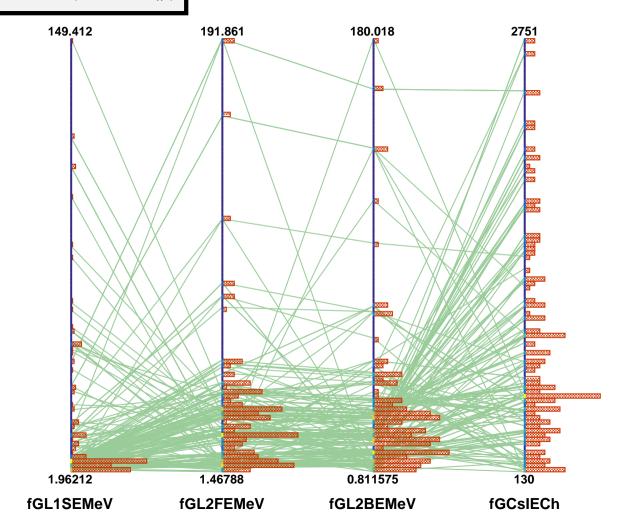
fGL1SNumStrip:fGL2FNumStrip:fGL2BNumStrip:fGCsINum(fGSSDNum==1 && fSSDGlobalMulti[1]==3)



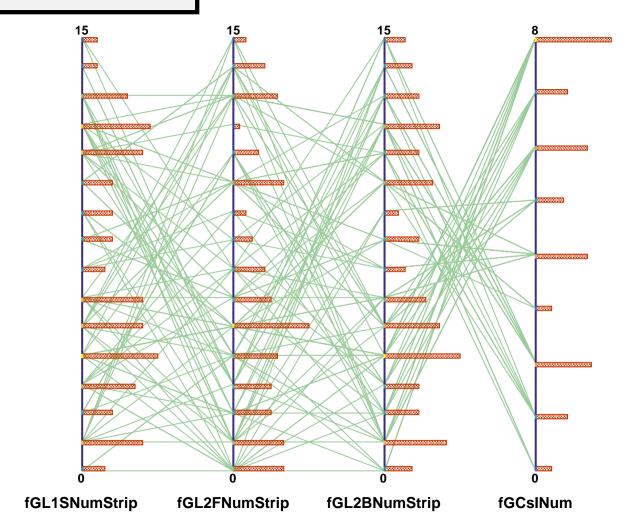
fGL1SNumStrip:fGL2BNumStrip:fGCslNum{fGSSDNum==1 && fSSDGlobalMulti[1]==3}

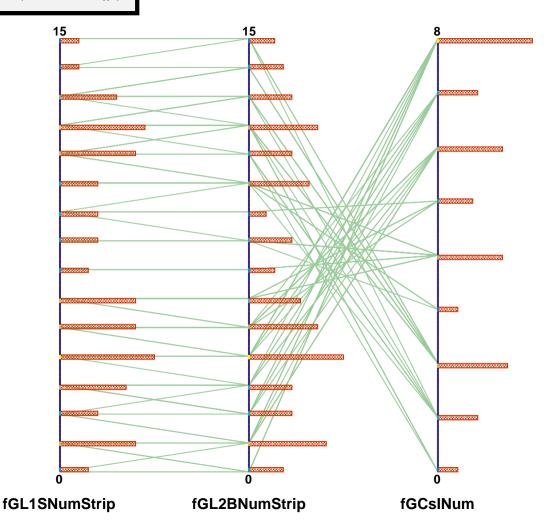




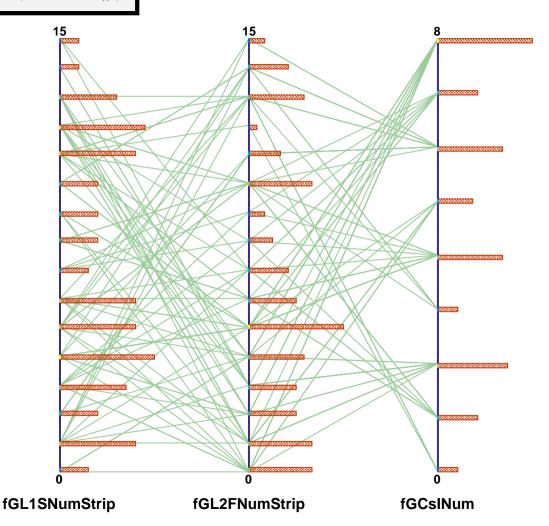


fGL1SNumStrip:fGL2FNumStrip:fGL2BNumStrip:fGCslNum(fGSSDNum==2 && fSSDGlobalMulti[2]==3)

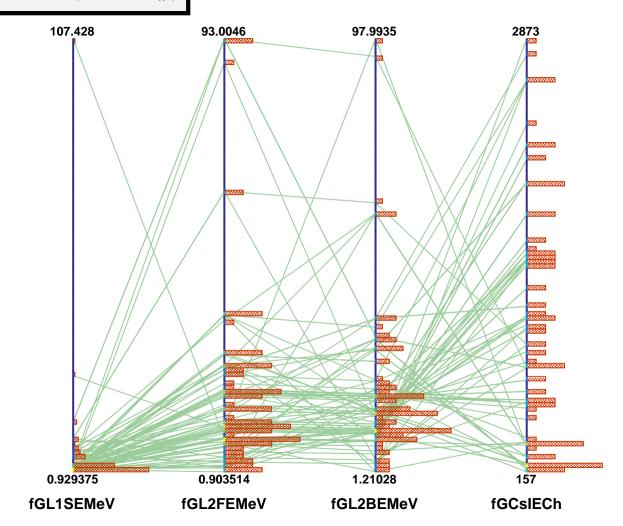




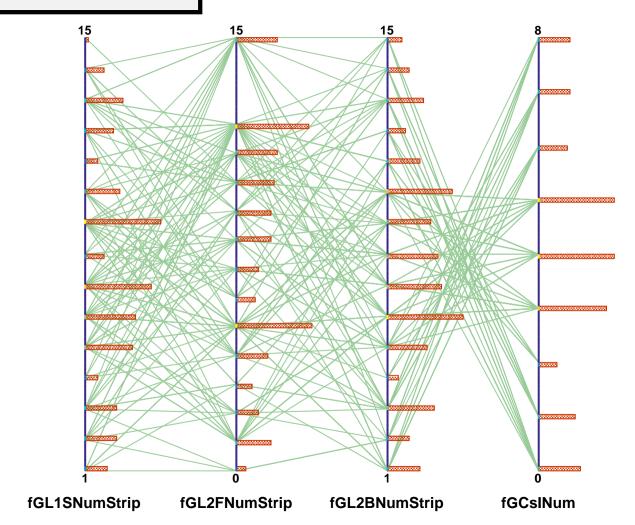
fGL1SNumStrip:fGL2FNumStrip:fGCslNum{fGSSDNum==2 && fSSDGlobalMulti[2]==3}



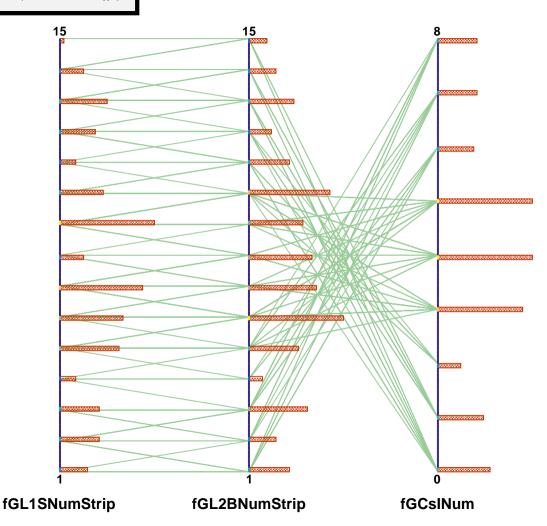
fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh{fGSSDNum==2 && fSSDGlobalMulti[2]==3}



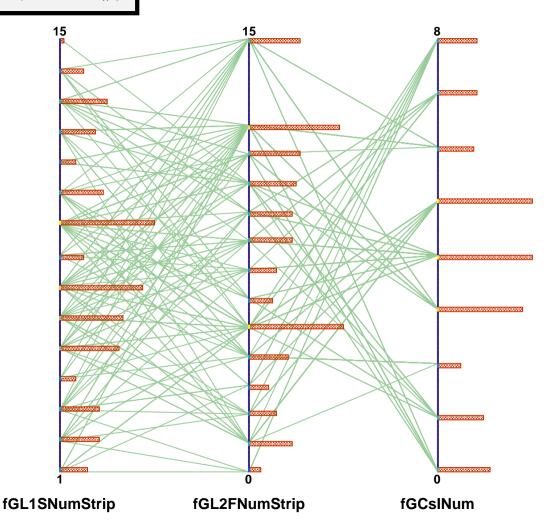
fGL1SNumStrip:fGL2FNumStrip:fGL2BNumStrip:fGCslNum(fGSSDNum==3 && fSSDGlobalMulti[3]==3)



fGL1SNumStrip:fGL2BNumStrip:fGCslNum{fGSSDNum==3 && fSSDGlobalMulti[3]==3}



fGL1SNumStrip:fGL2FNumStrip:fGCslNum{fGSSDNum==3 && fSSDGlobalMulti[3]==3}



fGL1SEMeV:fGL2FEMeV:fGL2BEMeV:fGCsIECh{fGSSDNum==3 && fSSDGlobalMulti[3]==3}

