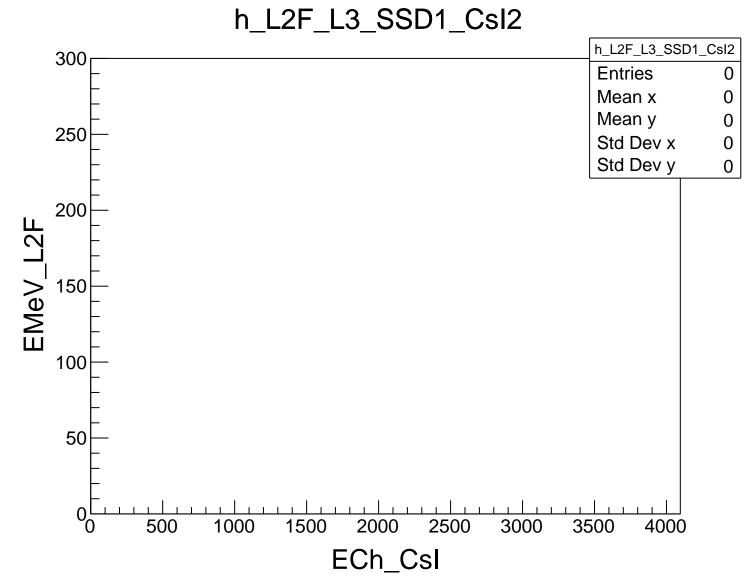
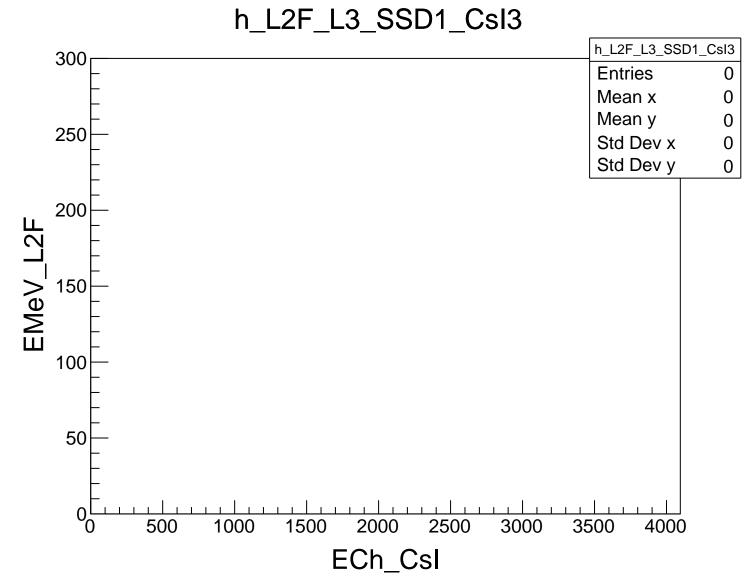
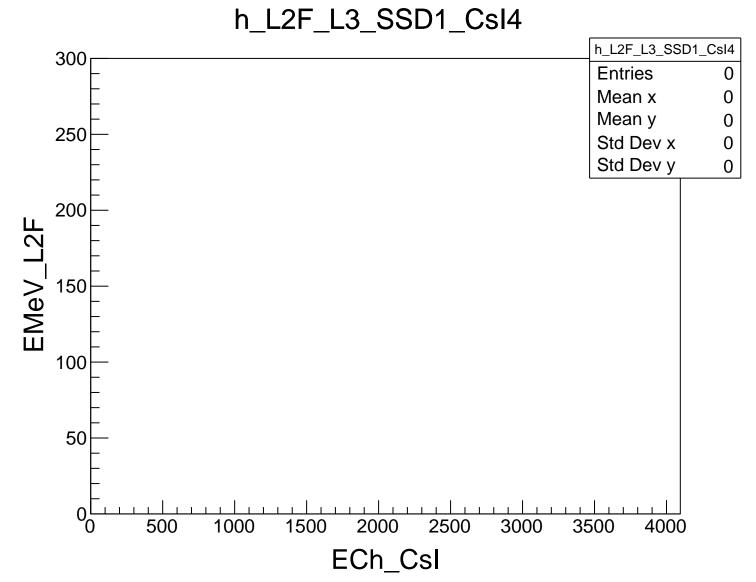
h_L2F_L3_SSD1_CsI0 h_L2F_L3_SSD1_Csl0 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD1_CsI1 h_L2F_L3_SSD1_Csl1 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

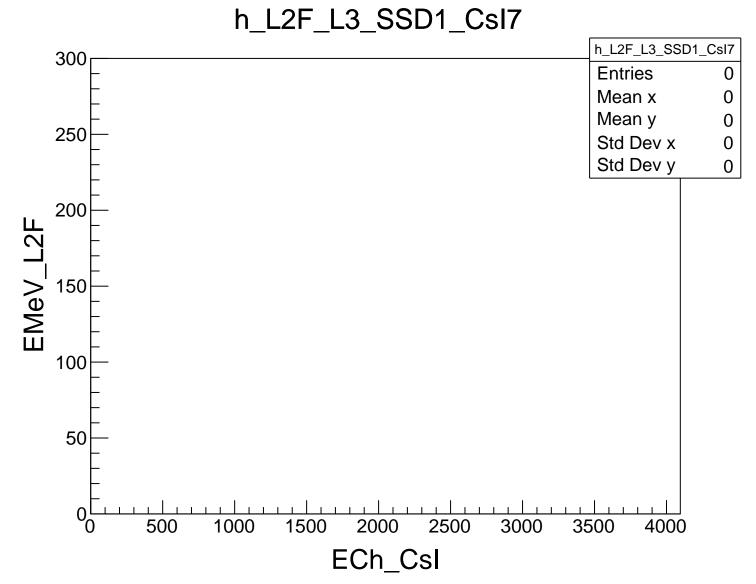




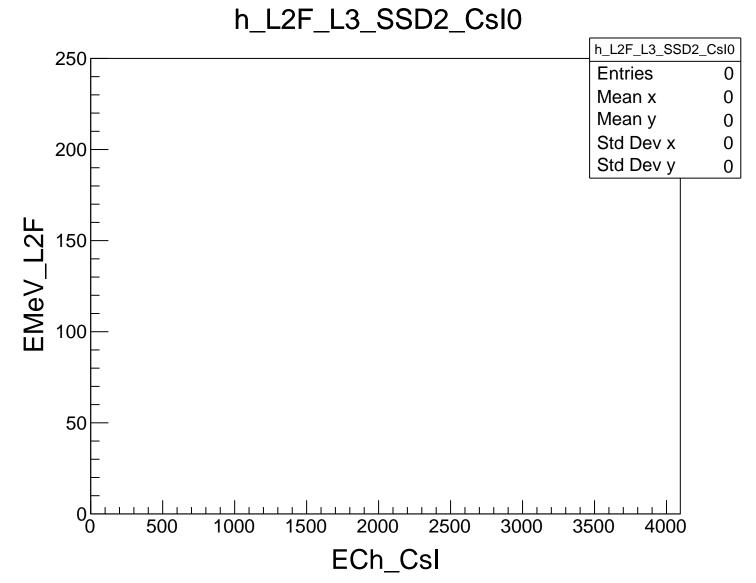


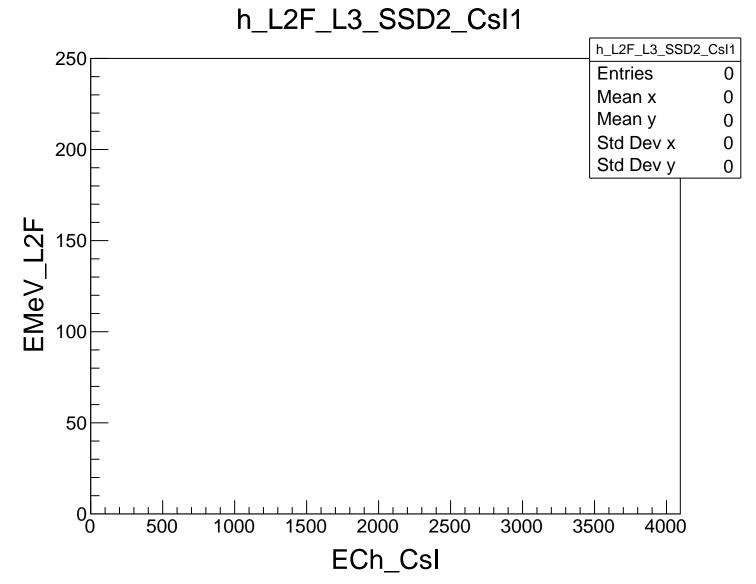
h_L2F_L3_SSD1_Csl5 h_L2F_L3_SSD1_Csl5 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

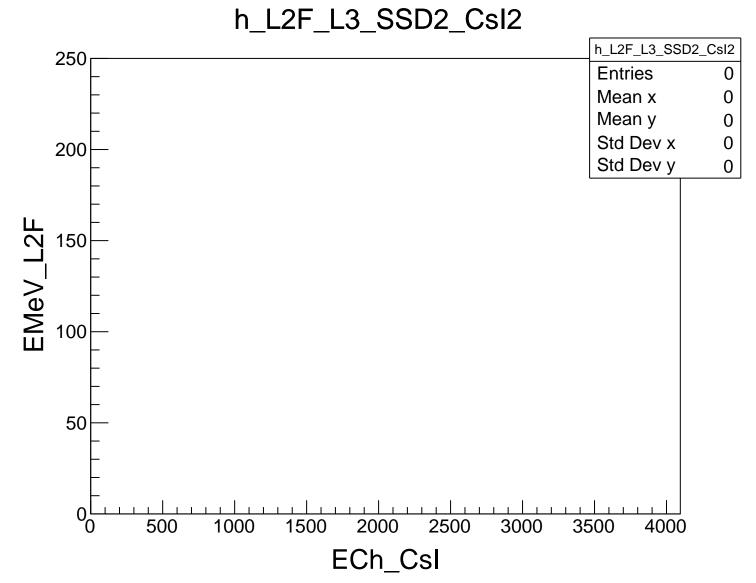
h_L2F_L3_SSD1_Csl6 h_L2F_L3_SSD1_Csl6 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

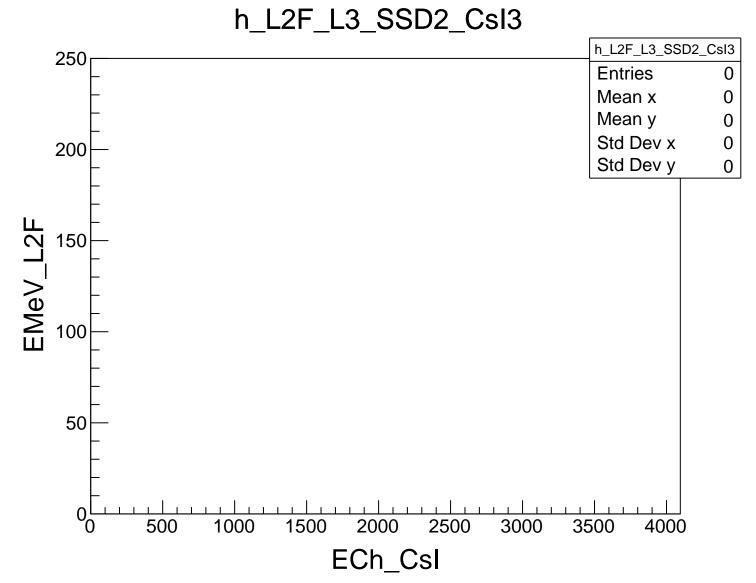


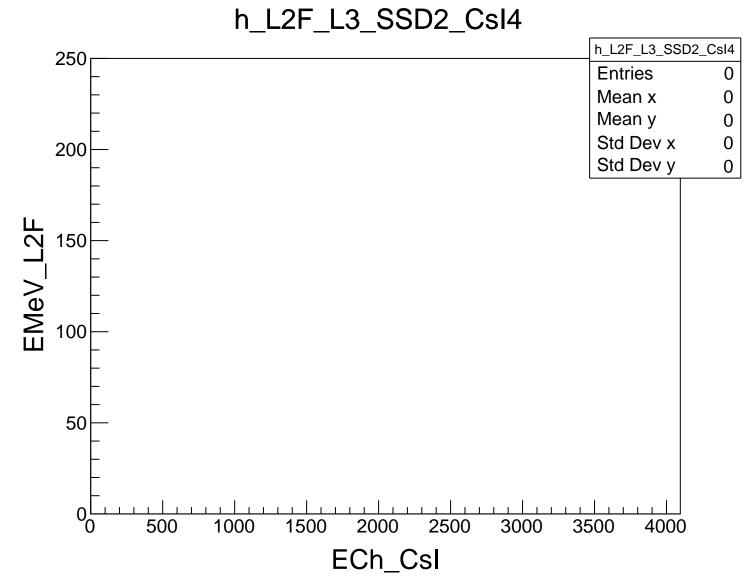
h_L2F_L3_SSD1_Csl8 h_L2F_L3_SSD1_Csl8 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

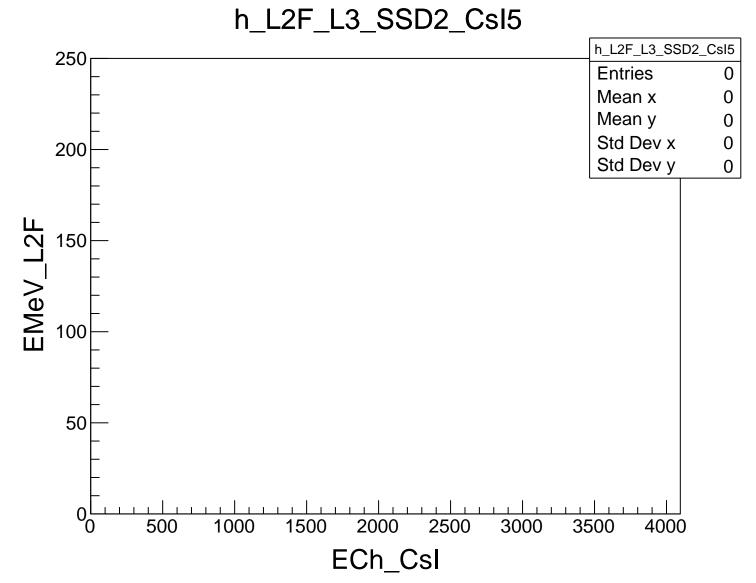


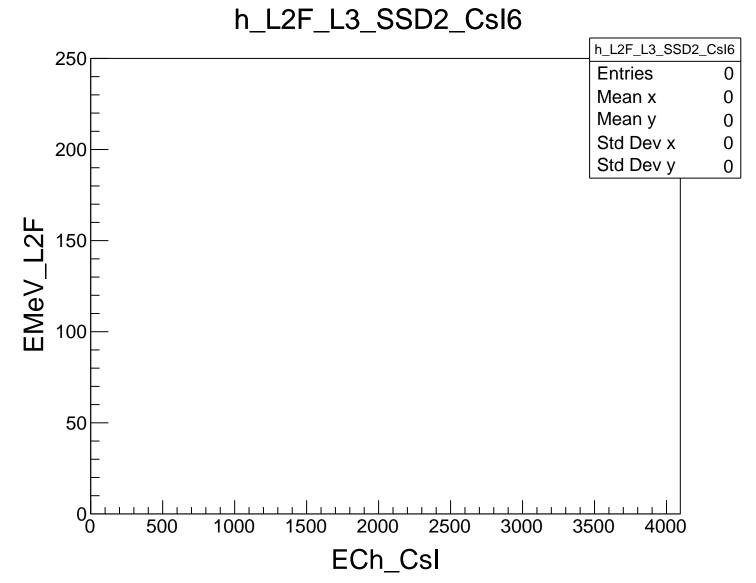


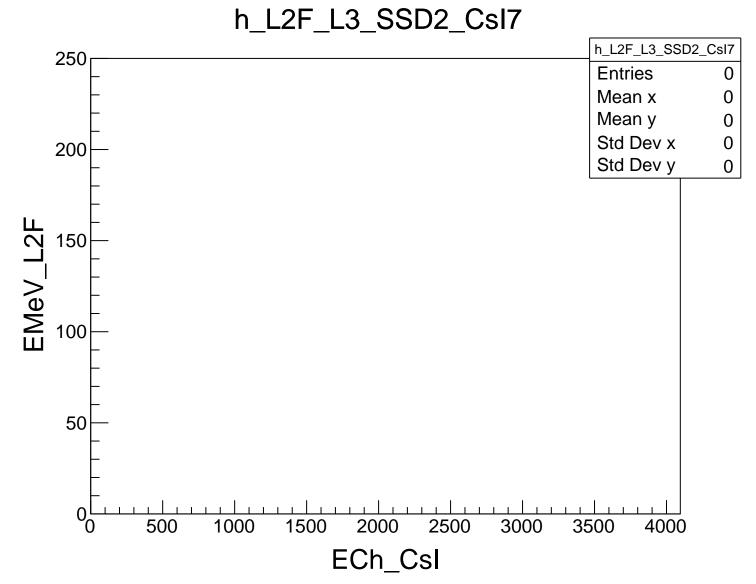


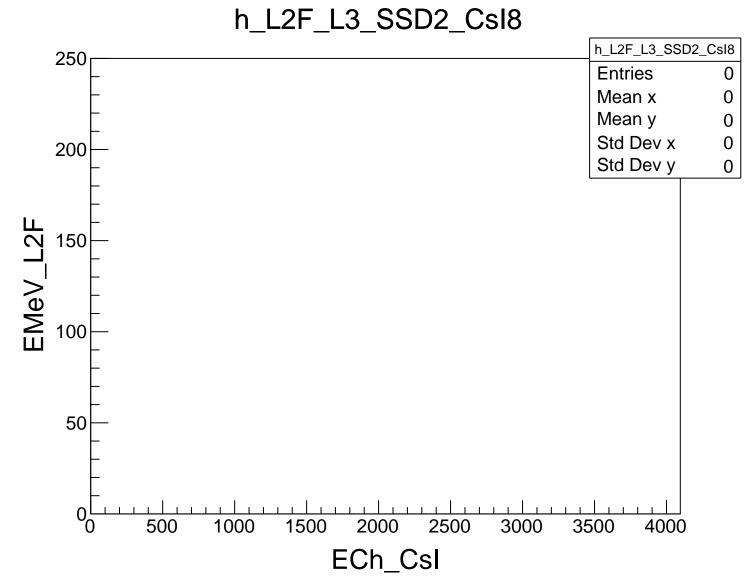




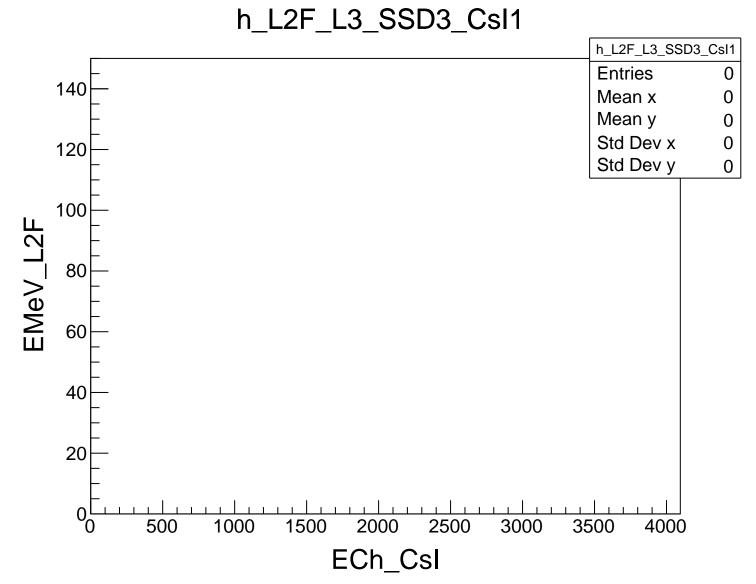




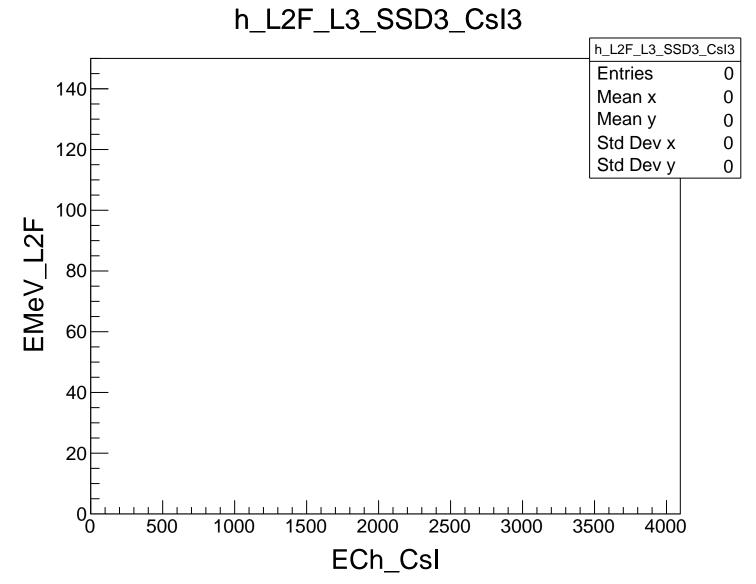




h_L2F_L3_SSD3_Csl0 h_L2F_L3_SSD3_Csl0 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI



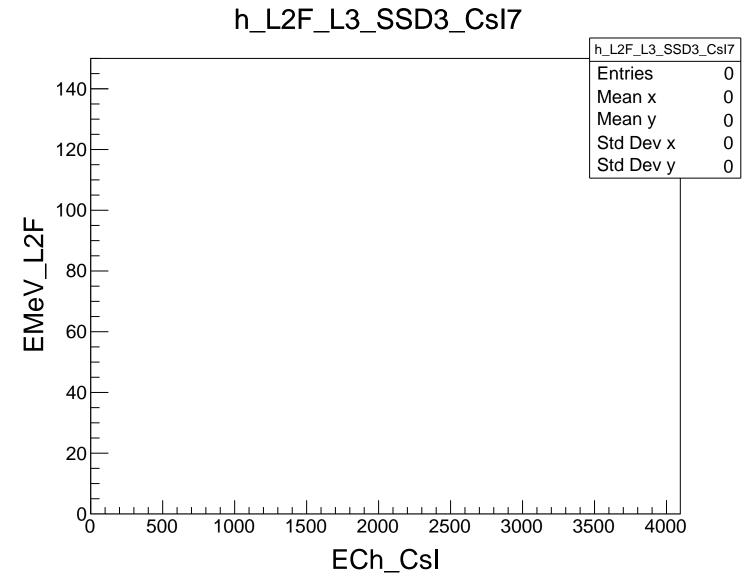
h_L2F_L3_SSD3_Csl2 h_L2F_L3_SSD3_Csl2 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI



h_L2F_L3_SSD3_CsI4 h_L2F_L3_SSD3_Csl4 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD3_Csl5 h_L2F_L3_SSD3_Csl5 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD3_Csl6 h_L2F_L3_SSD3_Csl6 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI



h_L2F_L3_SSD3_Csl8 h_L2F_L3_SSD3_Csl8 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_CsI0 h_L2F_L3_SSD4_Csl0 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_CsI1 h_L2F_L3_SSD4_Csl1 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_Csl2 h_L2F_L3_SSD4_Csl2 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_Csl3 h_L2F_L3_SSD4_Csl3 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_CsI4 h_L2F_L3_SSD4_Csl4 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_Csl5 h_L2F_L3_SSD4_Csl5 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_Csl6 h_L2F_L3_SSD4_Csl6 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_Csl7 h_L2F_L3_SSD4_Csl7 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI

h_L2F_L3_SSD4_Csl8 h_L2F_L3_SSD4_Csl8 **Entries** Mean x Mean y Std Dev x Std Dev y EMeV_L2F ECh_CsI