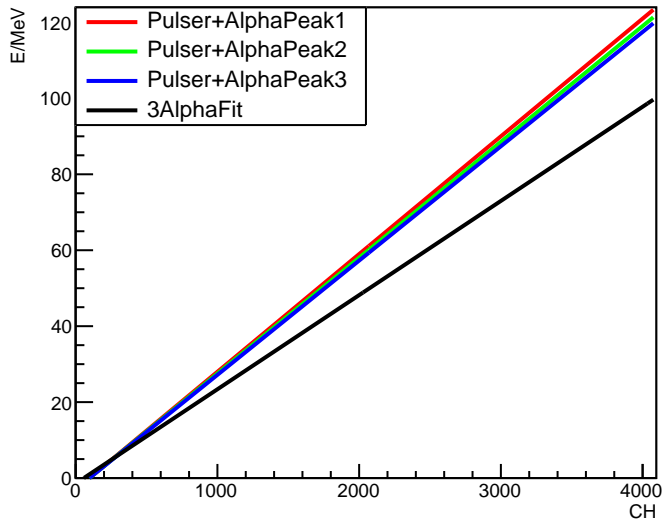
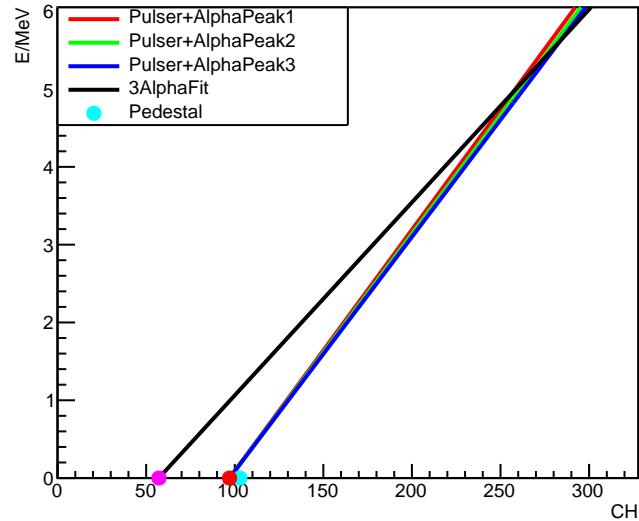


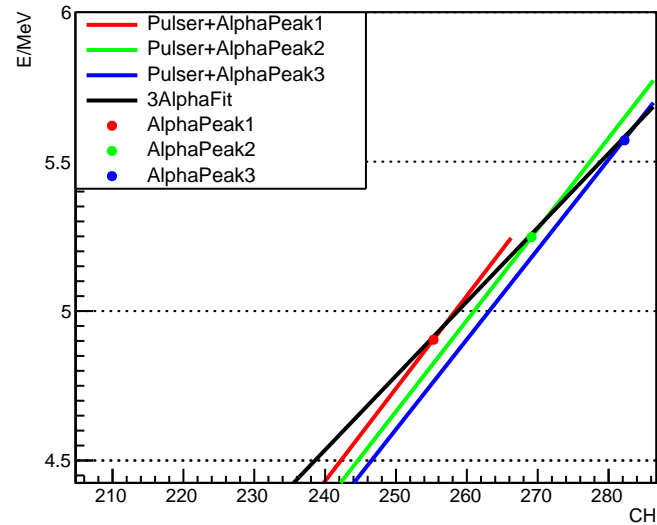
SSD1_L1SS_Switch_CH00



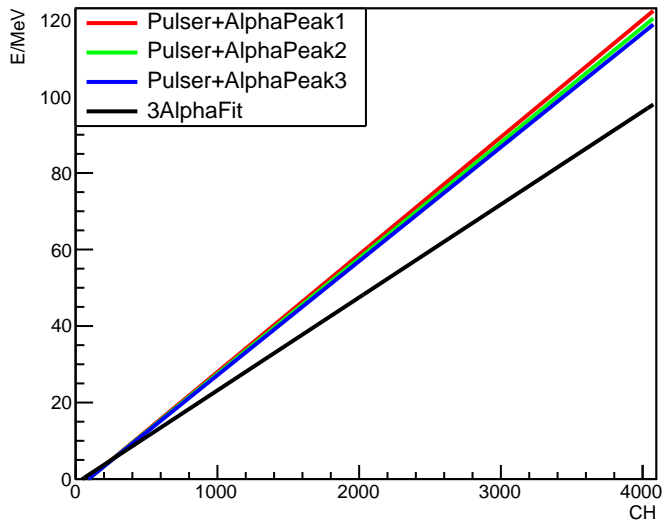
SSD1_L1SS_Switch_CH00



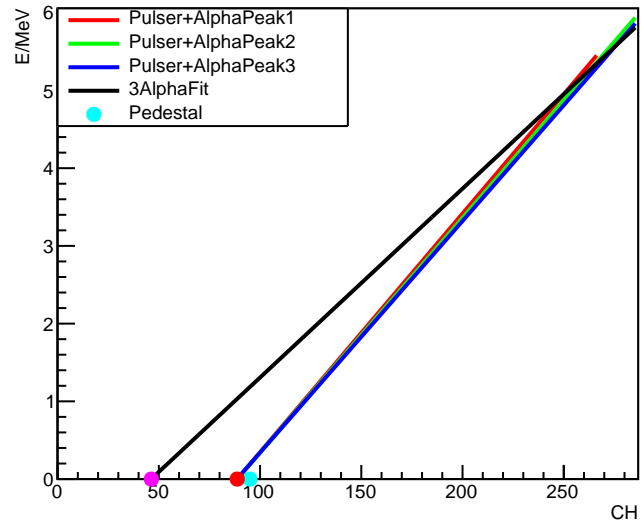
SSD1_L1SS_Switch_CH00



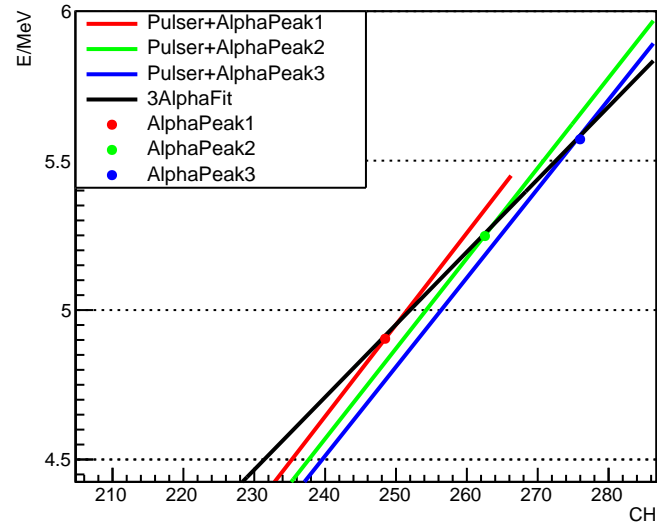
SSD1_L1SS_Switch_CH01



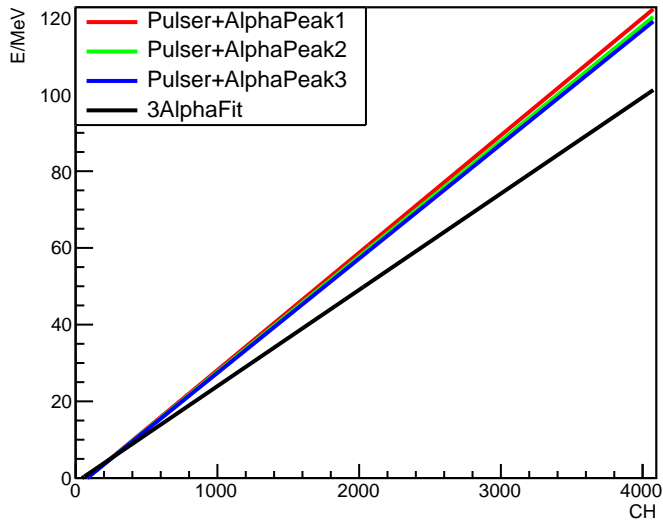
SSD1_L1SS_Switch_CH01



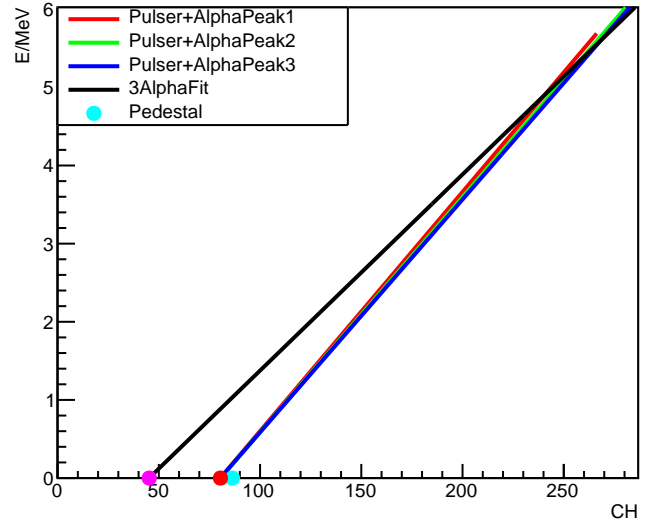
SSD1_L1SS_Switch_CH01



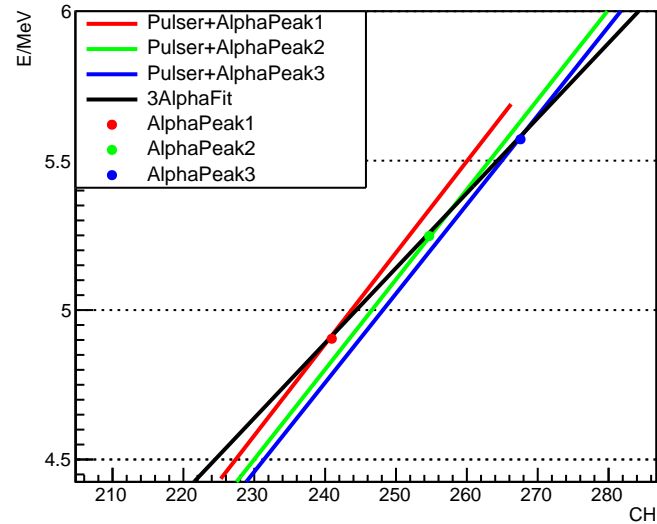
SSD1_L1SS_Switch_CH02



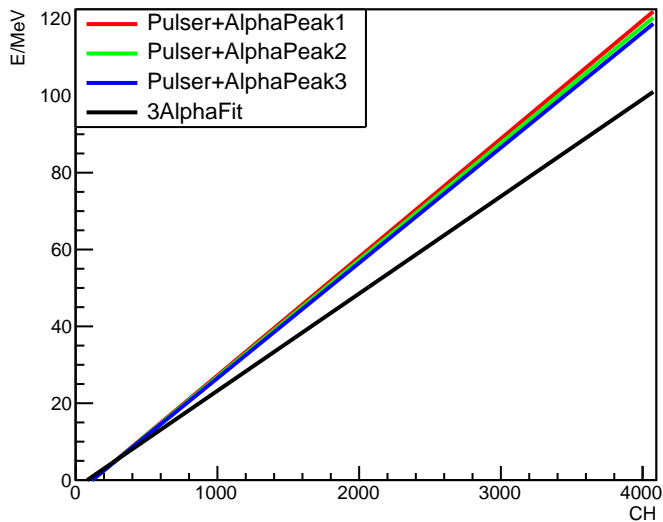
SSD1_L1SS_Switch_CH02



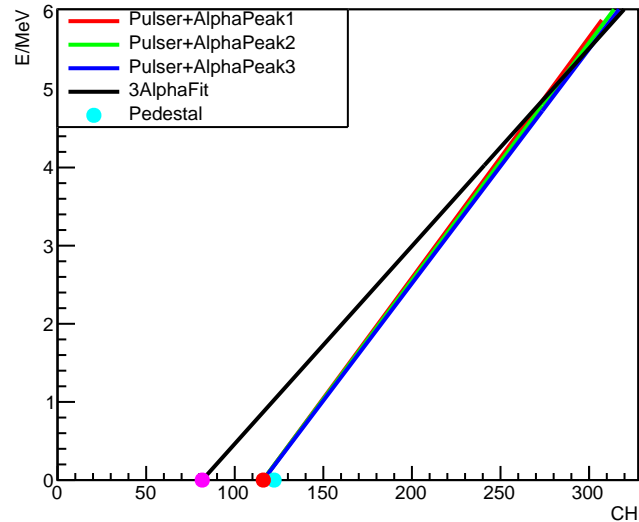
SSD1_L1SS_Switch_CH02



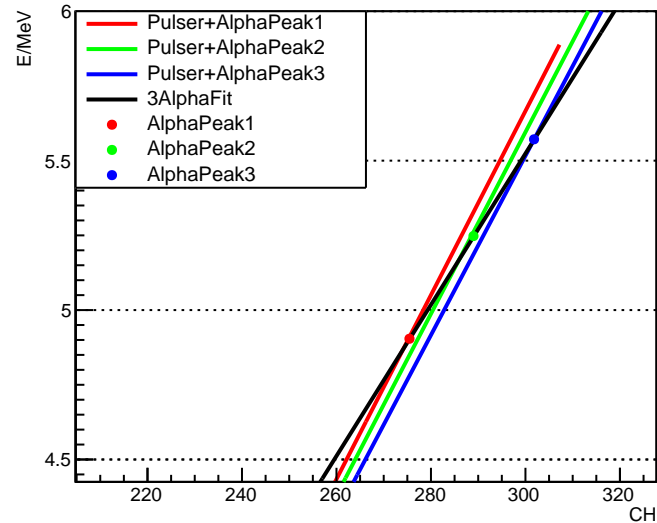
SSD1_L1SS_Switch_CH03



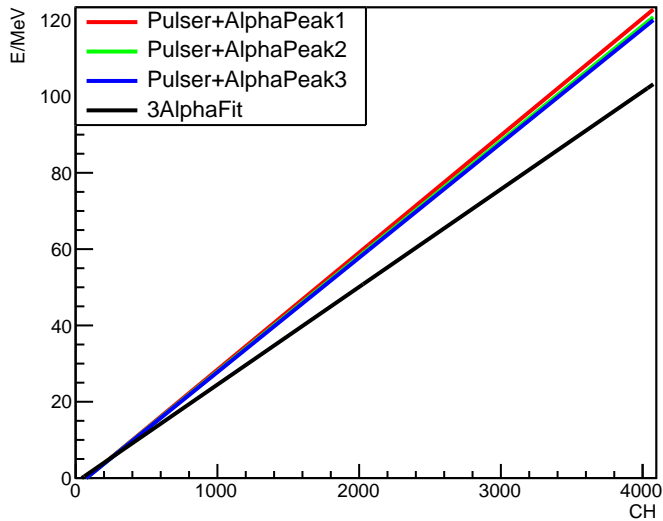
SSD1_L1SS_Switch_CH03



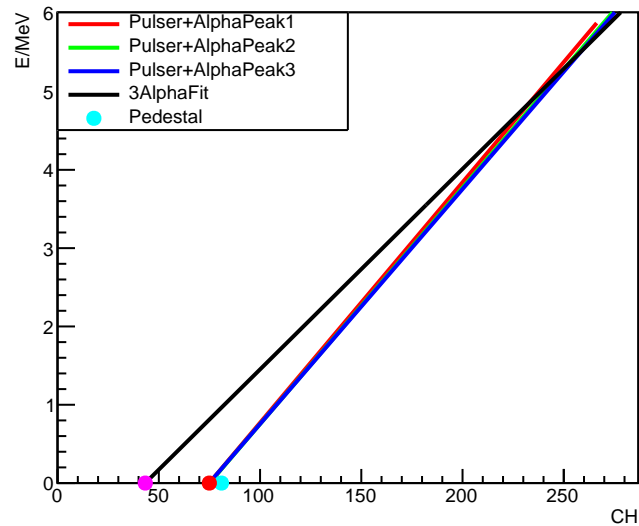
SSD1_L1SS_Switch_CH03



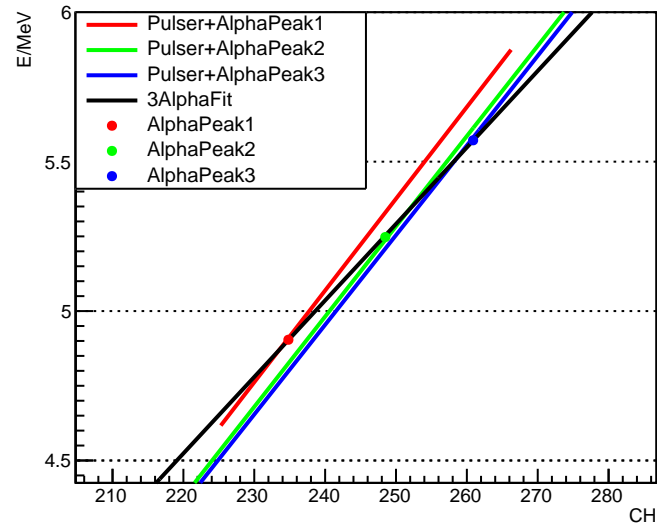
SSD1_L1SS_Switch_CH04



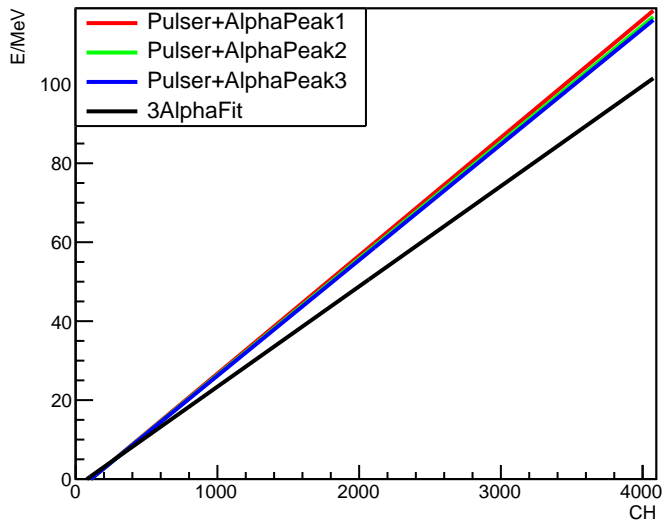
SSD1_L1SS_Switch_CH04



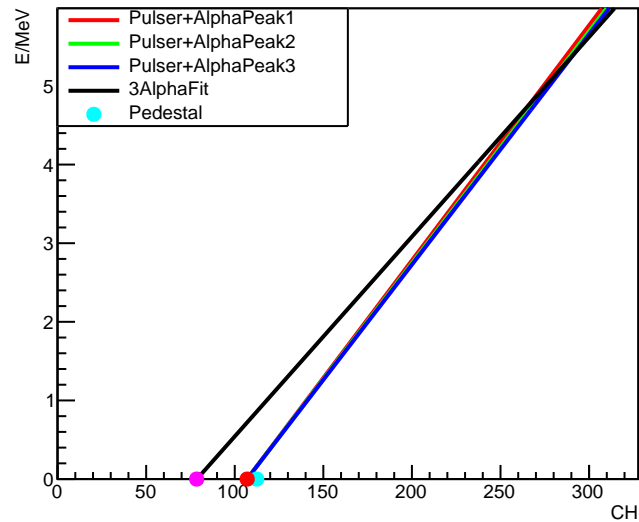
SSD1_L1SS_Switch_CH04



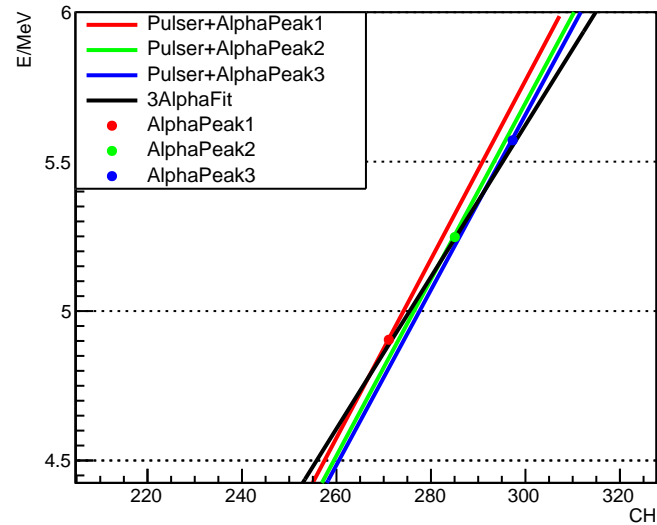
SSD1_L1SS_Switch_CH05



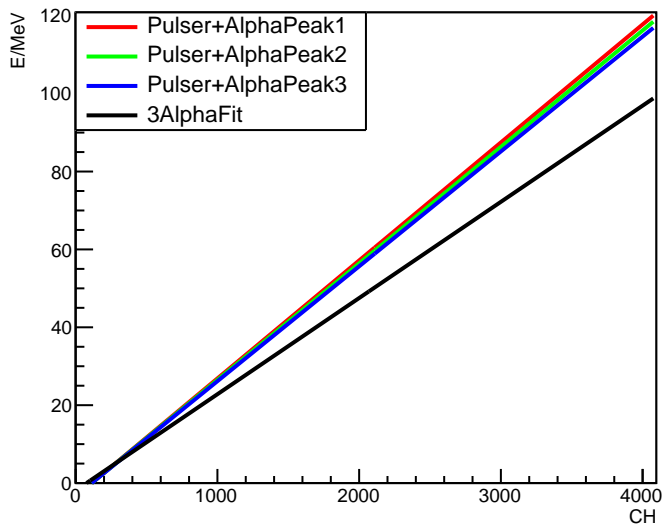
SSD1_L1SS_Switch_CH05



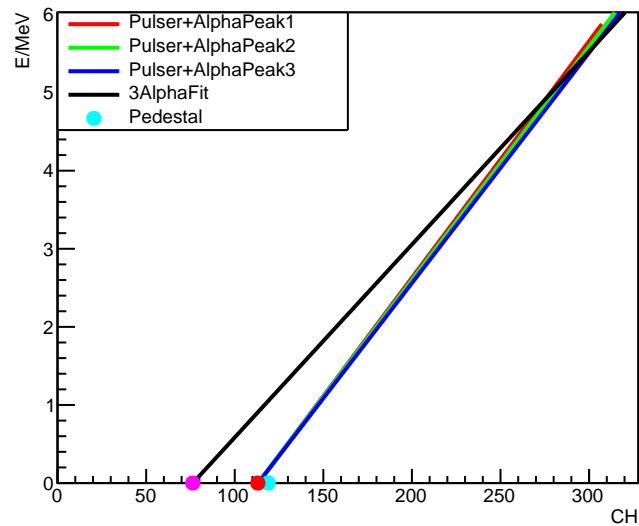
SSD1_L1SS_Switch_CH05



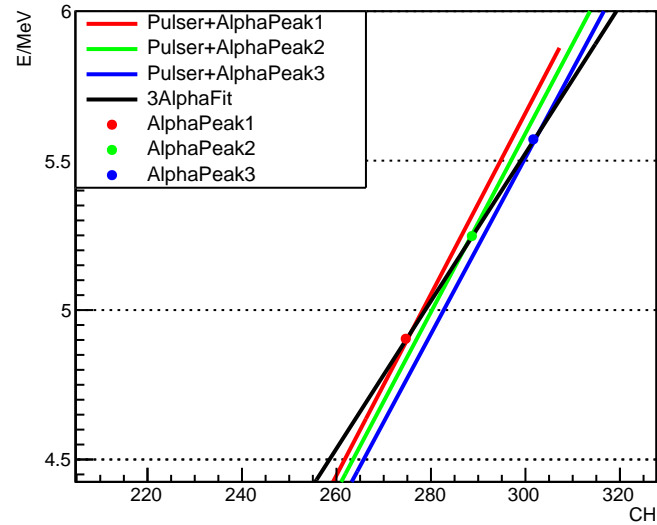
SSD1_L1SS_Switch_CH06



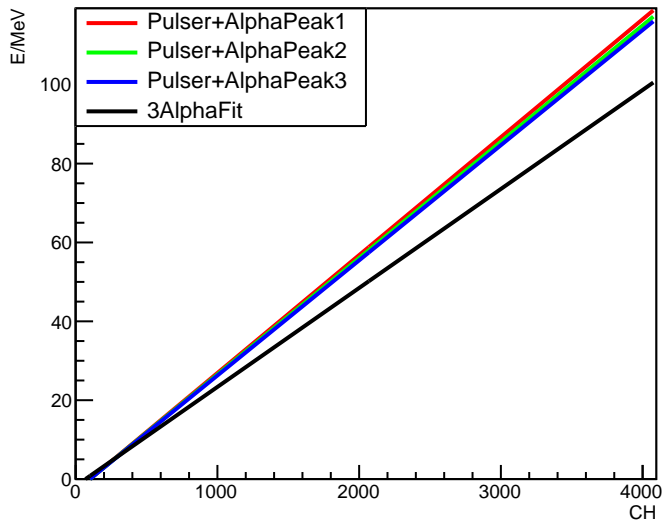
SSD1_L1SS_Switch_CH06



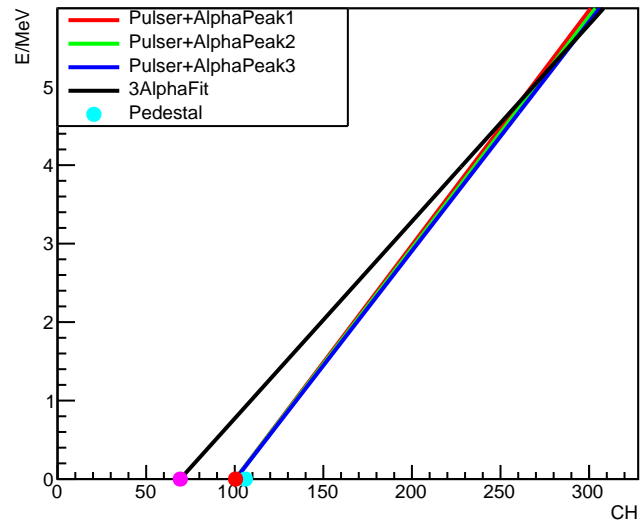
SSD1_L1SS_Switch_CH06



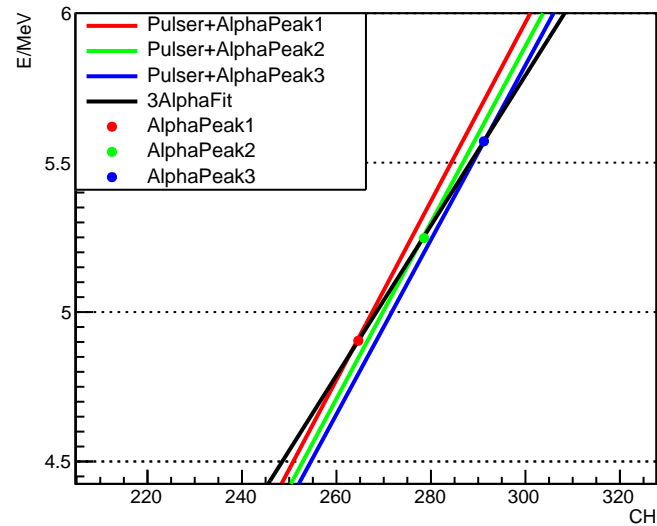
SSD1_L1SS_Switch_CH07



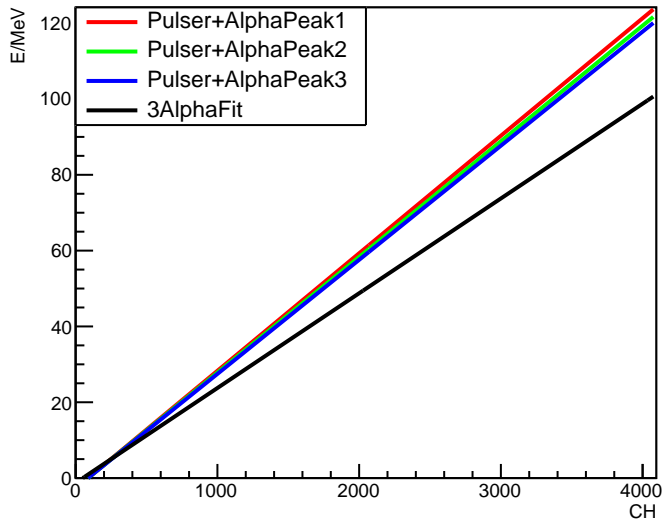
SSD1_L1SS_Switch_CH07



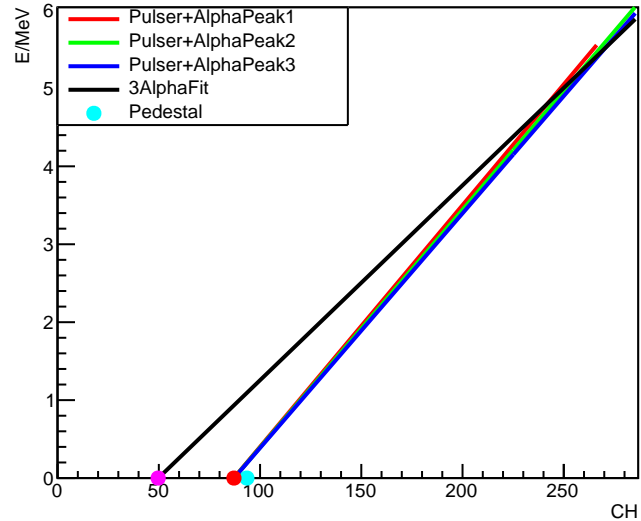
SSD1_L1SS_Switch_CH07



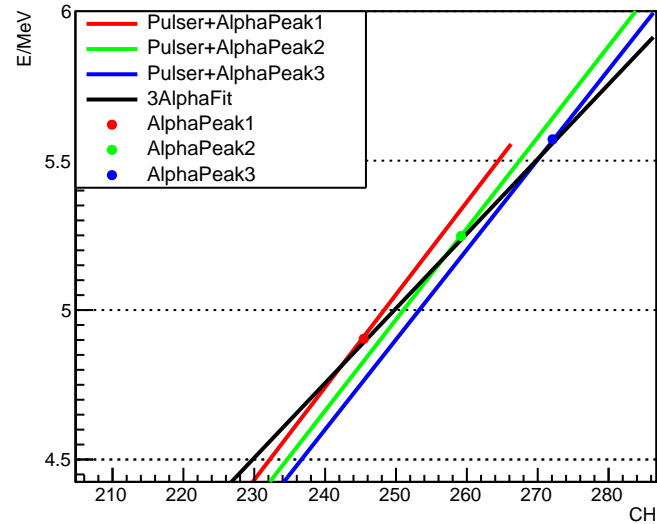
SSD1_L1SS_Switch_CH08



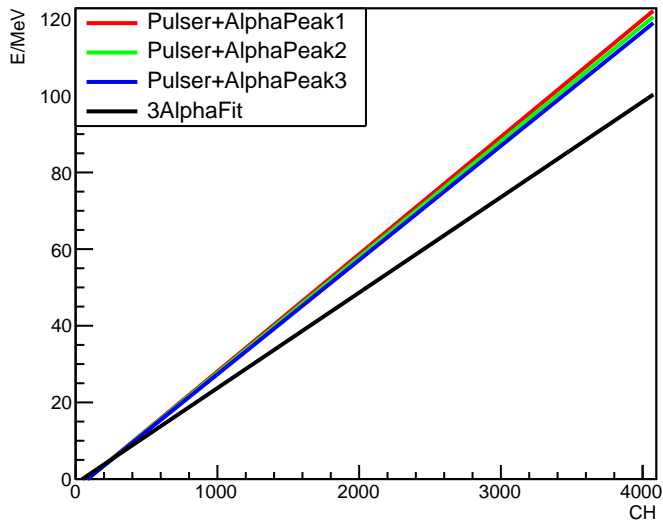
SSD1_L1SS_Switch_CH08



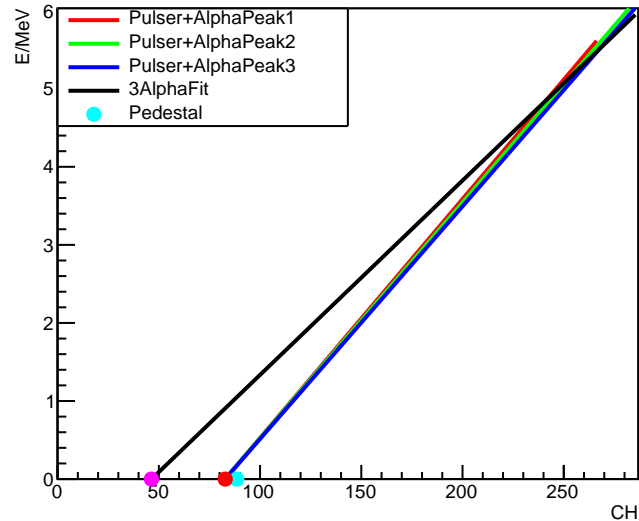
SSD1_L1SS_Switch_CH08



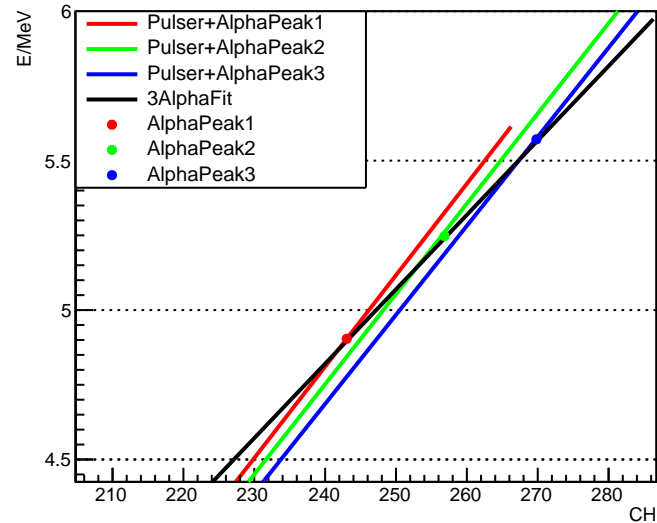
SSD1_L1SS_Switch_CH09



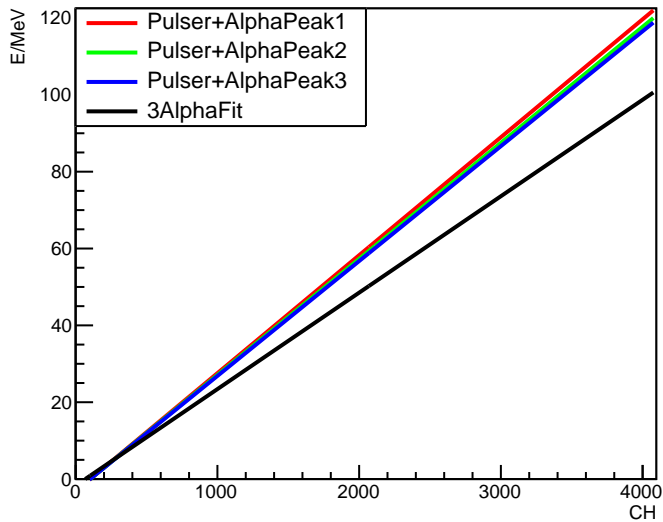
SSD1_L1SS_Switch_CH09



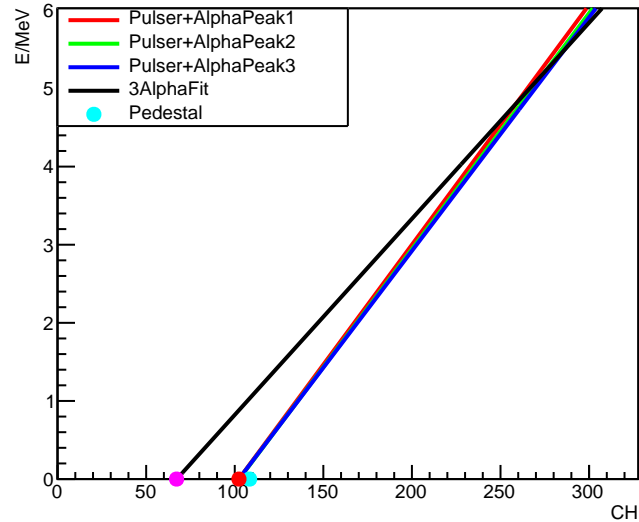
SSD1_L1SS_Switch_CH09



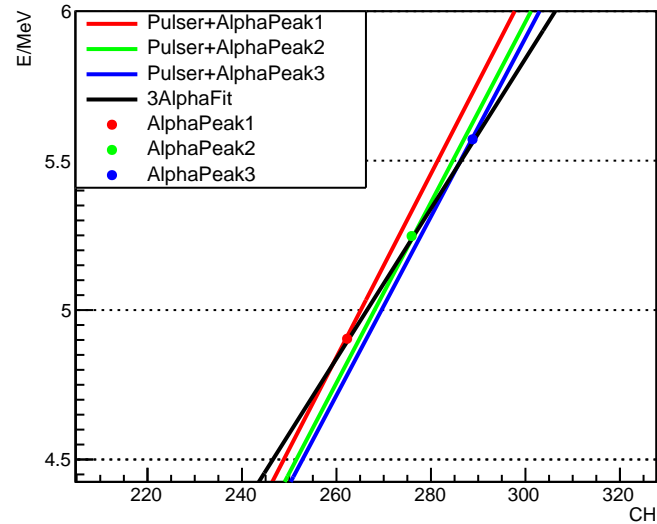
SSD1_L1SS_Switch_CH10



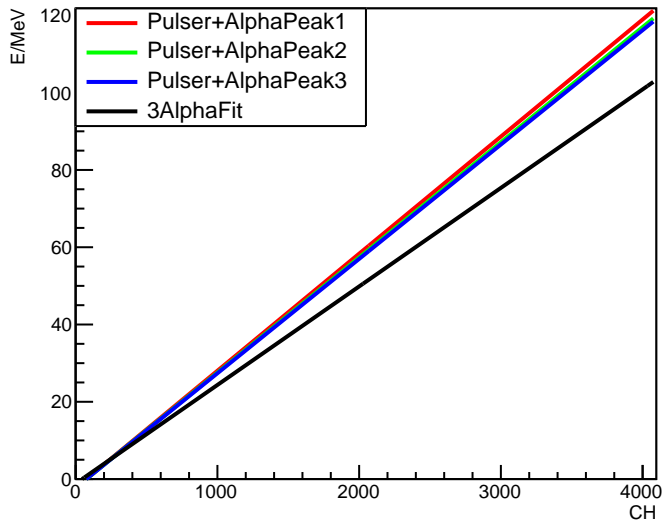
SSD1_L1SS_Switch_CH10



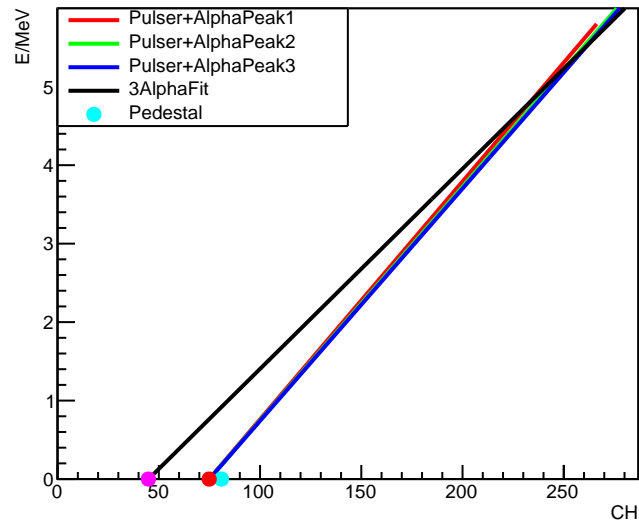
SSD1_L1SS_Switch_CH10



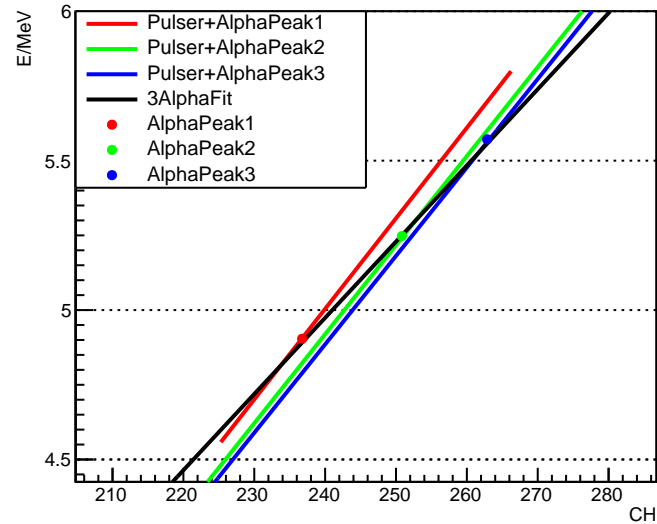
SSD1_L1SS_Switch_CH11



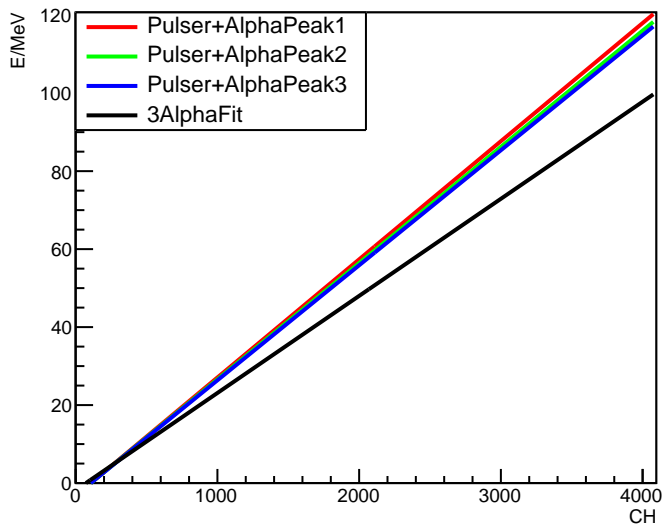
SSD1_L1SS_Switch_CH11



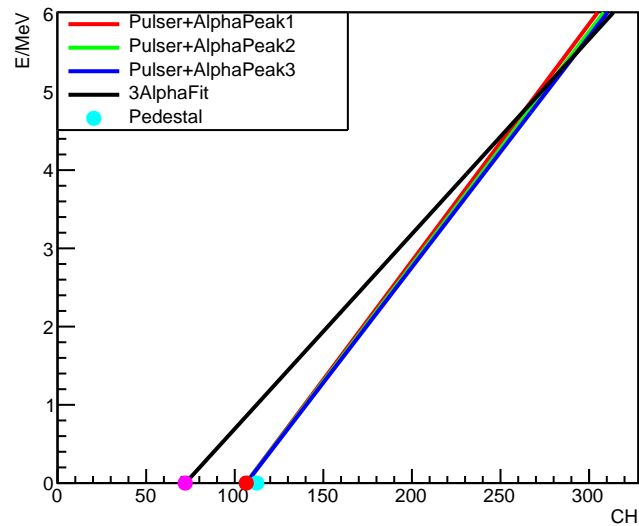
SSD1_L1SS_Switch_CH11



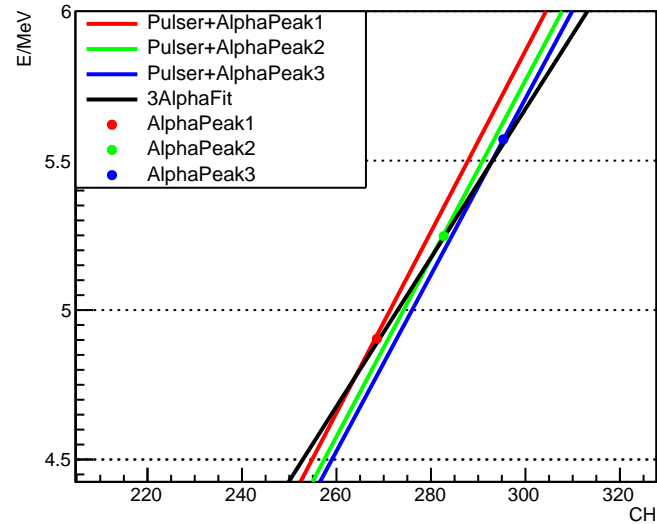
SSD1_L1SS_Switch_CH12



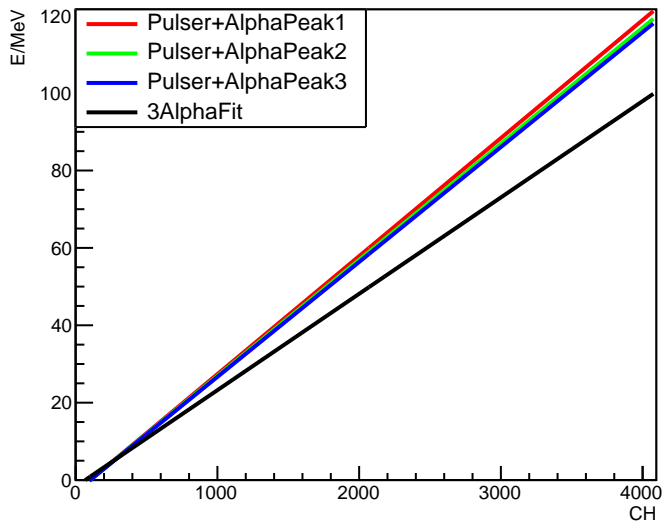
SSD1_L1SS_Switch_CH12



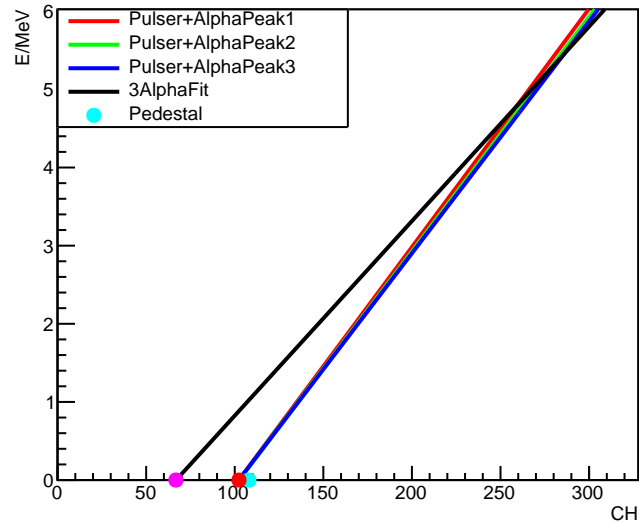
SSD1_L1SS_Switch_CH12



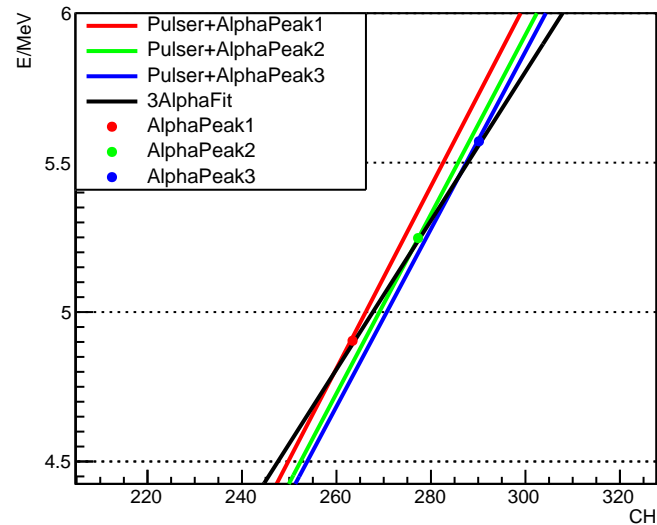
SSD1_L1SS_Switch_CH13



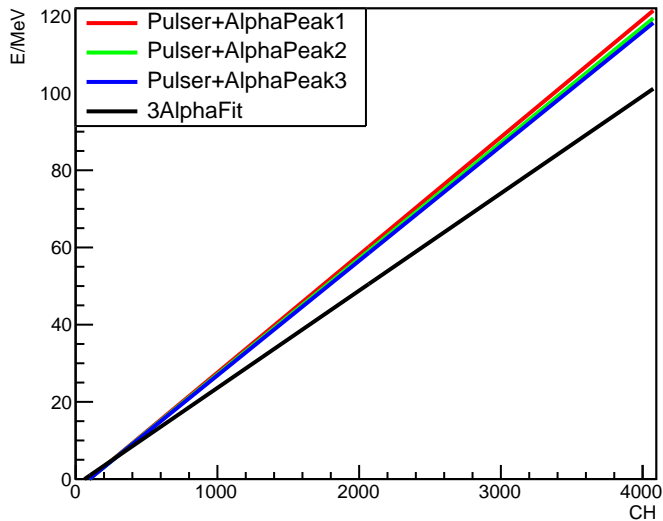
SSD1_L1SS_Switch_CH13



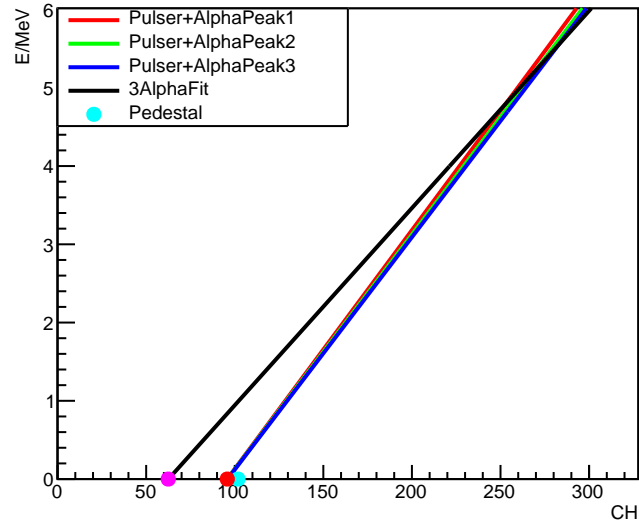
SSD1_L1SS_Switch_CH13



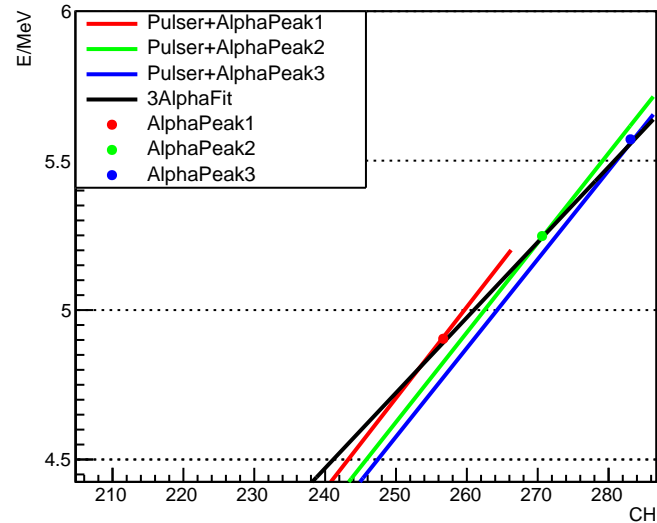
SSD1_L1SS_Switch_CH14



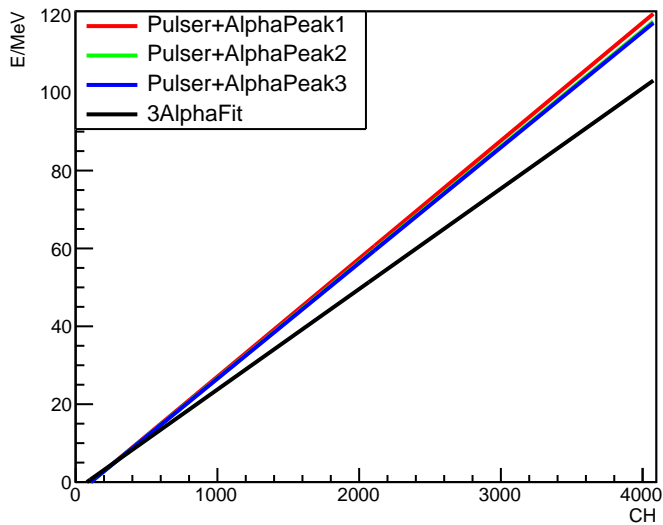
SSD1_L1SS_Switch_CH14



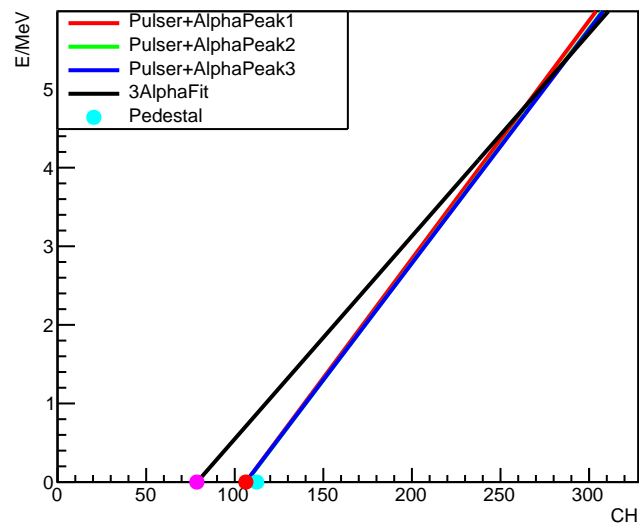
SSD1_L1SS_Switch_CH14



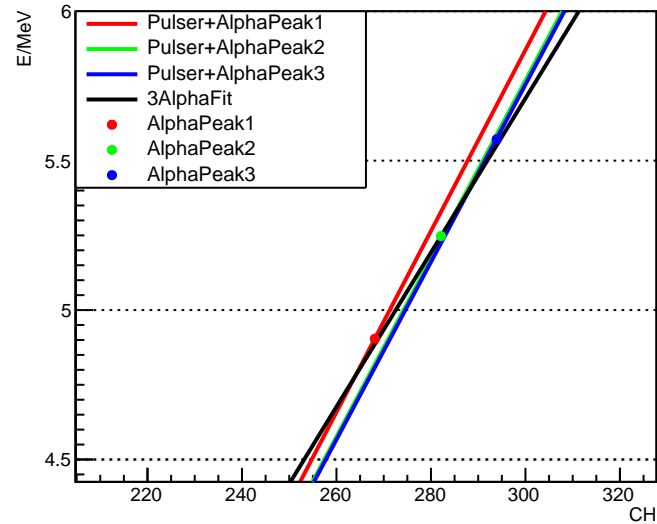
SSD1_L1SS_Switch_CH15



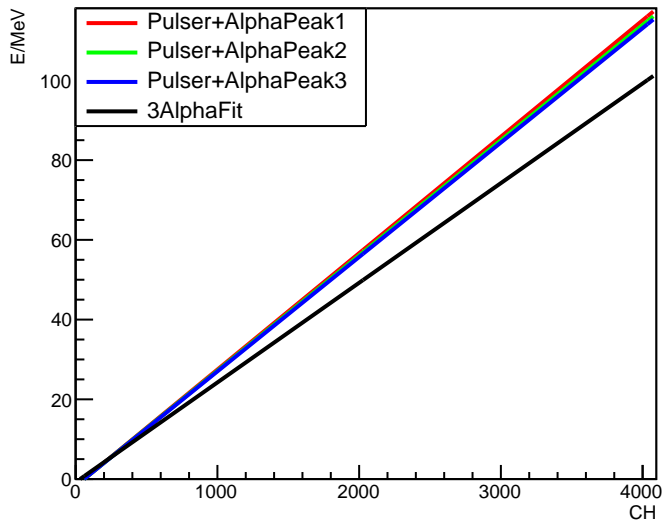
SSD1_L1SS_Switch_CH15



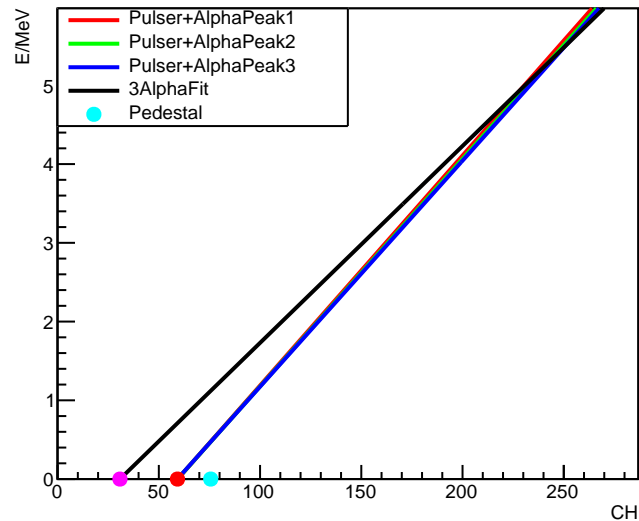
SSD1_L1SS_Switch_CH15



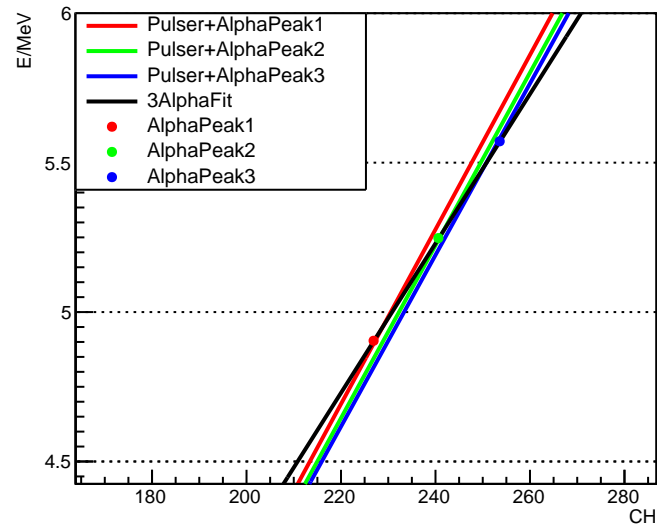
SSD2_L1SS_Switch_CH00



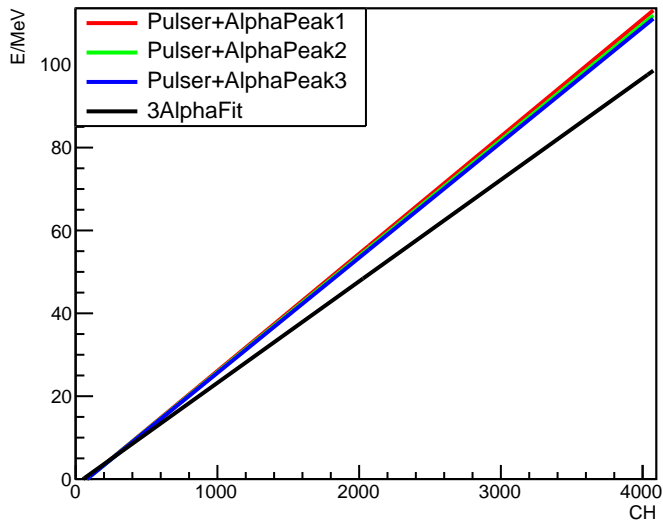
SSD2_L1SS_Switch_CH00



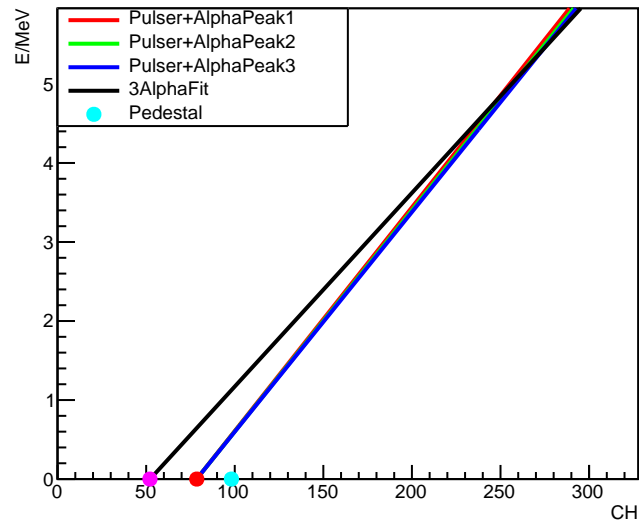
SSD2_L1SS_Switch_CH00



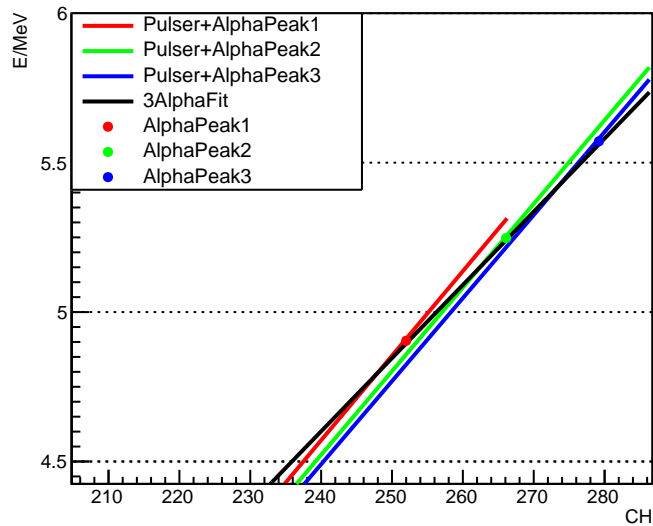
SSD2_L1SS_Switch_CH01



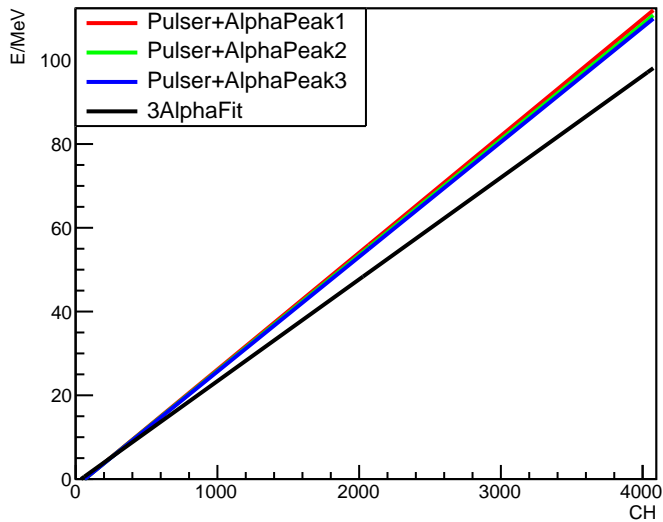
SSD2_L1SS_Switch_CH01



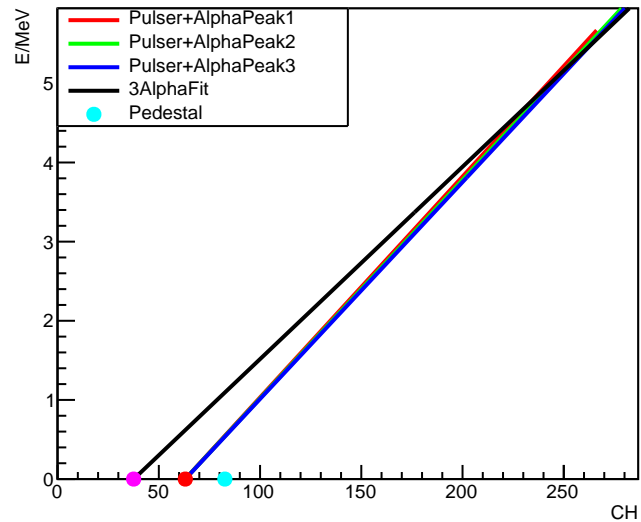
SSD2_L1SS_Switch_CH01



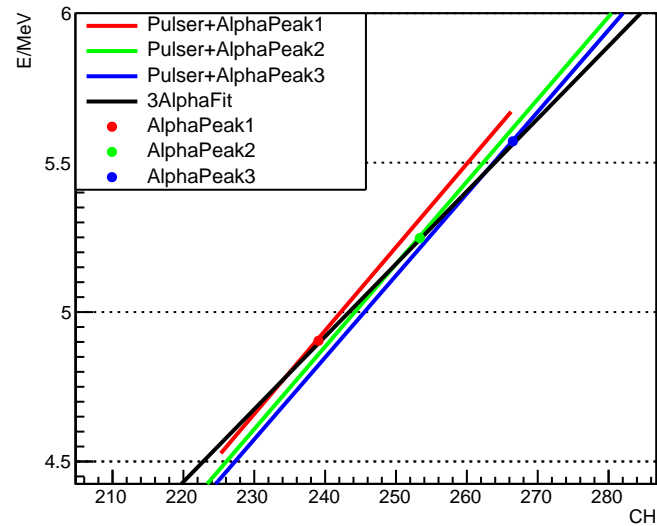
SSD2_L1SS_Switch_CH02



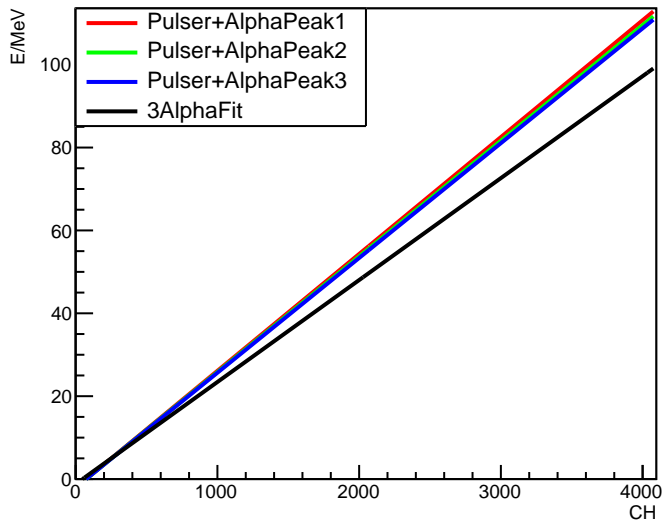
SSD2_L1SS_Switch_CH02



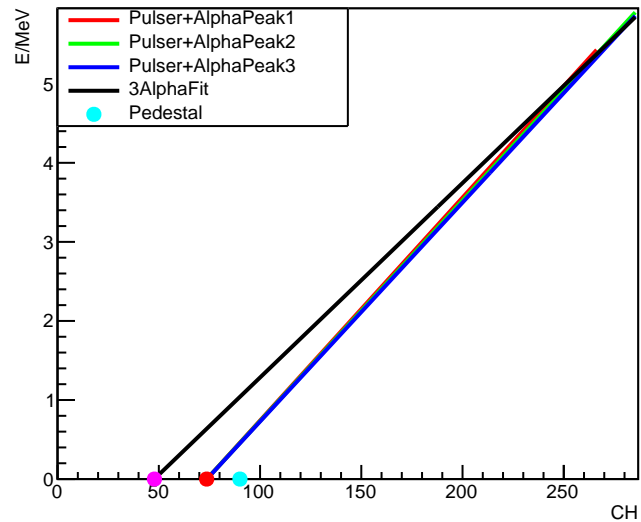
SSD2_L1SS_Switch_CH02



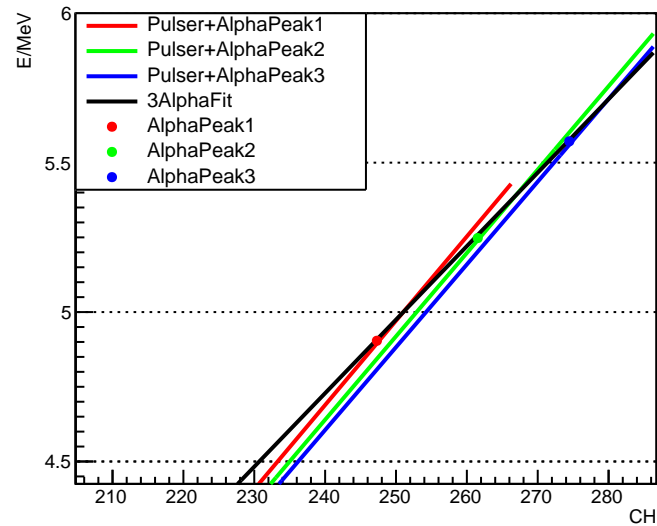
SSD2_L1SS_Switch_CH03



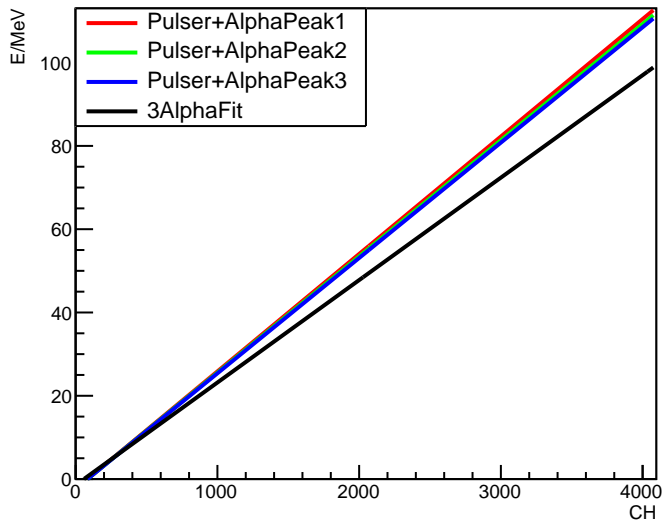
SSD2_L1SS_Switch_CH03



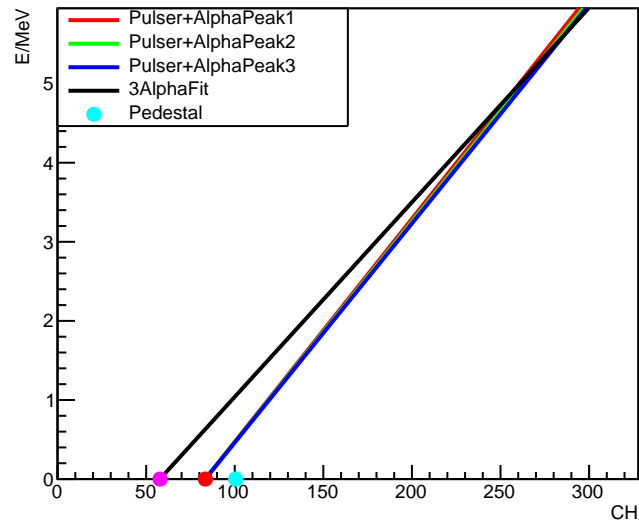
SSD2_L1SS_Switch_CH03



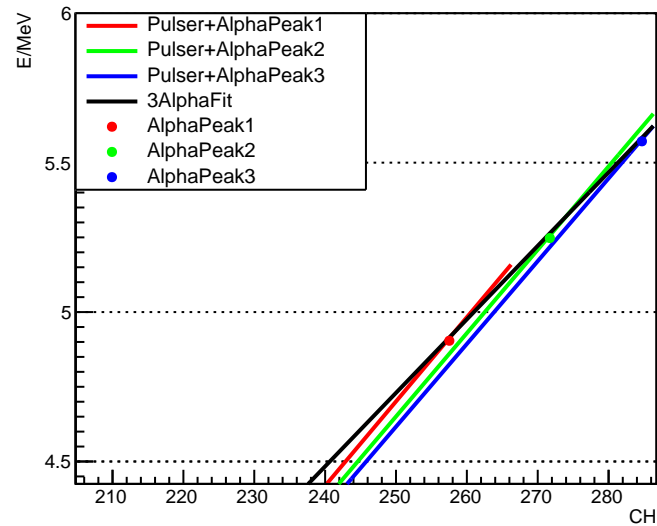
SSD2_L1SS_Switch_CH04



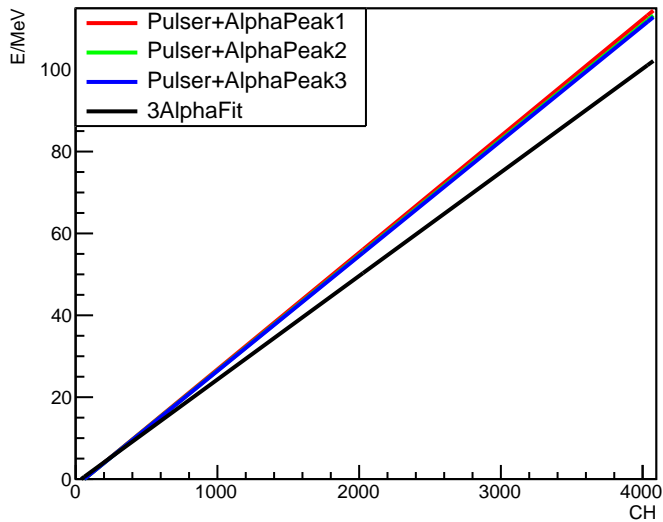
SSD2_L1SS_Switch_CH04



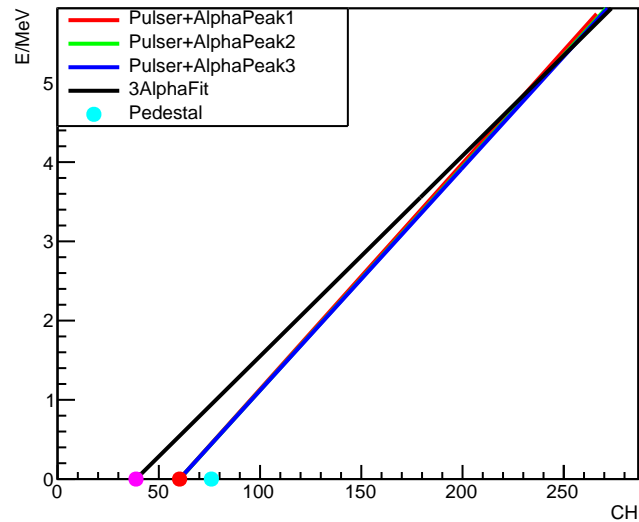
SSD2_L1SS_Switch_CH04



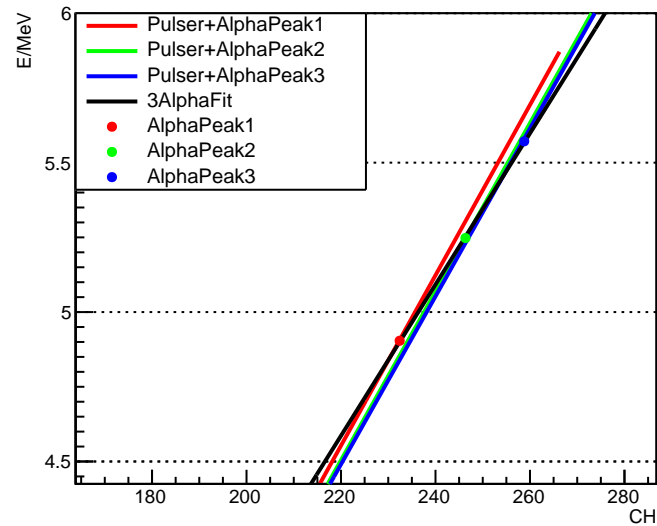
SSD2_L1SS_Switch_CH05



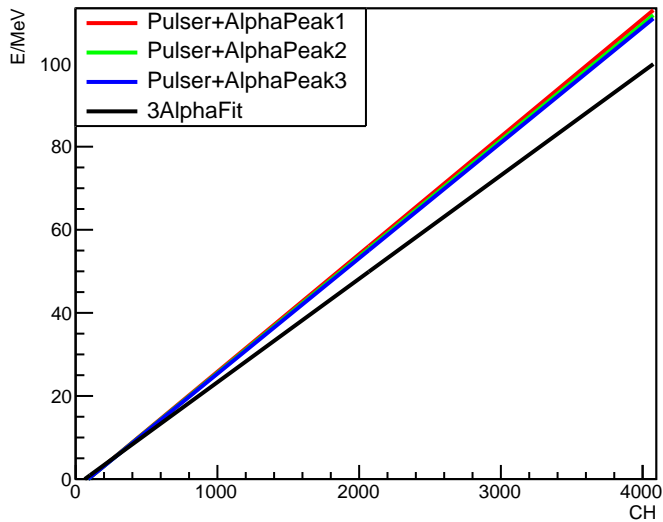
SSD2_L1SS_Switch_CH05



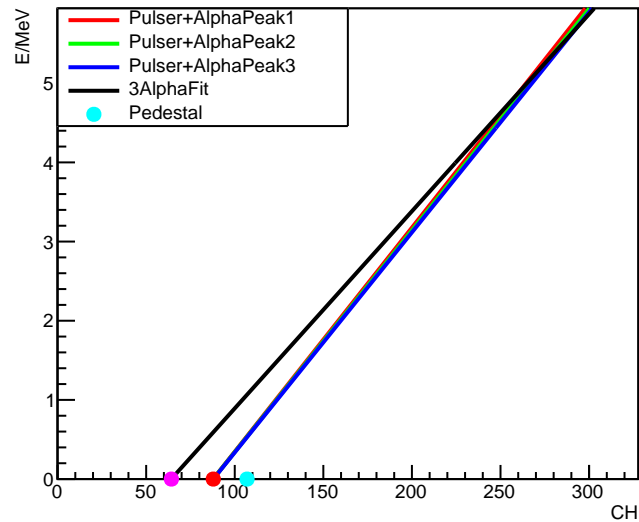
SSD2_L1SS_Switch_CH05



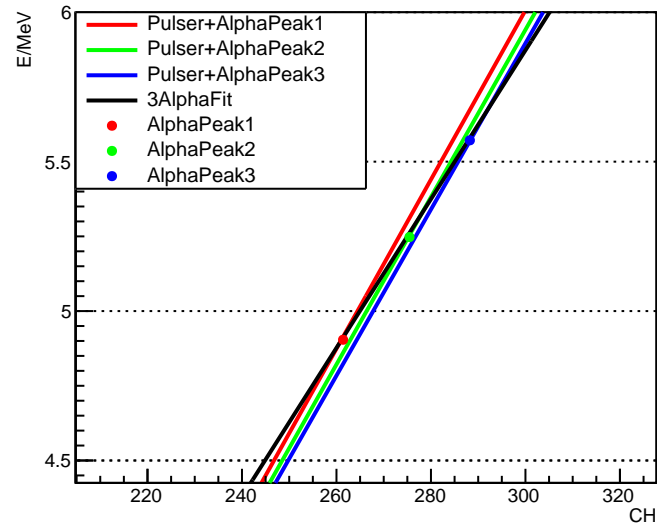
SSD2_L1SS_Switch_CH06



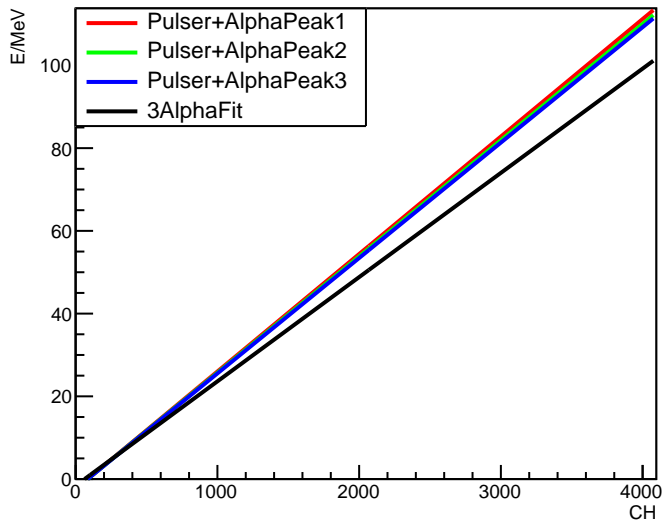
SSD2_L1SS_Switch_CH06



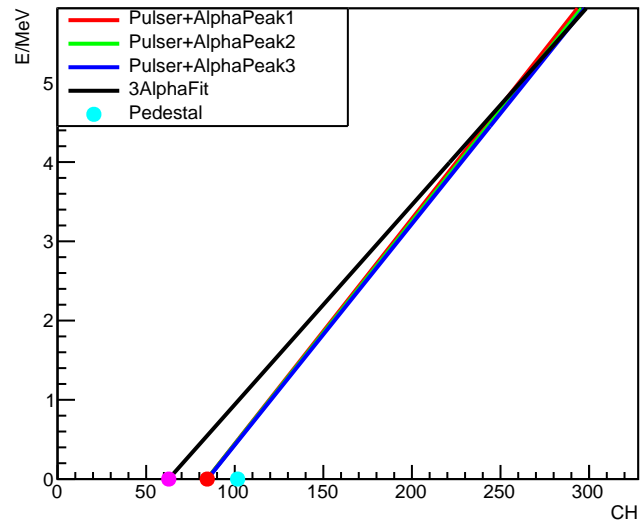
SSD2_L1SS_Switch_CH06



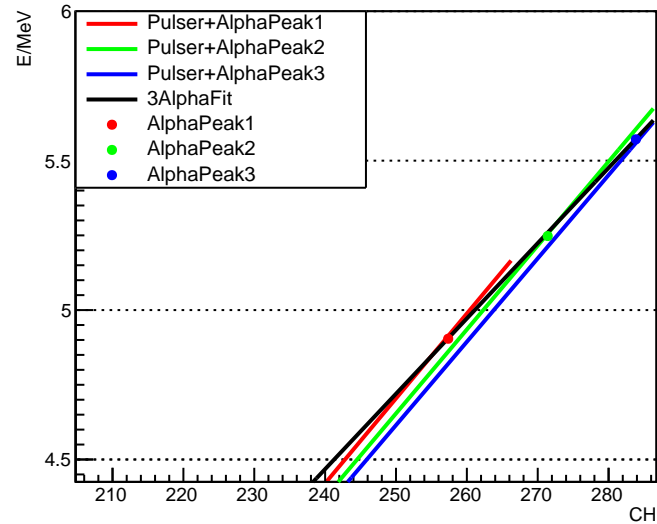
SSD2_L1SS_Switch_CH07



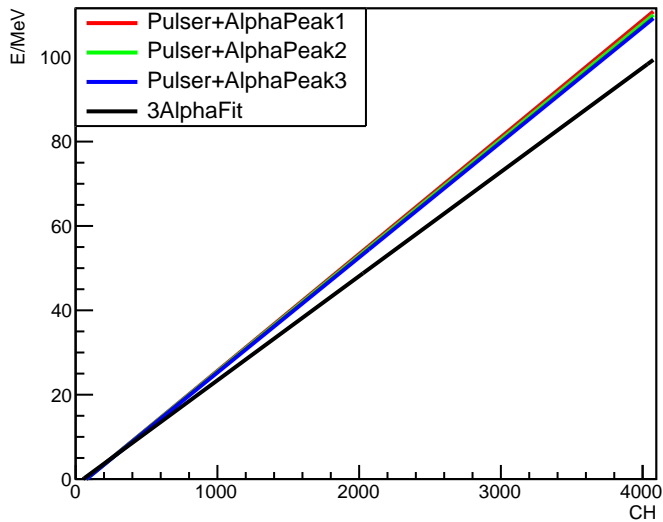
SSD2_L1SS_Switch_CH07



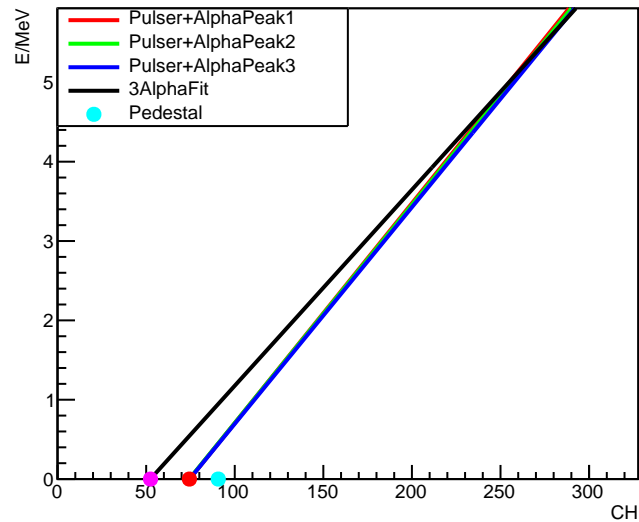
SSD2_L1SS_Switch_CH07



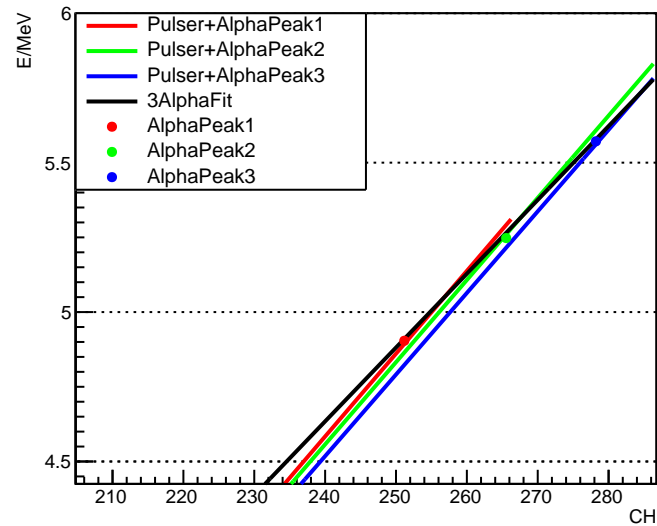
SSD2_L1SS_Switch_CH08



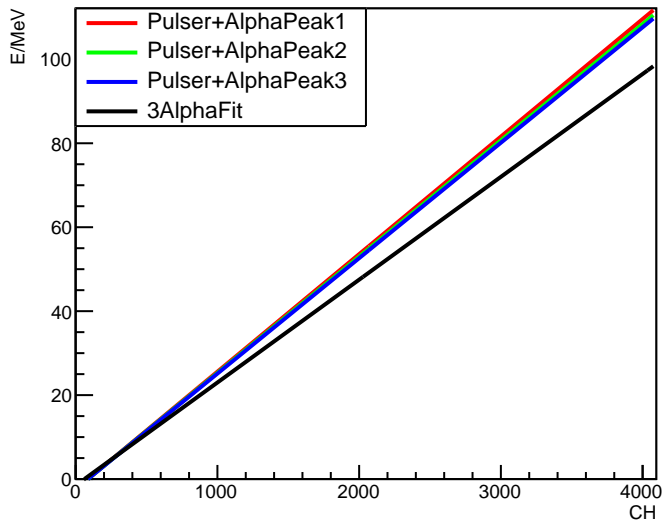
SSD2_L1SS_Switch_CH08



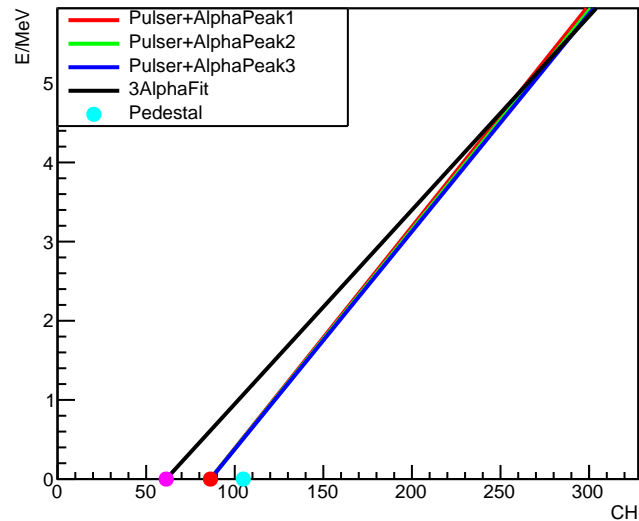
SSD2_L1SS_Switch_CH08



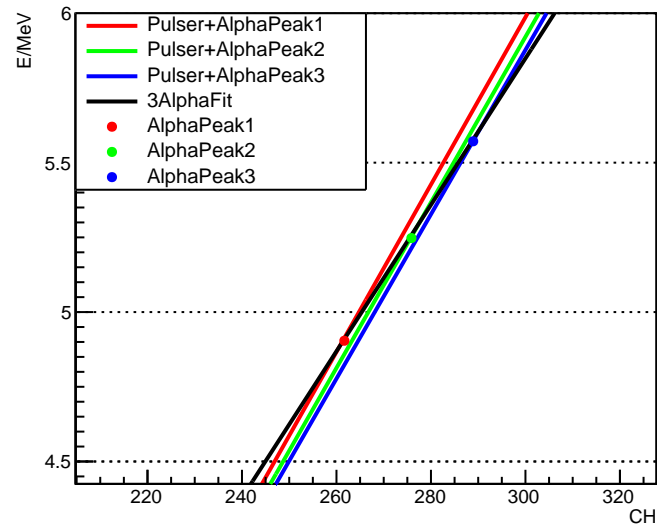
SSD2_L1SS_Switch_CH09



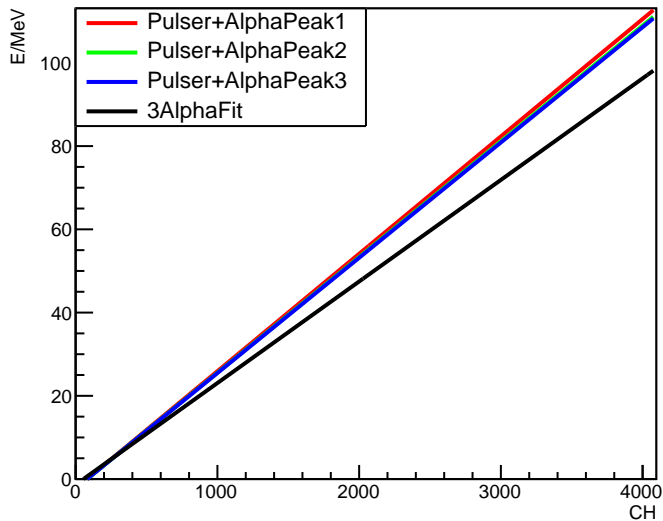
SSD2_L1SS_Switch_CH09



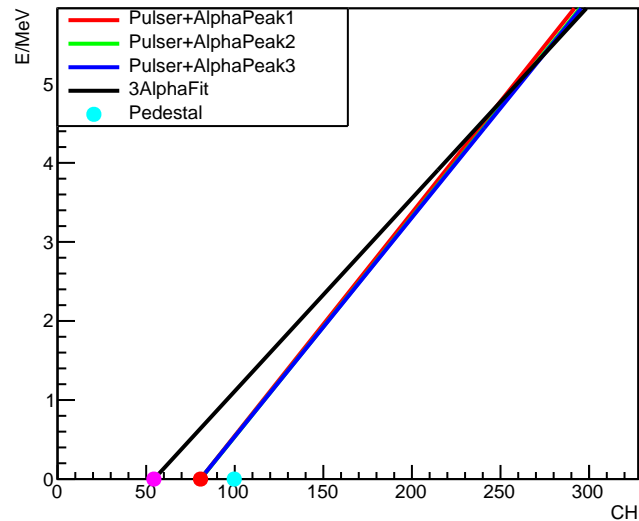
SSD2_L1SS_Switch_CH09



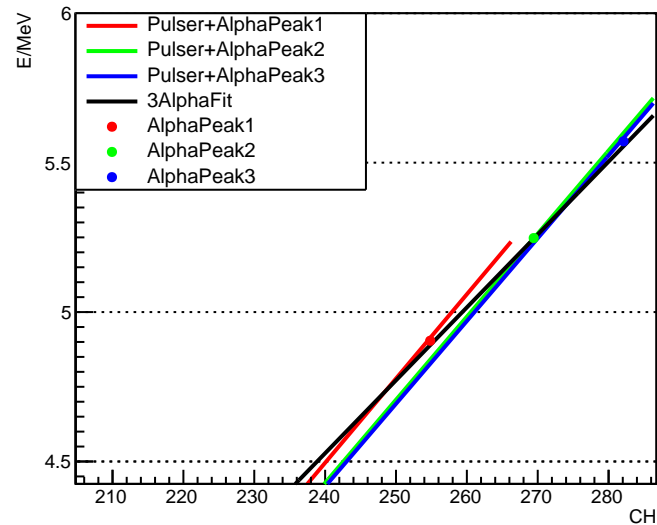
SSD2_L1SS_Switch_CH10



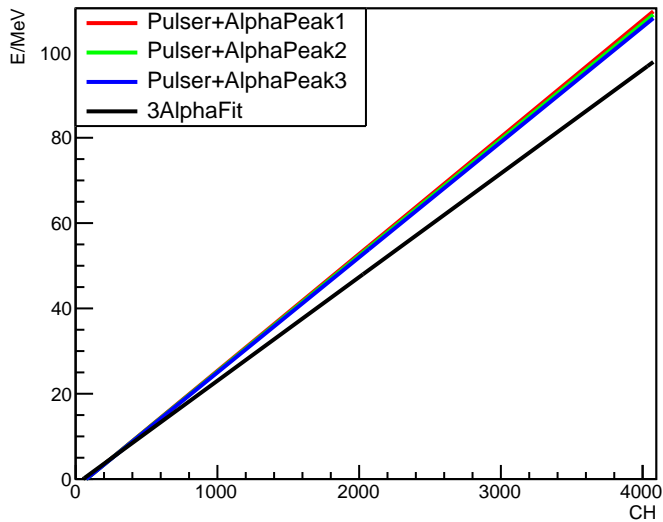
SSD2_L1SS_Switch_CH10



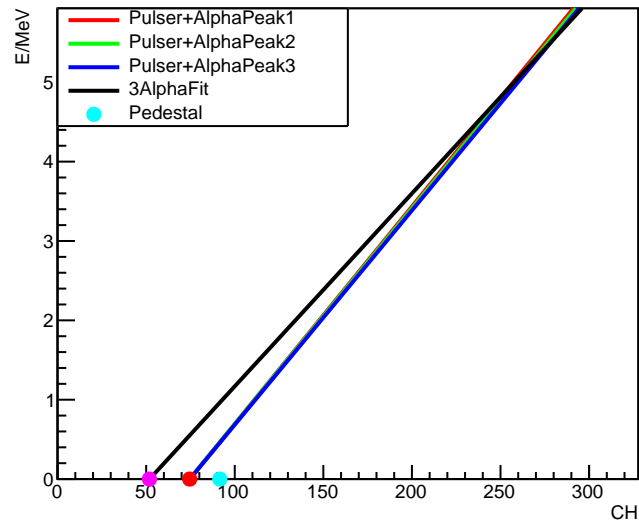
SSD2_L1SS_Switch_CH10



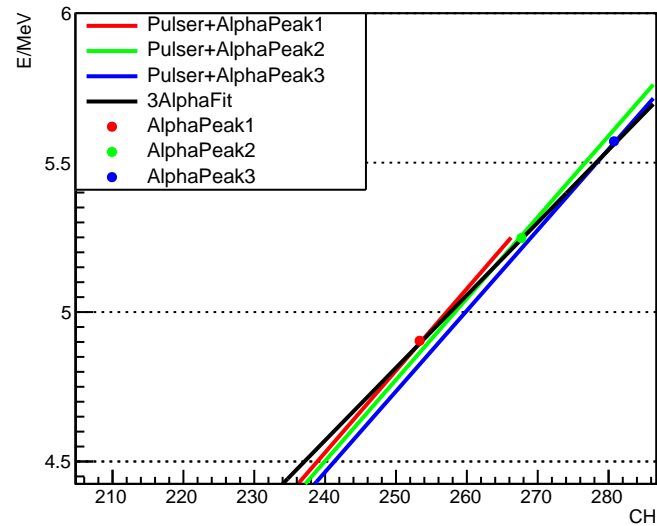
SSD2_L1SS_Switch_CH11



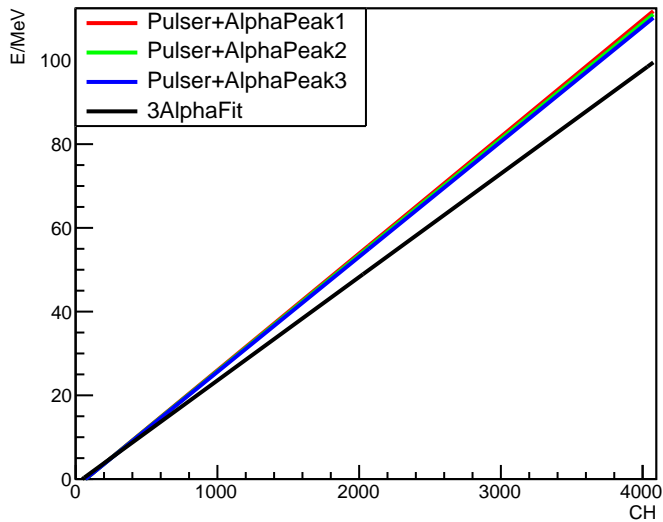
SSD2_L1SS_Switch_CH11



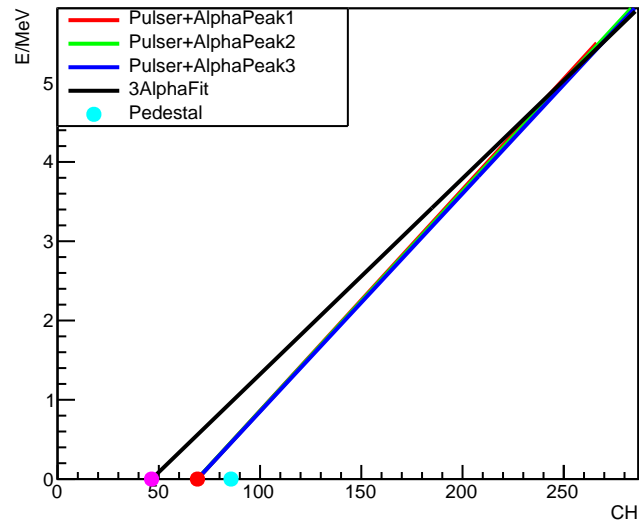
SSD2_L1SS_Switch_CH11



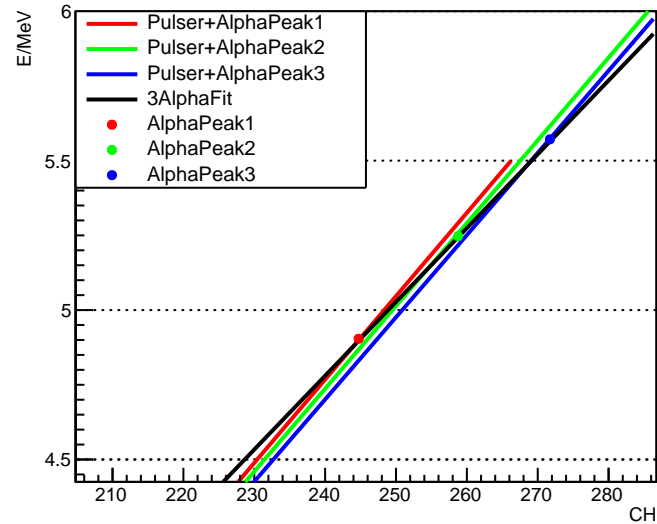
SSD2_L1SS_Switch_CH12



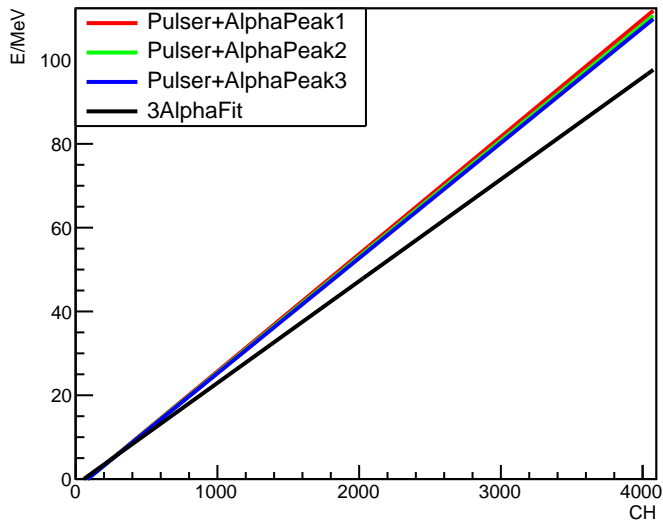
SSD2_L1SS_Switch_CH12



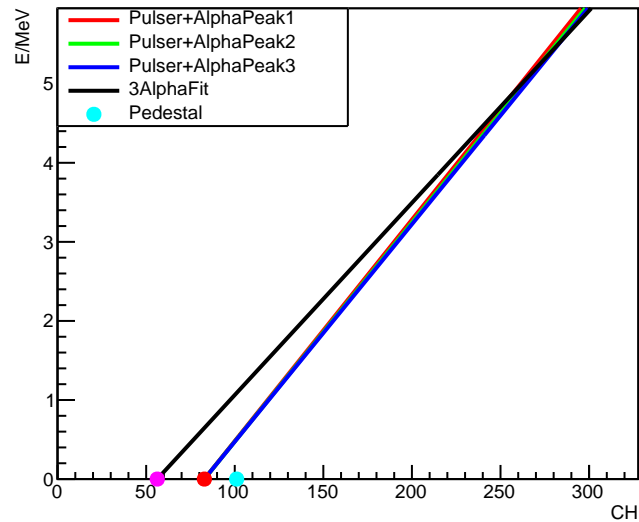
SSD2_L1SS_Switch_CH12



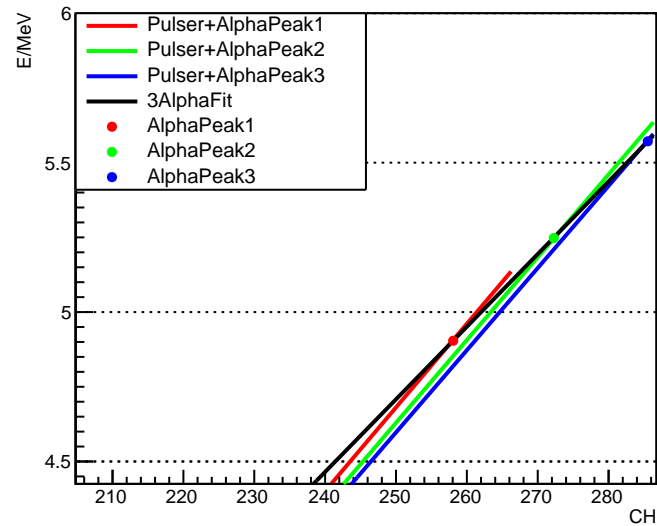
SSD2_L1SS_Switch_CH13



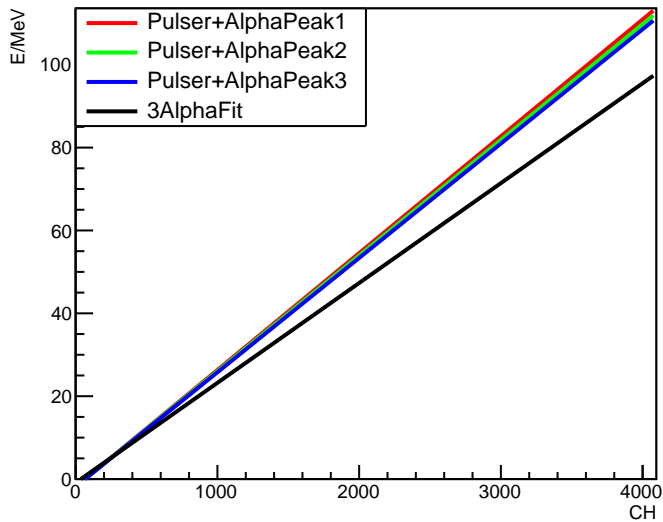
SSD2_L1SS_Switch_CH13



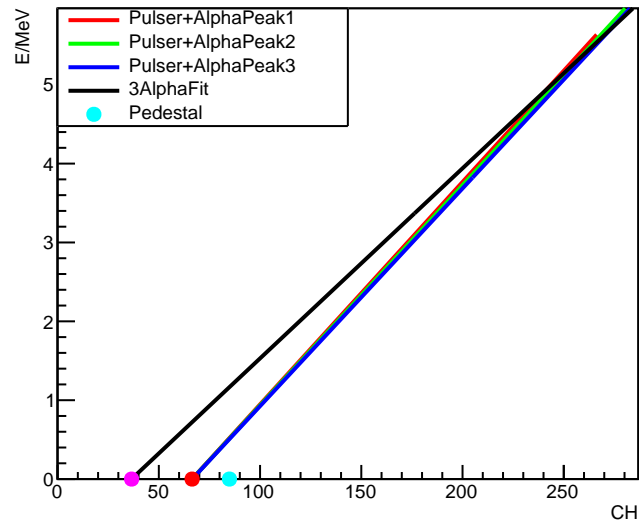
SSD2_L1SS_Switch_CH13



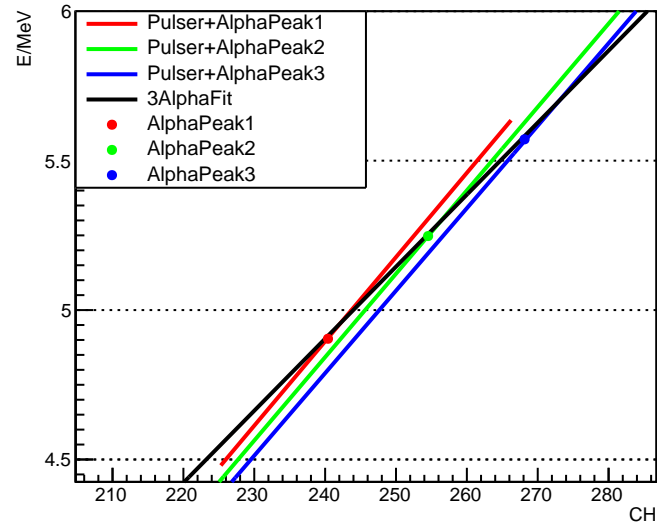
SSD2_L1SS_Switch_CH14



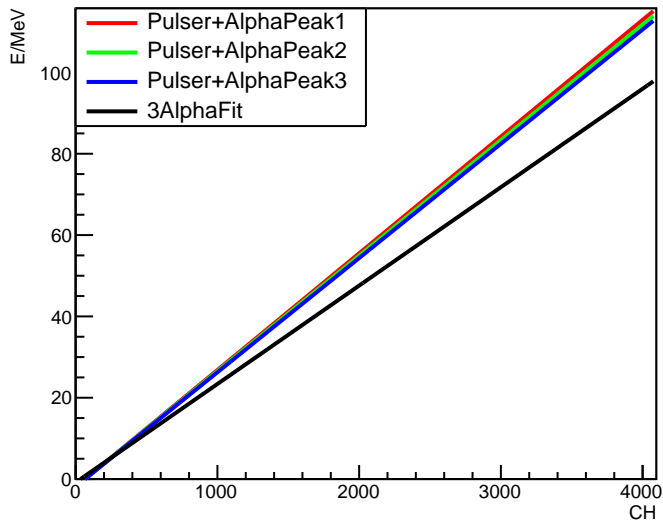
SSD2_L1SS_Switch_CH14



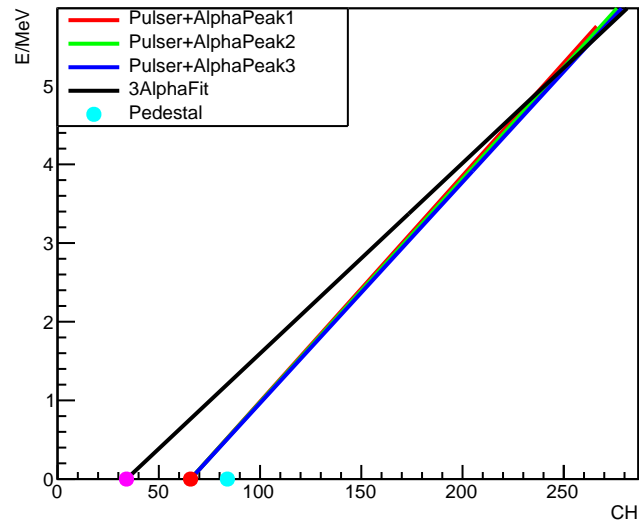
SSD2_L1SS_Switch_CH14



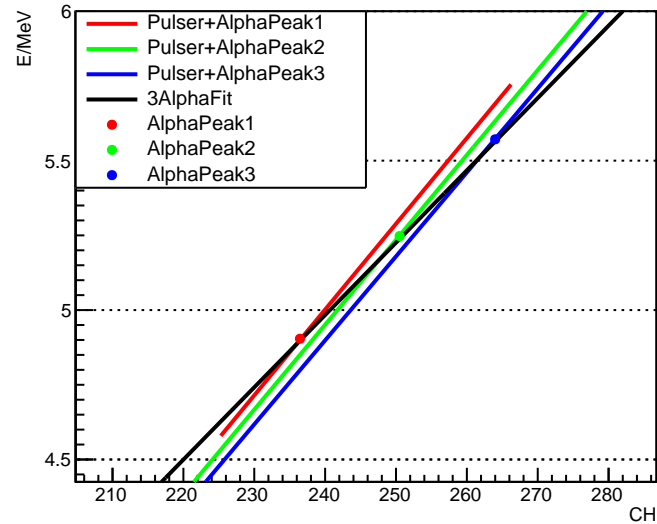
SSD2_L1SS_Switch_CH15



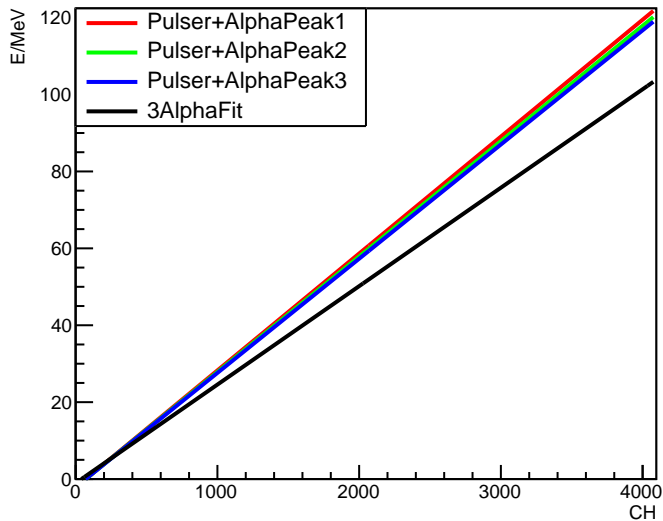
SSD2_L1SS_Switch_CH15



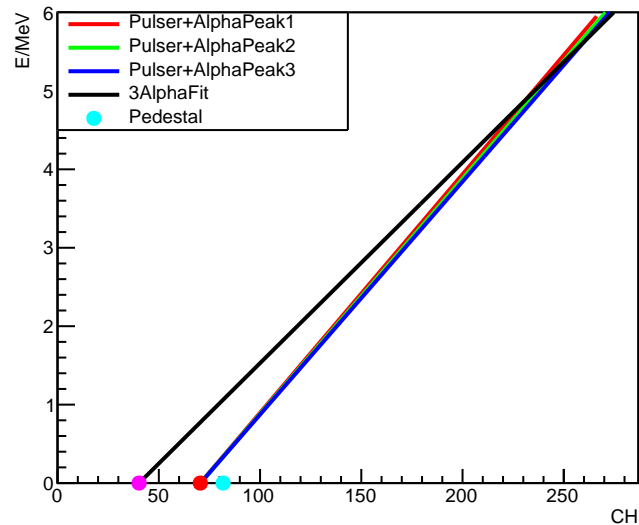
SSD2_L1SS_Switch_CH15



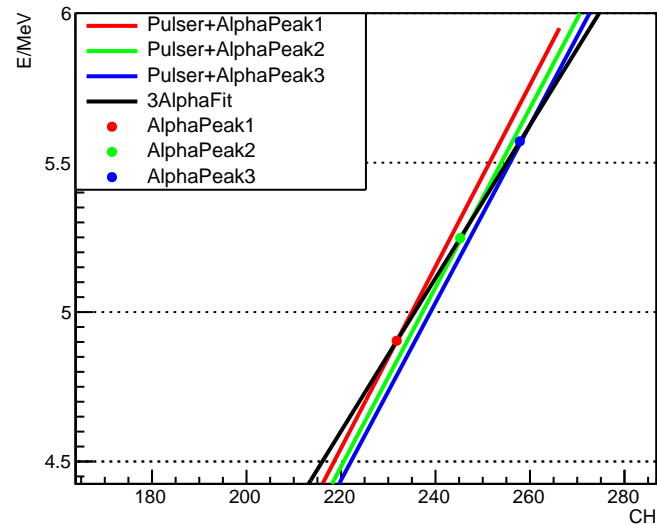
SSD3_L1SS_Switch_CH00



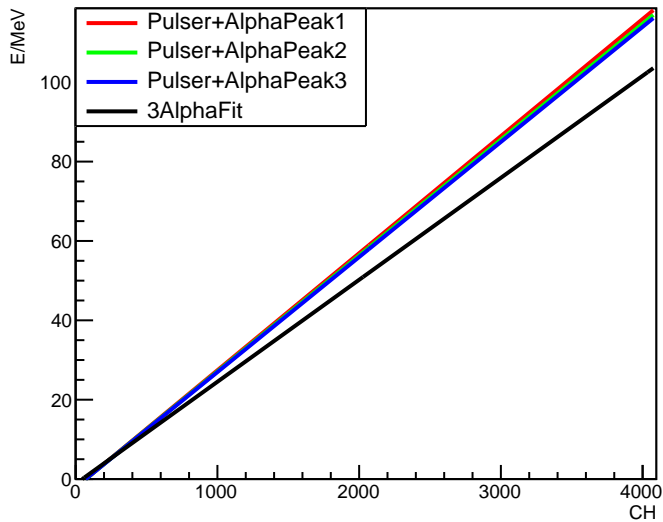
SSD3_L1SS_Switch_CH00



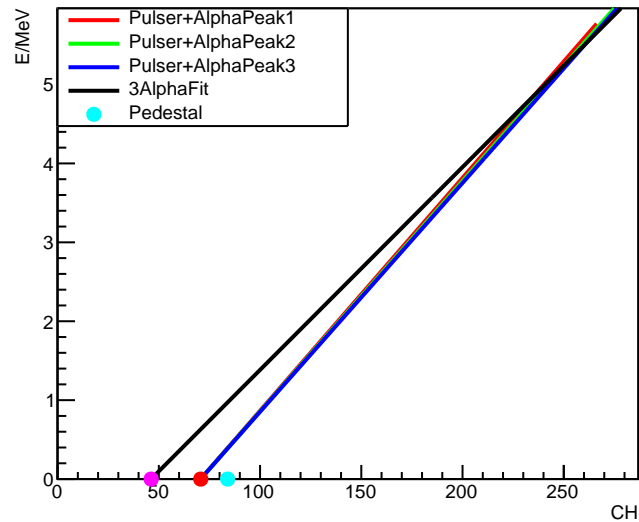
SSD3_L1SS_Switch_CH00



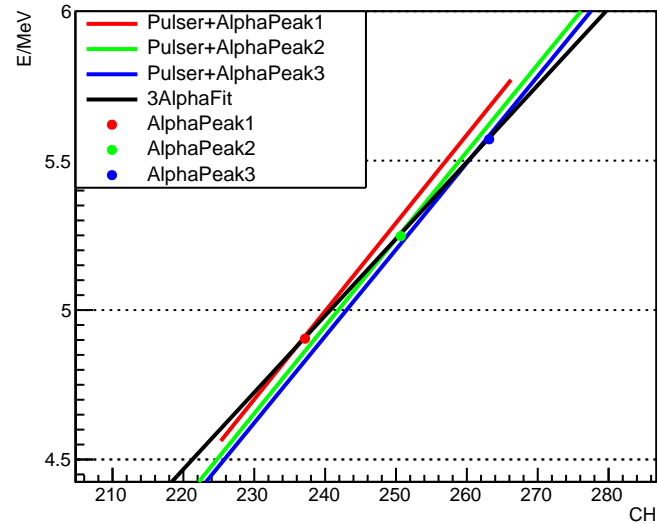
SSD3_L1SS_Switch_CH01



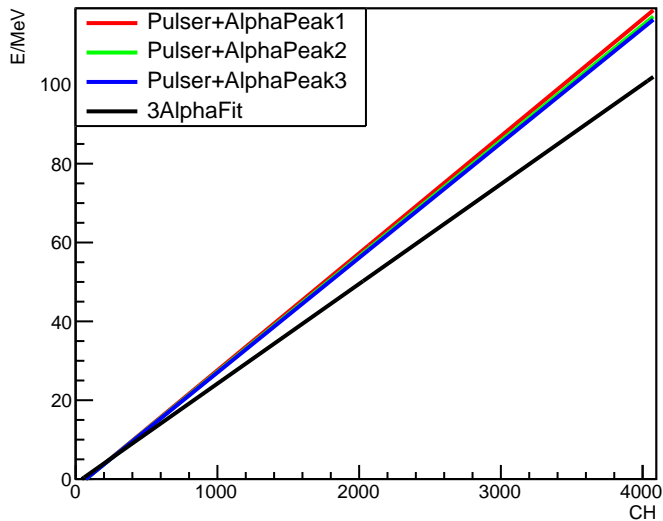
SSD3_L1SS_Switch_CH01



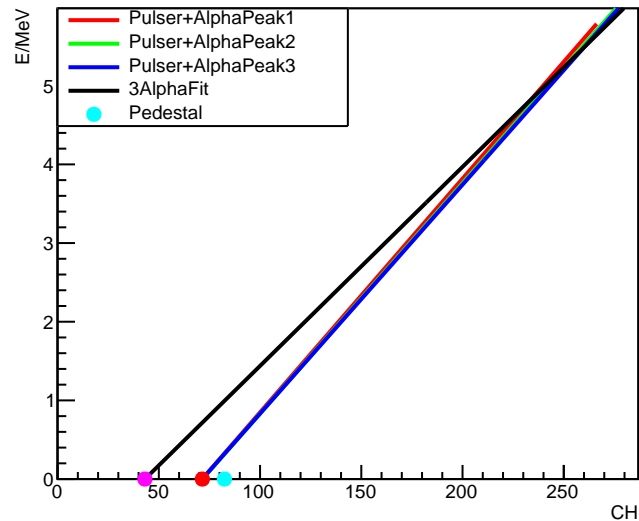
SSD3_L1SS_Switch_CH01



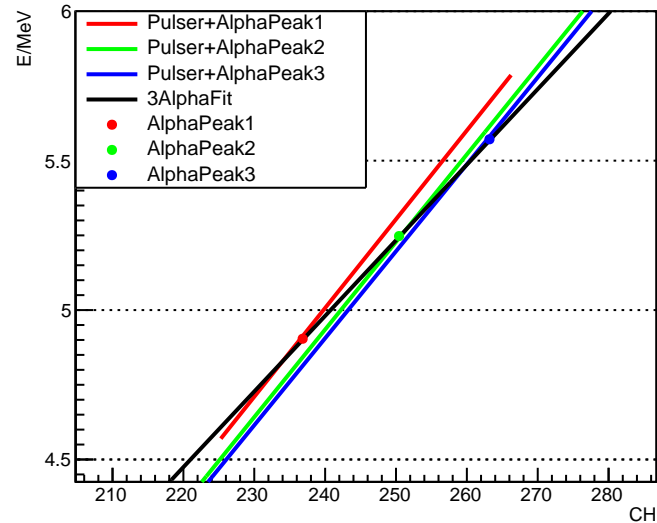
SSD3_L1SS_Switch_CH02



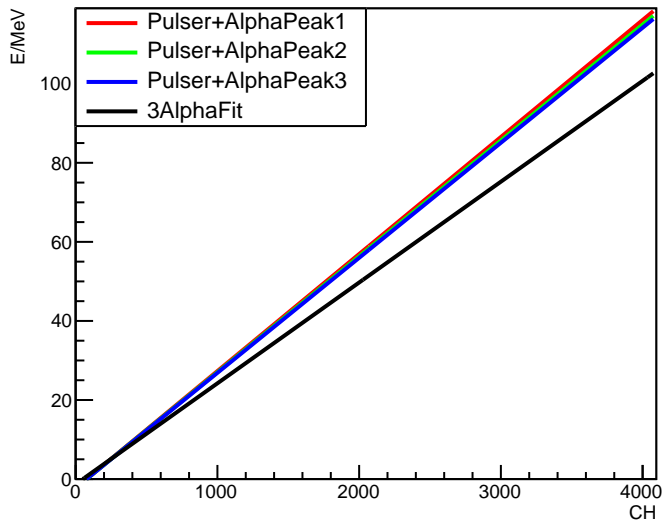
SSD3_L1SS_Switch_CH02



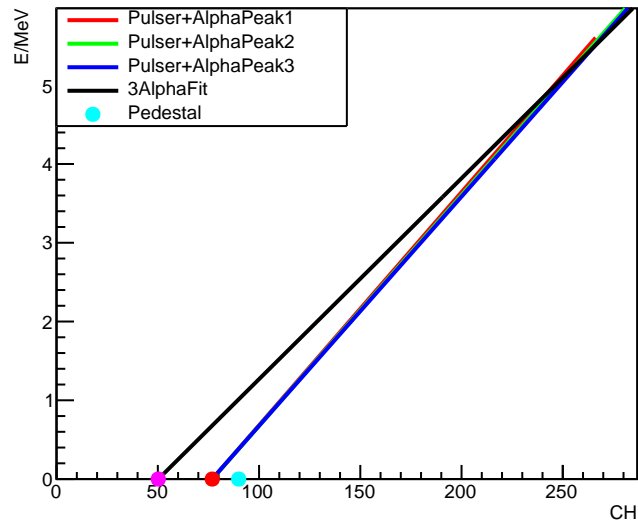
SSD3_L1SS_Switch_CH02



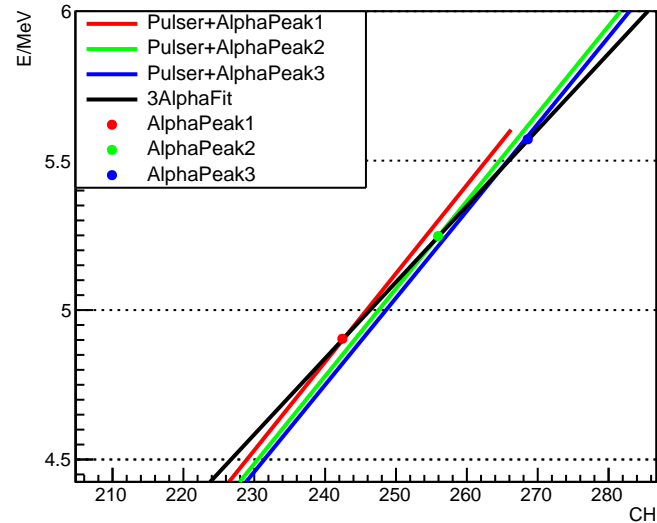
SSD3_L1SS_Switch_CH03



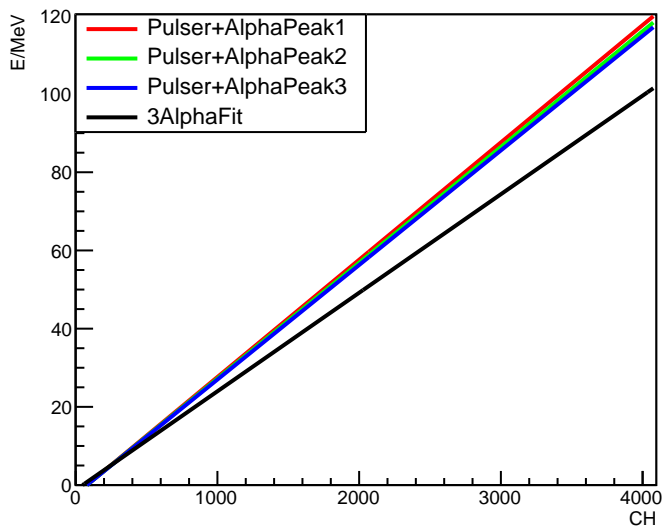
SSD3_L1SS_Switch_CH03



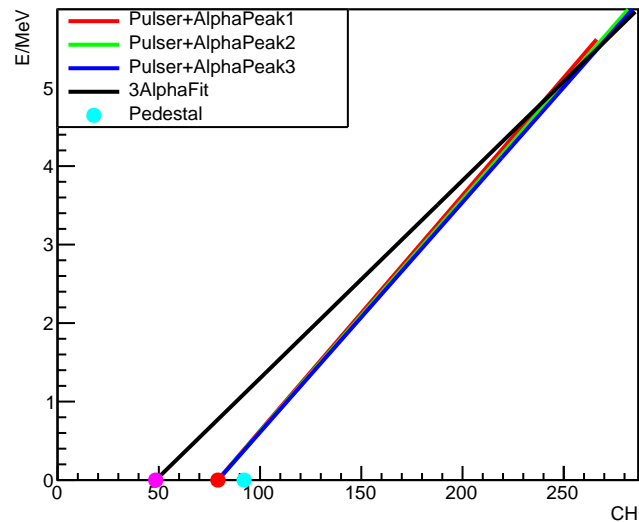
SSD3_L1SS_Switch_CH03



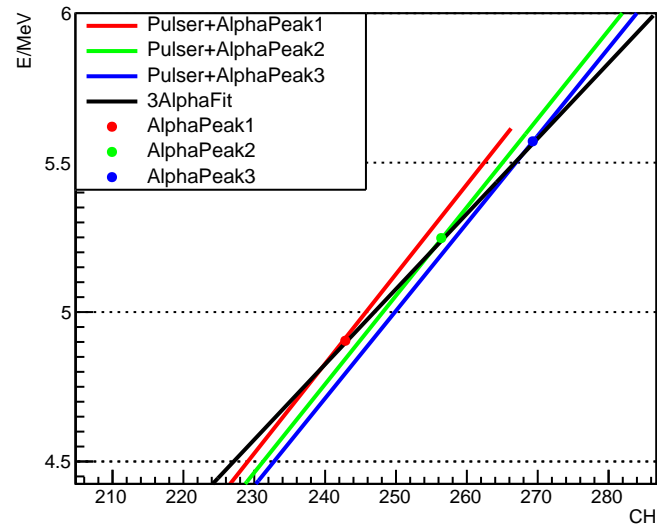
SSD3_L1SS_Switch_CH04



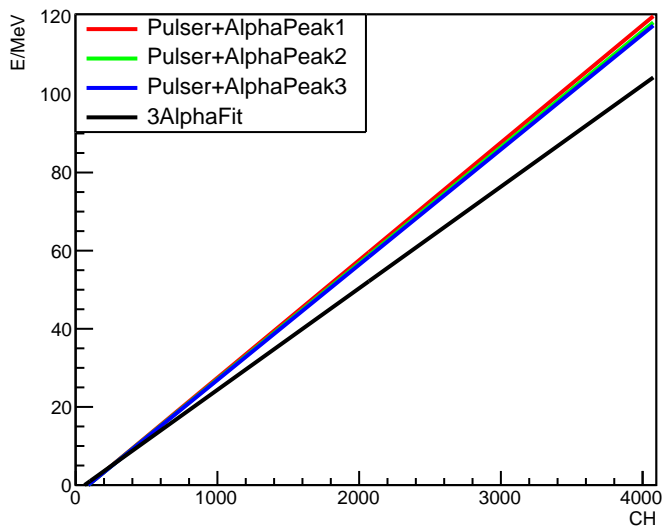
SSD3_L1SS_Switch_CH04



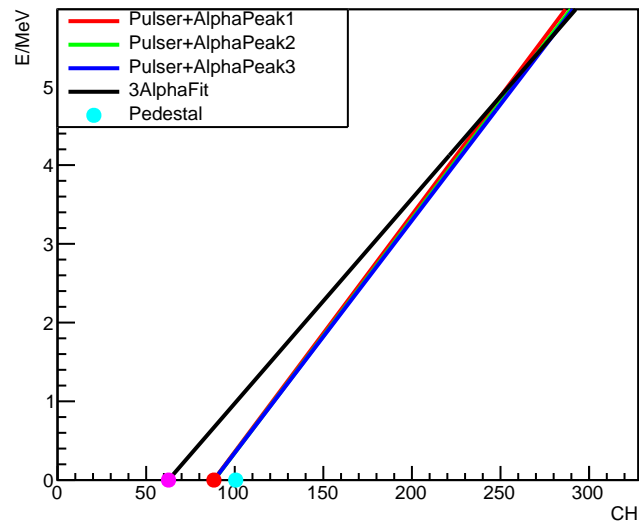
SSD3_L1SS_Switch_CH04



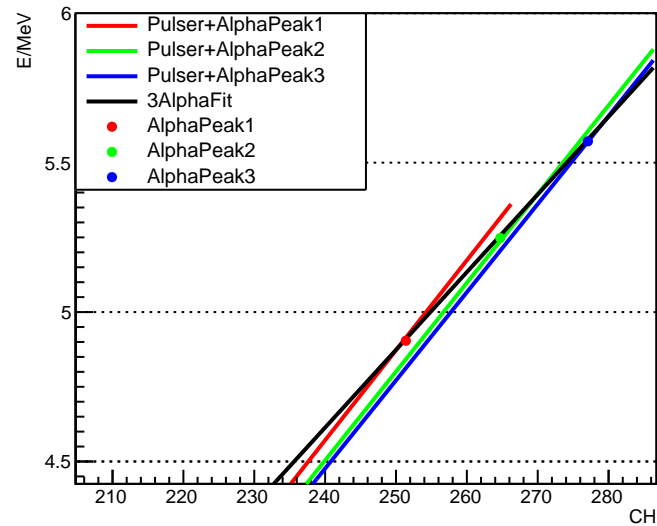
SSD3_L1SS_Switch_CH05



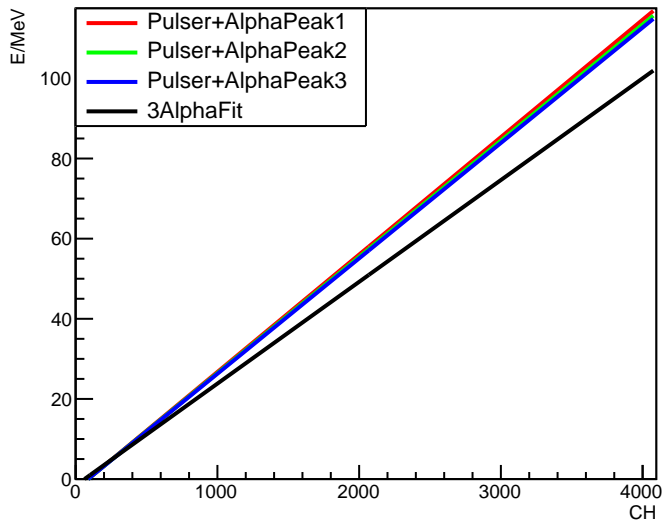
SSD3_L1SS_Switch_CH05



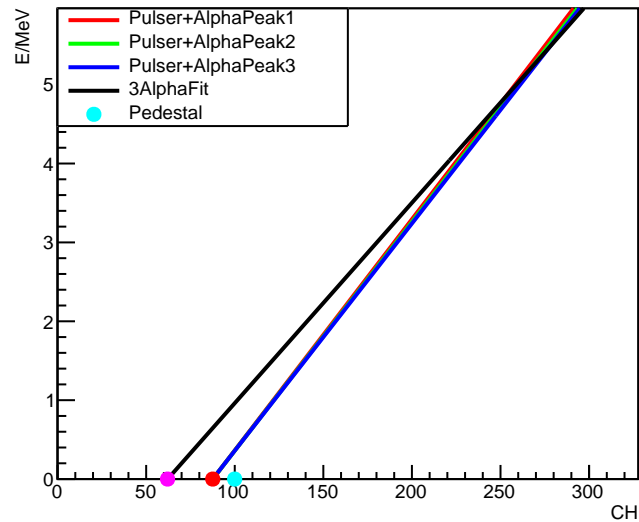
SSD3_L1SS_Switch_CH05



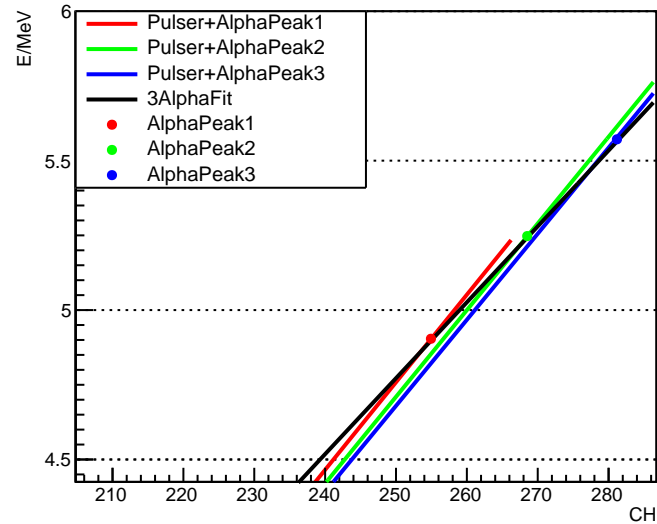
SSD3_L1SS_Switch_CH06



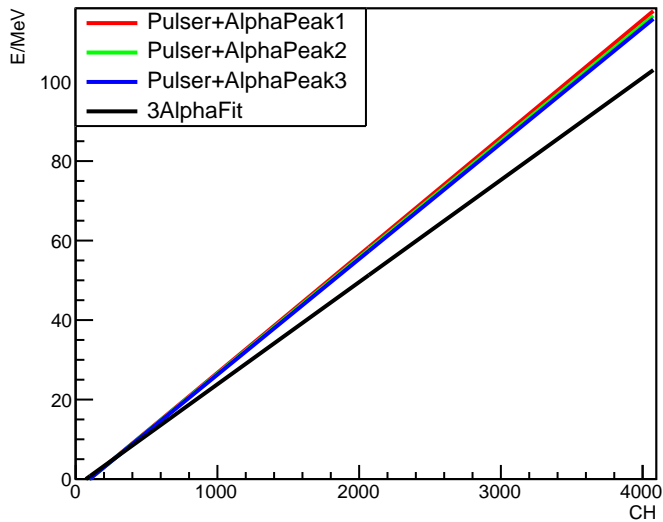
SSD3_L1SS_Switch_CH06



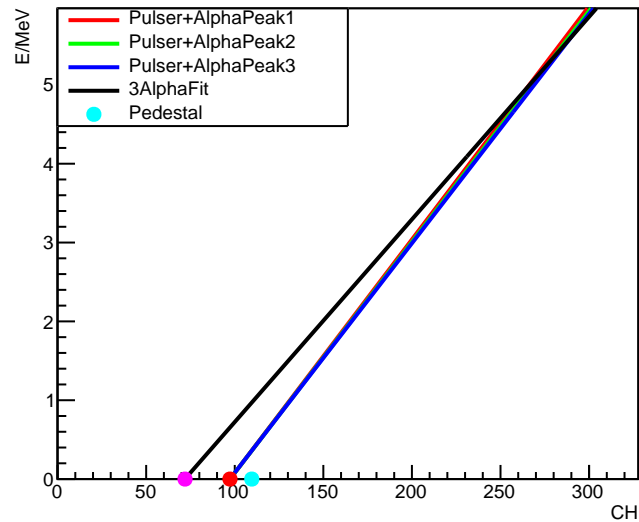
SSD3_L1SS_Switch_CH06



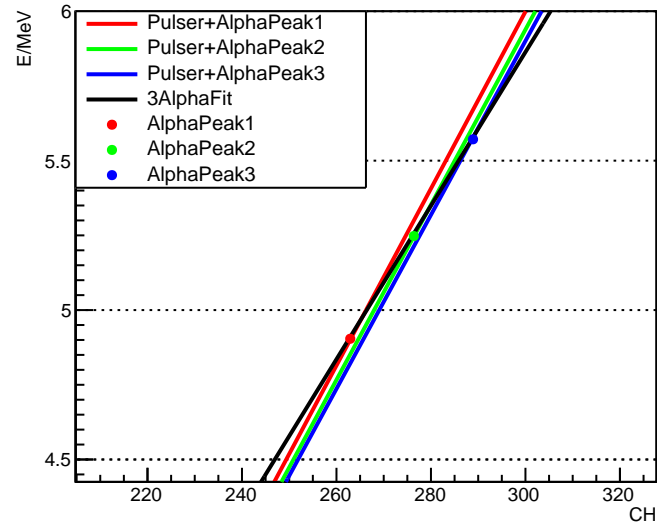
SSD3_L1SS_Switch_CH07



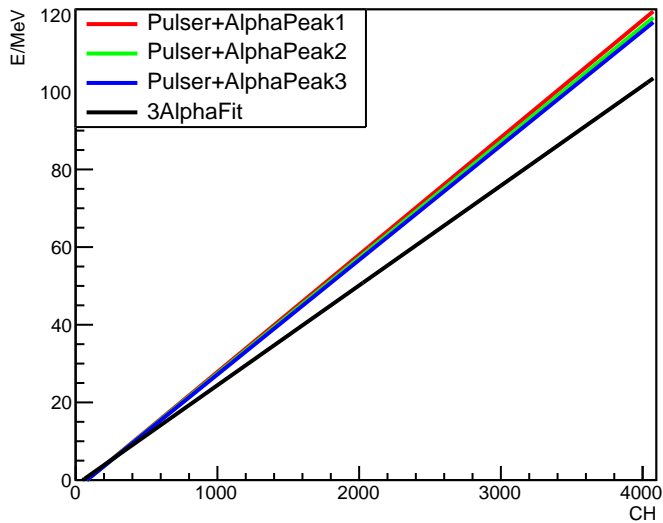
SSD3_L1SS_Switch_CH07



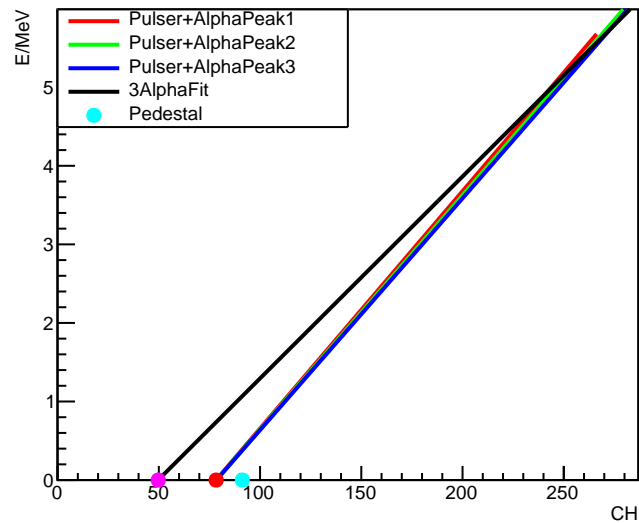
SSD3_L1SS_Switch_CH07



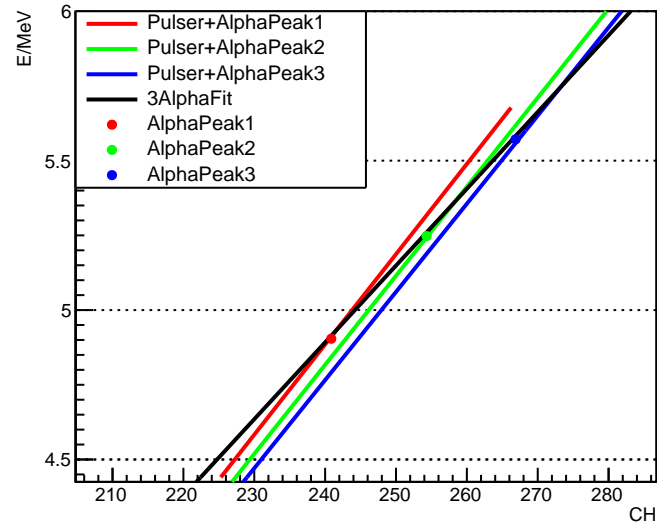
SSD3_L1SS_Switch_CH08



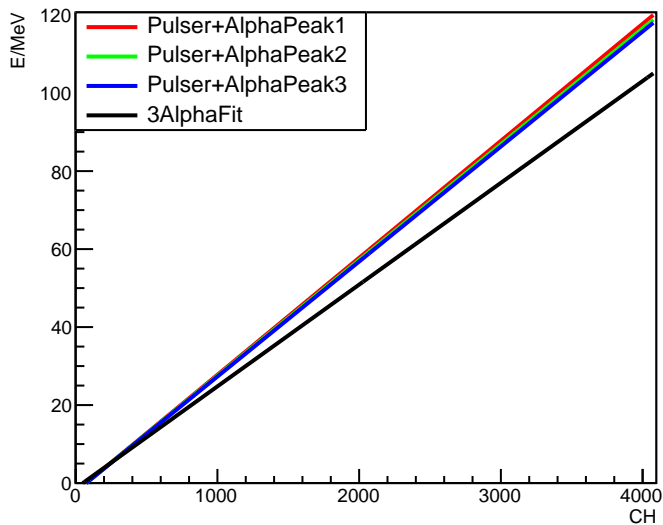
SSD3_L1SS_Switch_CH08



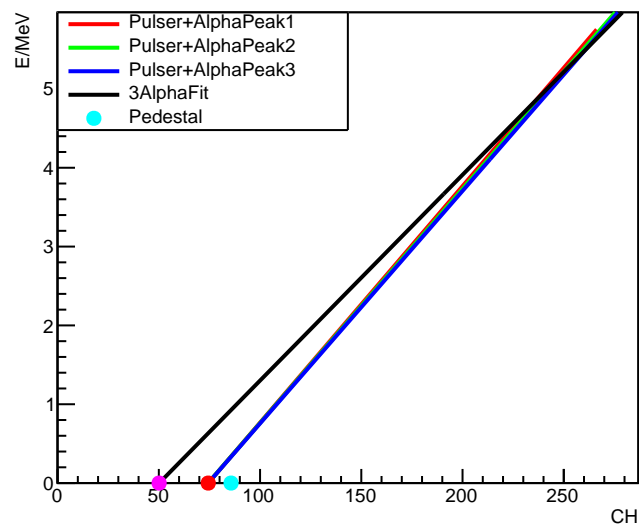
SSD3_L1SS_Switch_CH08



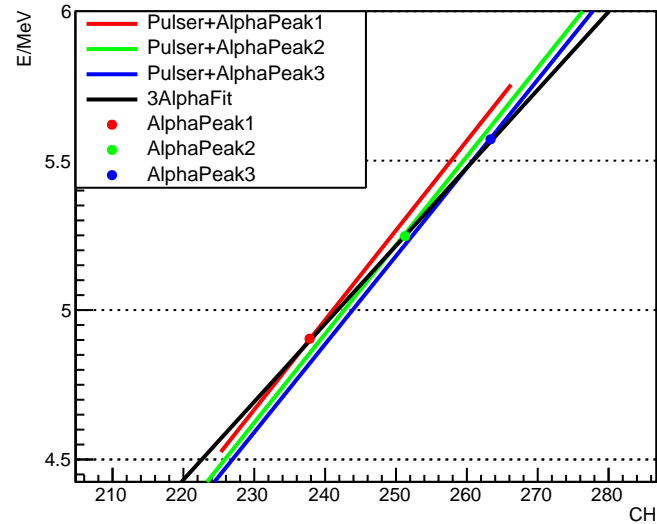
SSD3_L1SS_Switch_CH09



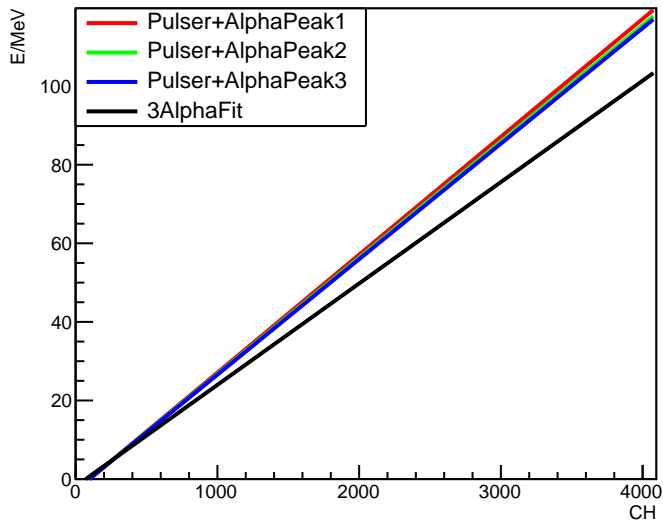
SSD3_L1SS_Switch_CH09



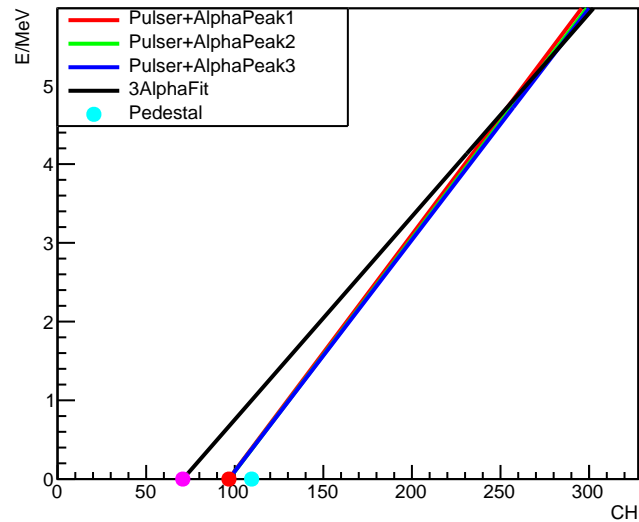
SSD3_L1SS_Switch_CH09



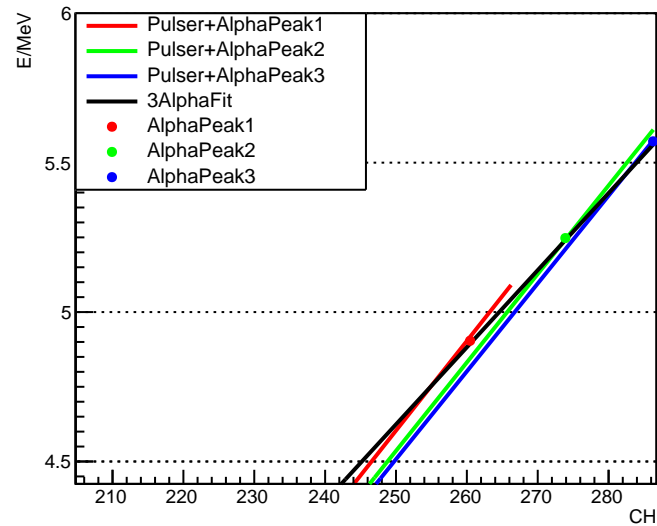
SSD3_L1SS_Switch_CH10



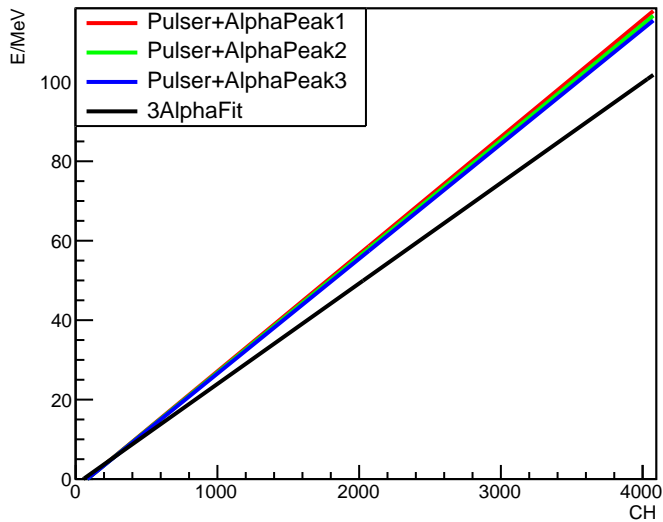
SSD3_L1SS_Switch_CH10



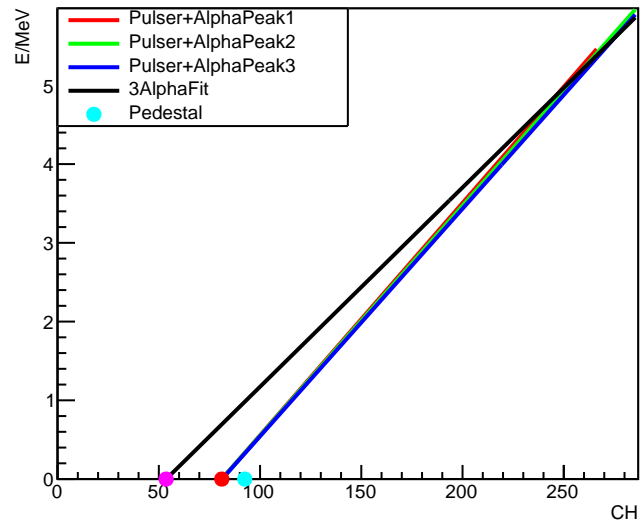
SSD3_L1SS_Switch_CH10



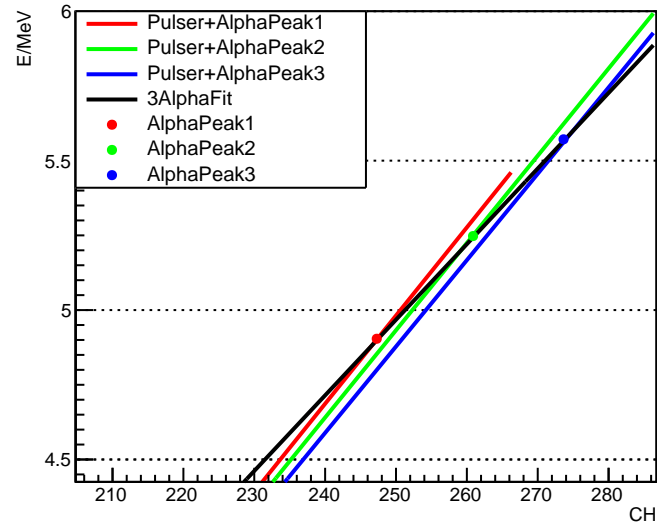
SSD3_L1SS_Switch_CH11



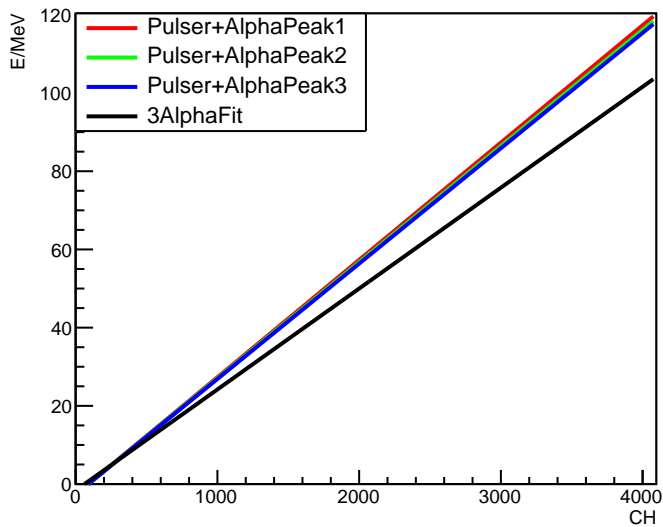
SSD3_L1SS_Switch_CH11



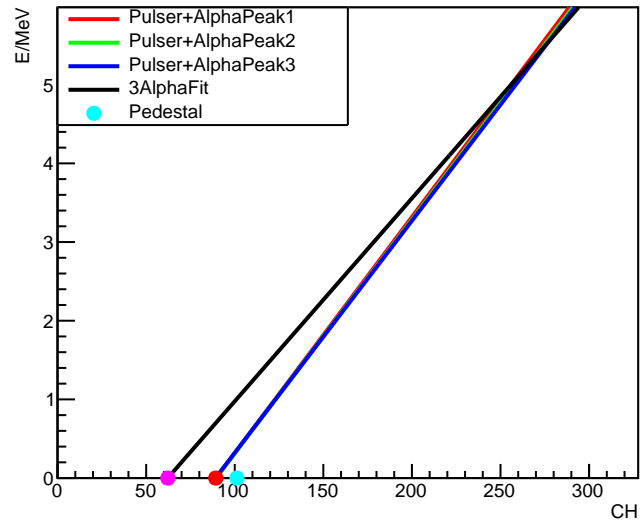
SSD3_L1SS_Switch_CH11



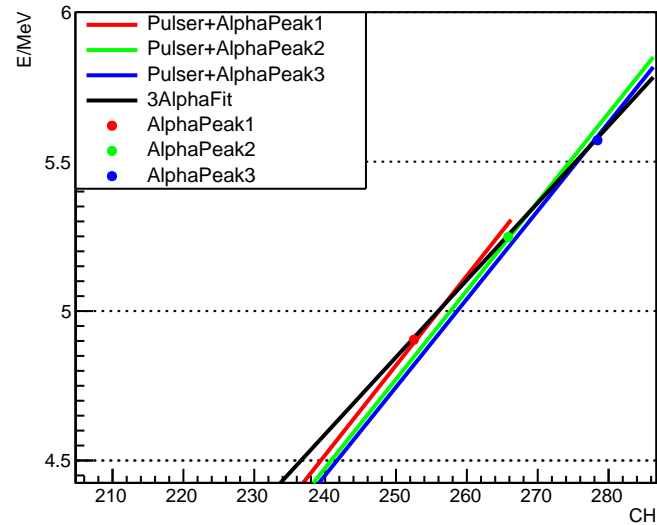
SSD3_L1SS_Switch_CH12



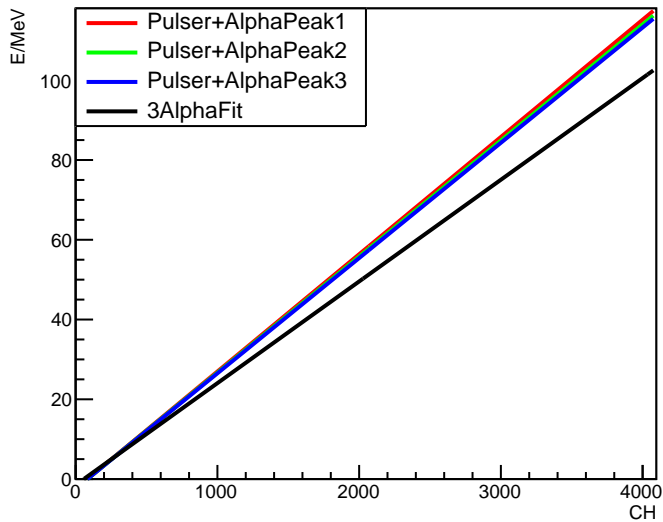
SSD3_L1SS_Switch_CH12



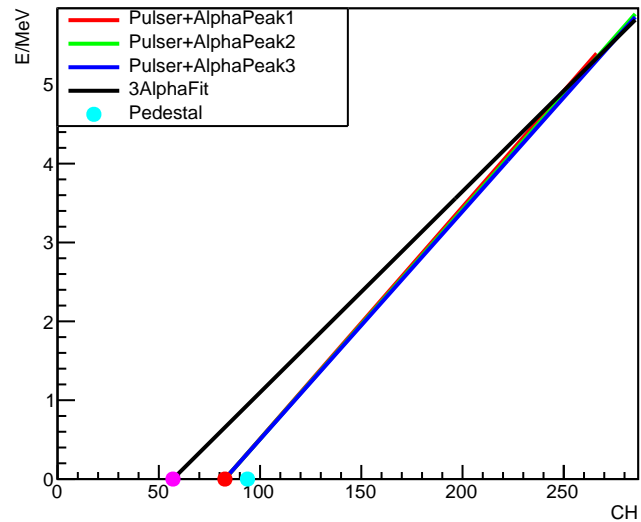
SSD3_L1SS_Switch_CH12



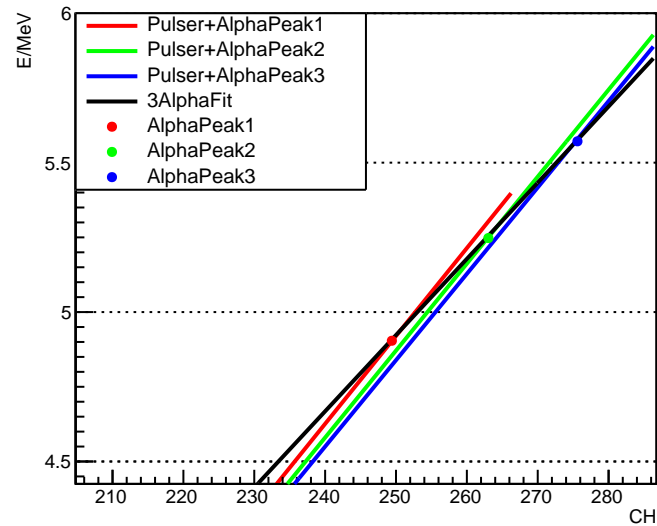
SSD3_L1SS_Switch_CH13



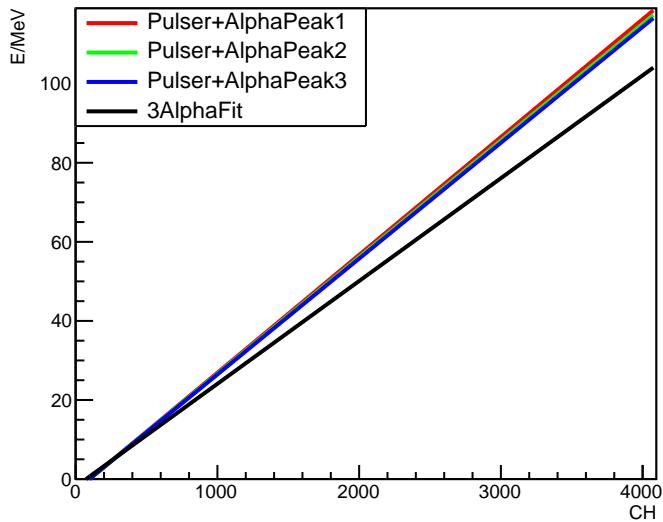
SSD3_L1SS_Switch_CH13



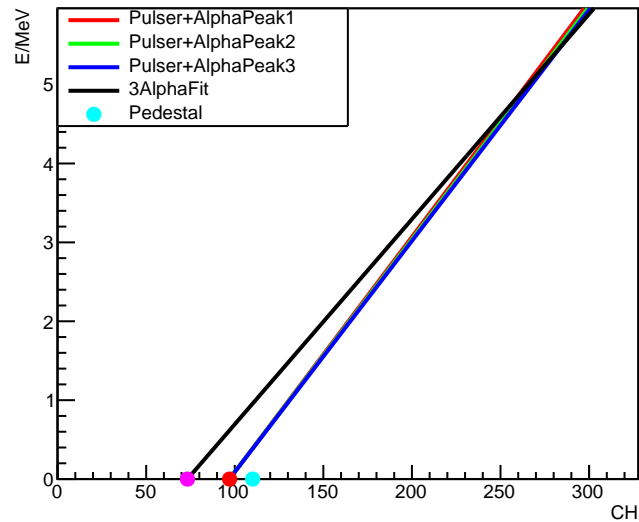
SSD3_L1SS_Switch_CH13



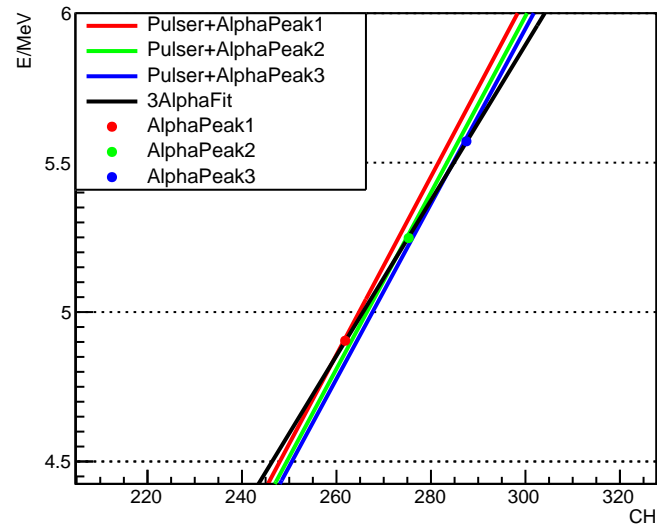
SSD3_L1SS_Switch_CH14



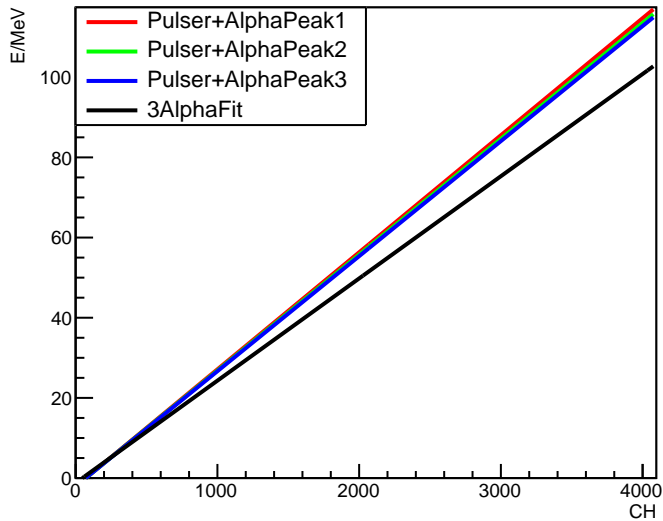
SSD3_L1SS_Switch_CH14



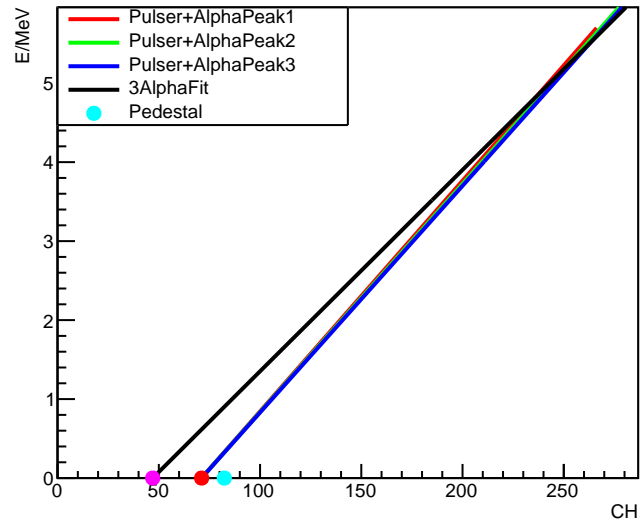
SSD3_L1SS_Switch_CH14



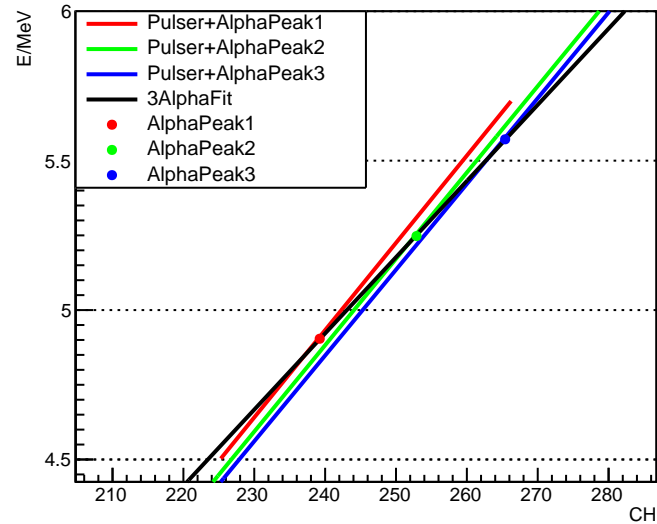
SSD3_L1SS_Switch_CH15



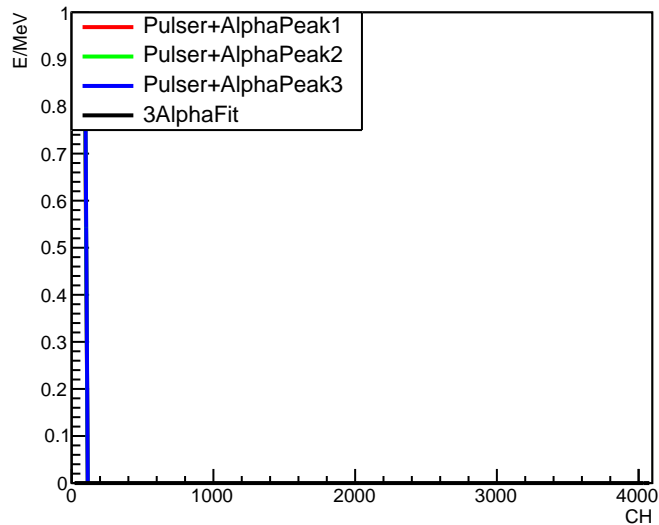
SSD3_L1SS_Switch_CH15



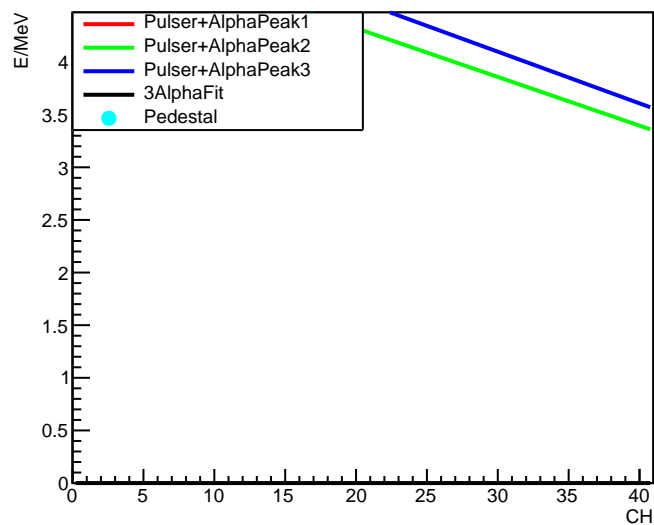
SSD3_L1SS_Switch_CH15



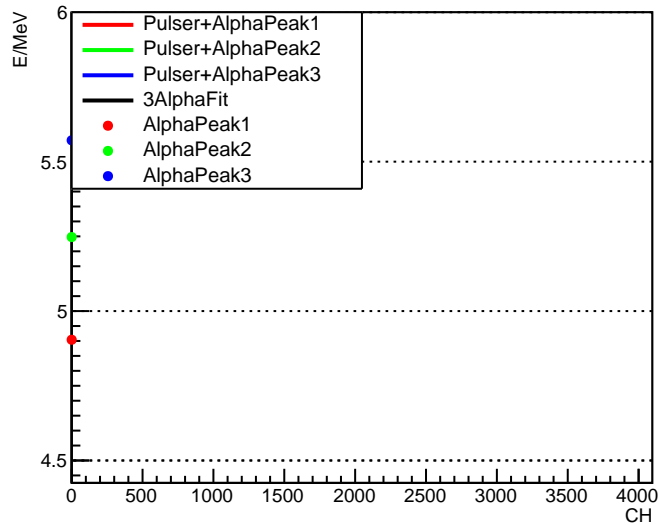
SSD4_L1SS_Switch_CH00



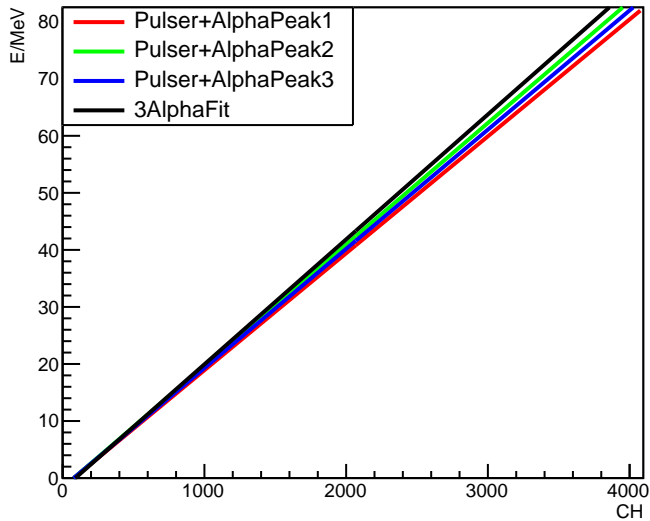
SSD4_L1SS_Switch_CH00



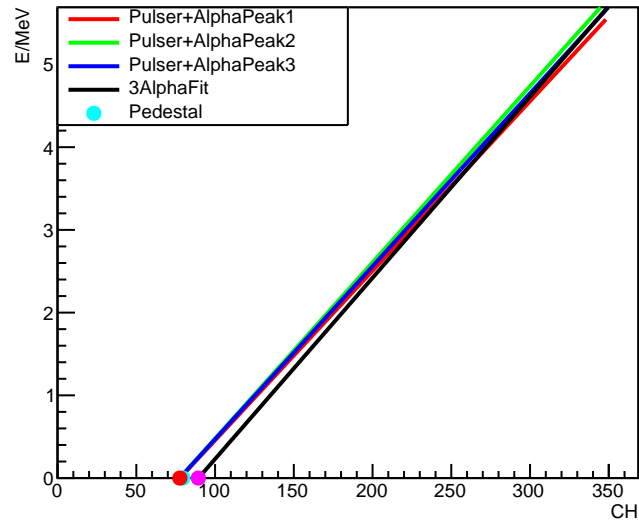
SSD4_L1SS_Switch_CH00



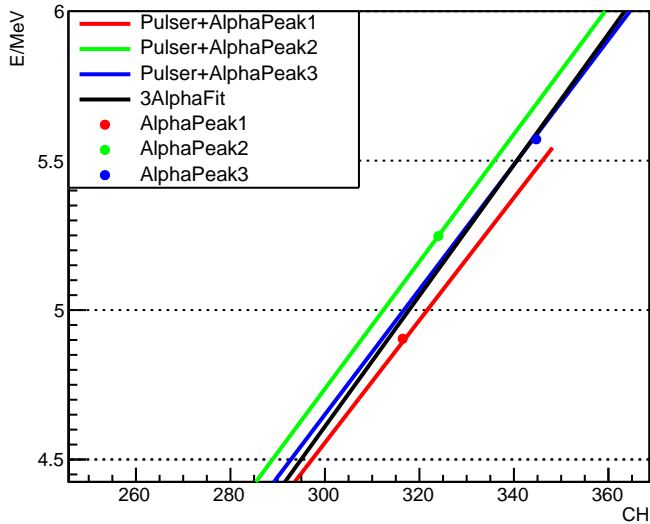
SSD4_L1SS_Switch_CH01



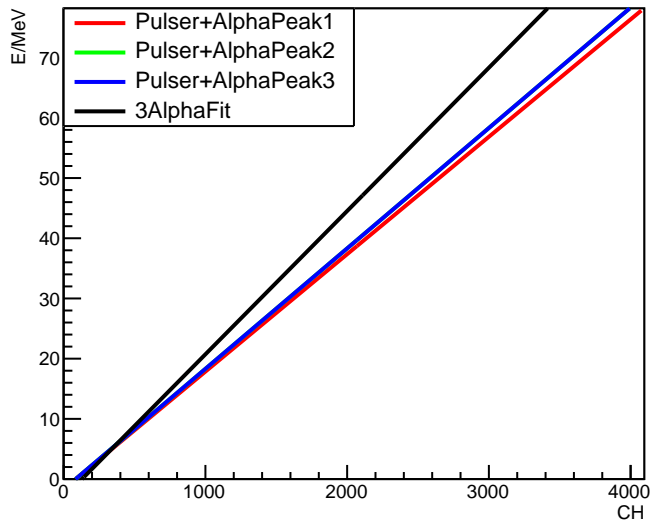
SSD4_L1SS_Switch_CH01



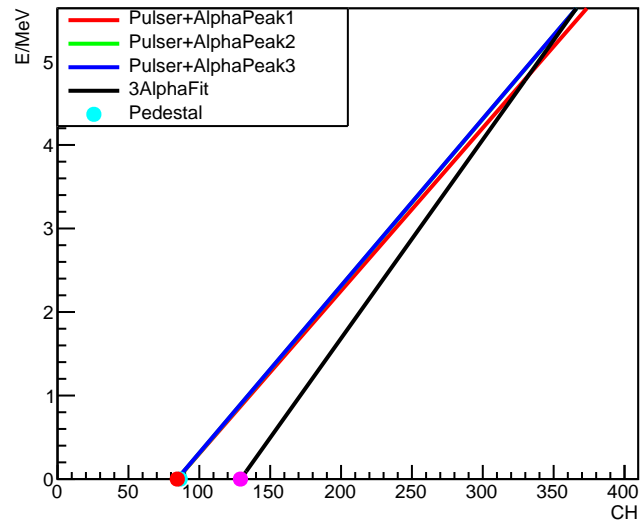
SSD4_L1SS_Switch_CH01



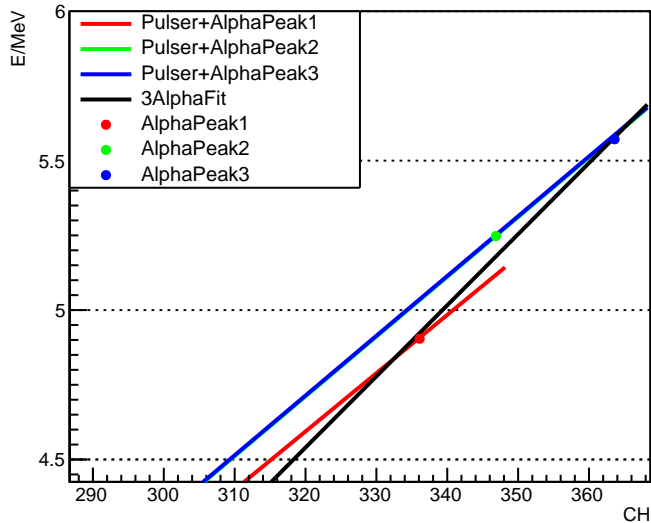
SSD4_L1SS_Switch_CH02



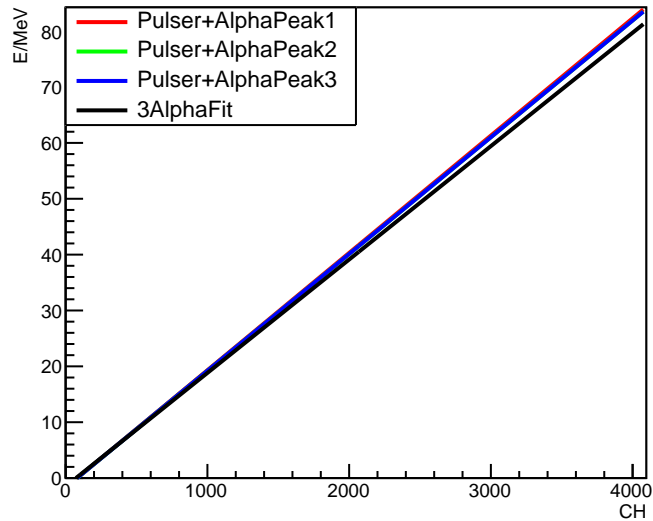
SSD4_L1SS_Switch_CH02



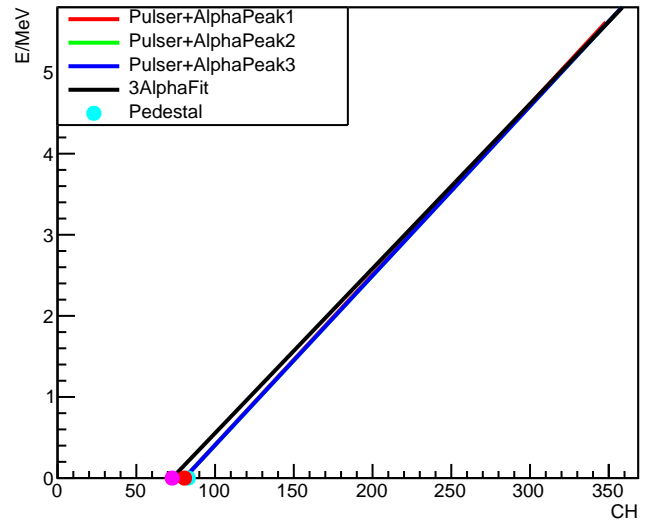
SSD4_L1SS_Switch_CH02



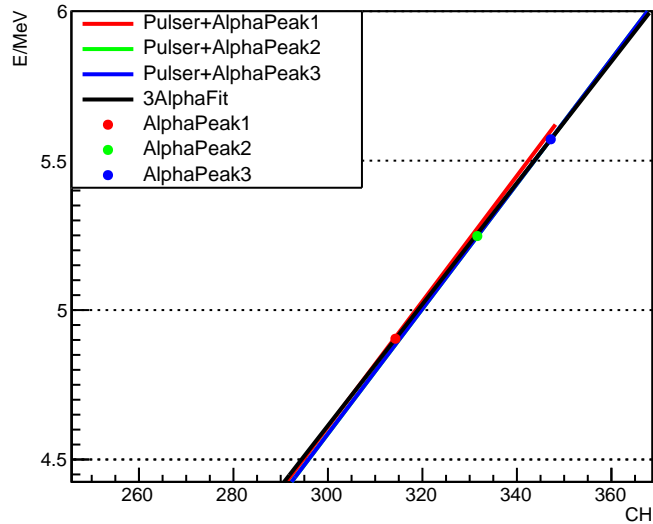
SSD4_L1SS_Switch_CH03



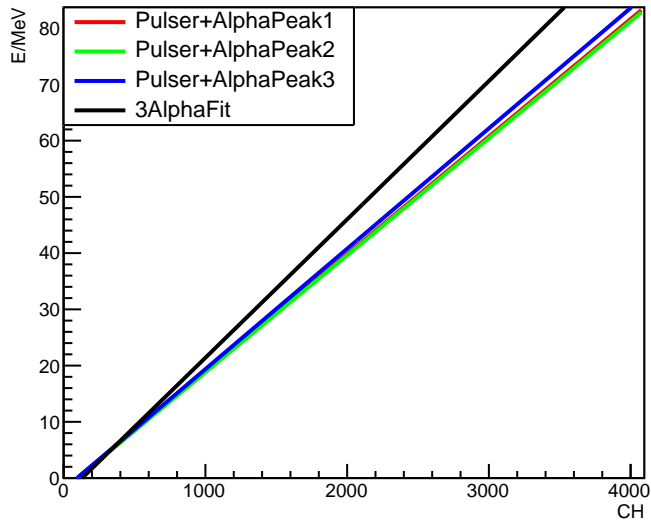
SSD4_L1SS_Switch_CH03



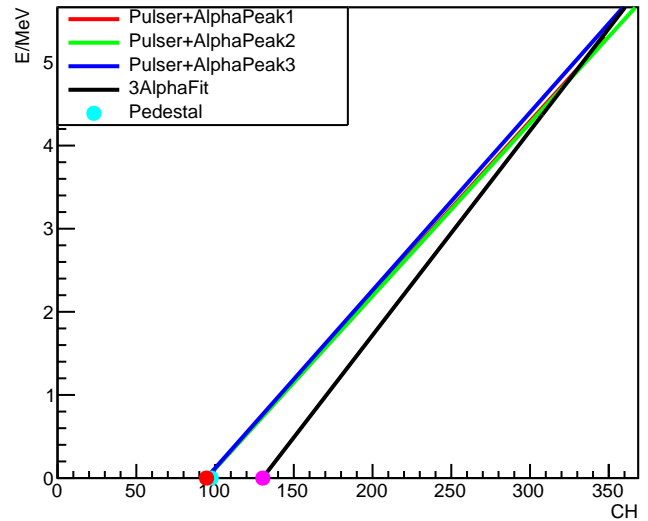
SSD4_L1SS_Switch_CH03



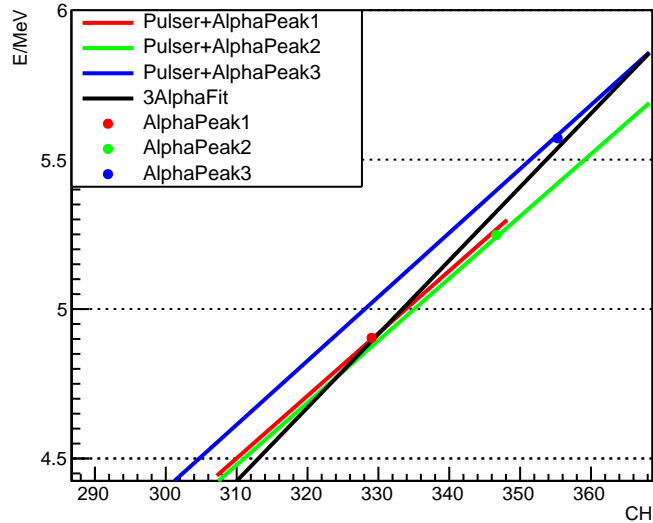
SSD4_L1SS_Switch_CH04



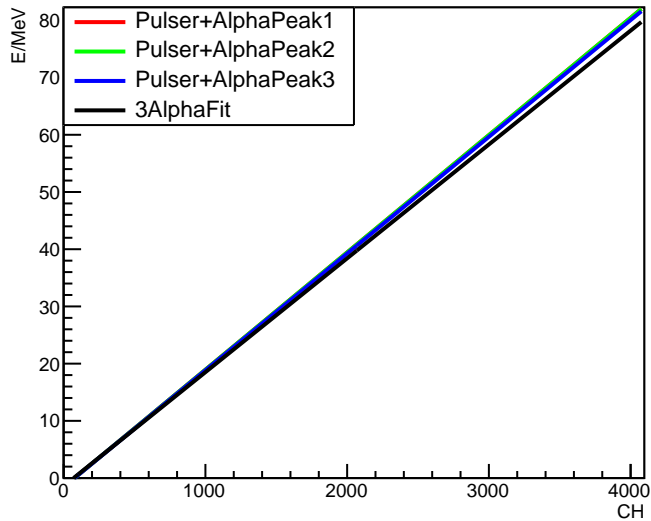
SSD4_L1SS_Switch_CH04



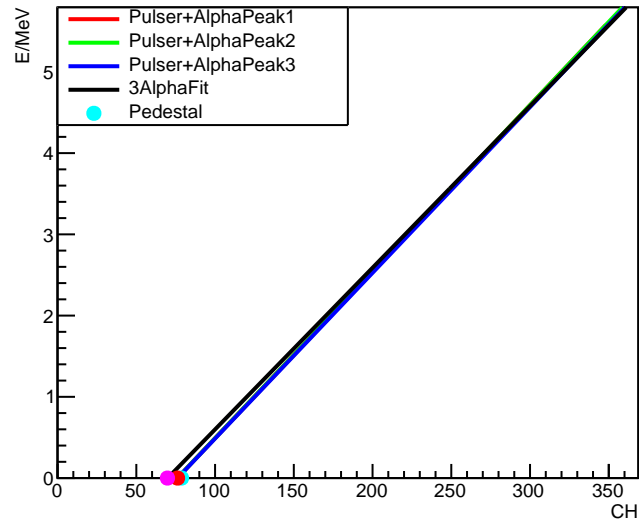
SSD4_L1SS_Switch_CH04



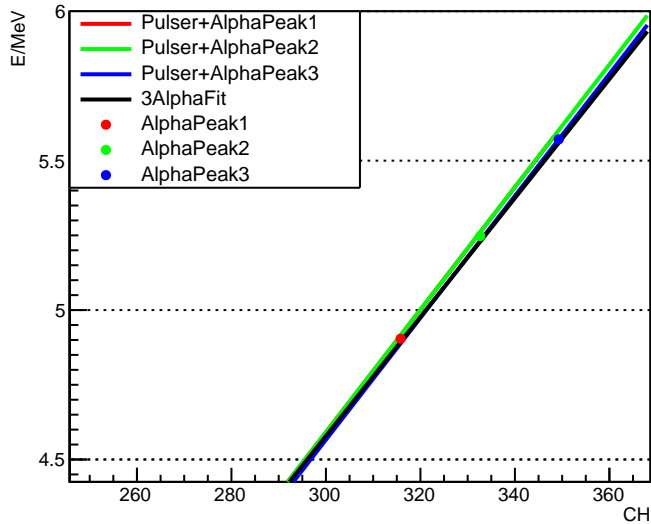
SSD4_L1SS_Switch_CH05



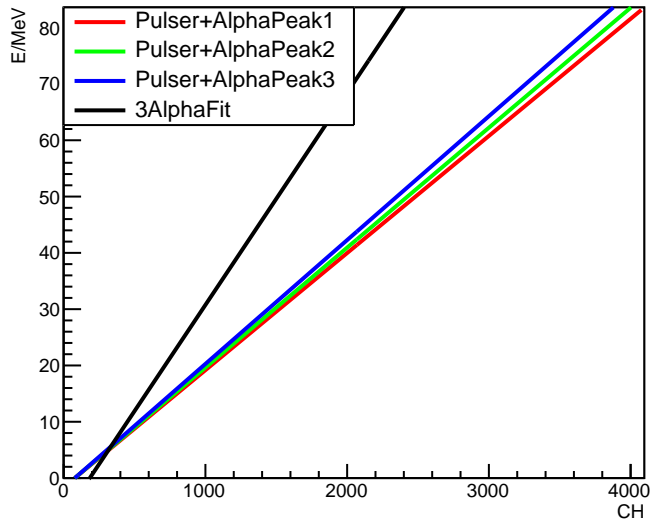
SSD4_L1SS_Switch_CH05



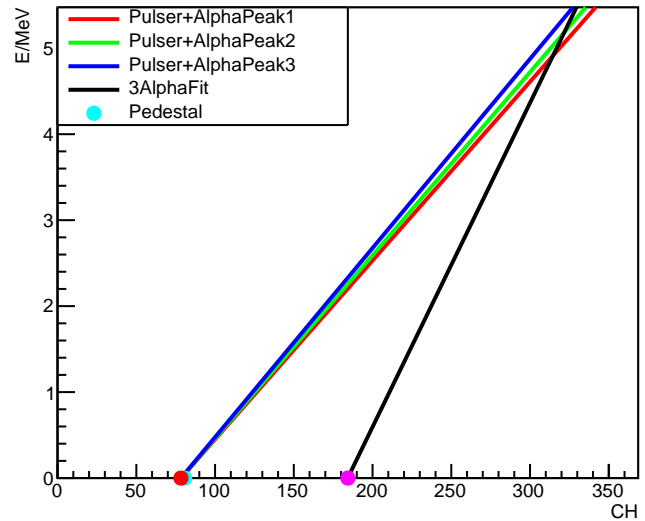
SSD4_L1SS_Switch_CH05



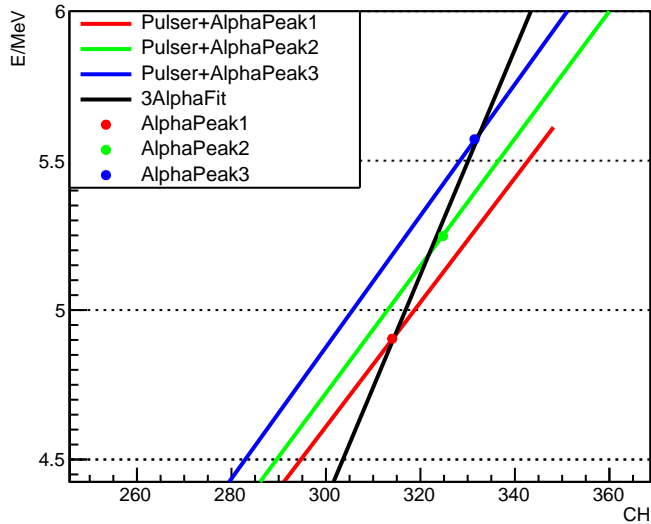
SSD4_L1SS_Switch_CH06



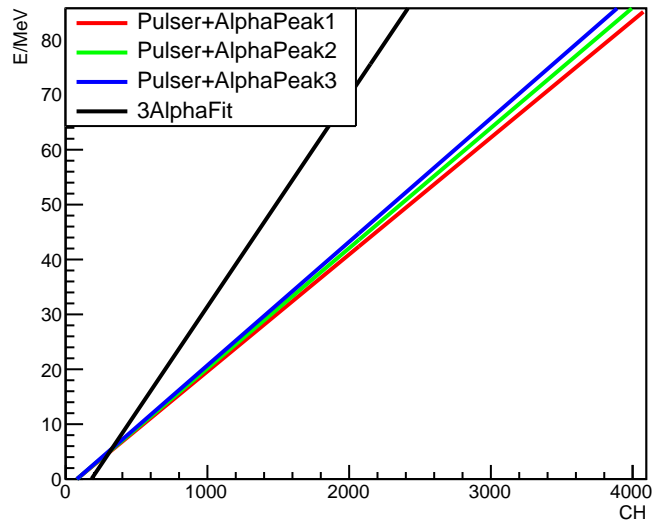
SSD4_L1SS_Switch_CH06



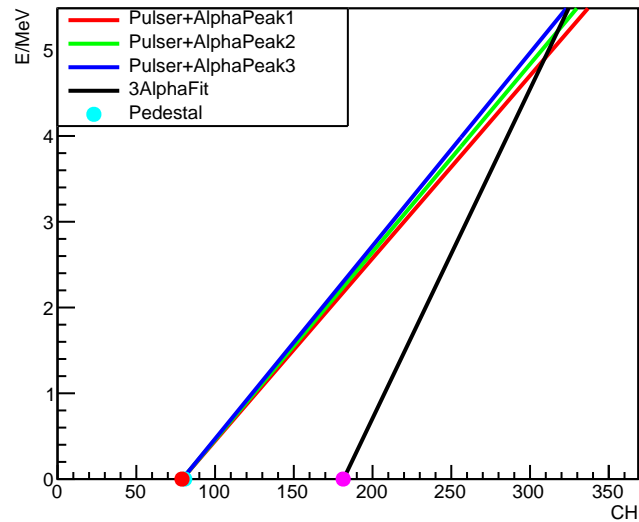
SSD4_L1SS_Switch_CH06



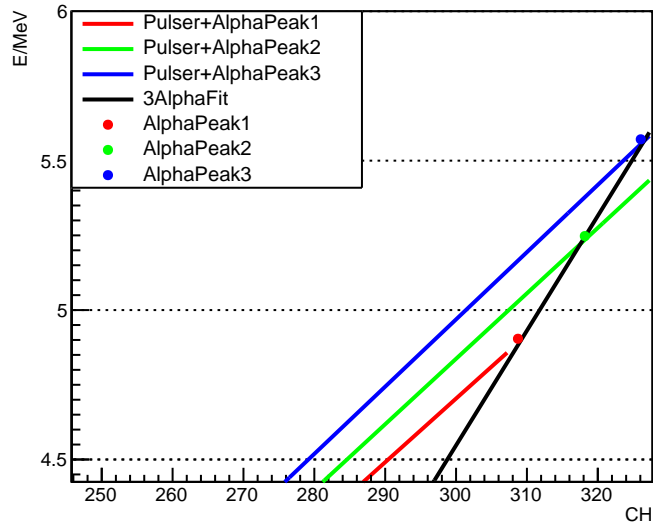
SSD4_L1SS_Switch_CH07



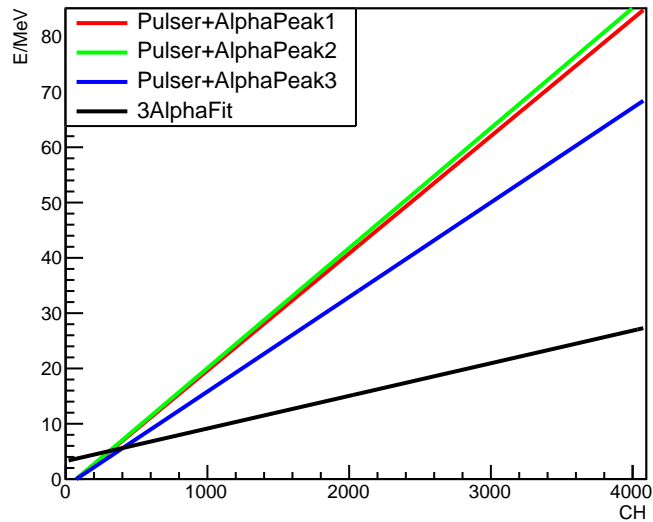
SSD4_L1SS_Switch_CH07



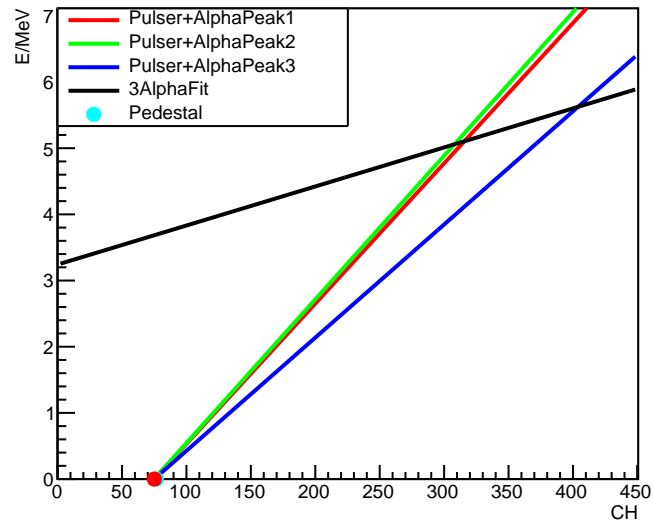
SSD4_L1SS_Switch_CH07



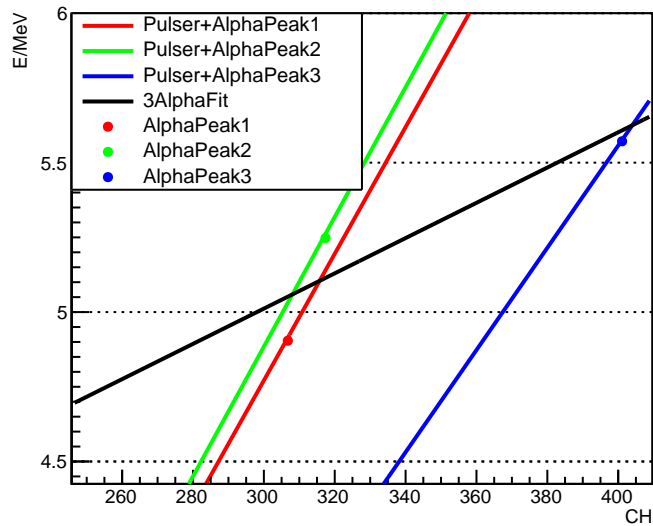
SSD4_L1SS_Switch_CH08



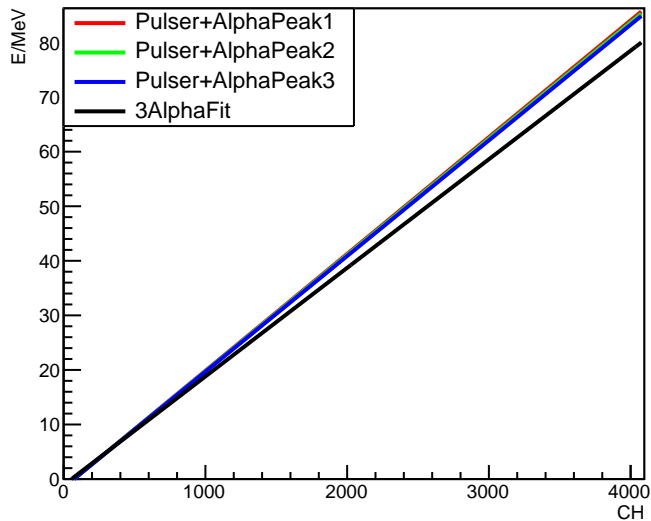
SSD4_L1SS_Switch_CH08



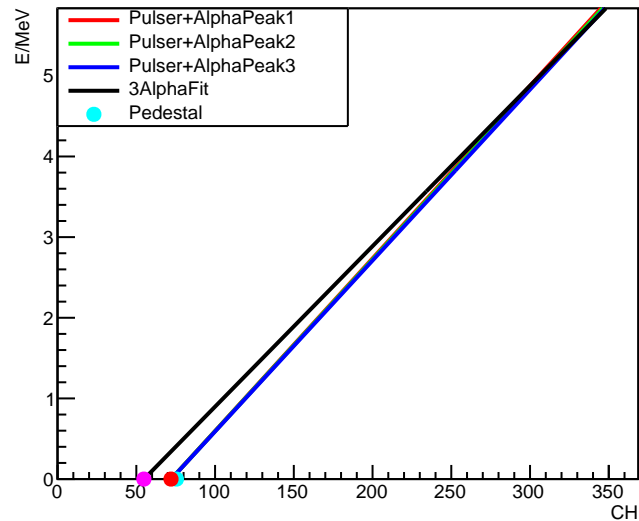
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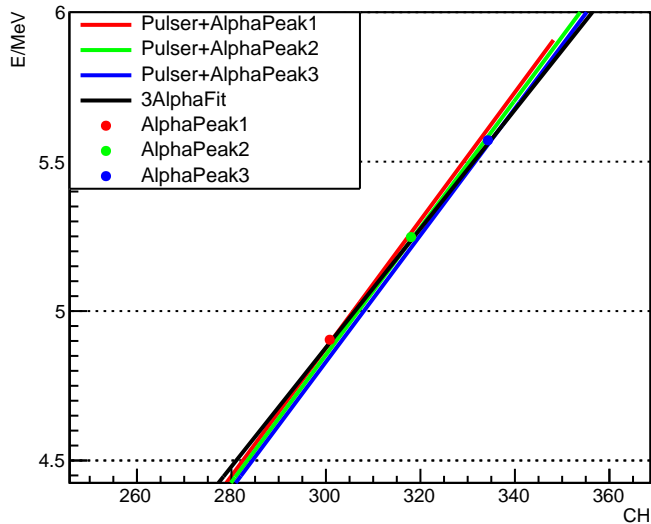
SSD4_L1SS_Switch_CH09



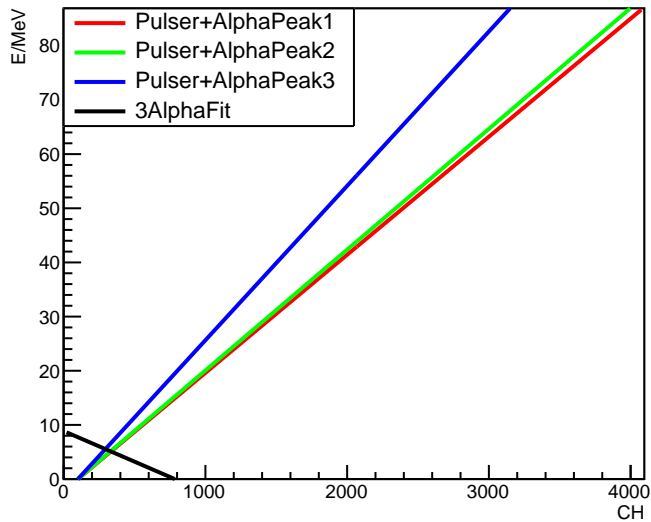
SSD4_L1SS_Switch_CH09



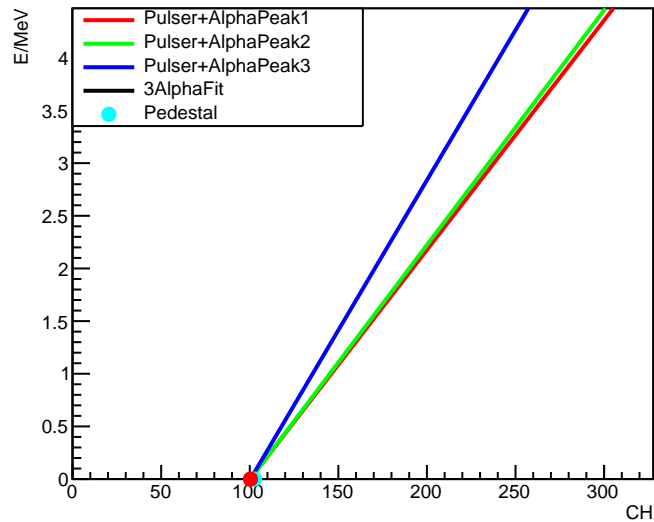
SSD4_L1SS_Switch_CH09



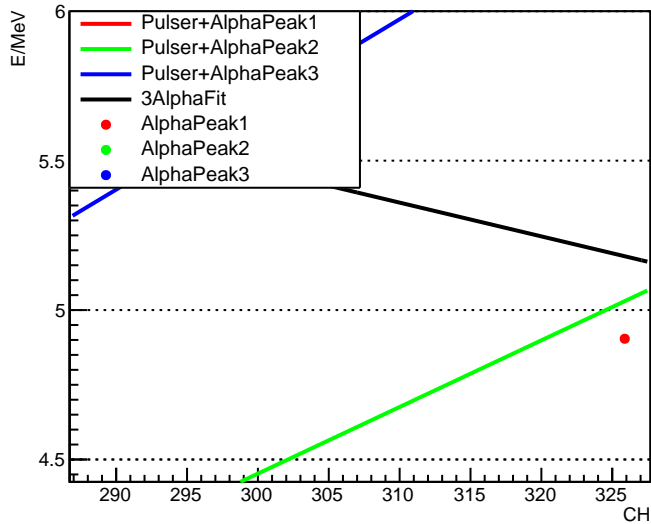
SSD4_L1SS_Switch_CH10



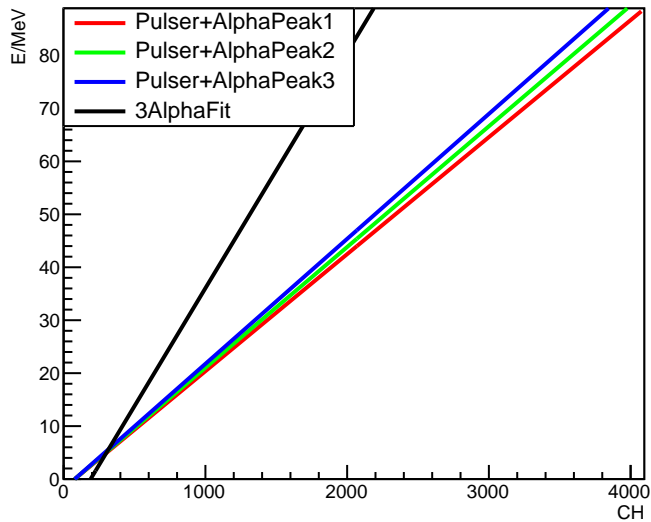
SSD4_L1SS_Switch_CH10



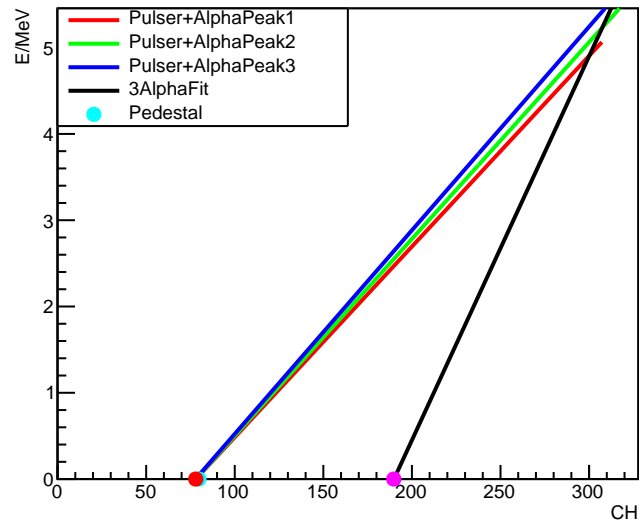
SSD4_L1SS_Switch_CH10



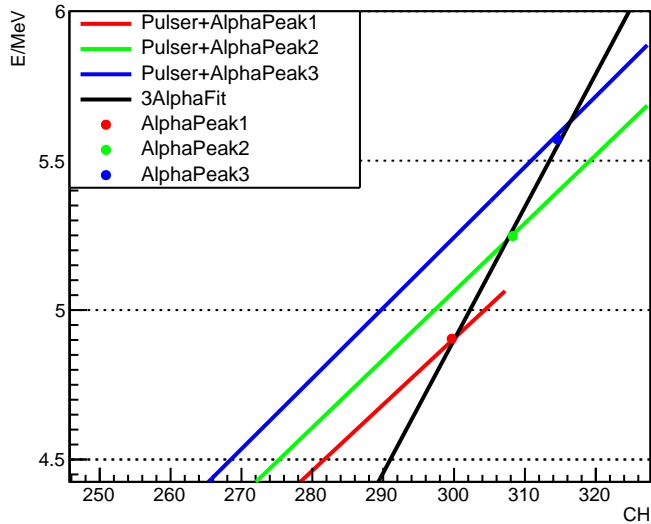
SSD4_L1SS_Switch_CH11



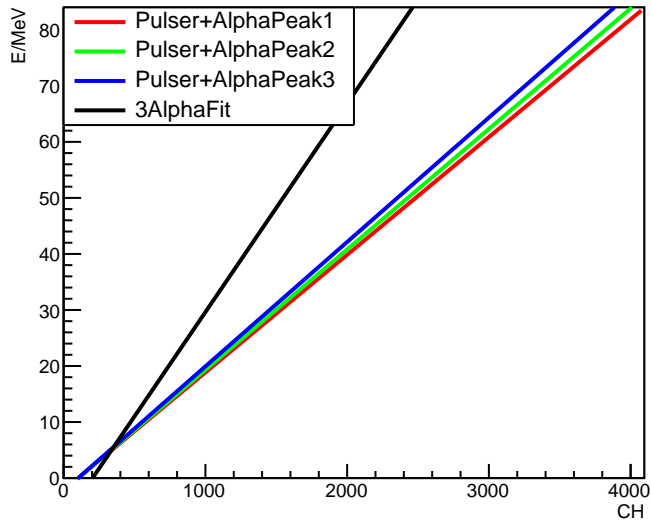
SSD4_L1SS_Switch_CH11



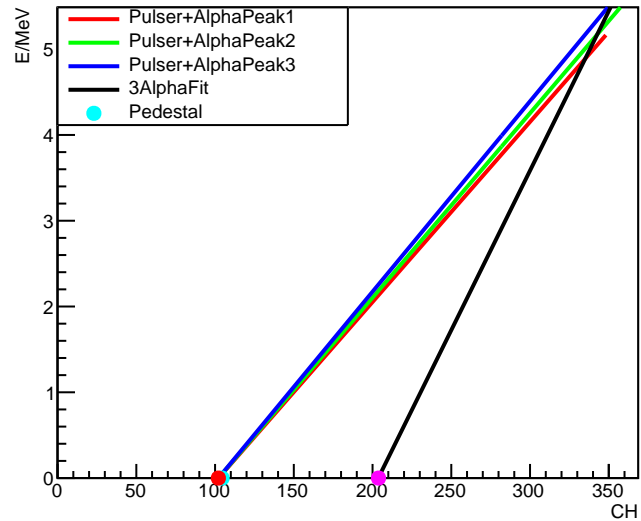
SSD4_L1SS_Switch_CH11



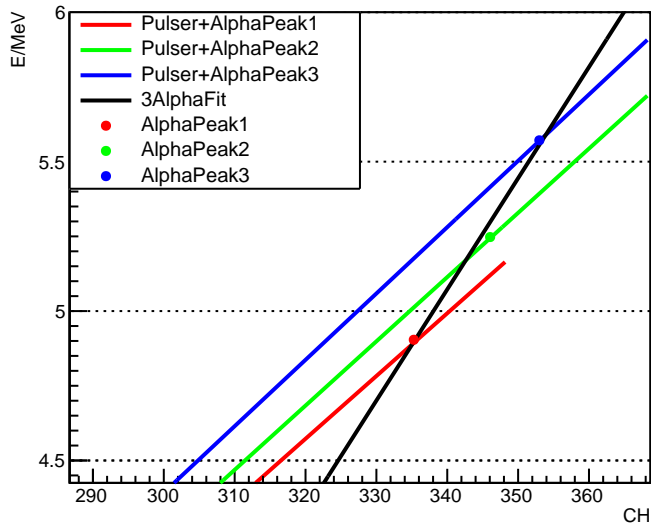
SSD4_L1SS_Switch_CH12



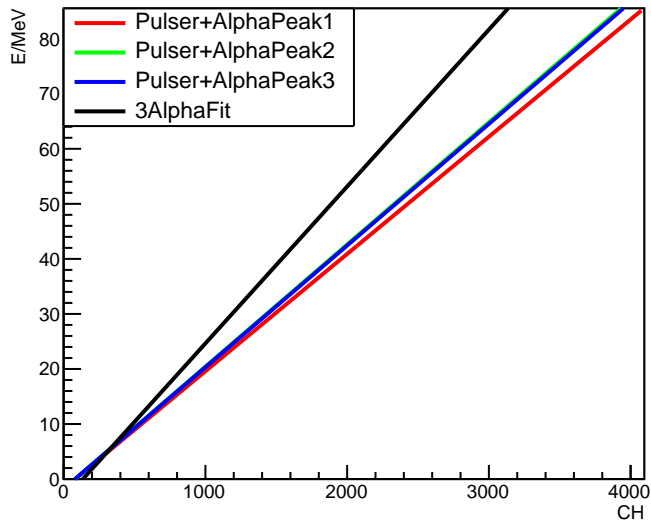
SSD4_L1SS_Switch_CH12



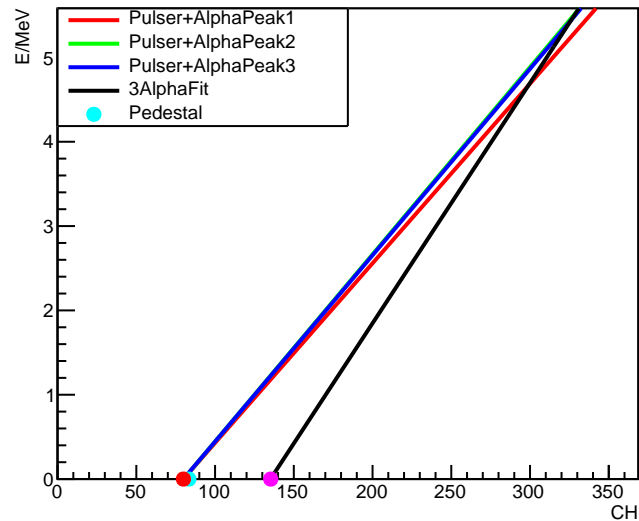
SSD4_L1SS_Switch_CH12



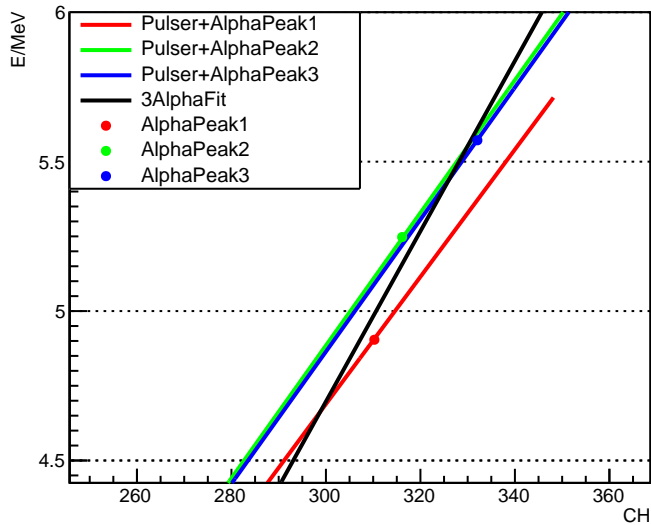
SSD4_L1SS_Switch_CH13



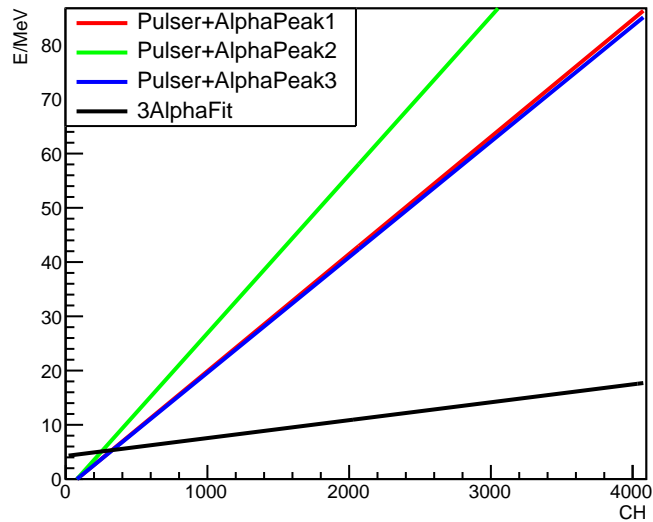
SSD4_L1SS_Switch_CH13



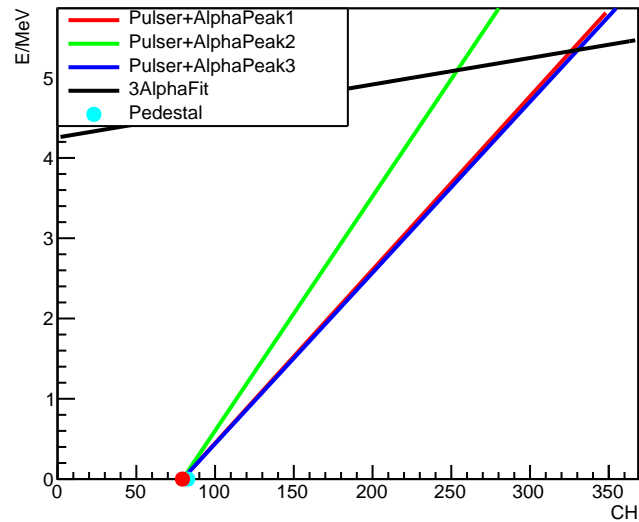
SSD4_L1SS_Switch_CH13



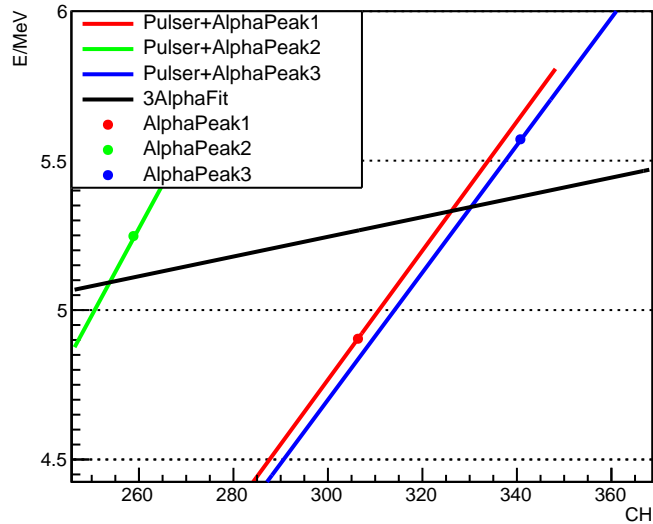
SSD4_L1SS_Switch_CH14



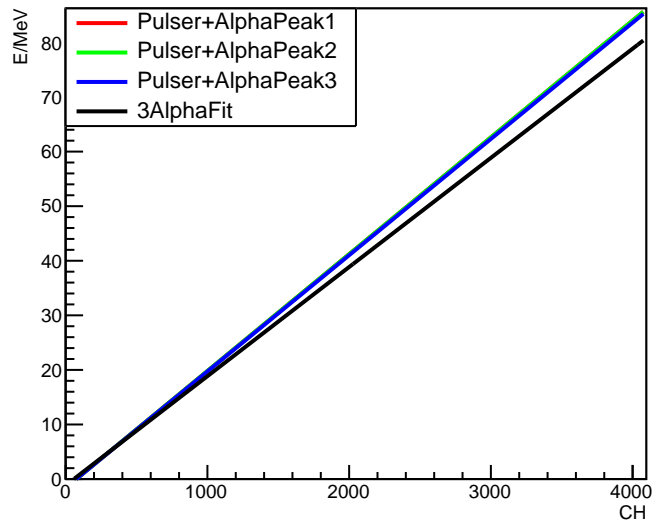
SSD4_L1SS_Switch_CH14



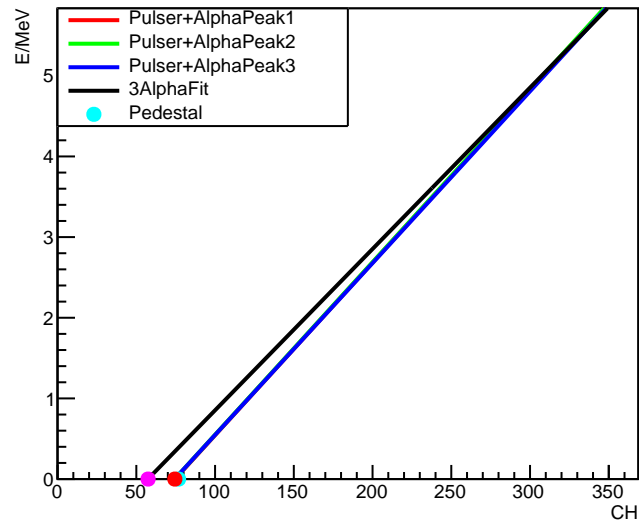
SSD4_L1SS_Switch_CH14



SSD4_L1SS_Switch_CH15



SSD4_L1SS_Switch_CH15



SSD4_L1SS_Switch_CH15

