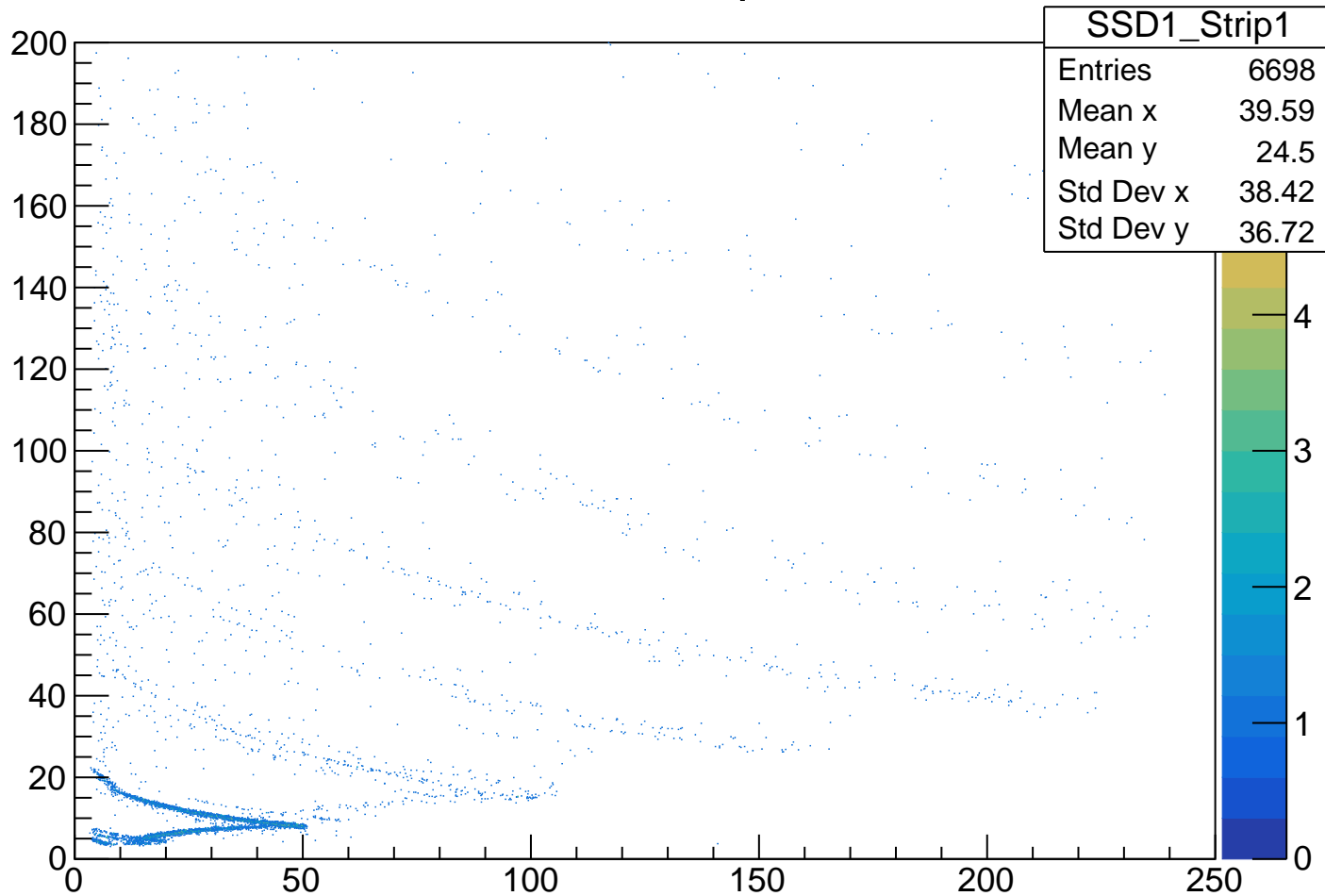
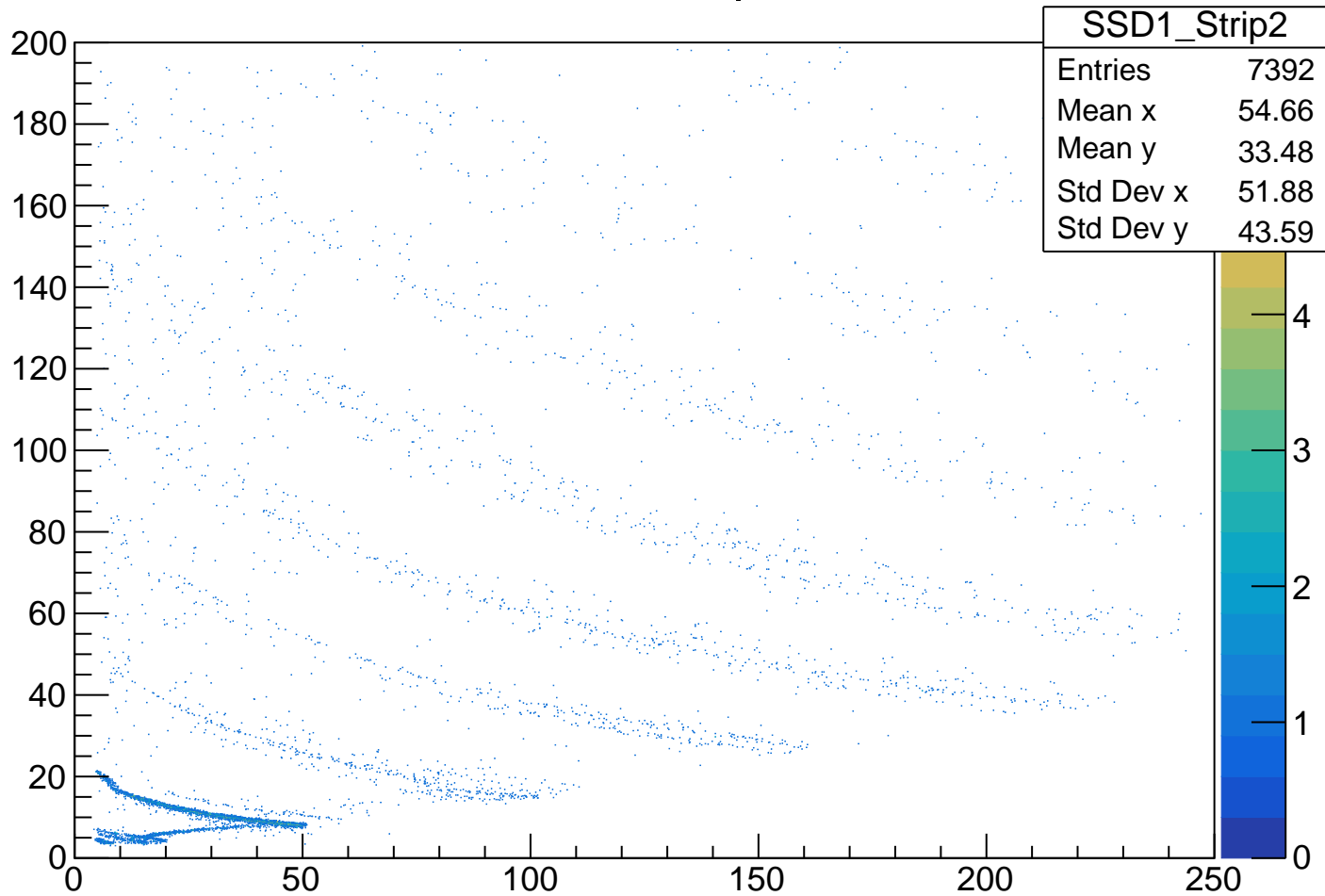


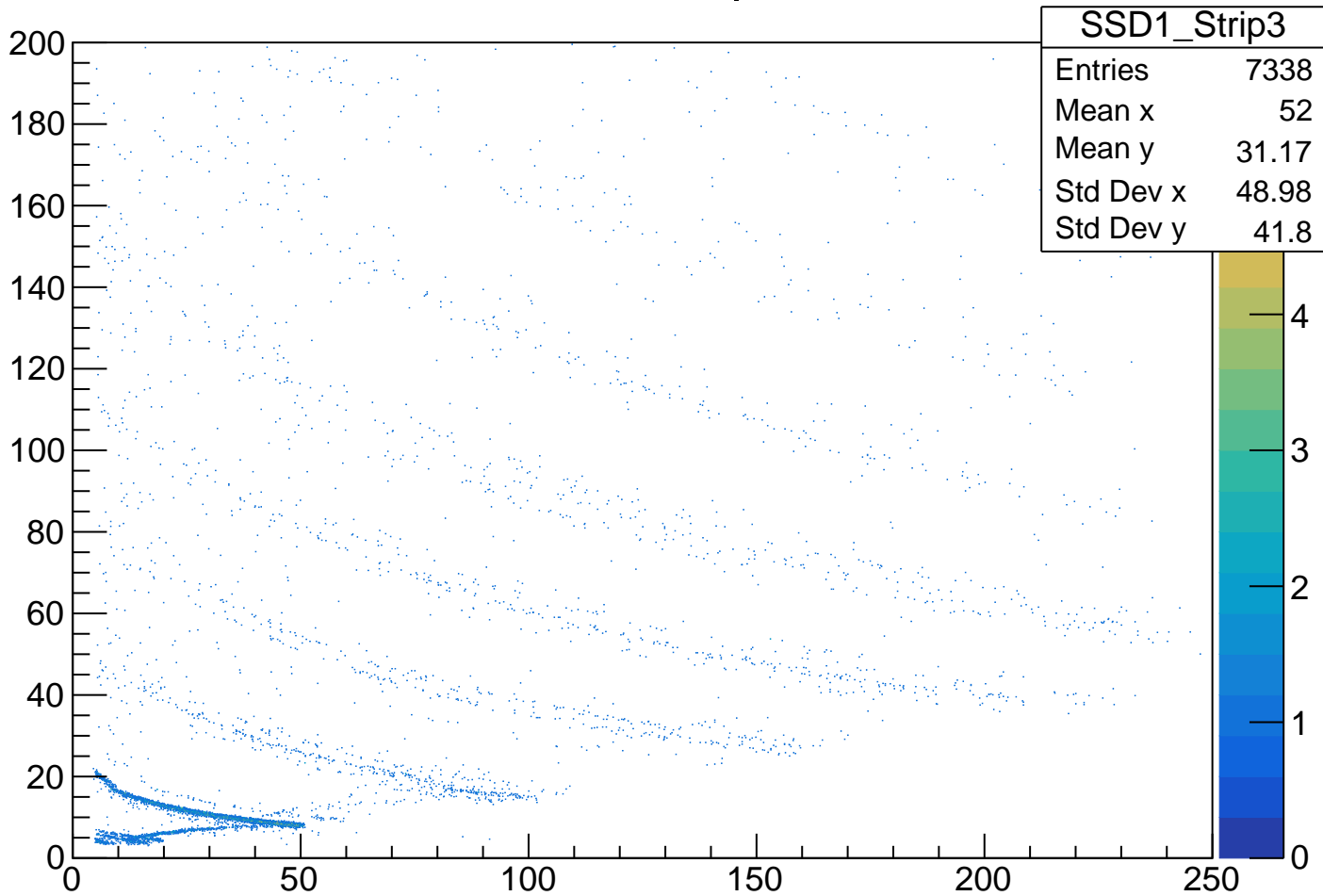
SSD1_Strip1



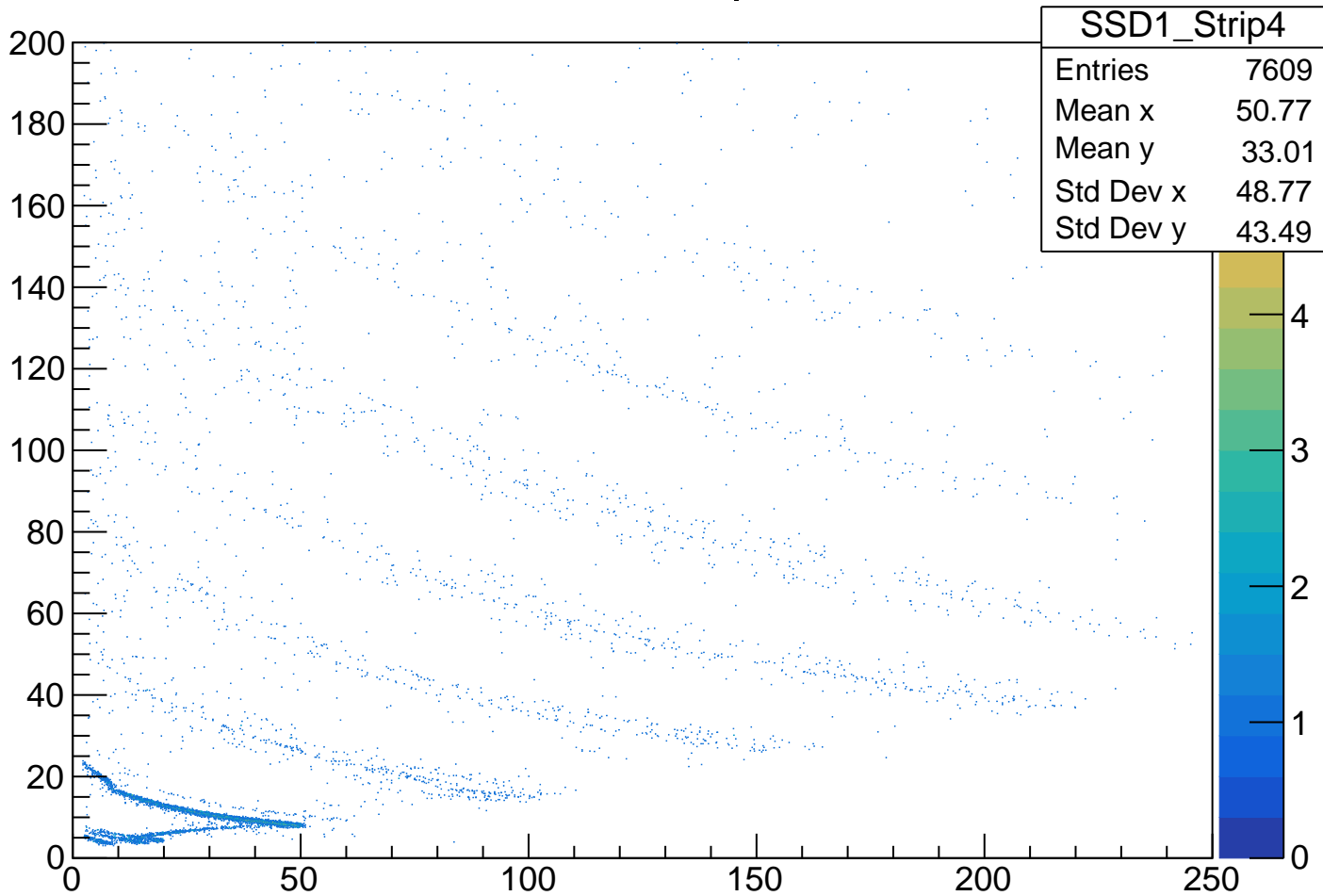
SSD1_Strip2



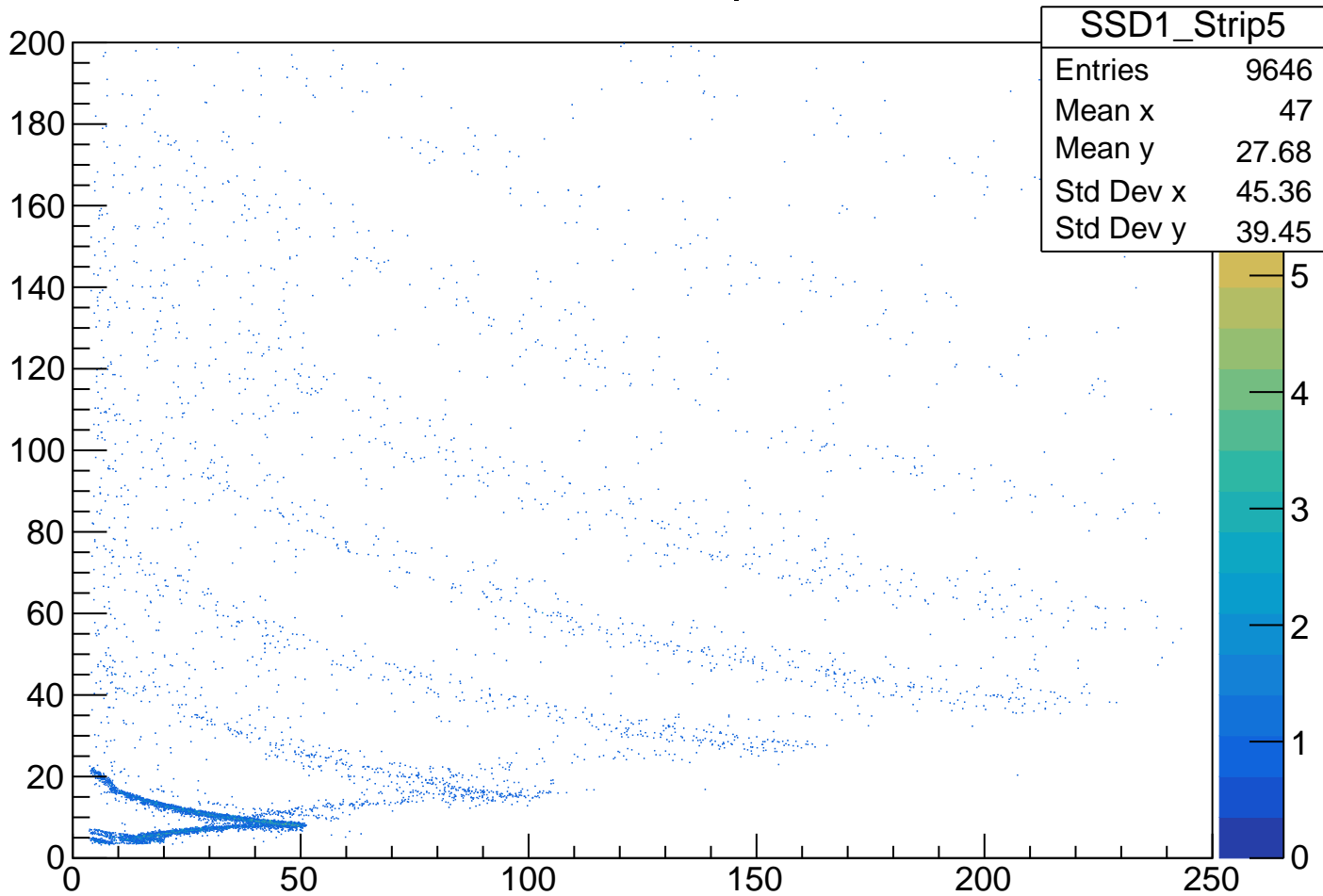
SSD1_Strip3



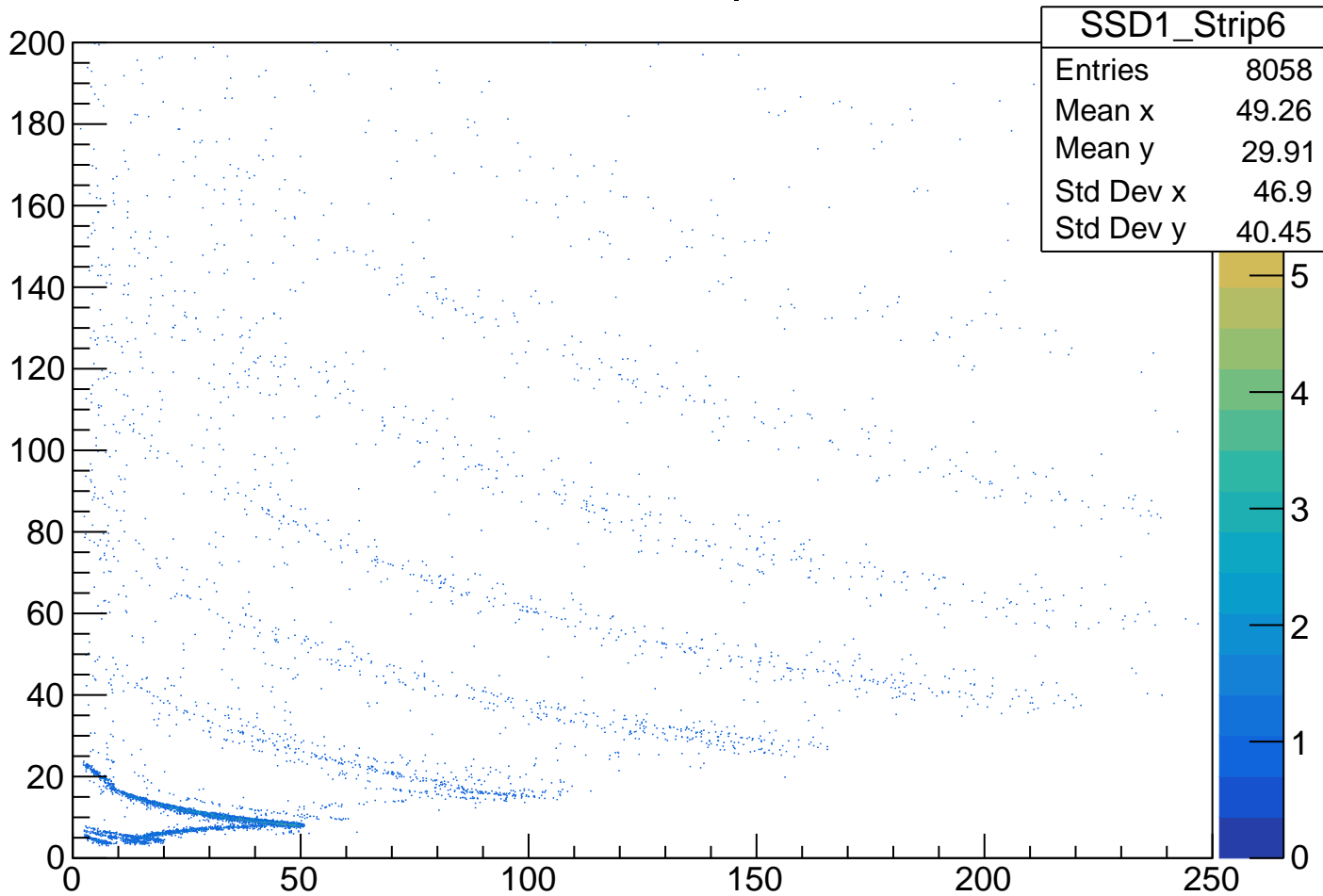
SSD1_Strip4



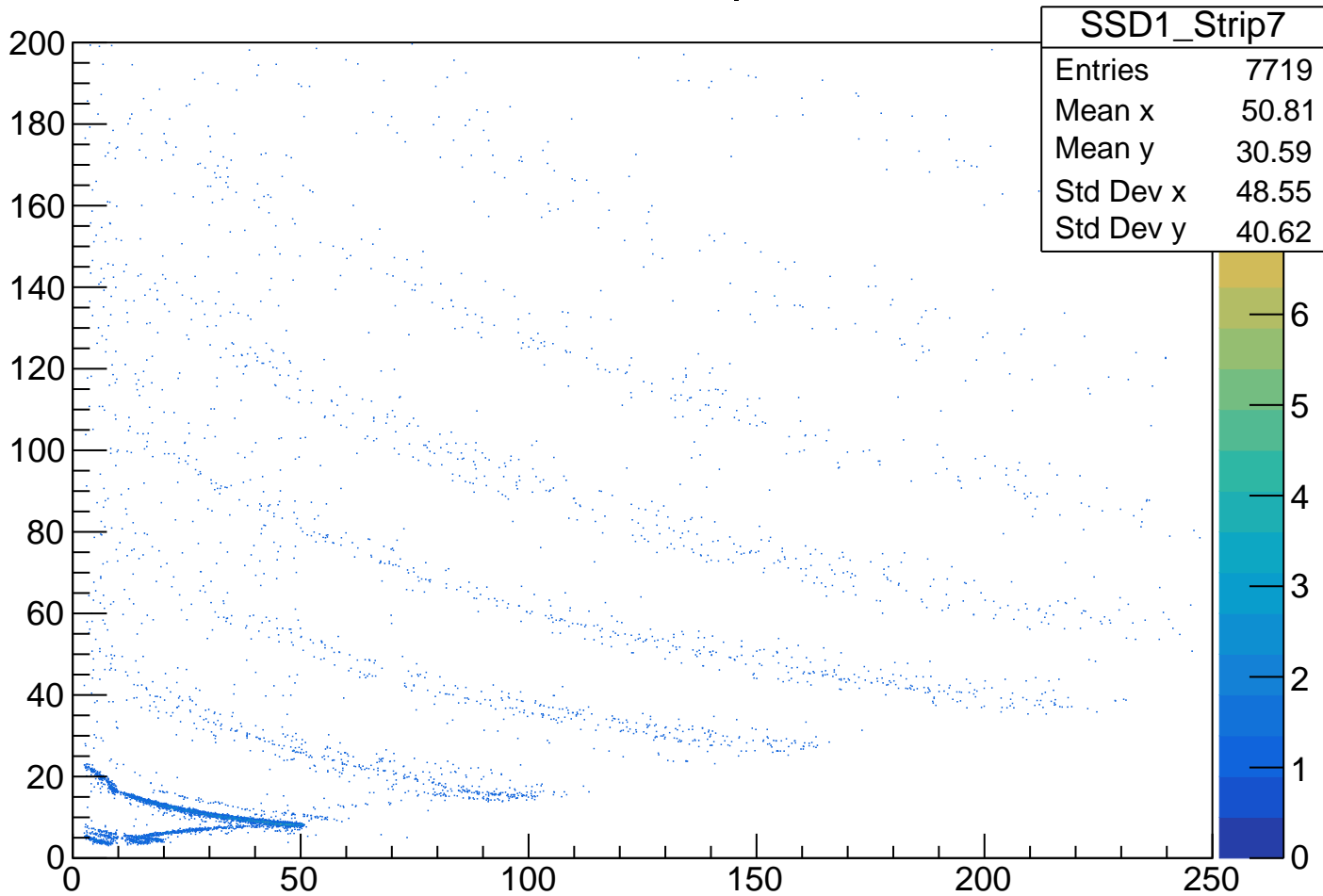
SSD1_Strip5



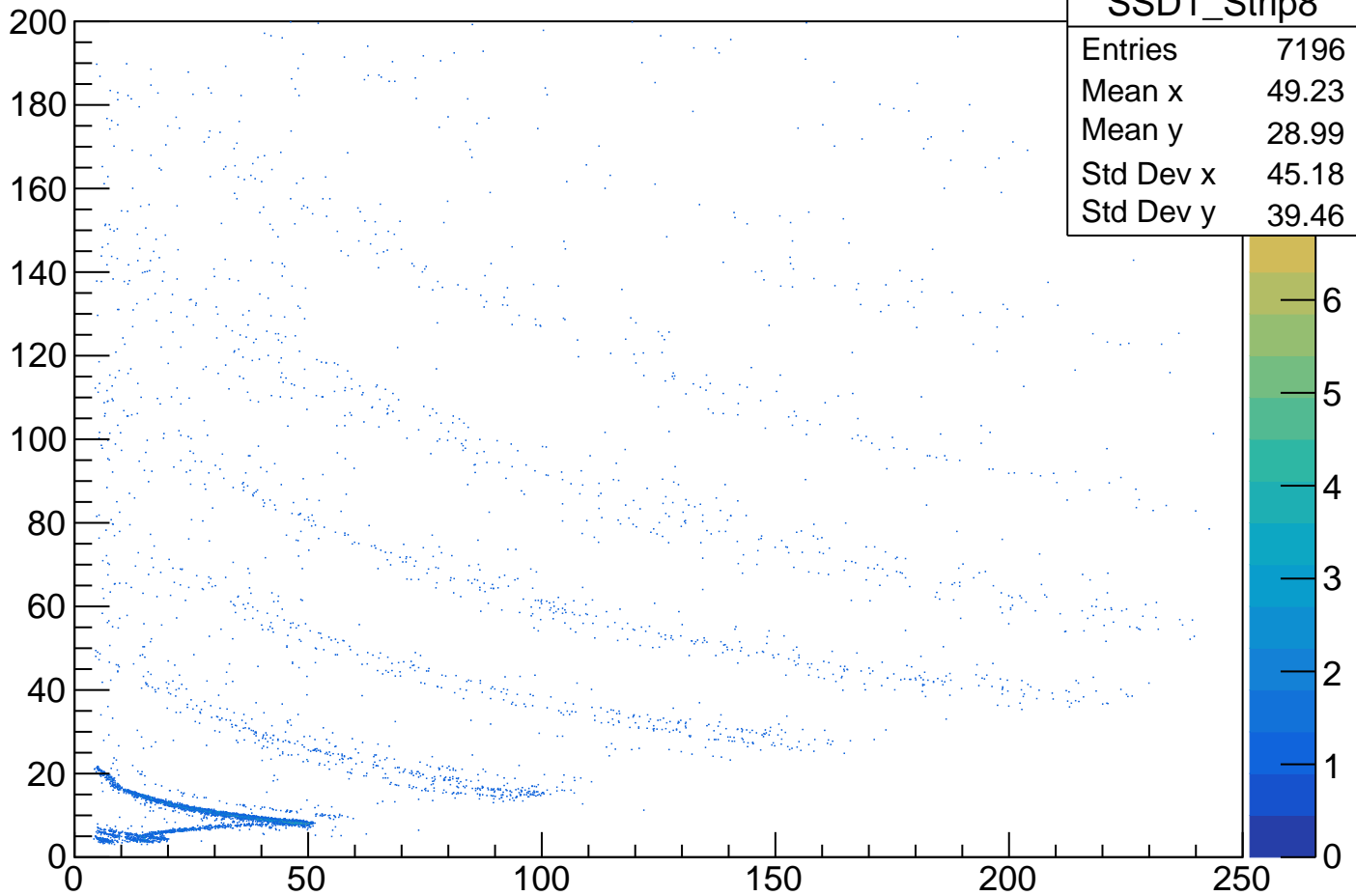
SSD1_Strip6



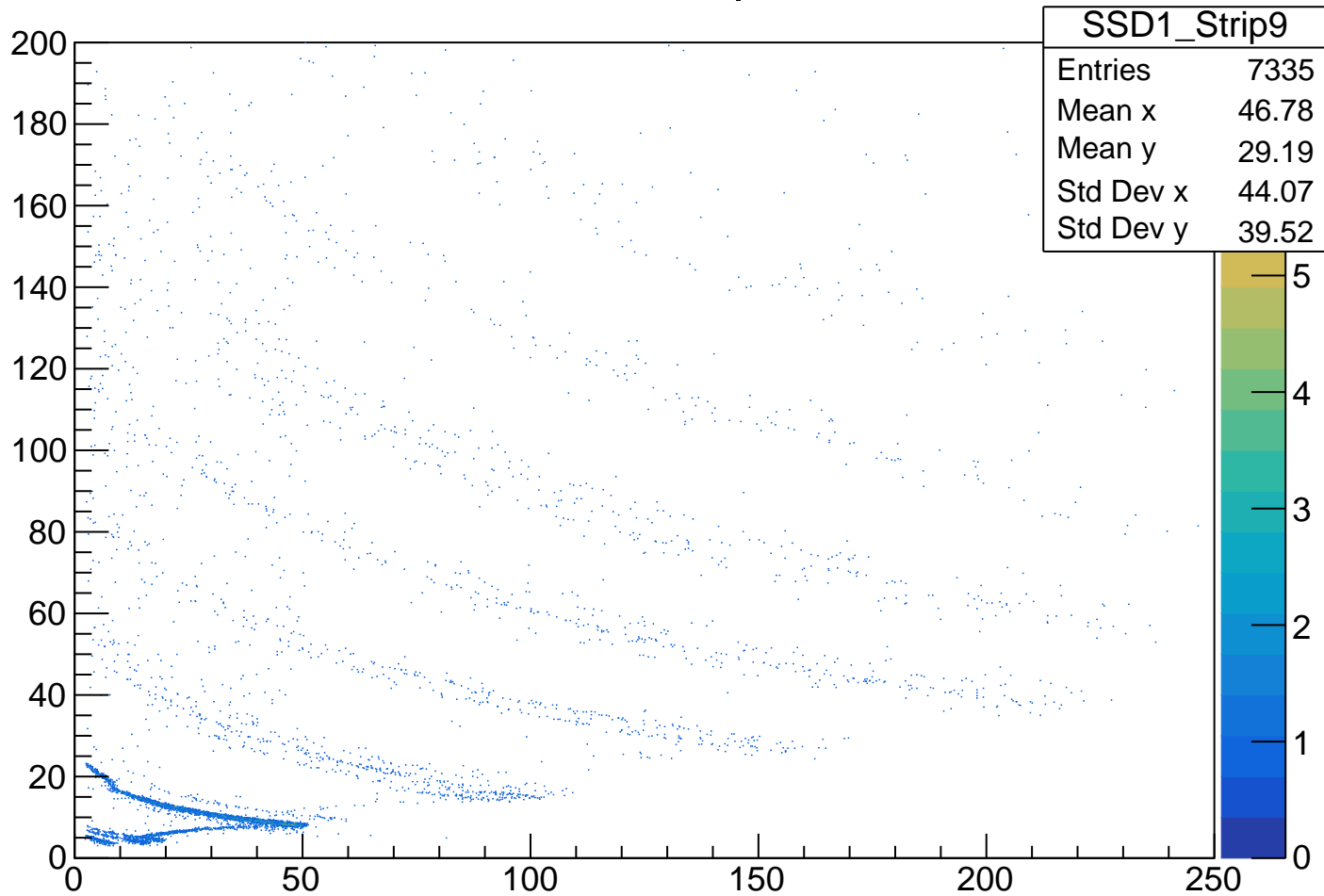
SSD1_Strip7



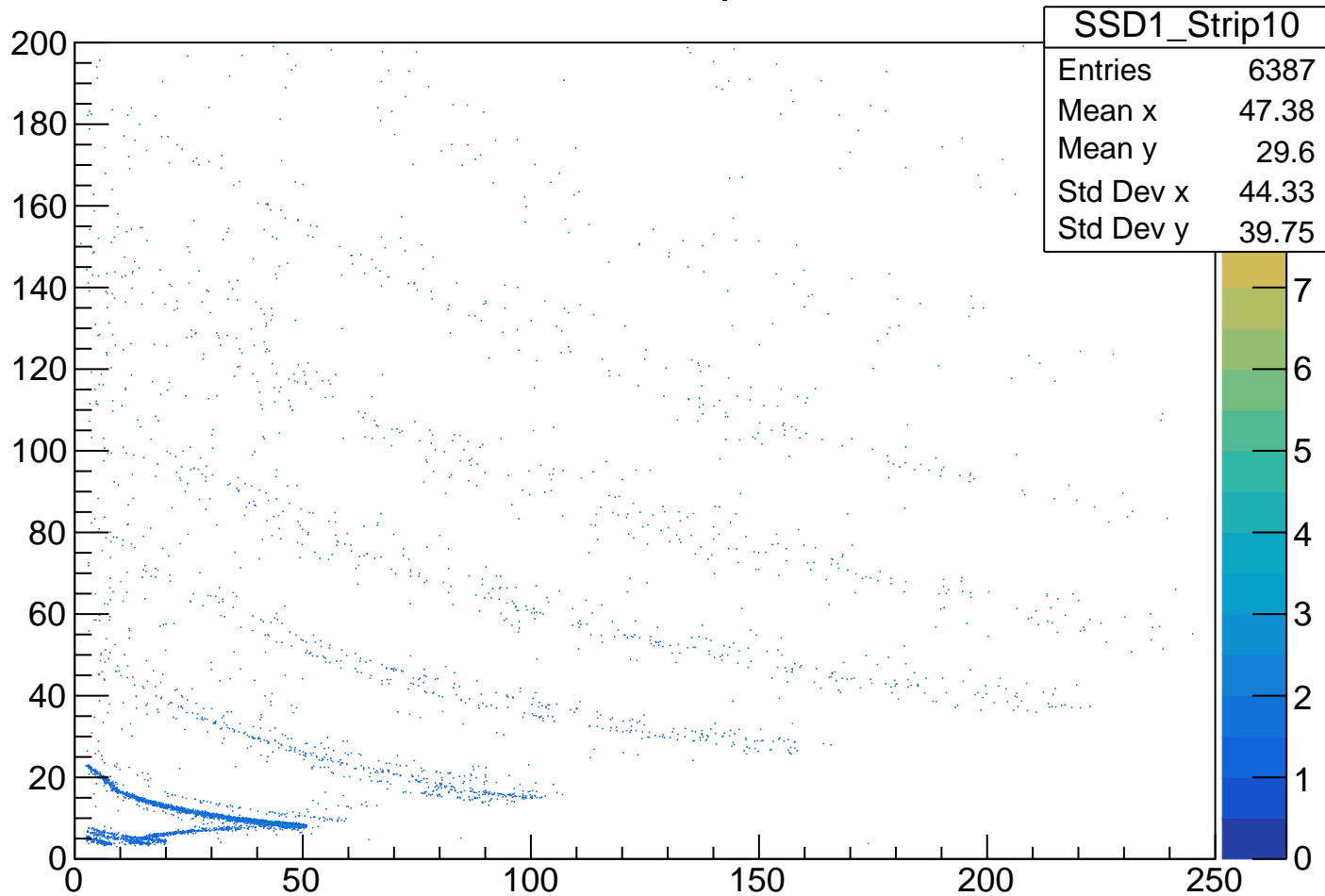
SSD1_Strip8



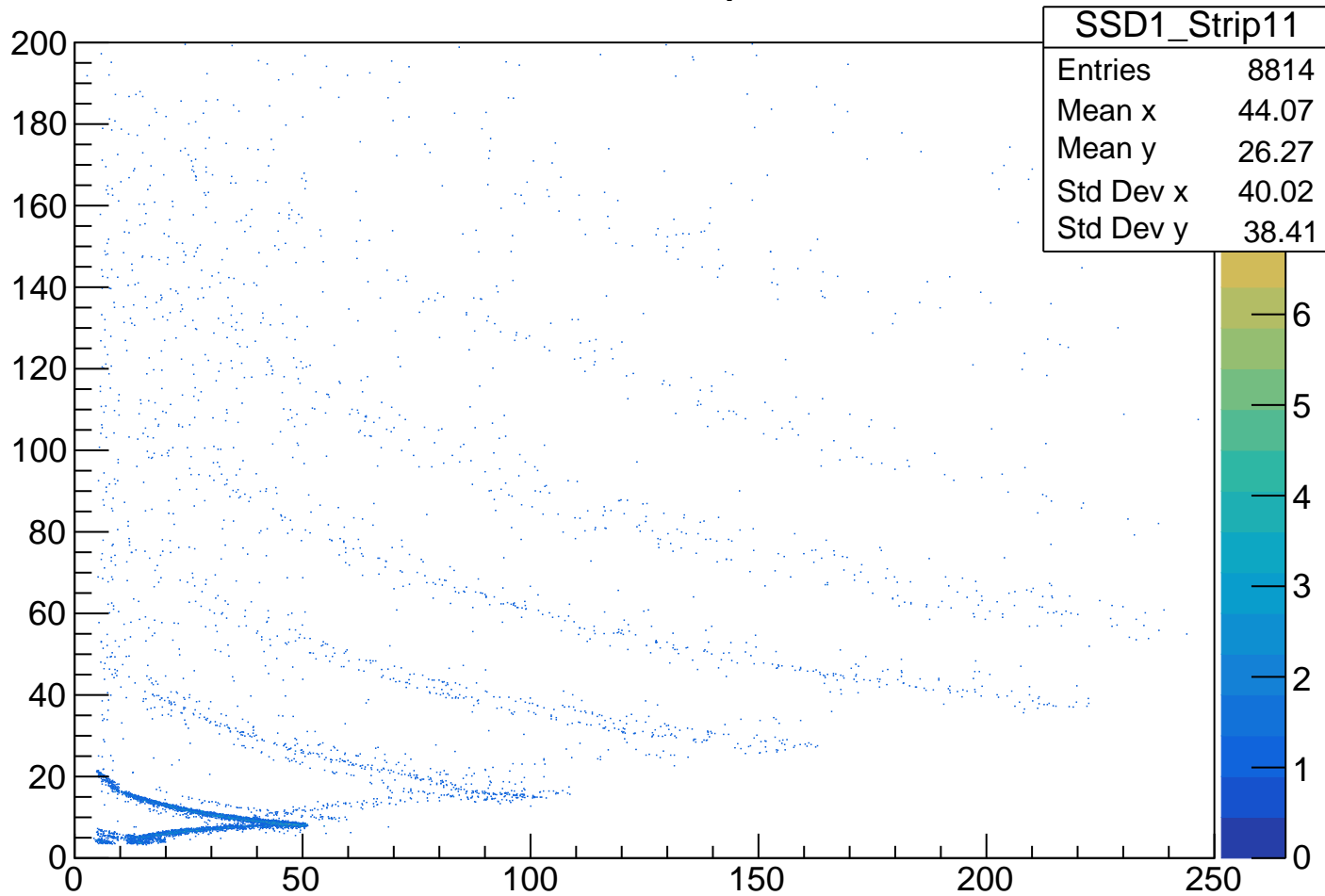
SSD1_Strip9



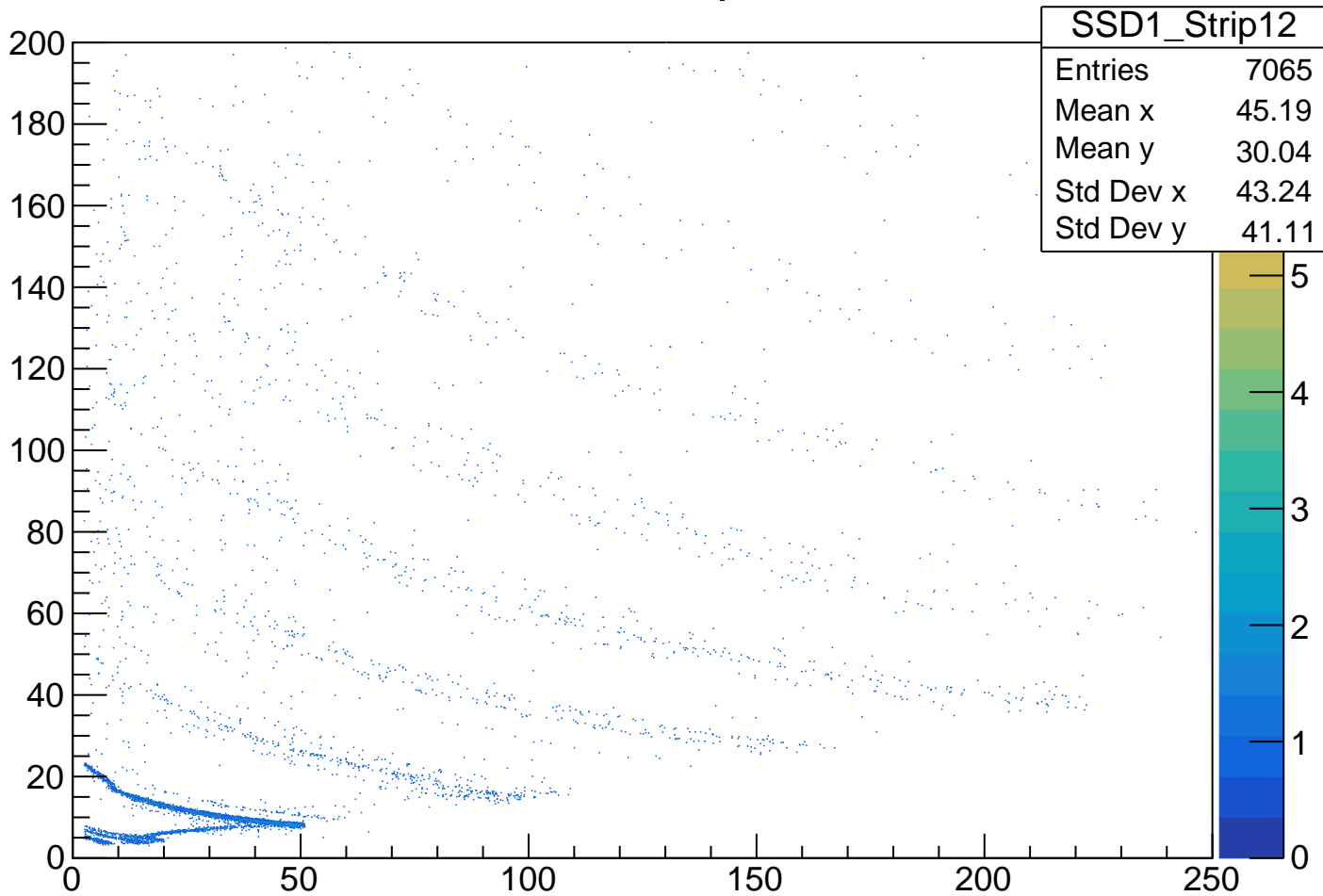
SSD1_Strip10



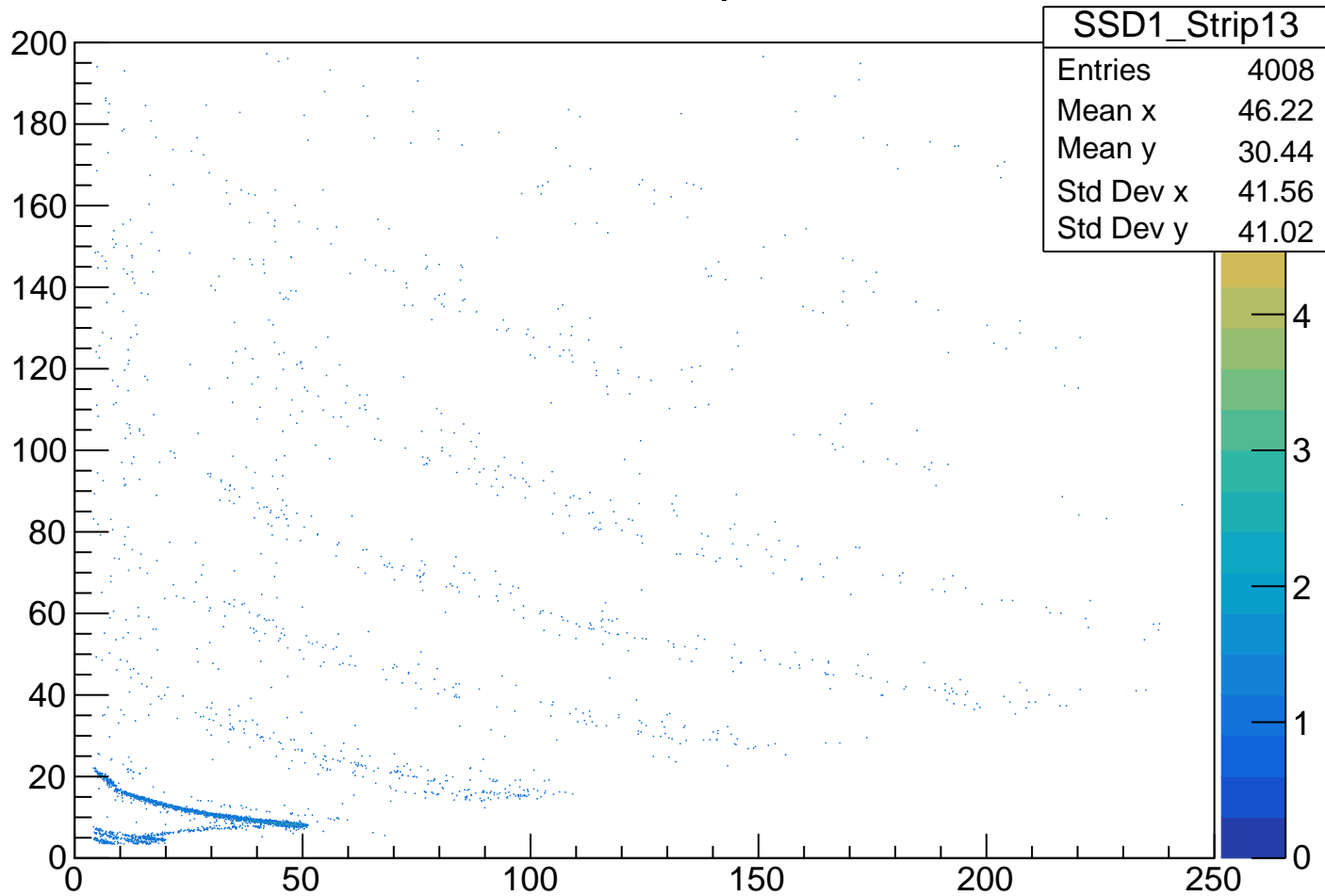
SSD1_Strip11



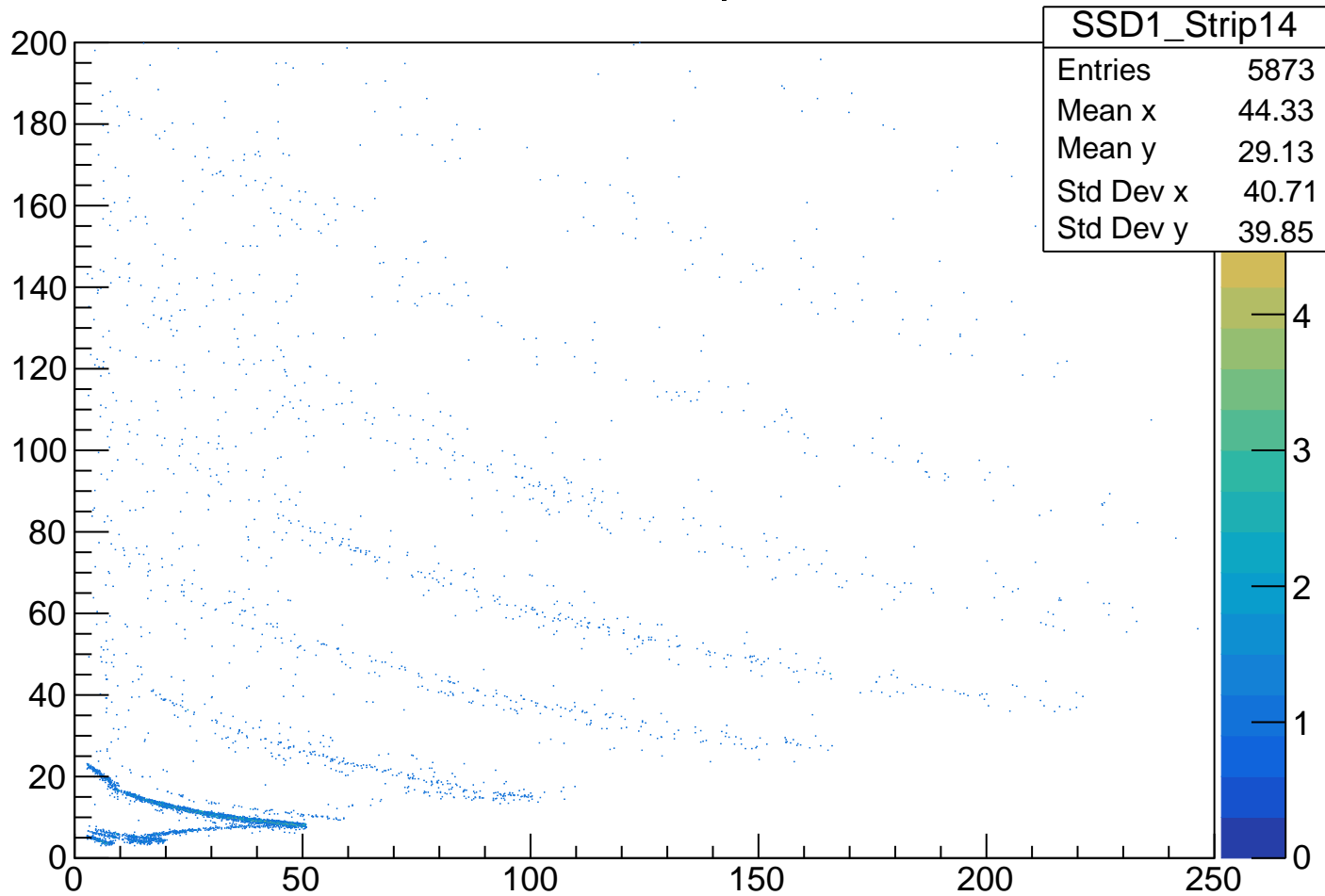
SSD1_Strip12



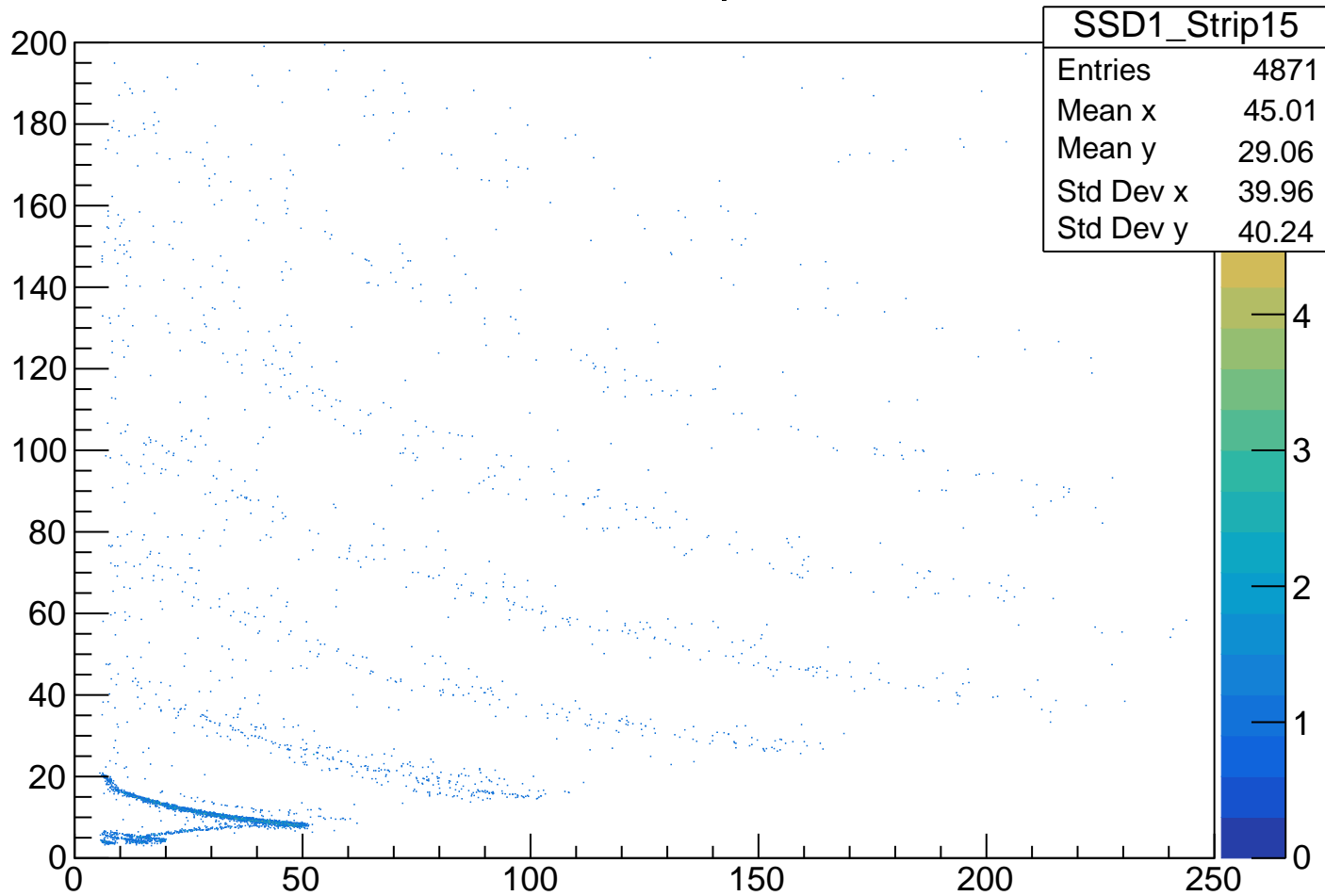
SSD1_Strip13



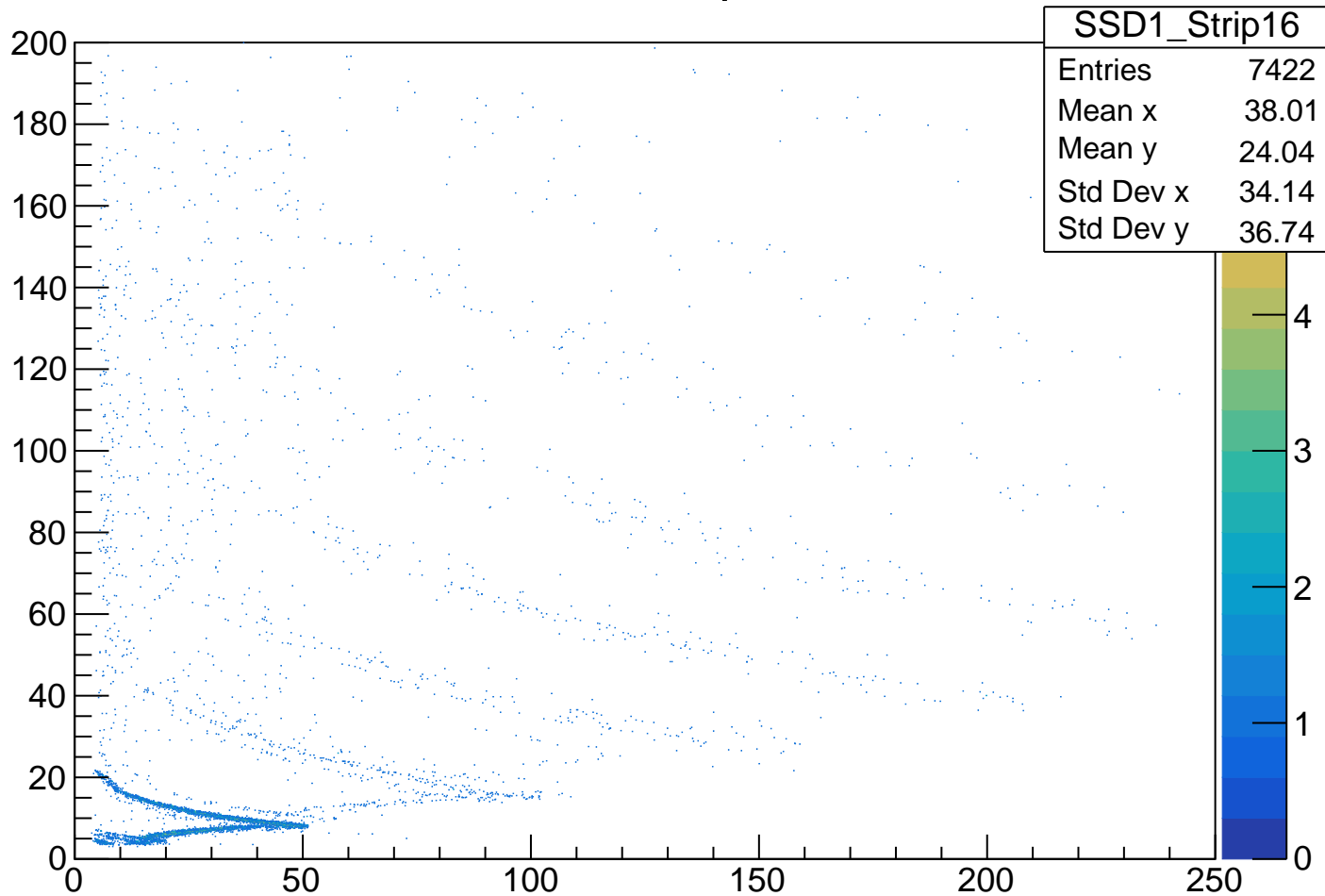
SSD1_Strip14



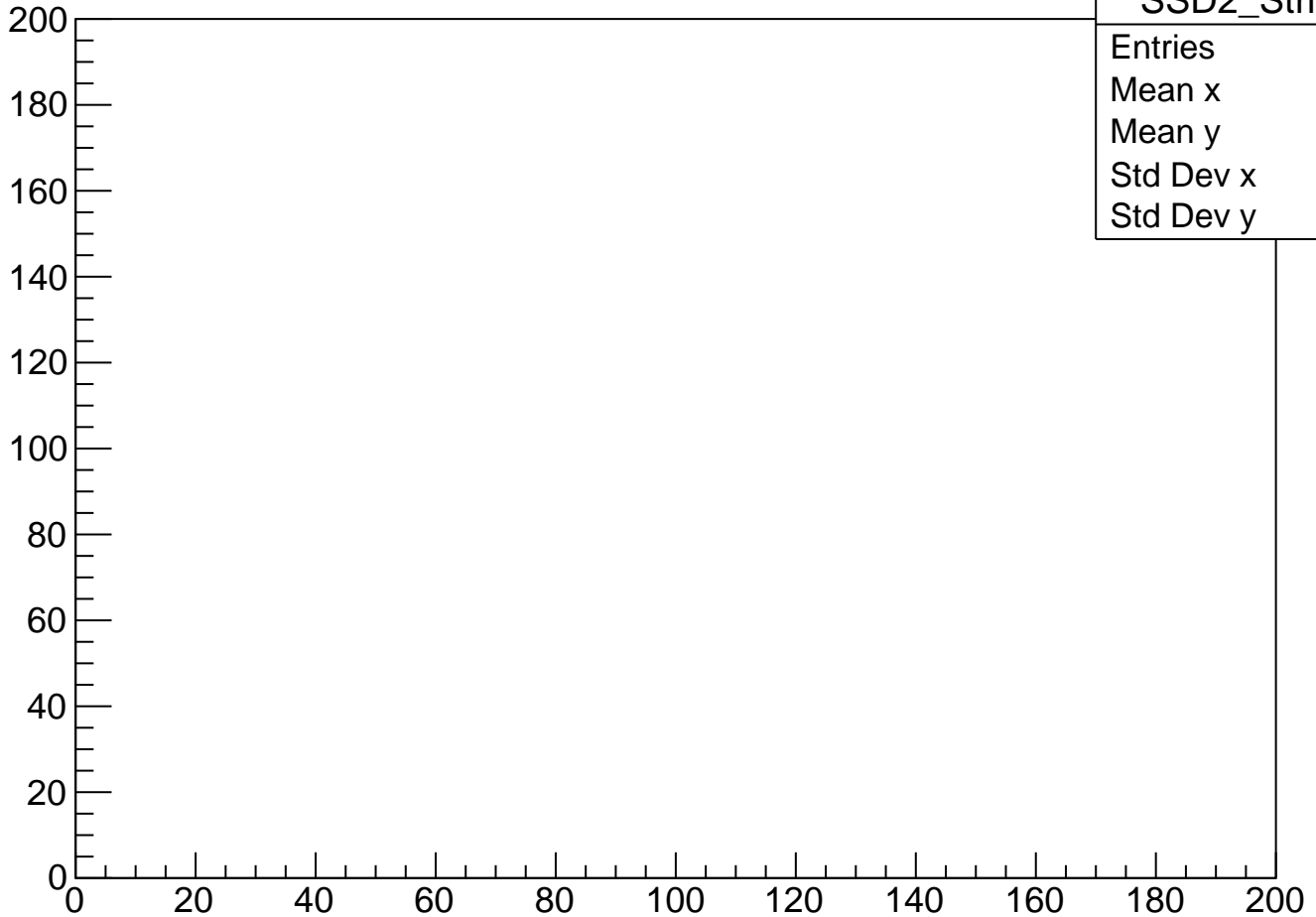
SSD1_Strip15



SSD1_Strip16

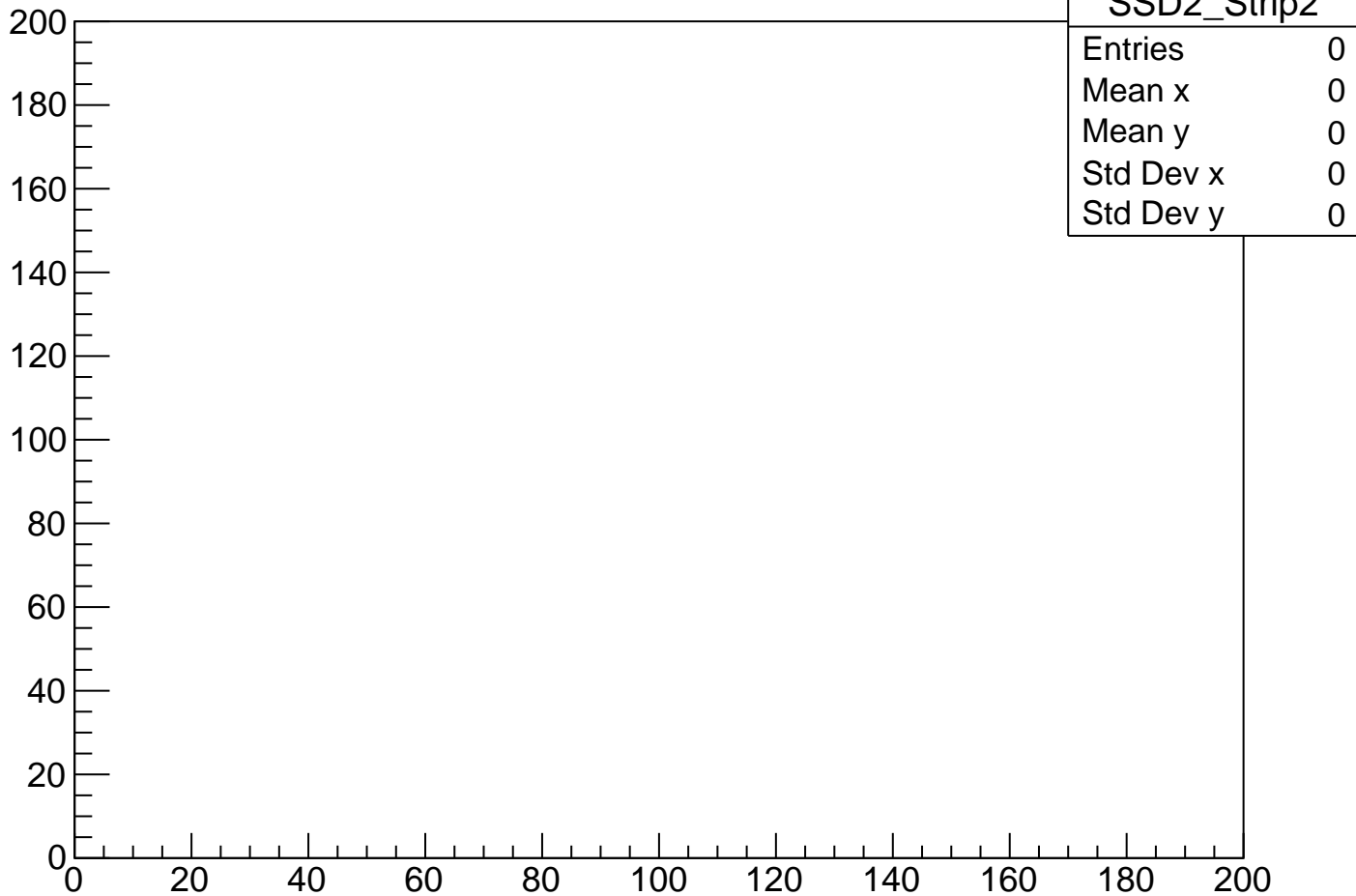


SSD2_Strip1

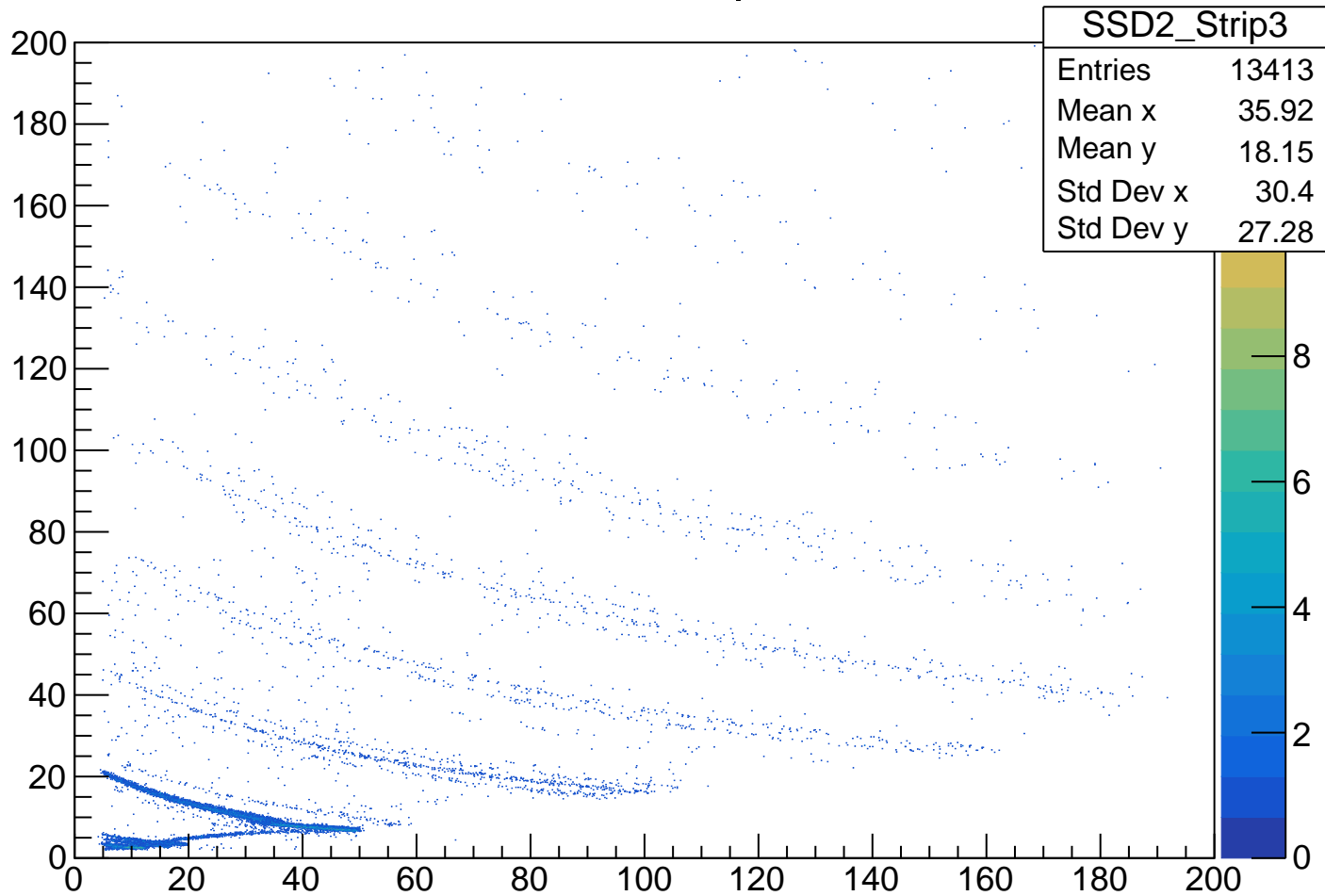


SSD2_Strip1	
Entries	0
Mean x	0
Mean y	0
Std Dev x	0
Std Dev y	0

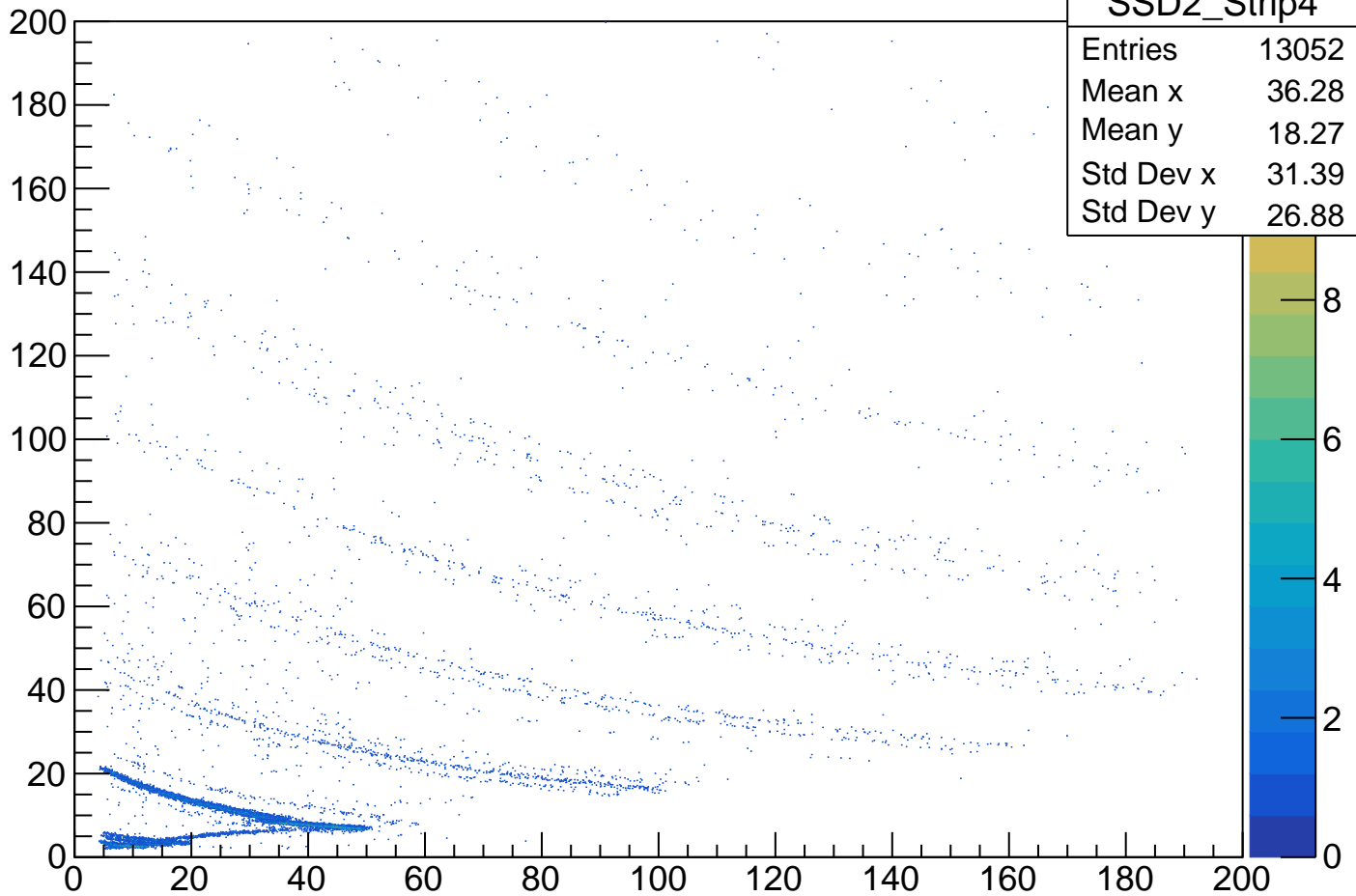
SSD2_Strip2



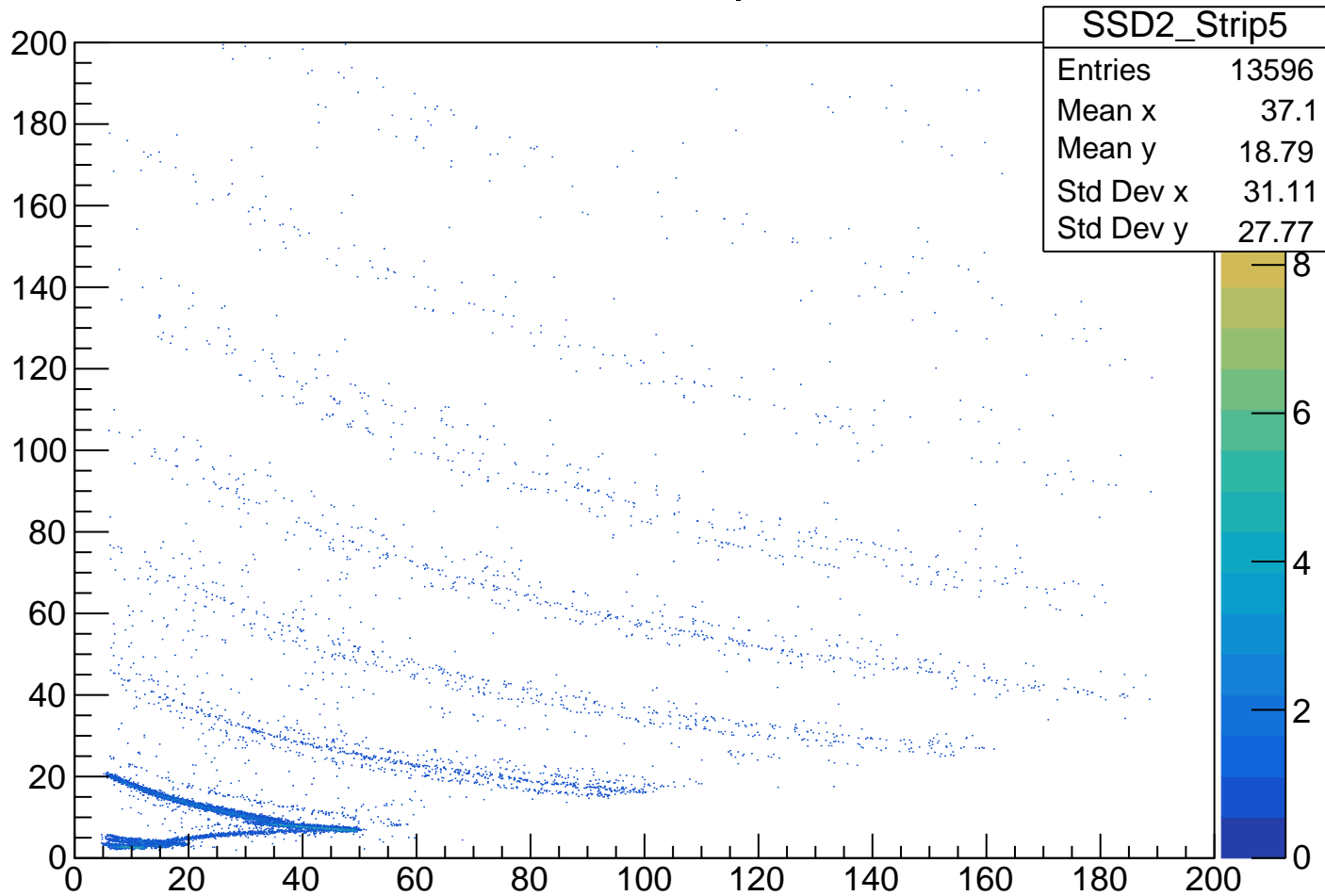
SSD2_Strip3



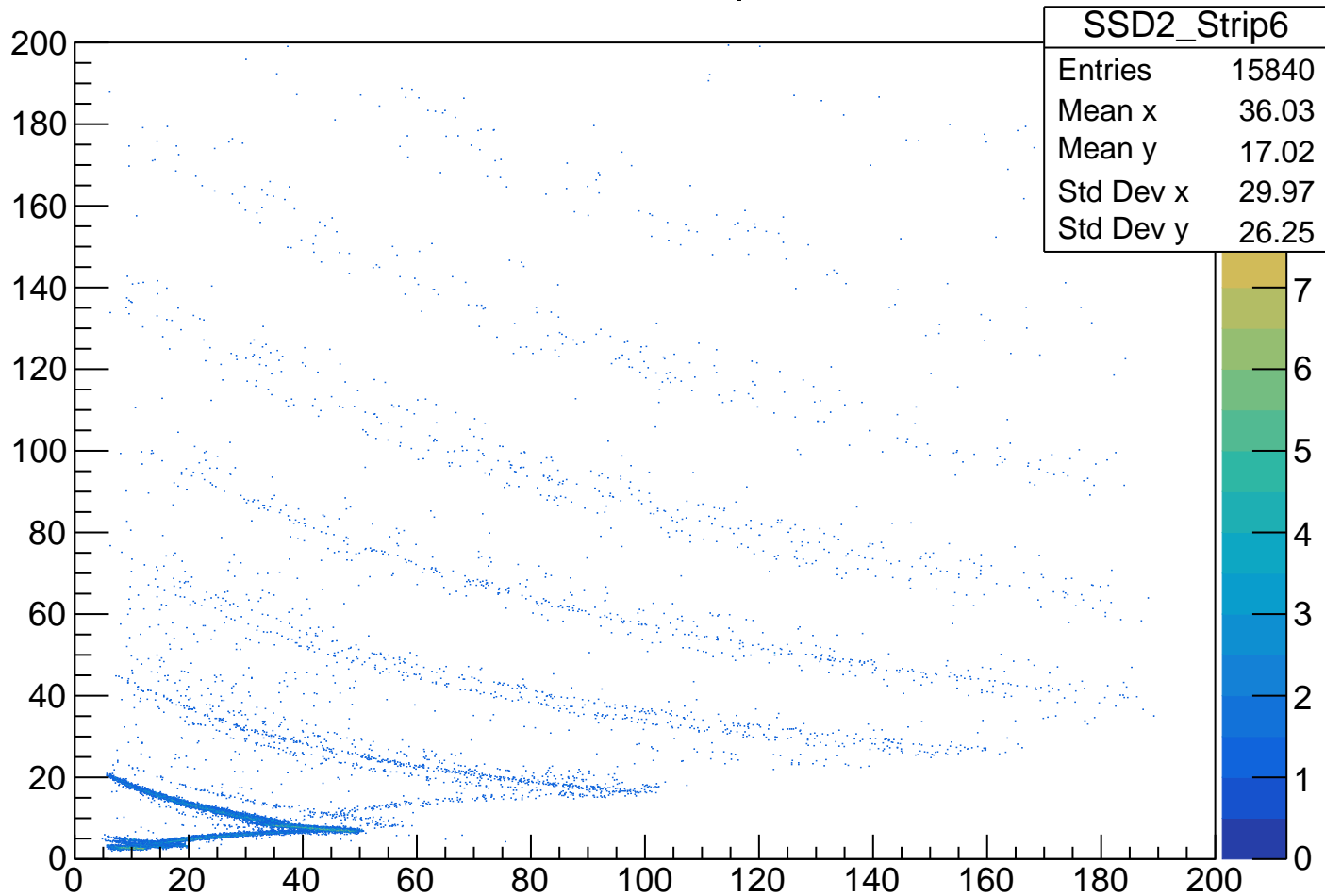
SSD2_Strip4



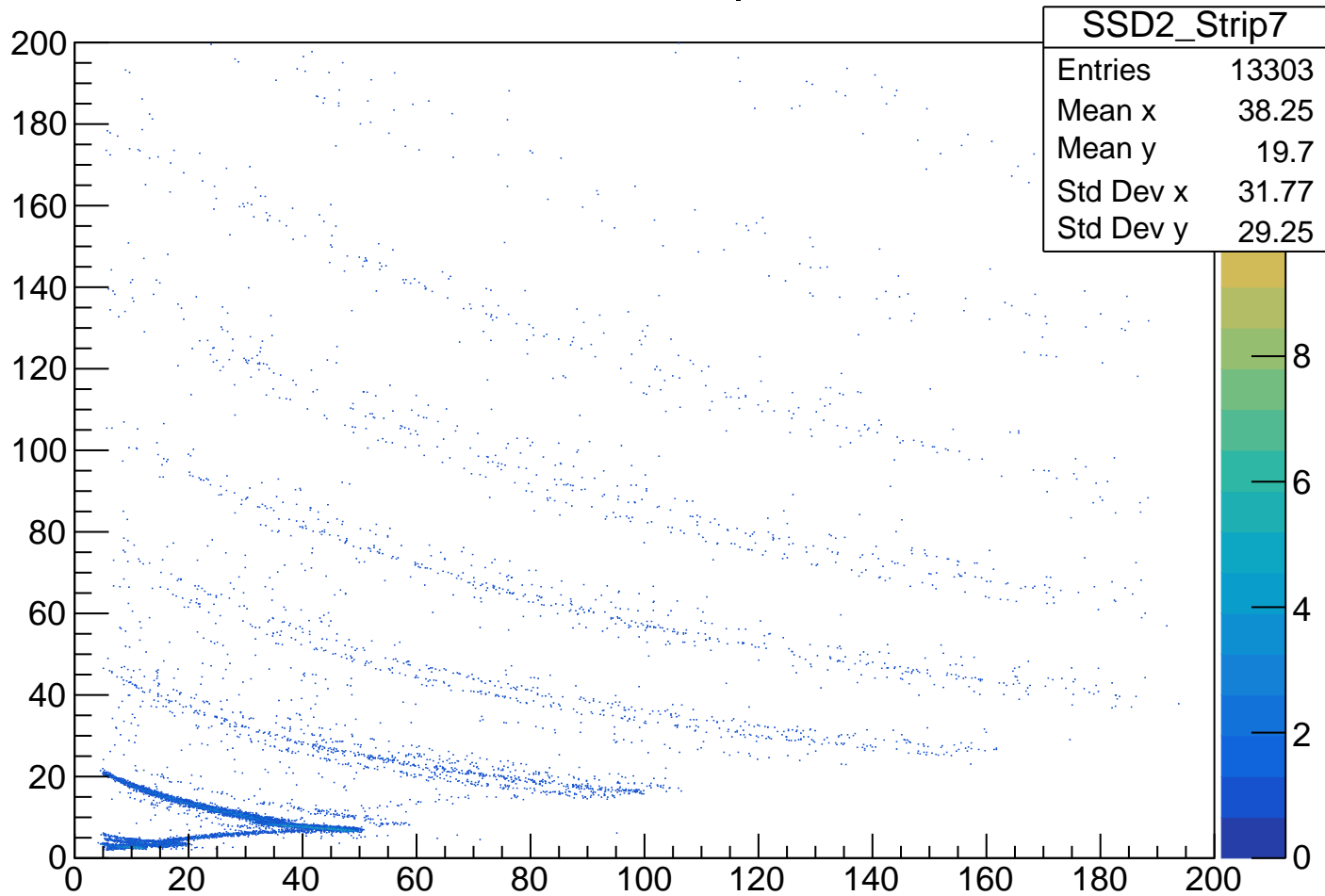
SSD2_Strip5



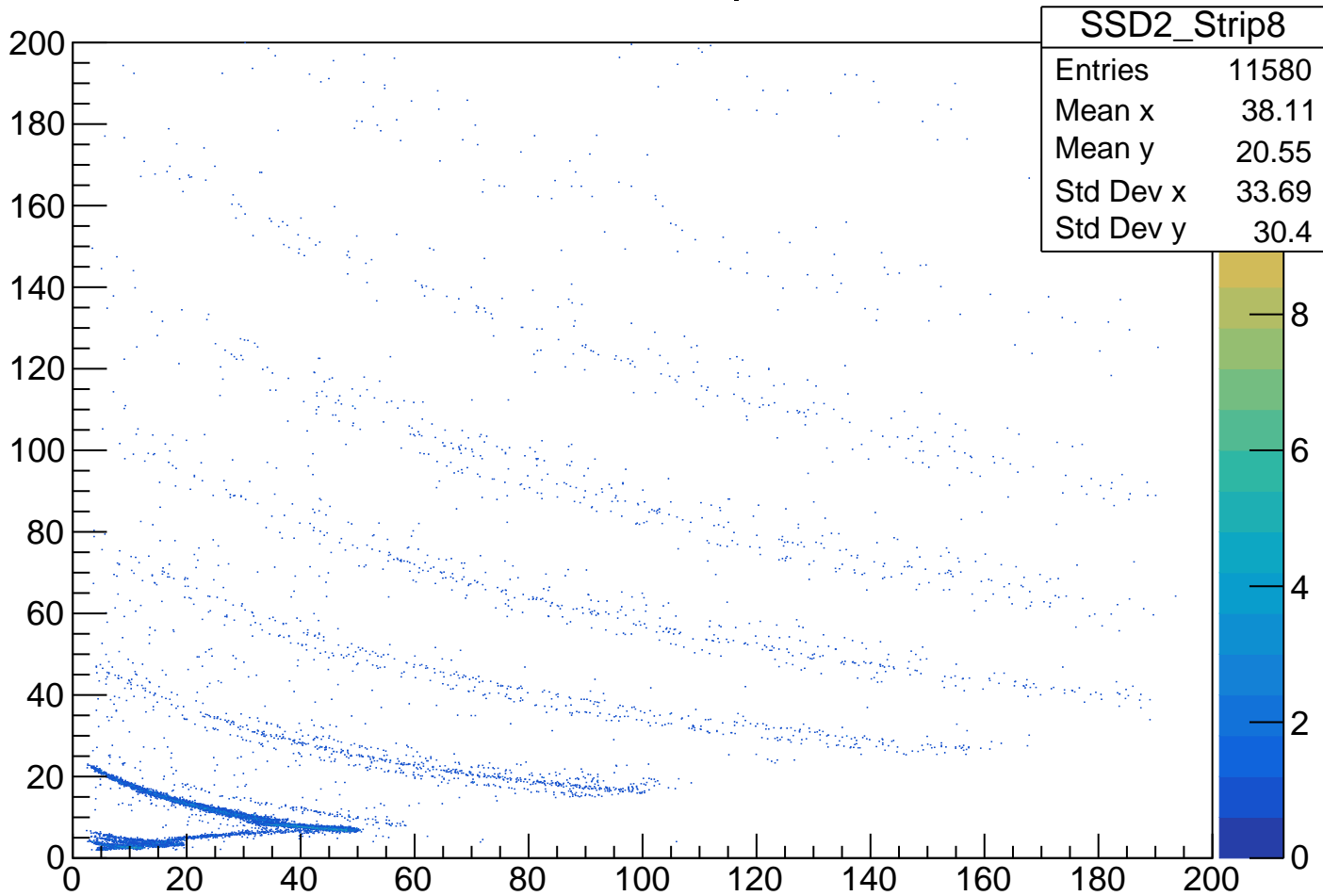
SSD2_Strip6



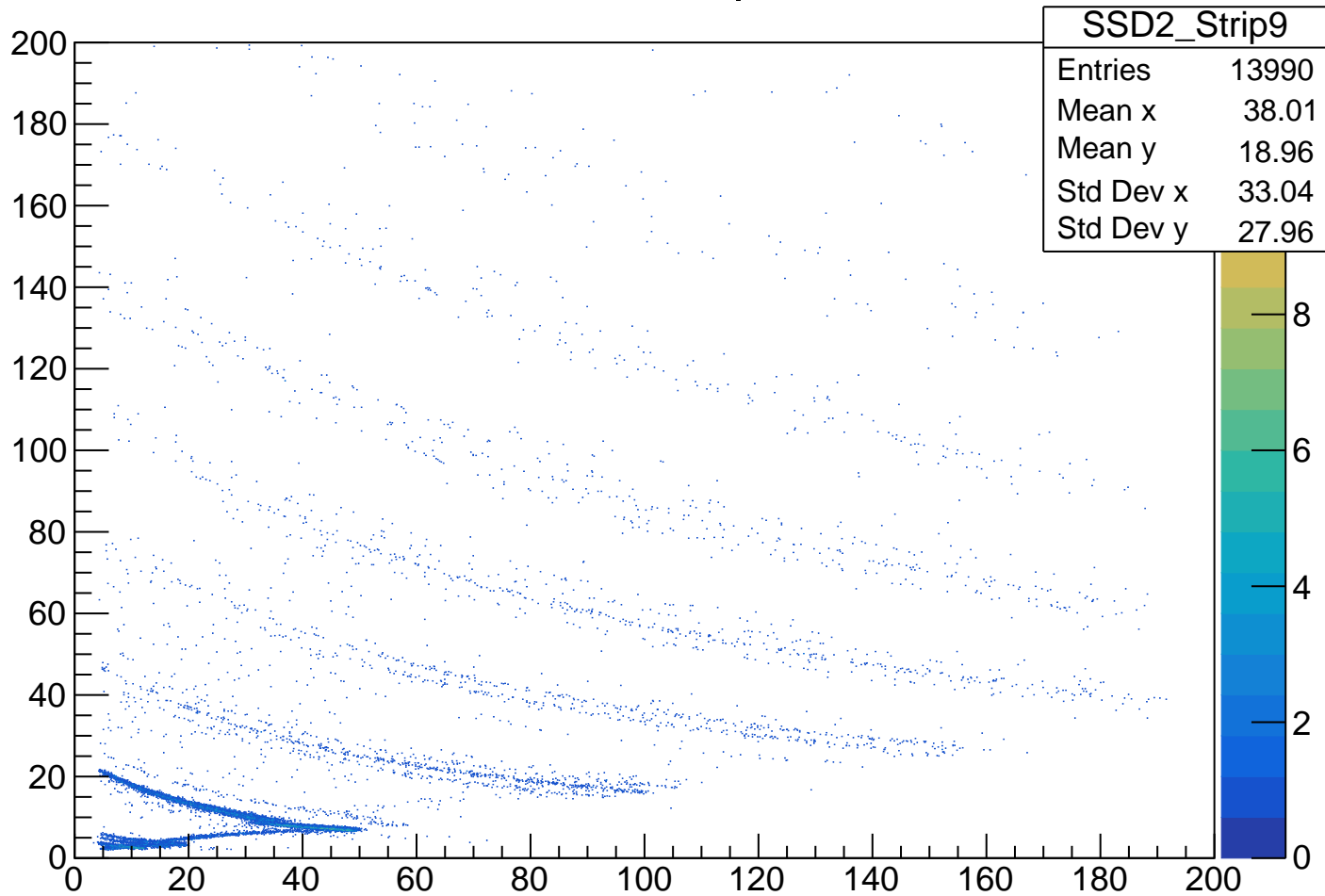
SSD2_Strip7



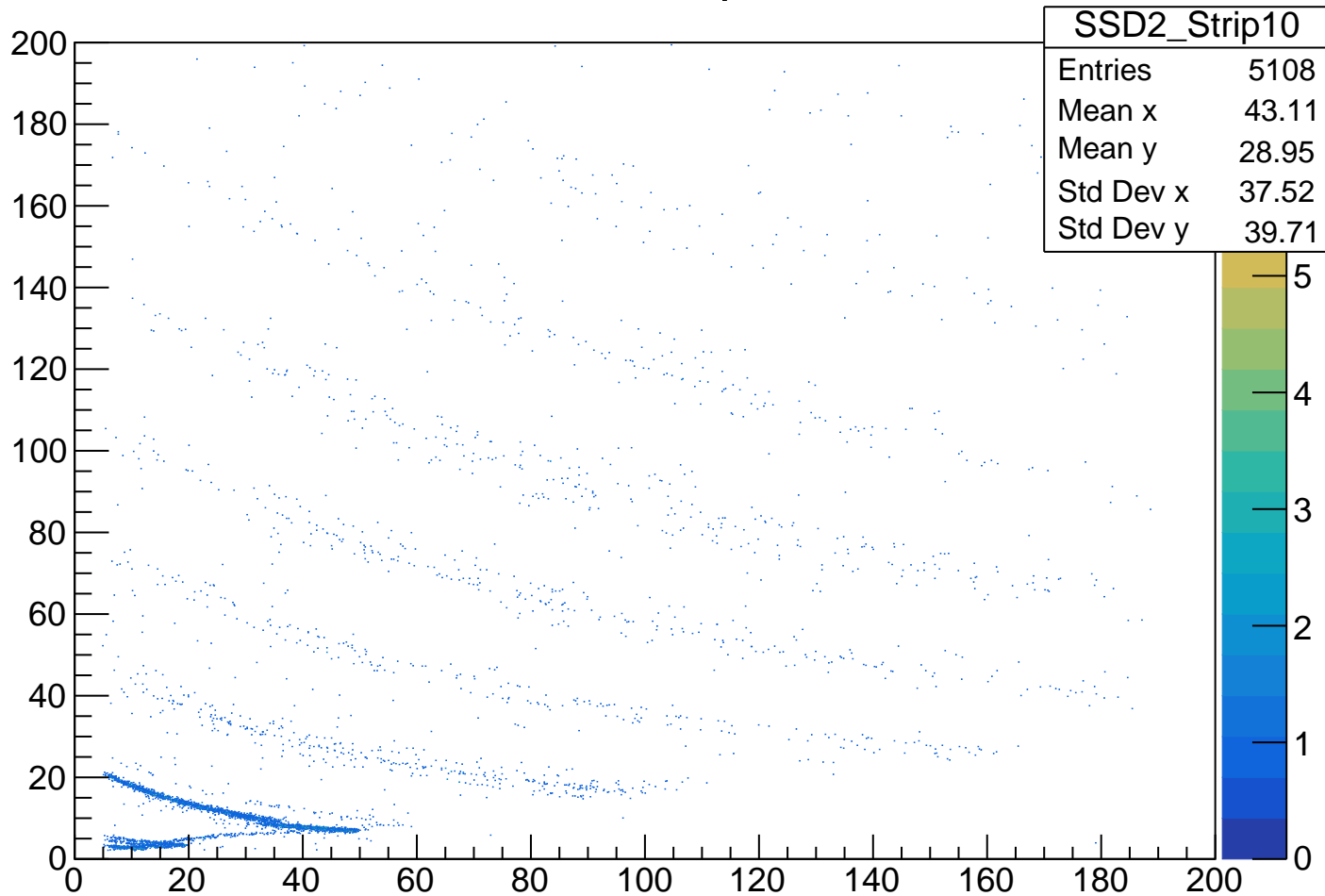
SSD2_Strip8



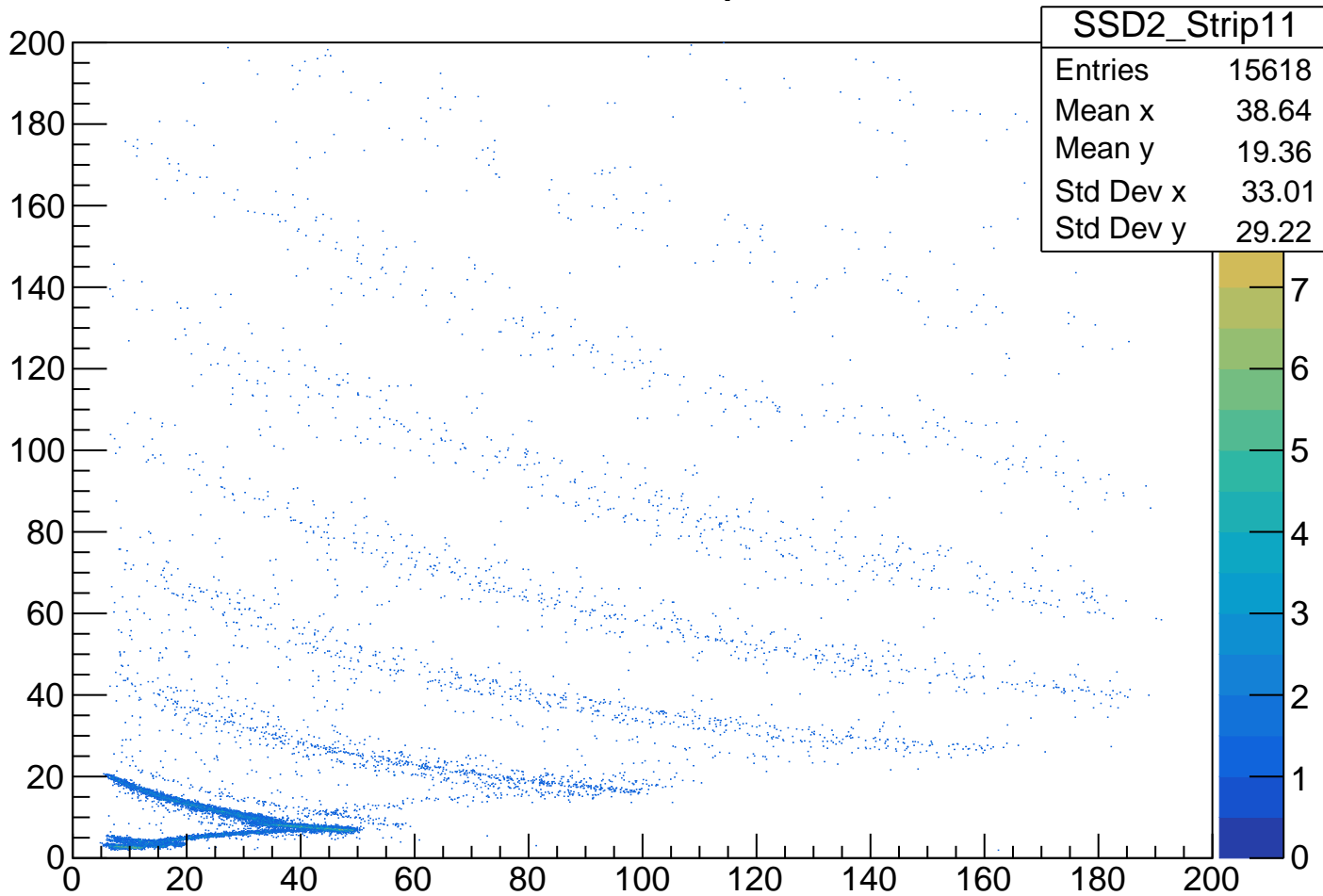
SSD2_Strip9



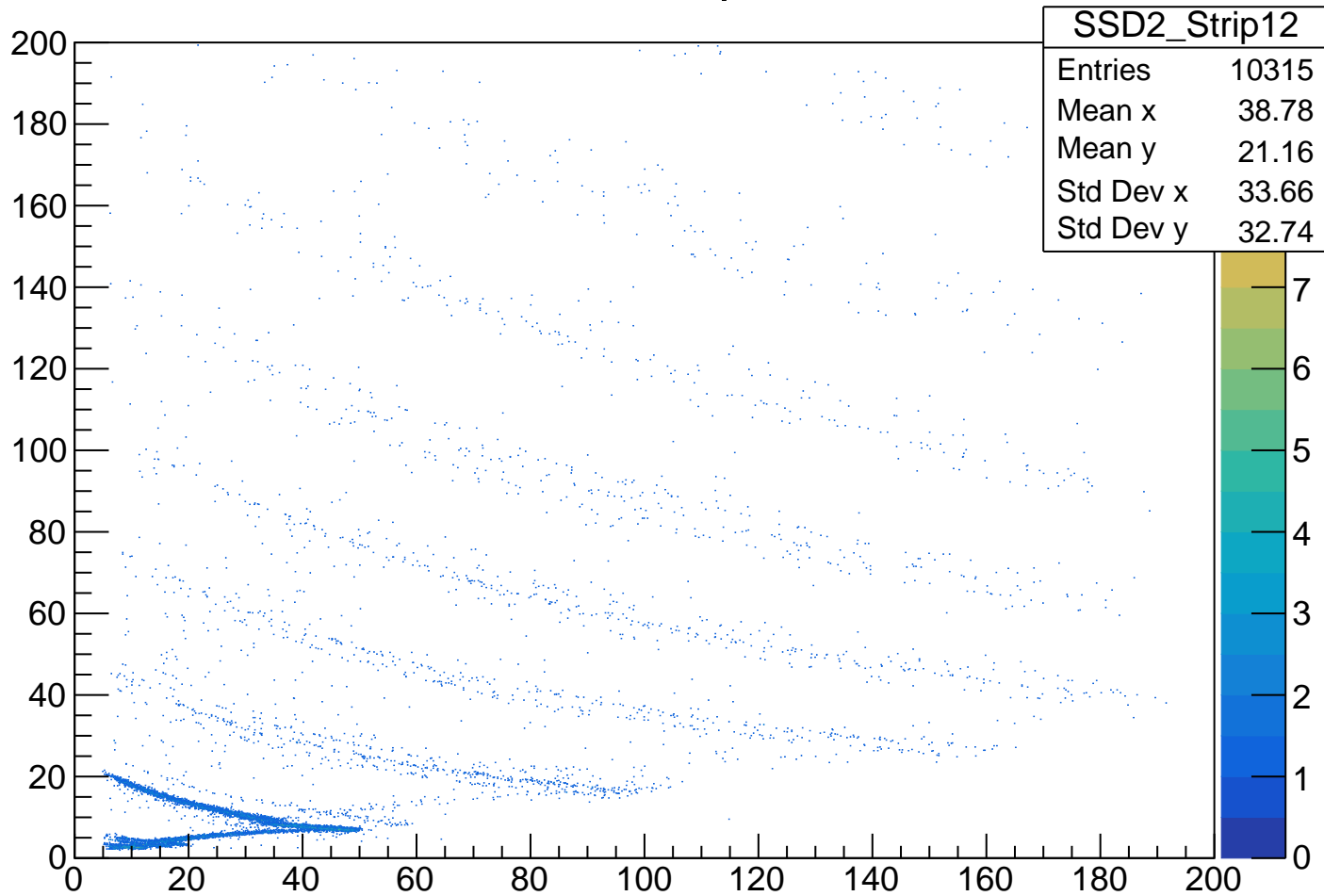
SSD2_Strip10



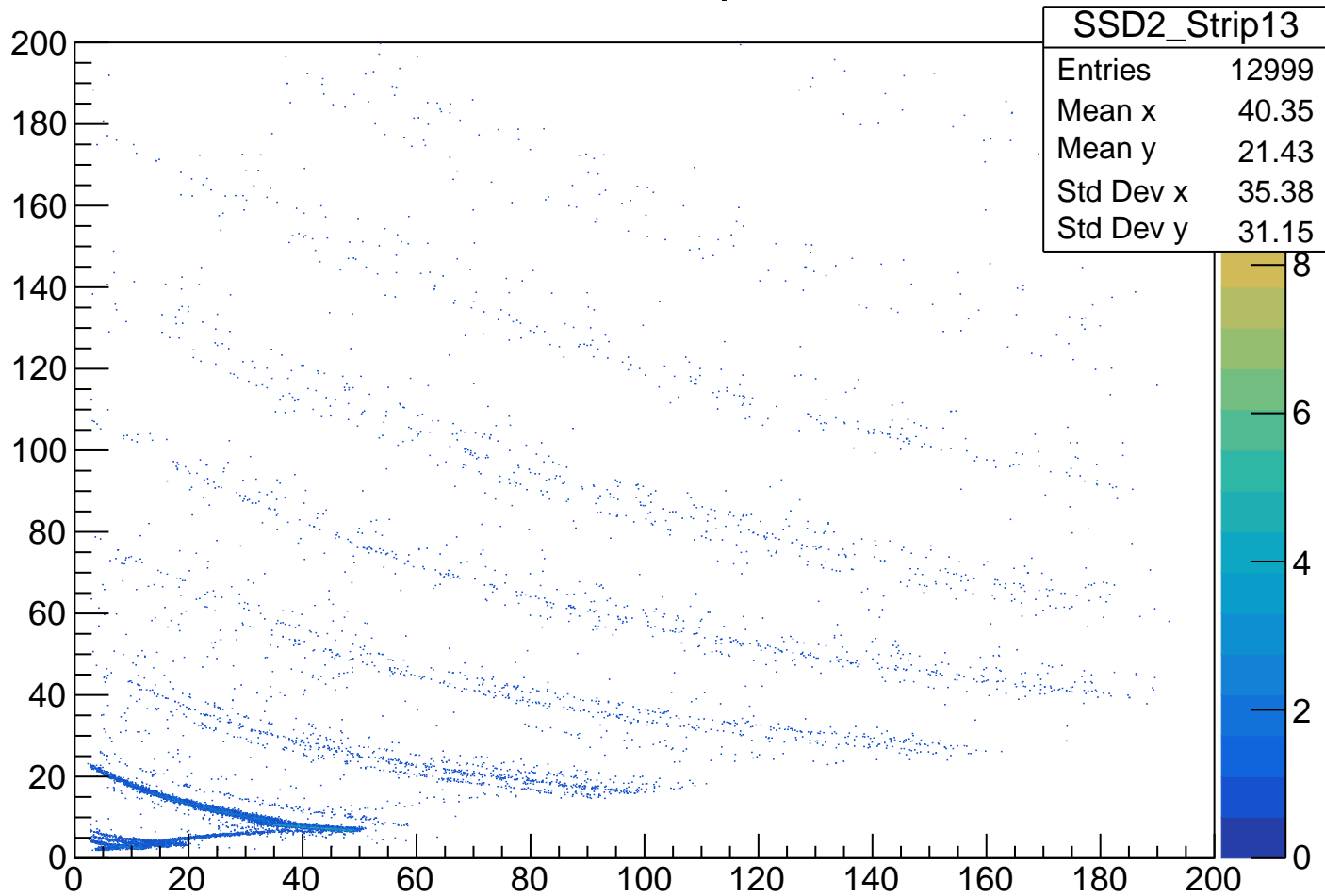
SSD2_Strip11



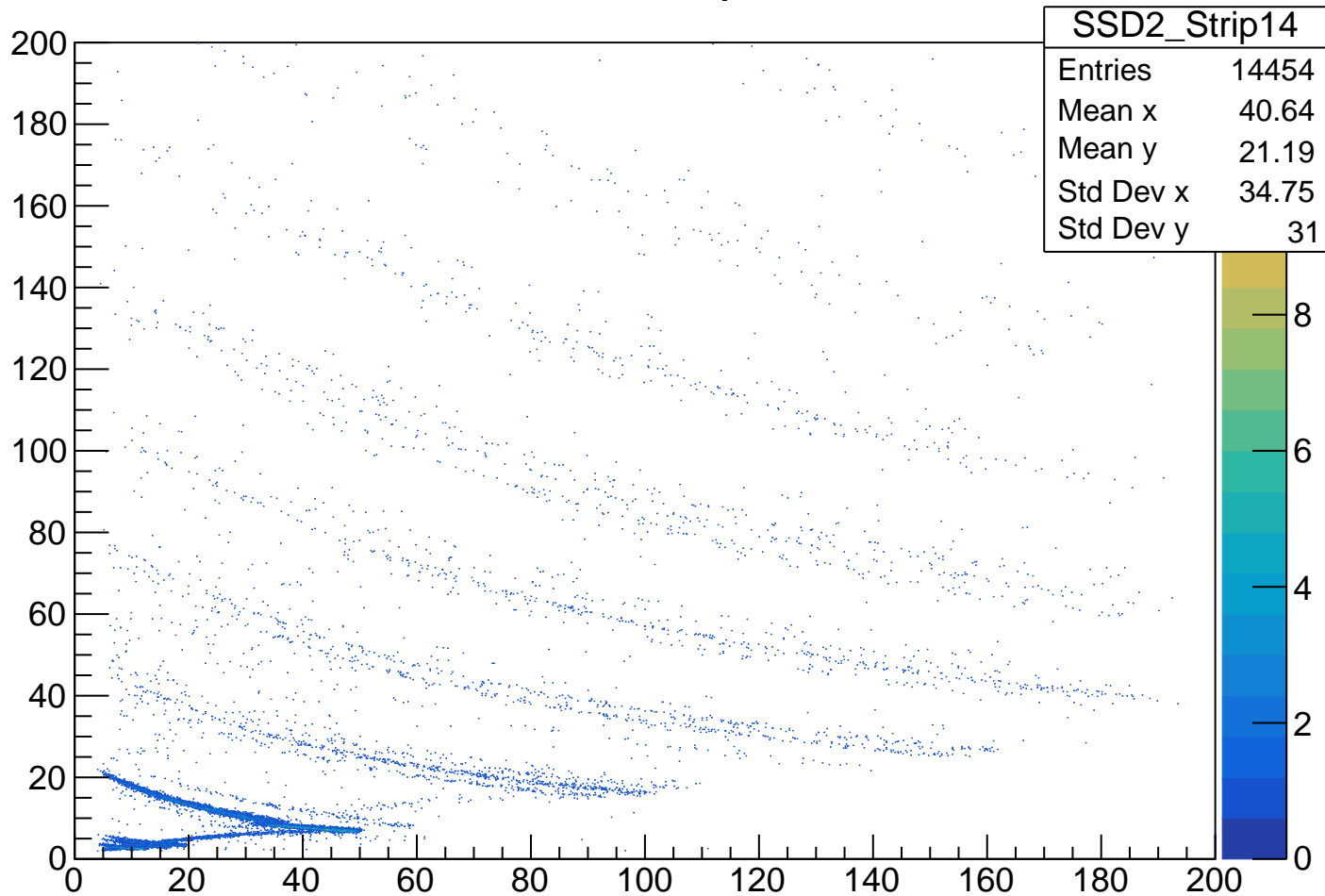
SSD2_Strip12



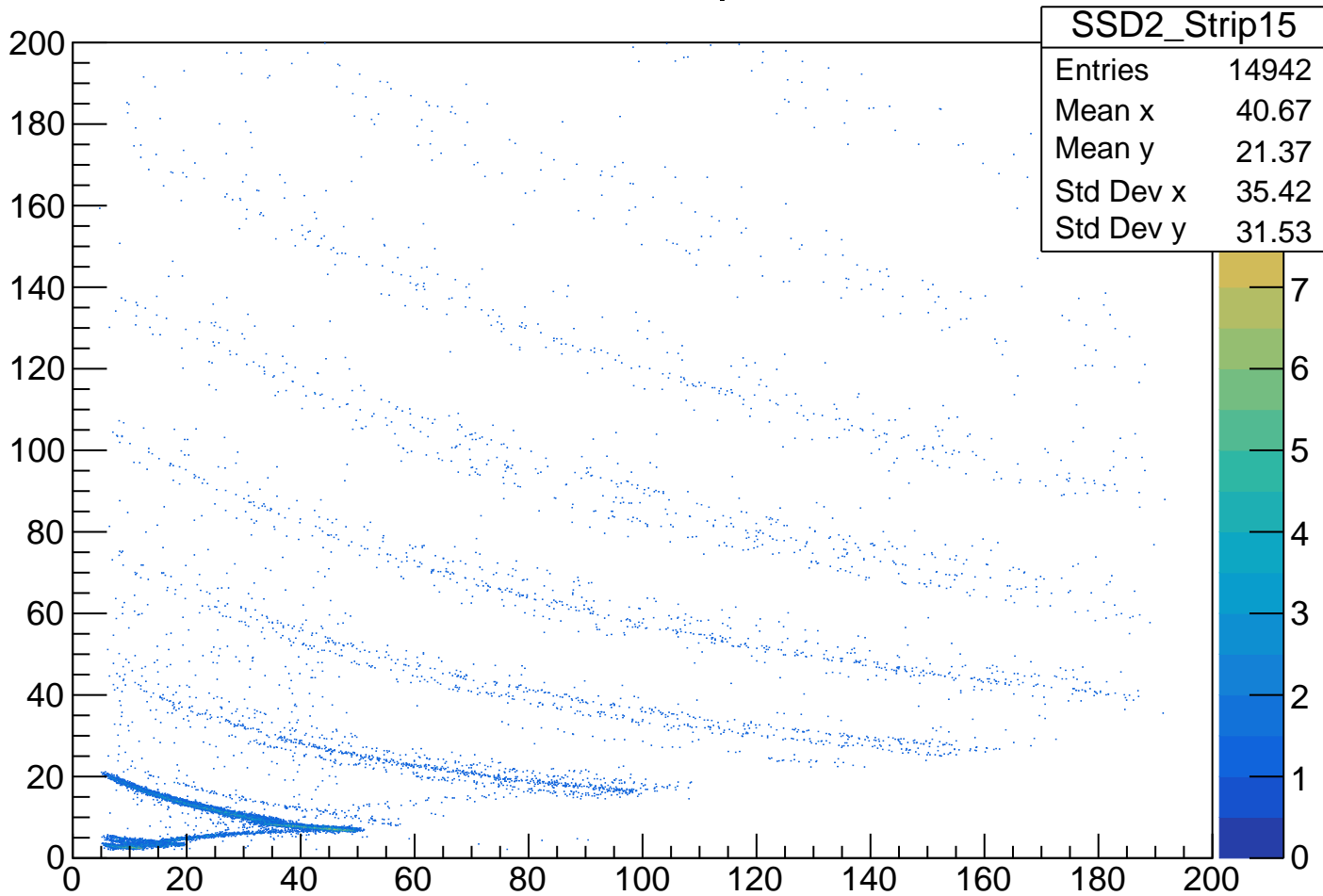
SSD2_Strip13



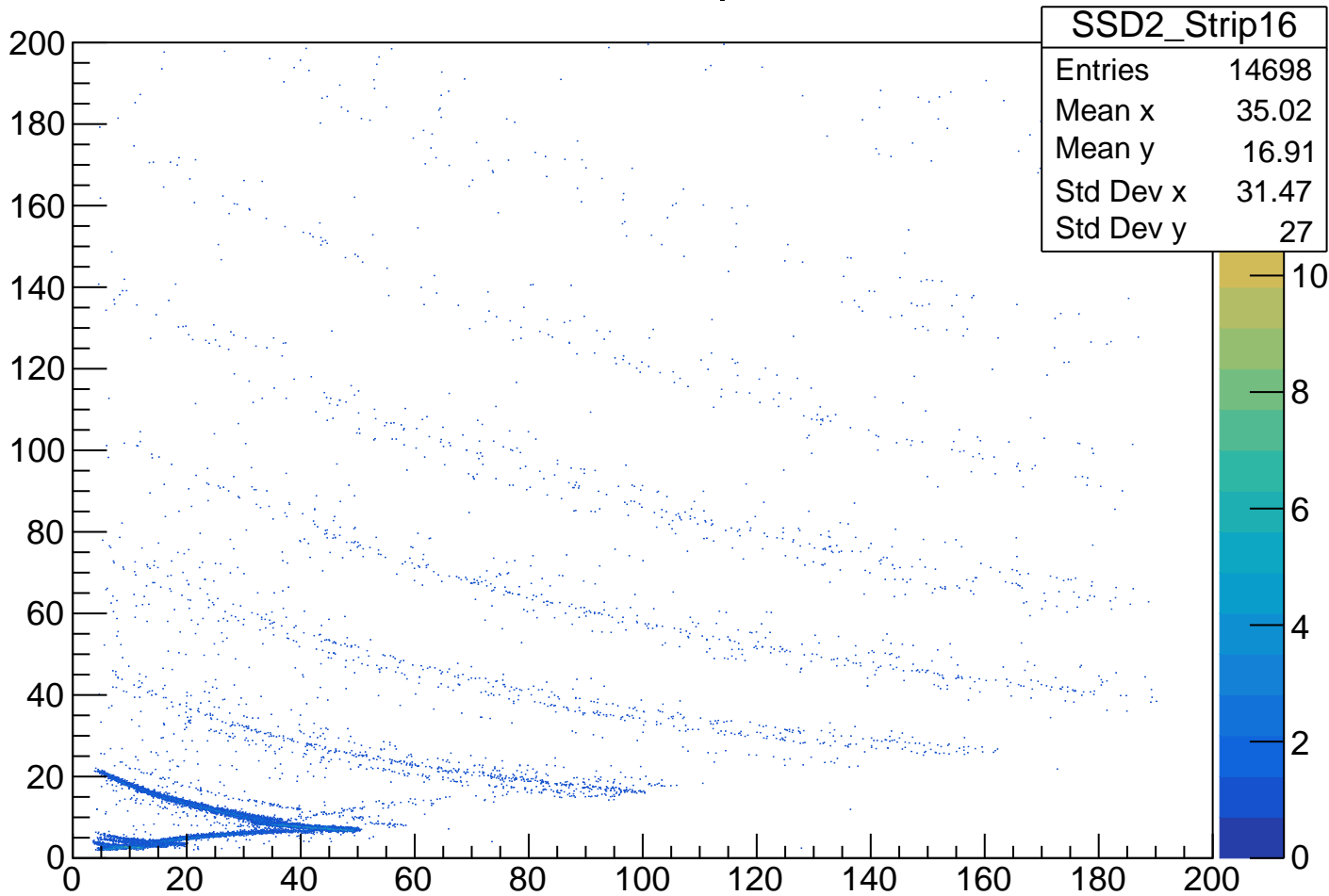
SSD2_Strip14



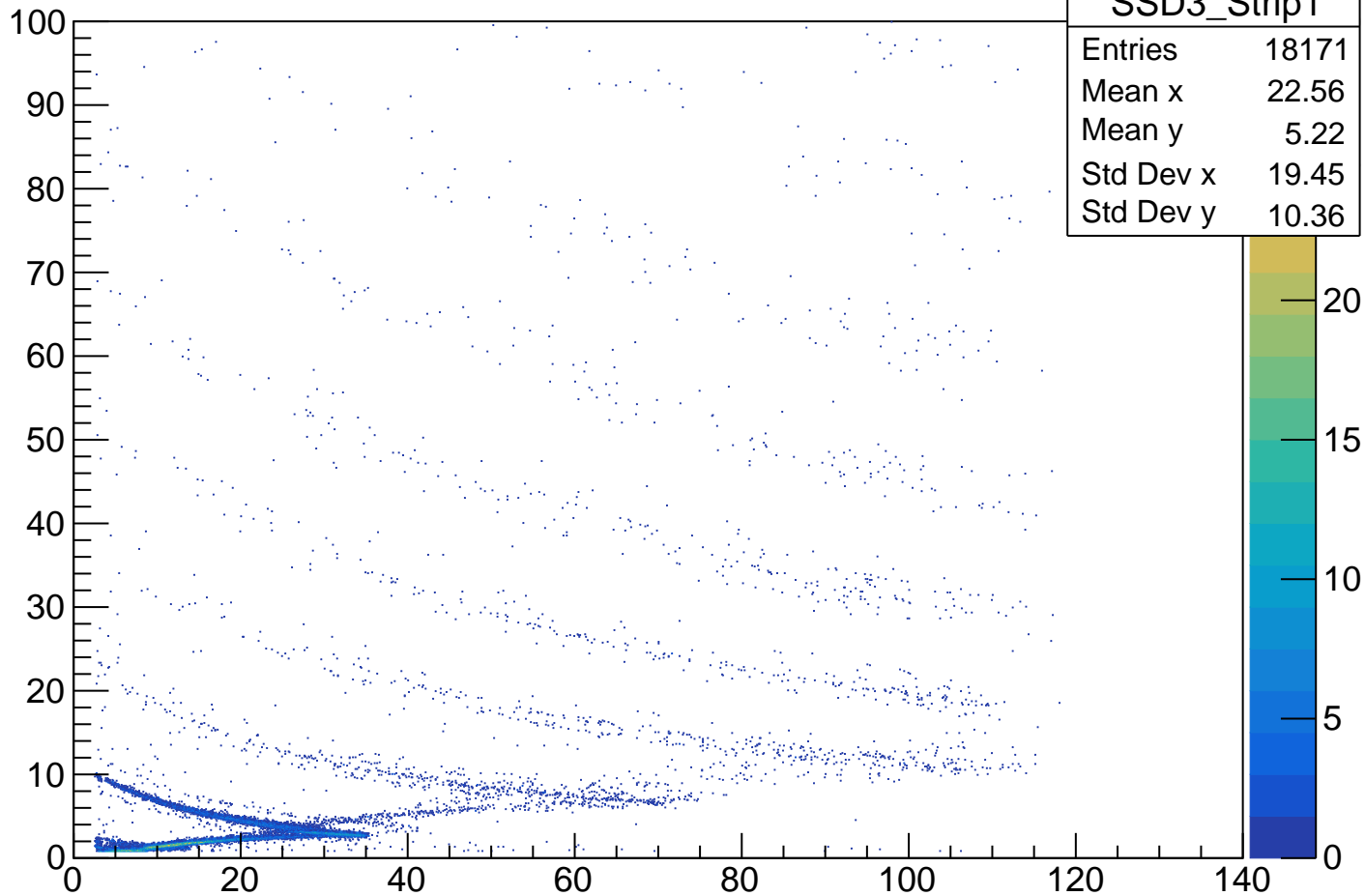
SSD2_Strip15



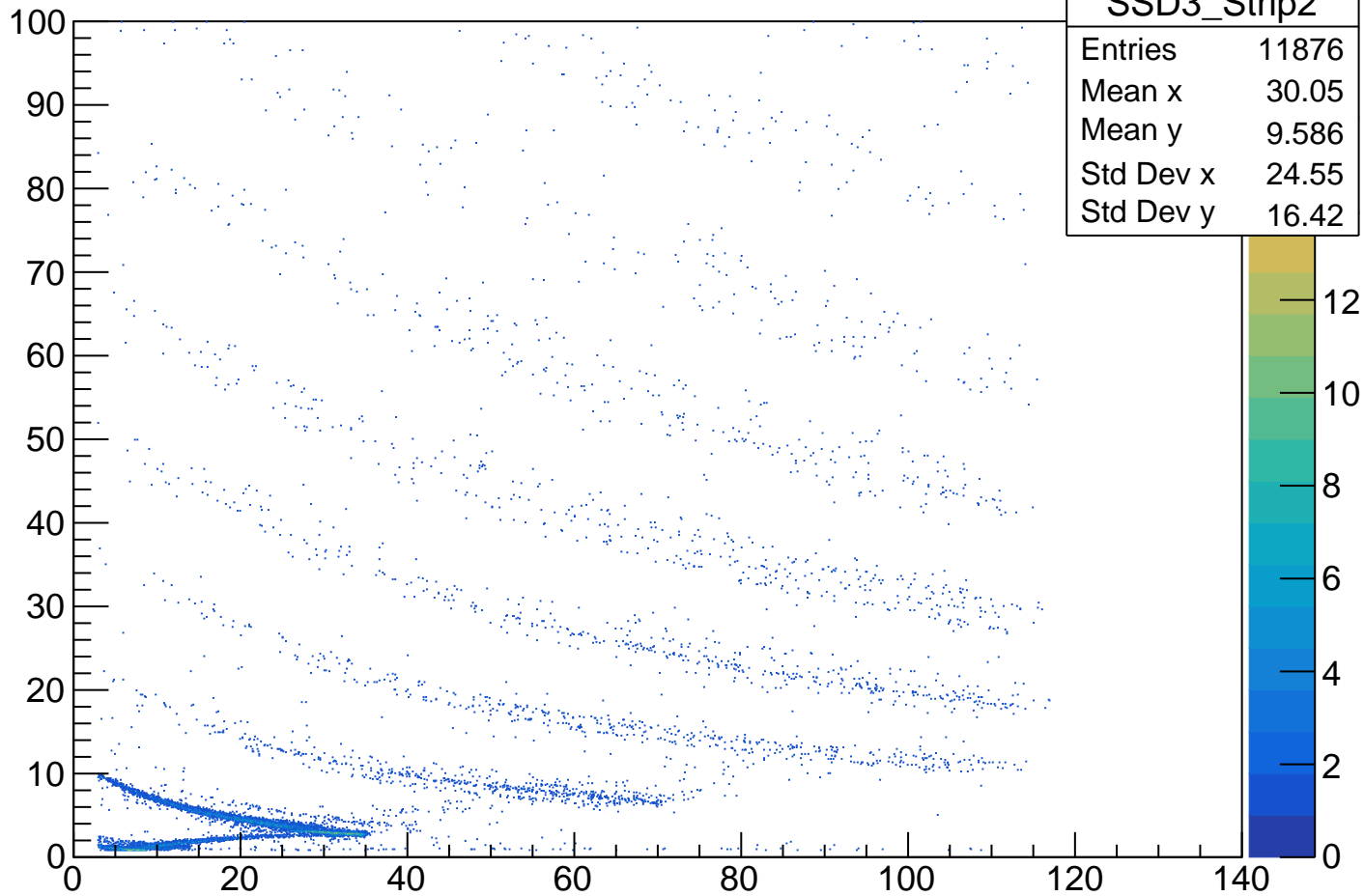
SSD2_Strip16



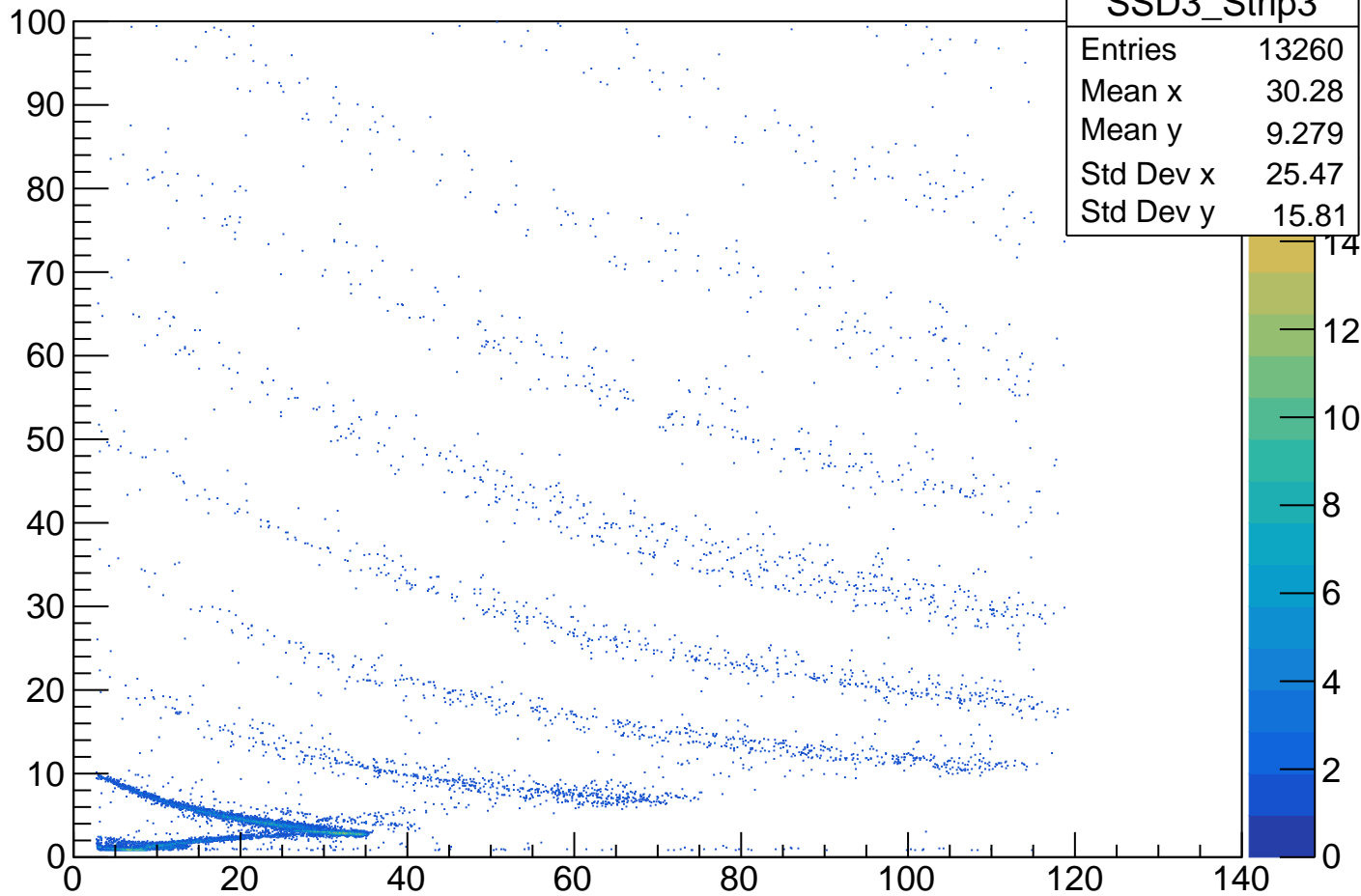
SSD3_Strip1



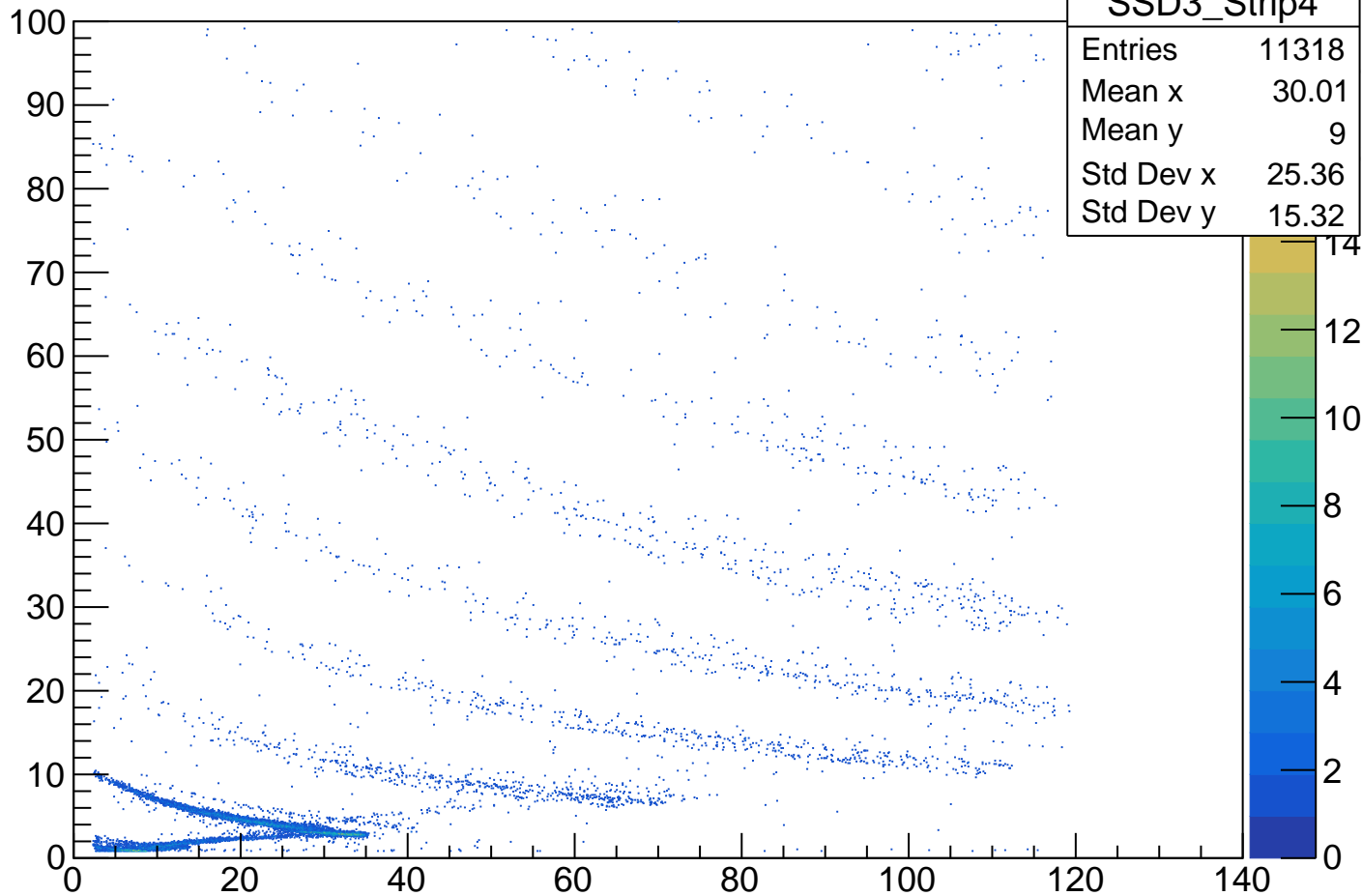
SSD3_Strip2



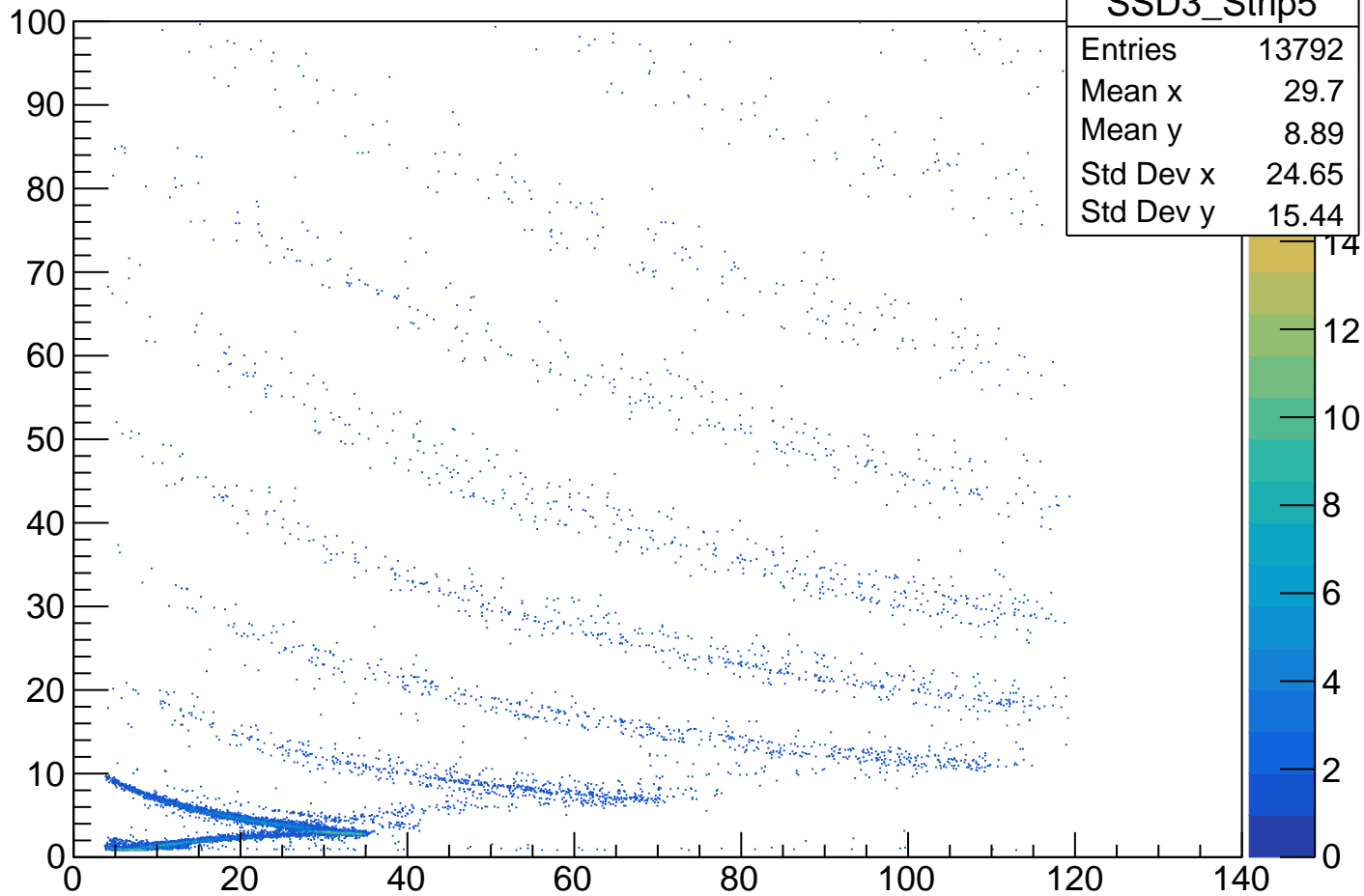
SSD3_Strip3



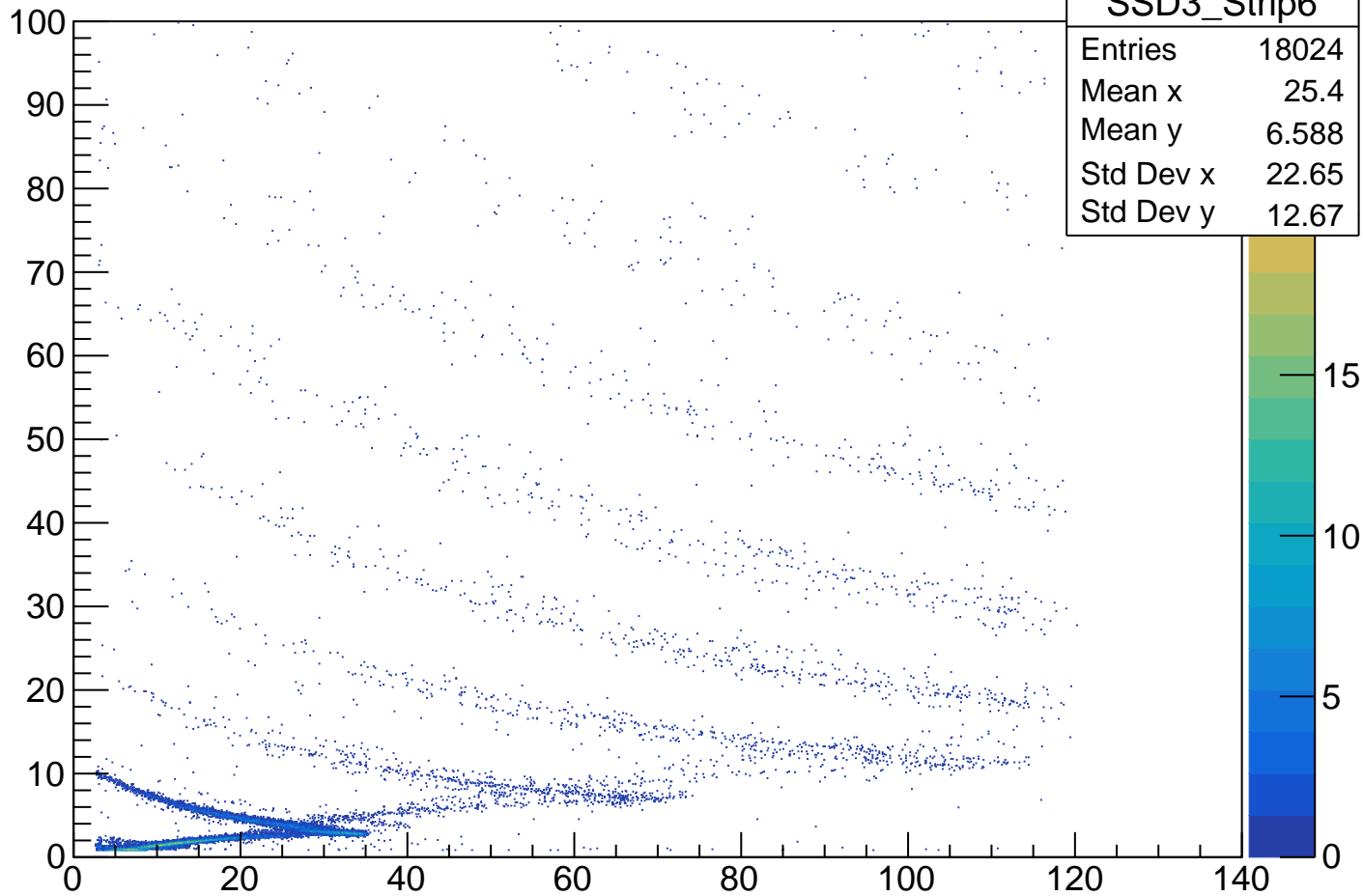
SSD3_Strip4



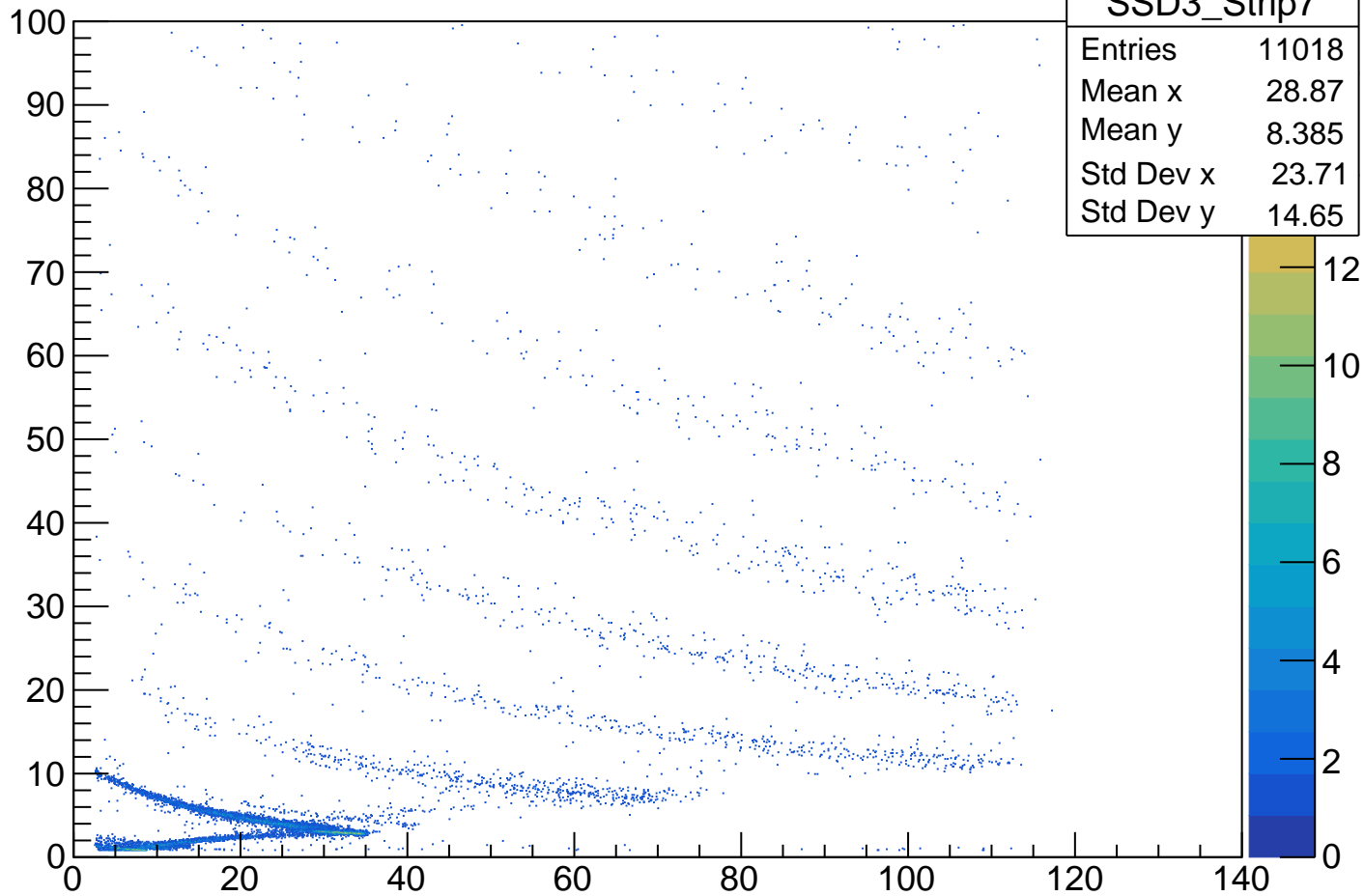
SSD3_Strip5



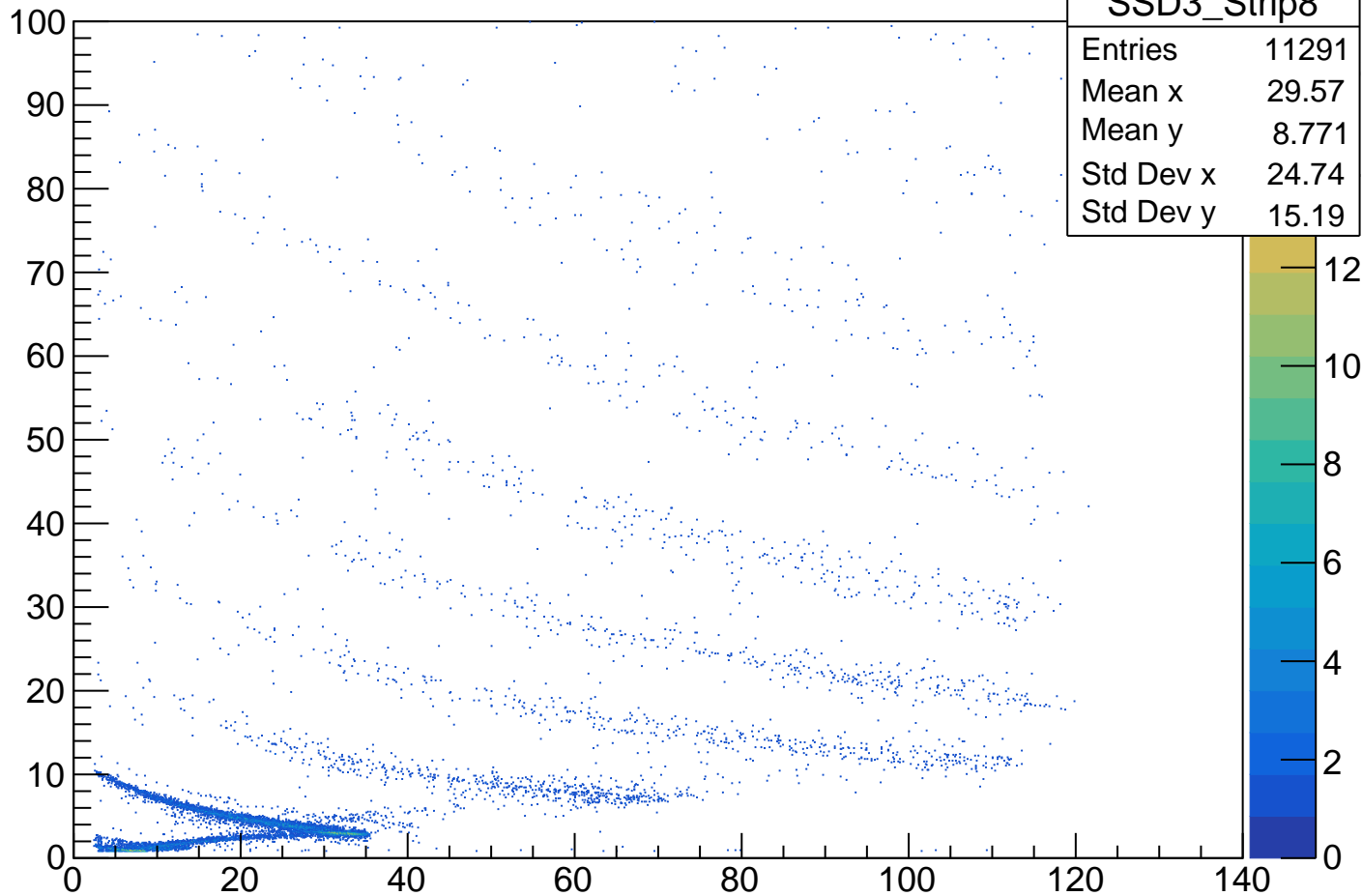
SSD3_Strip6



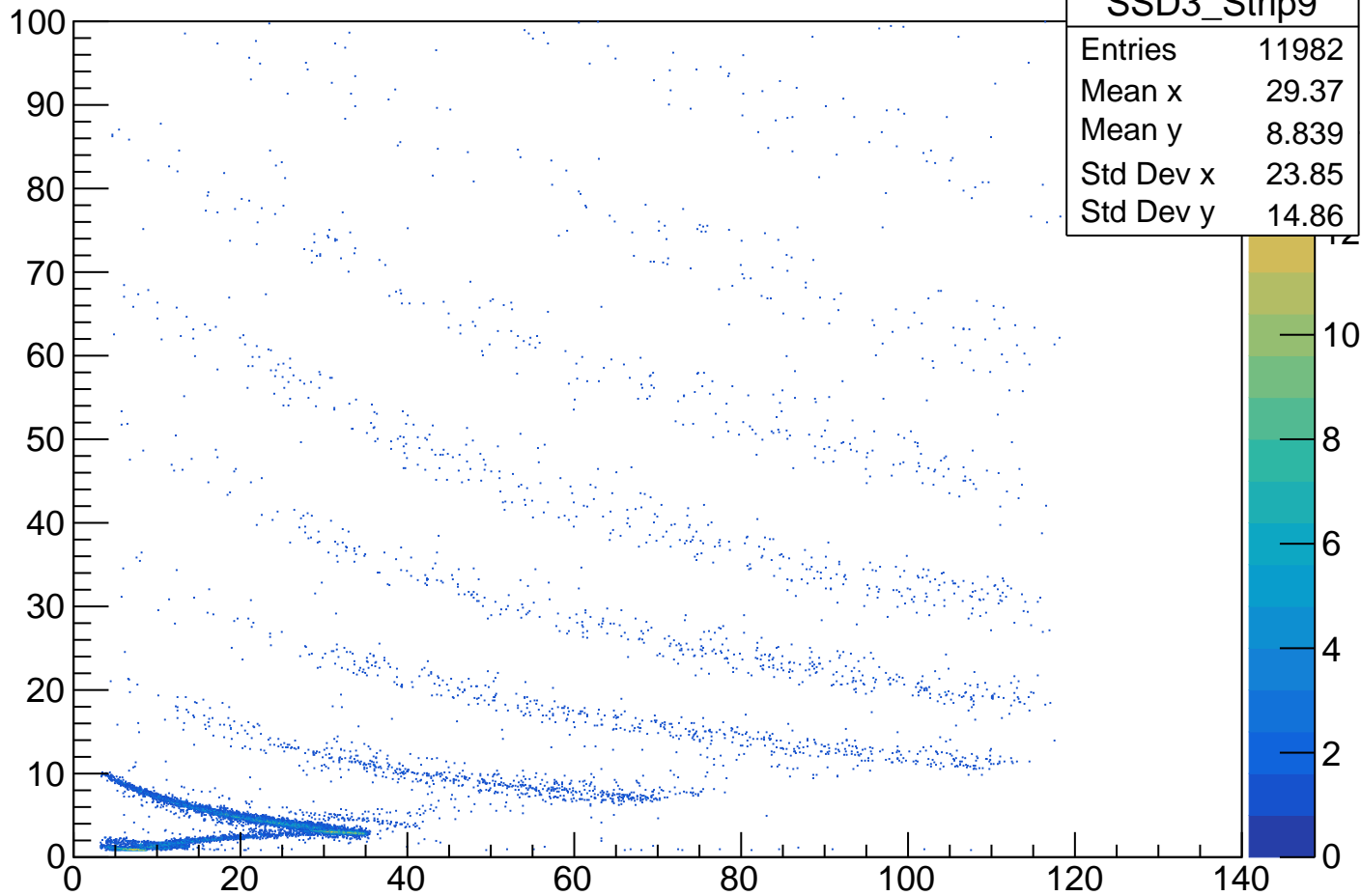
SSD3_Strip7



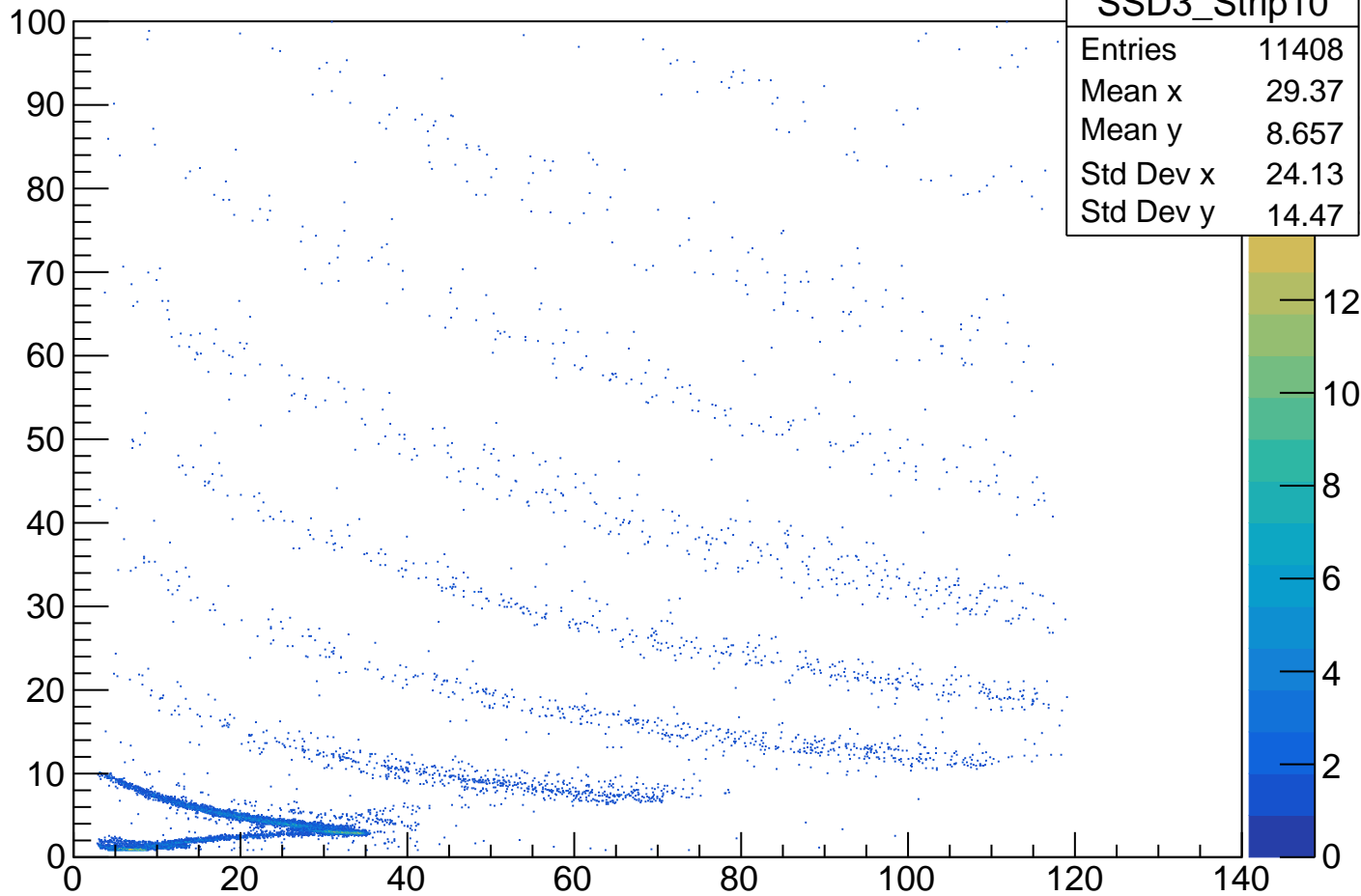
SSD3_Strip8



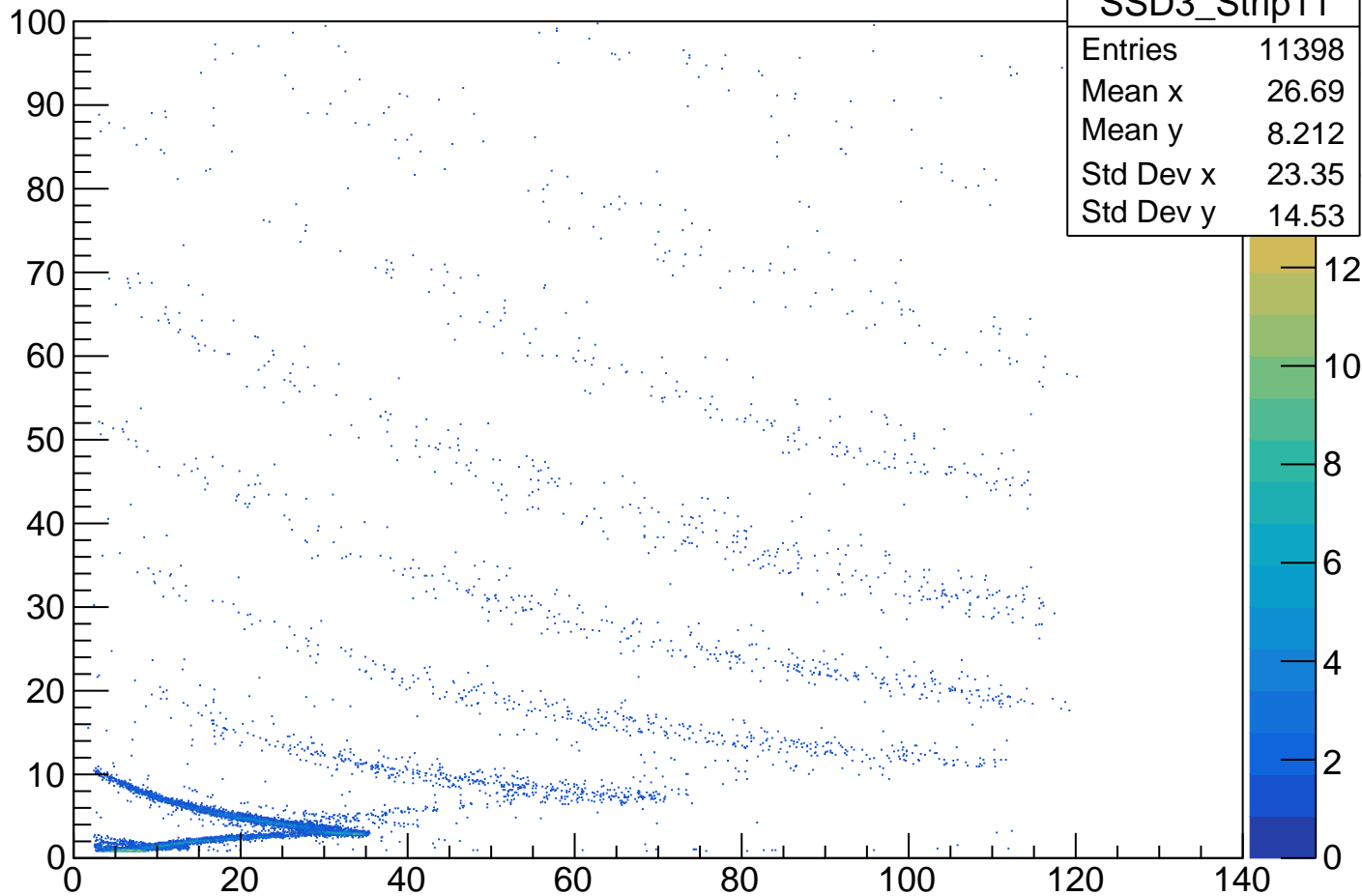
SSD3_Strip9



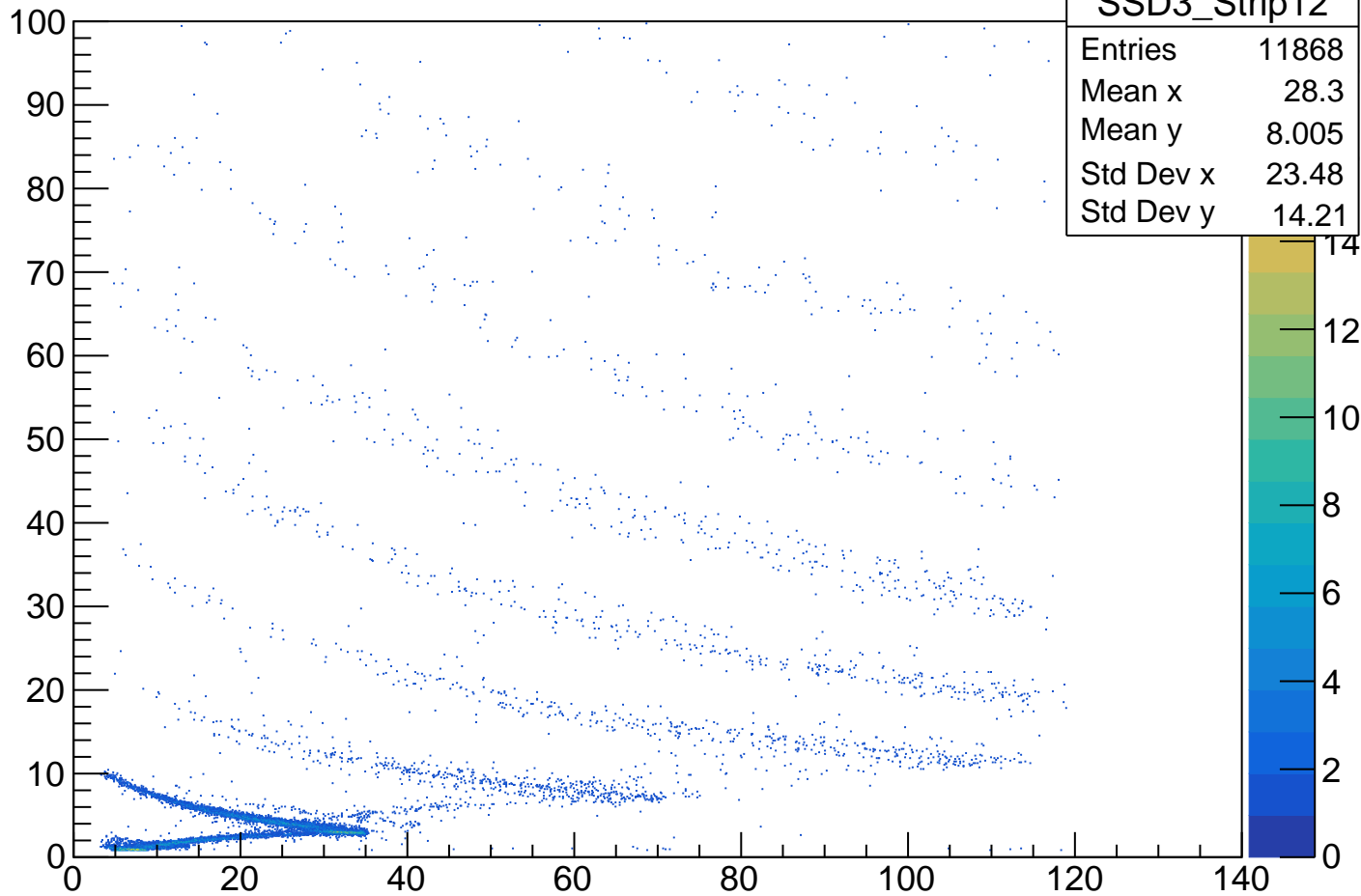
SSD3_Strip10



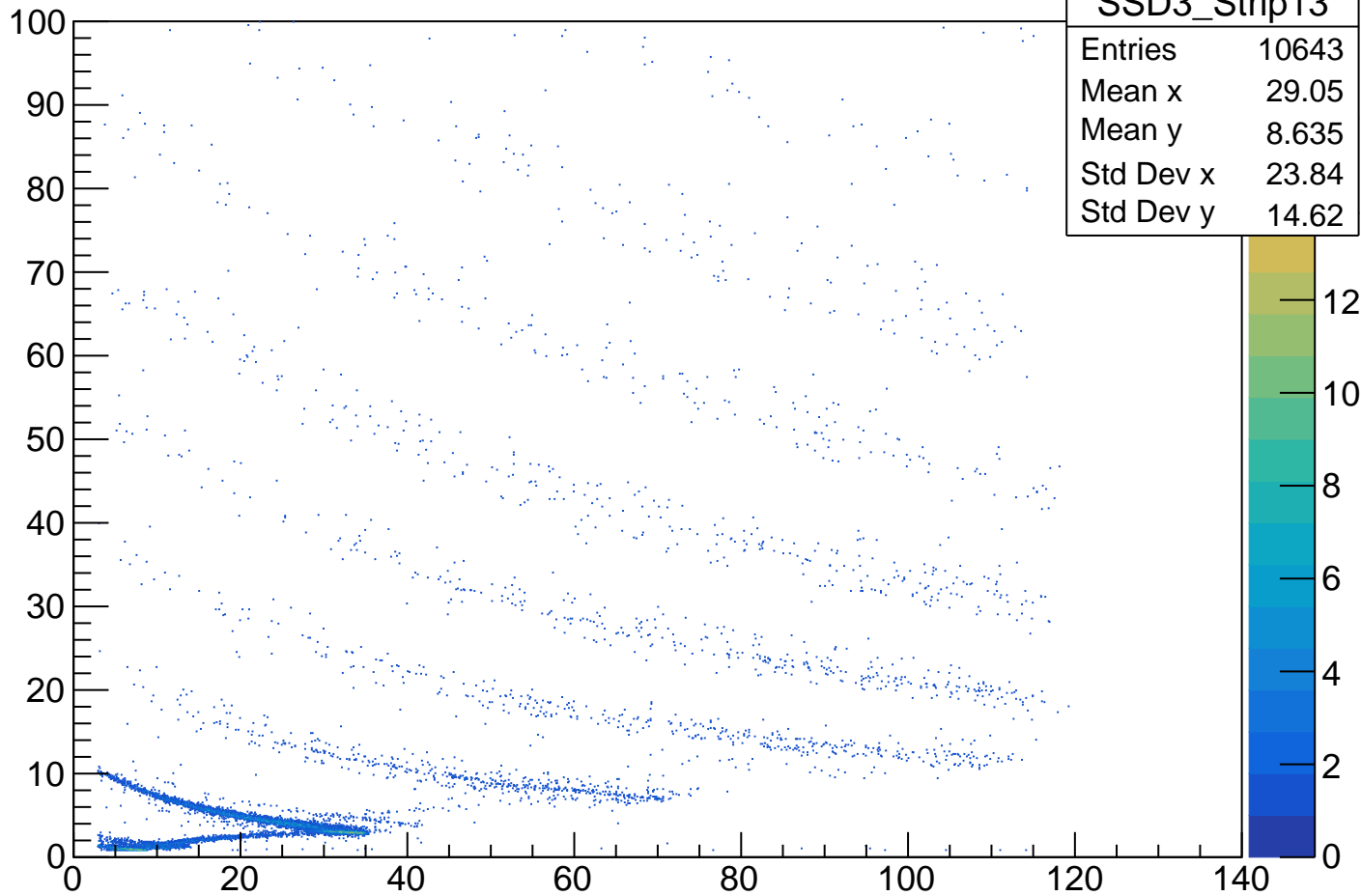
SSD3_Strip11



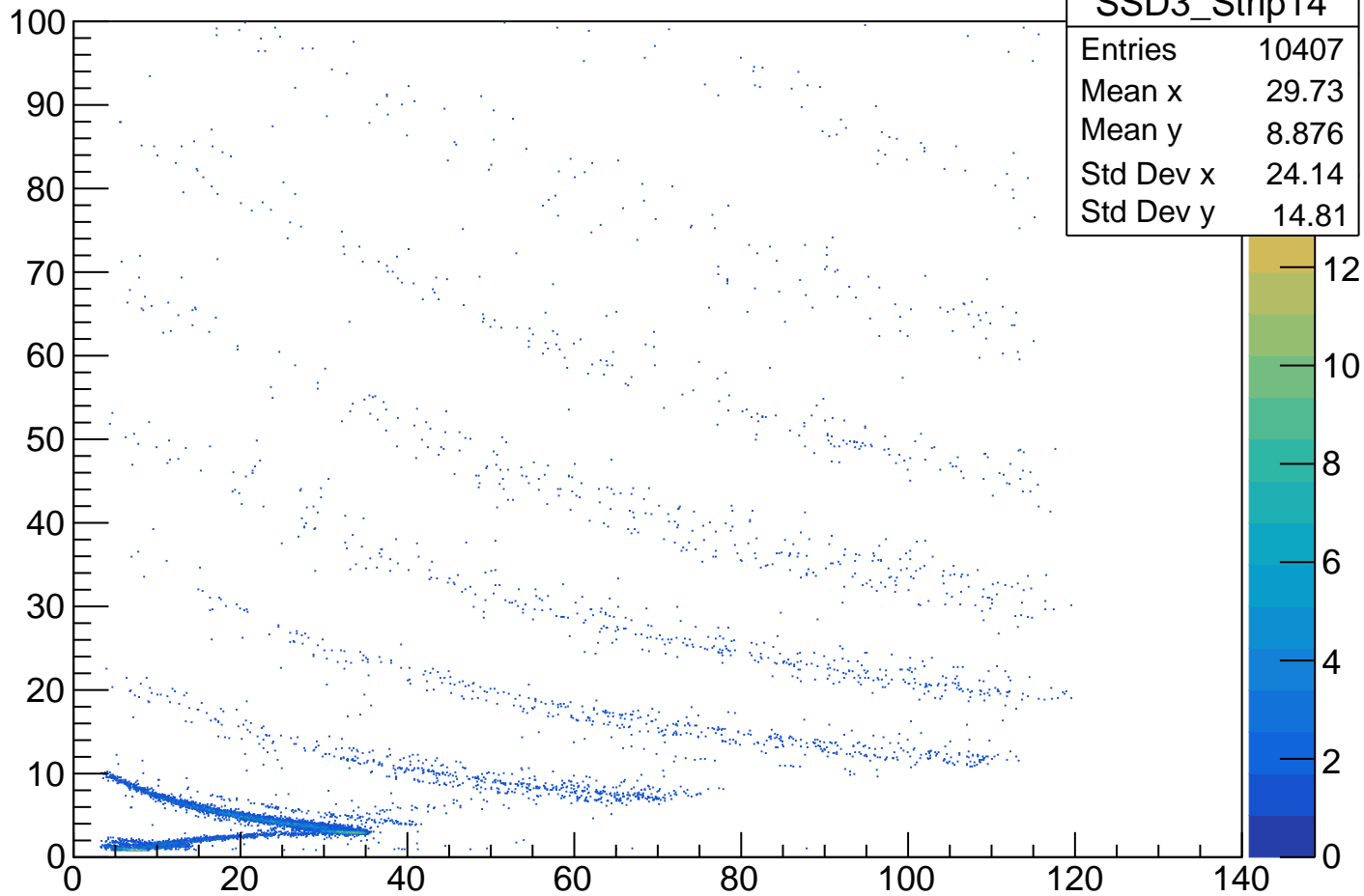
SSD3_Strip12



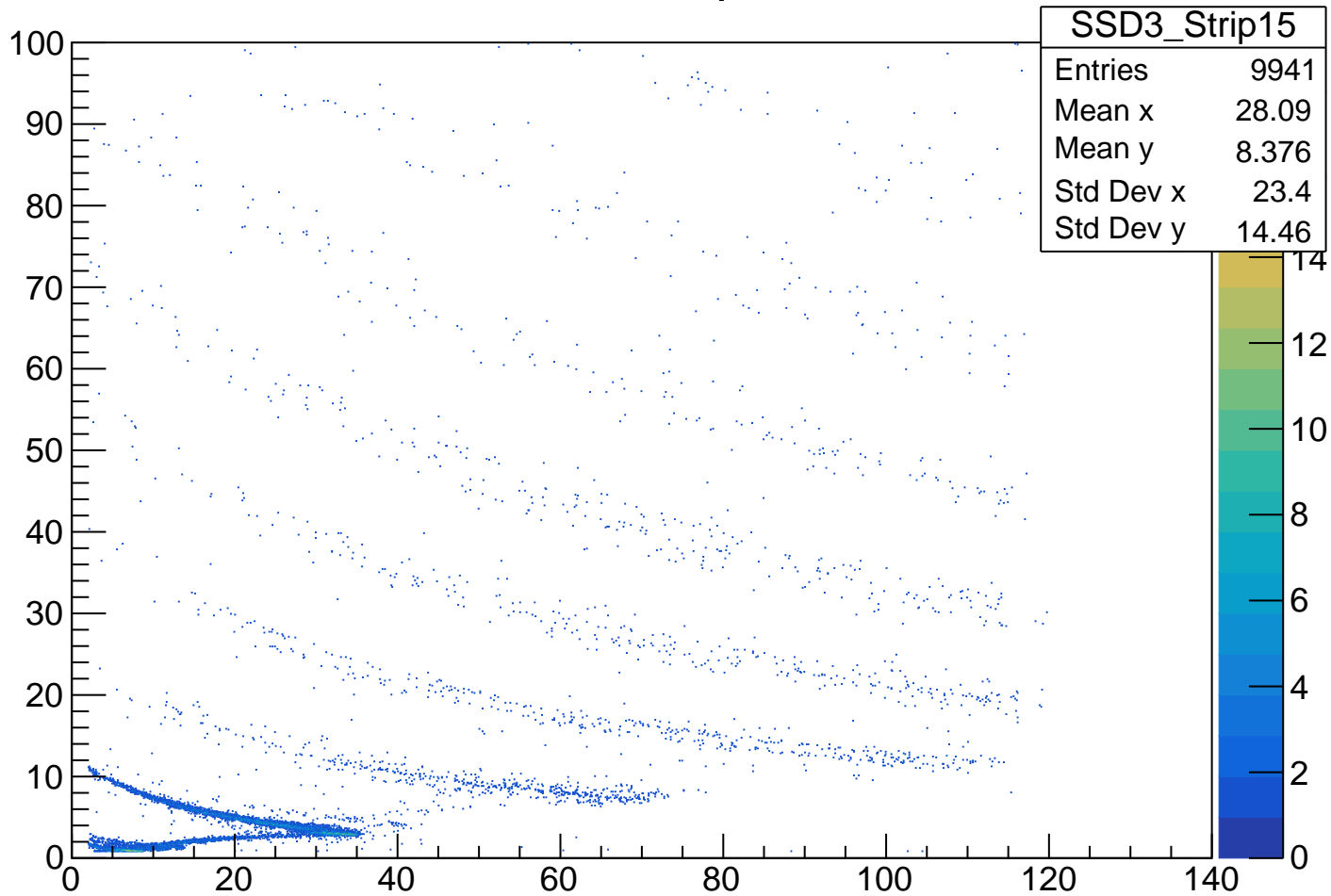
SSD3_Strip13



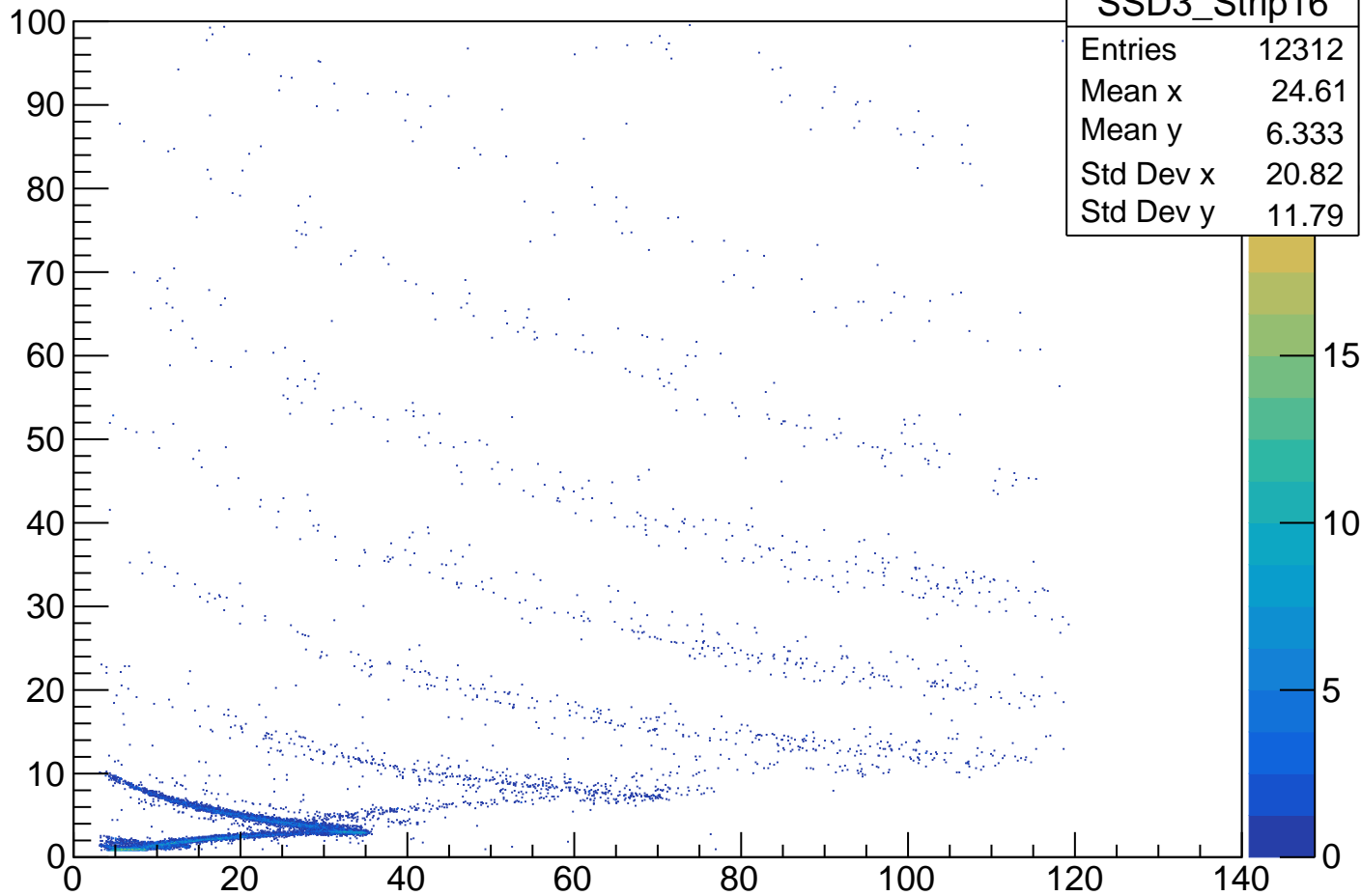
SSD3_Strip14



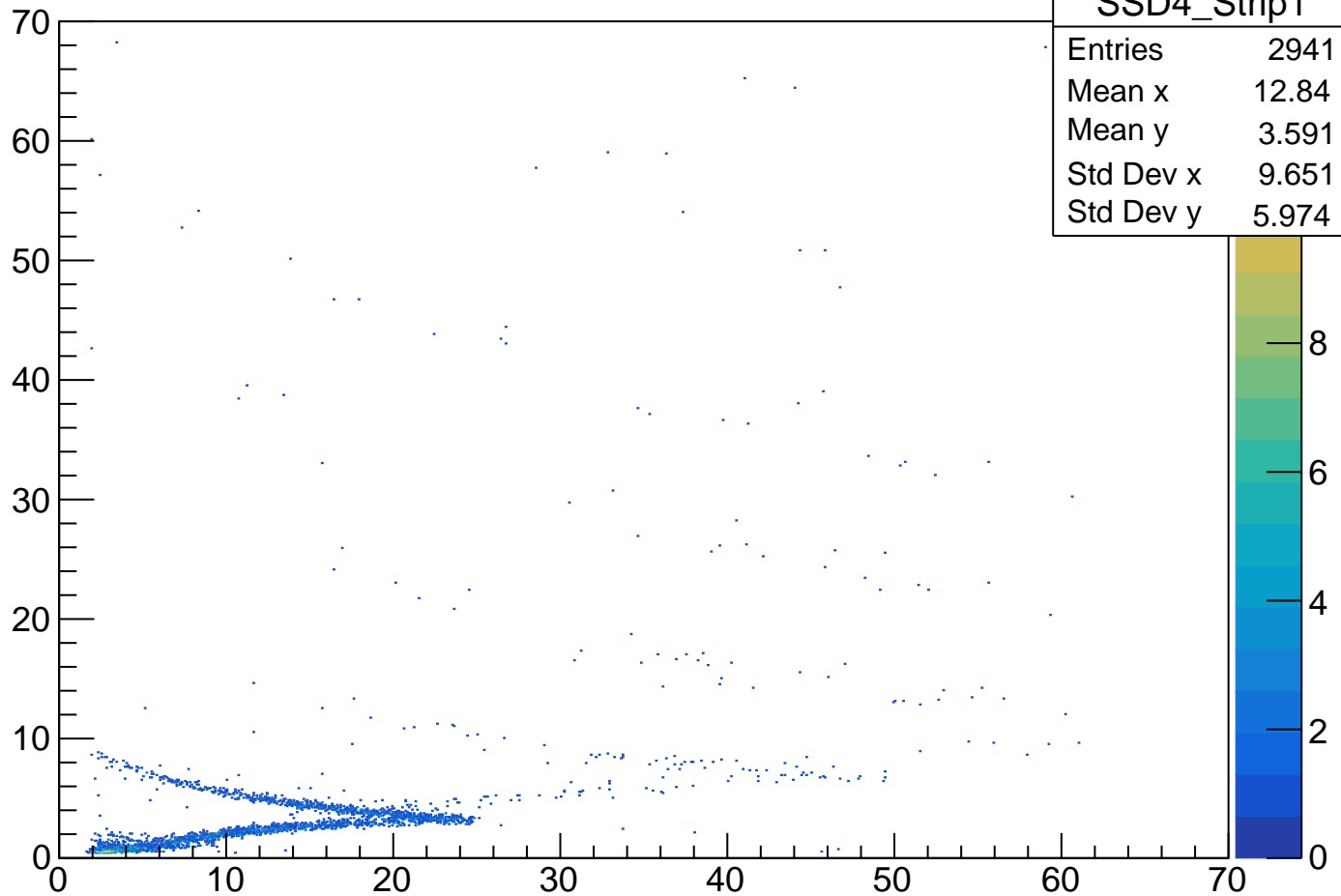
SSD3_Strip15



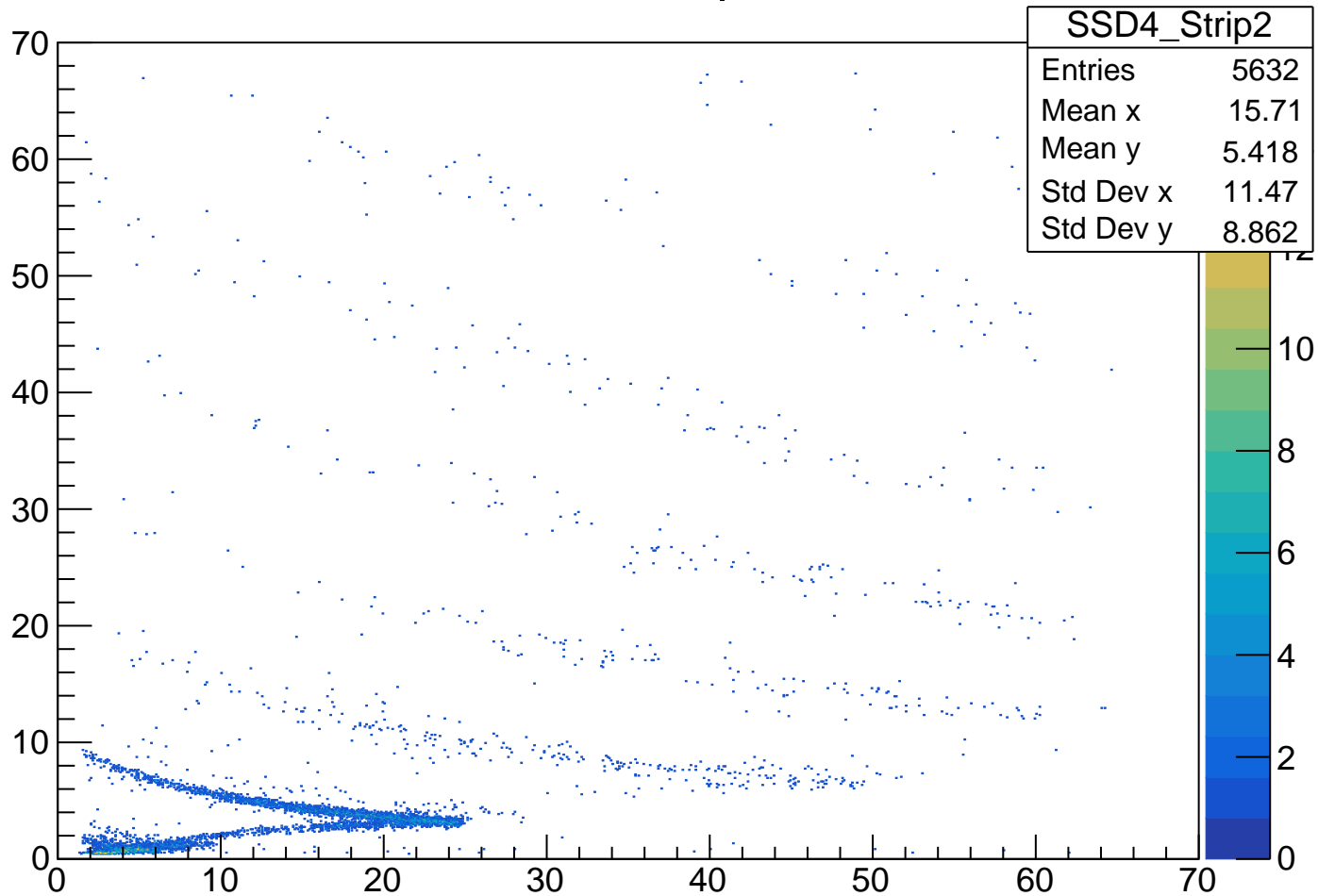
SSD3_Strip16



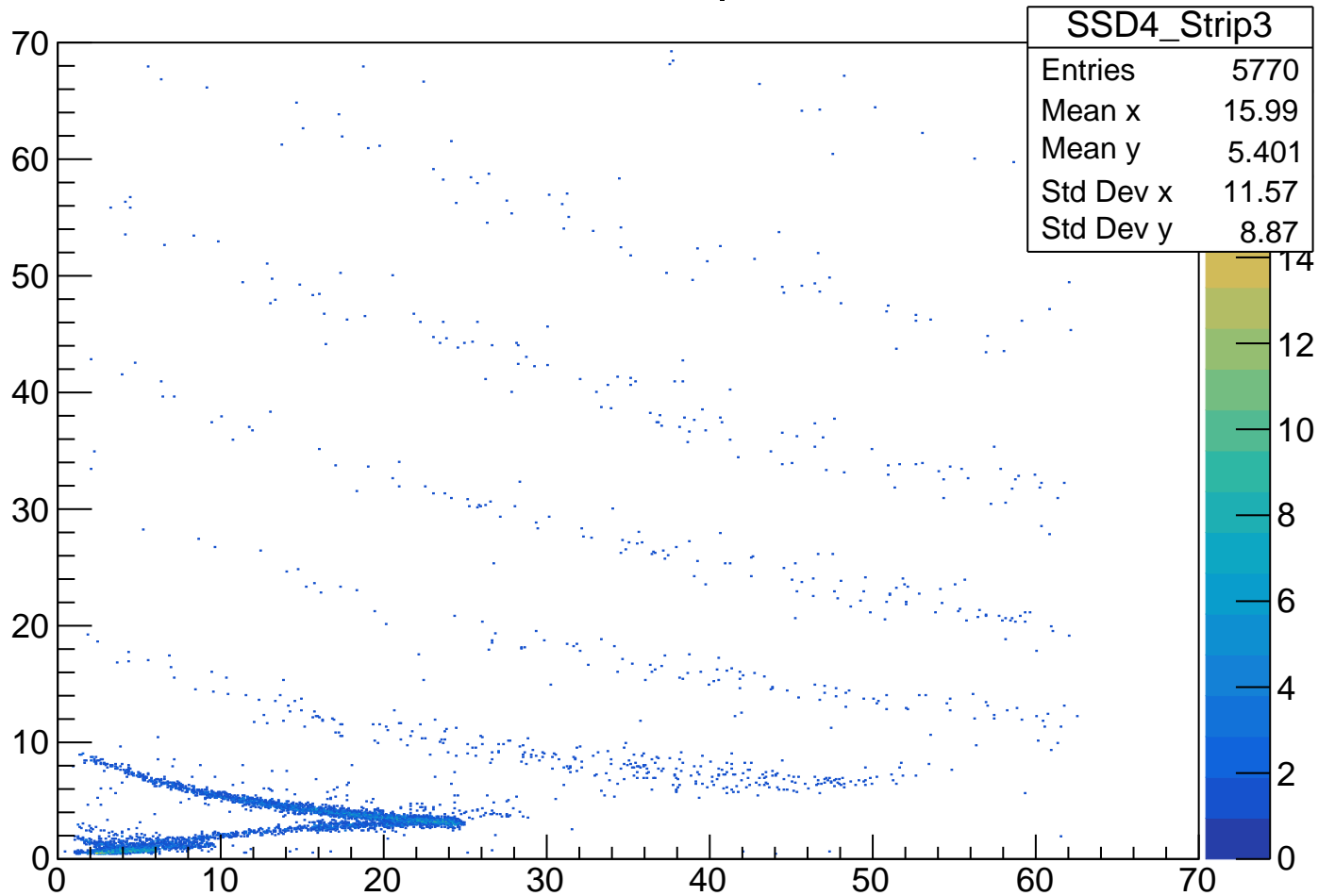
SSD4_Strip1



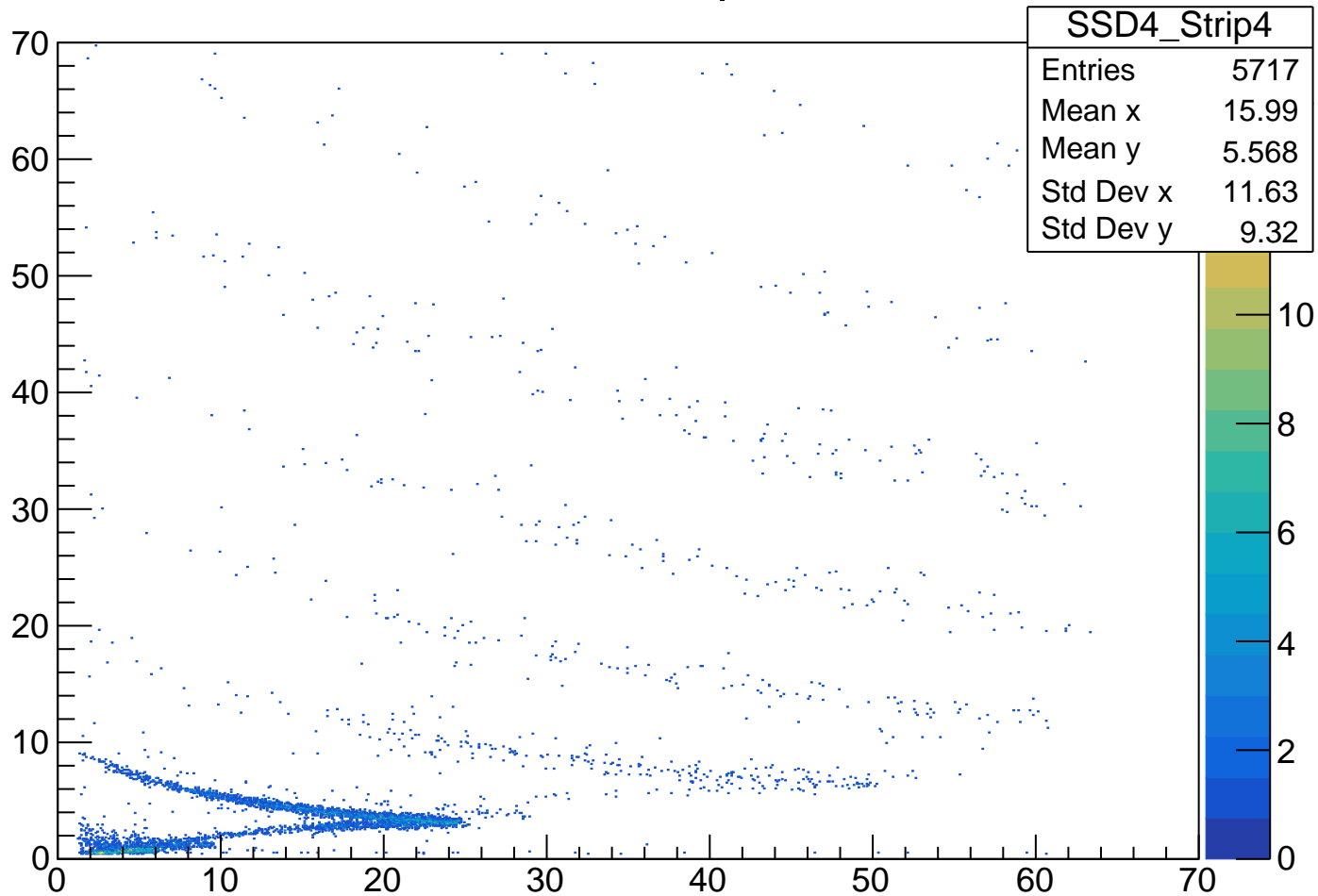
SSD4_Strip2



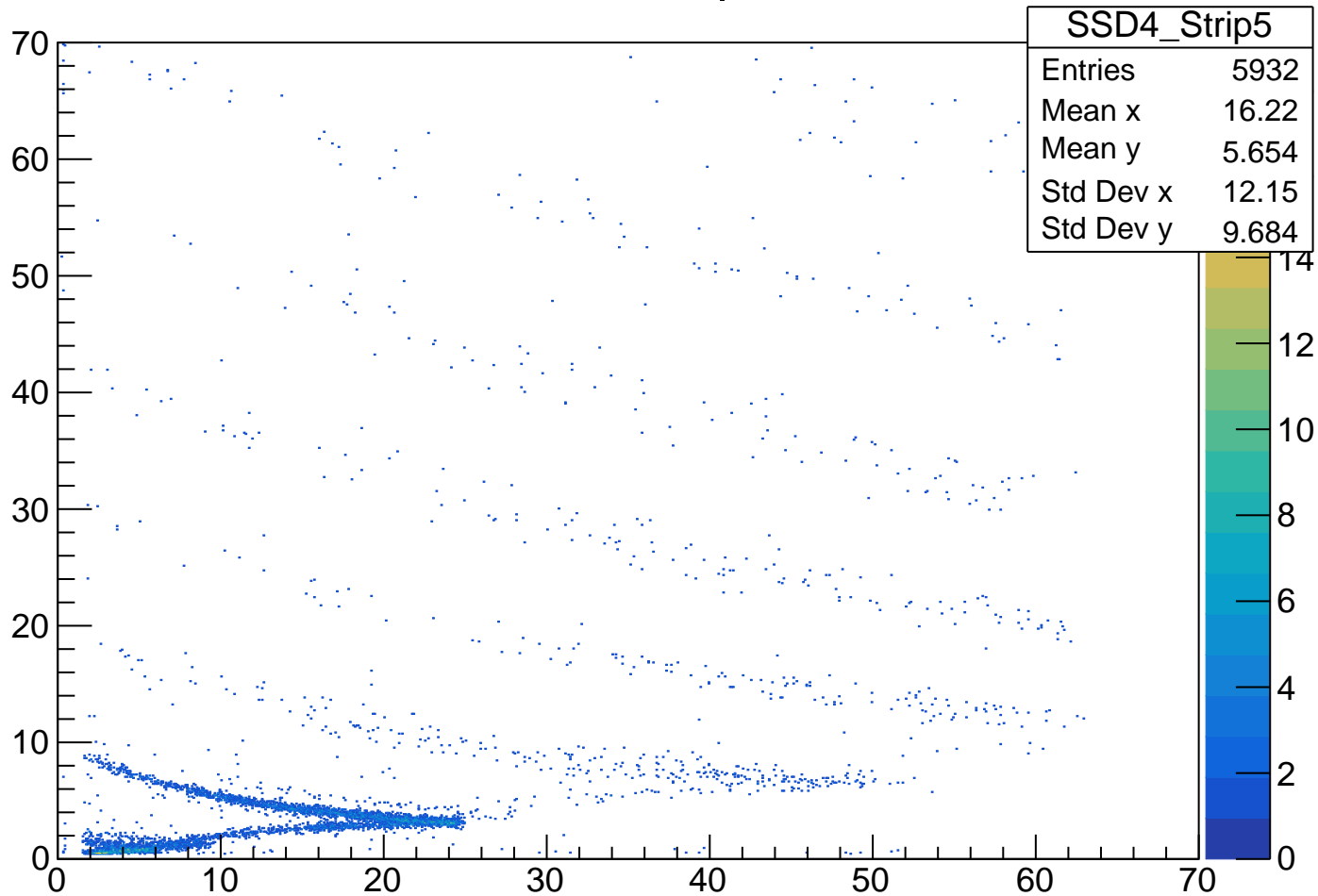
SSD4_Strip3



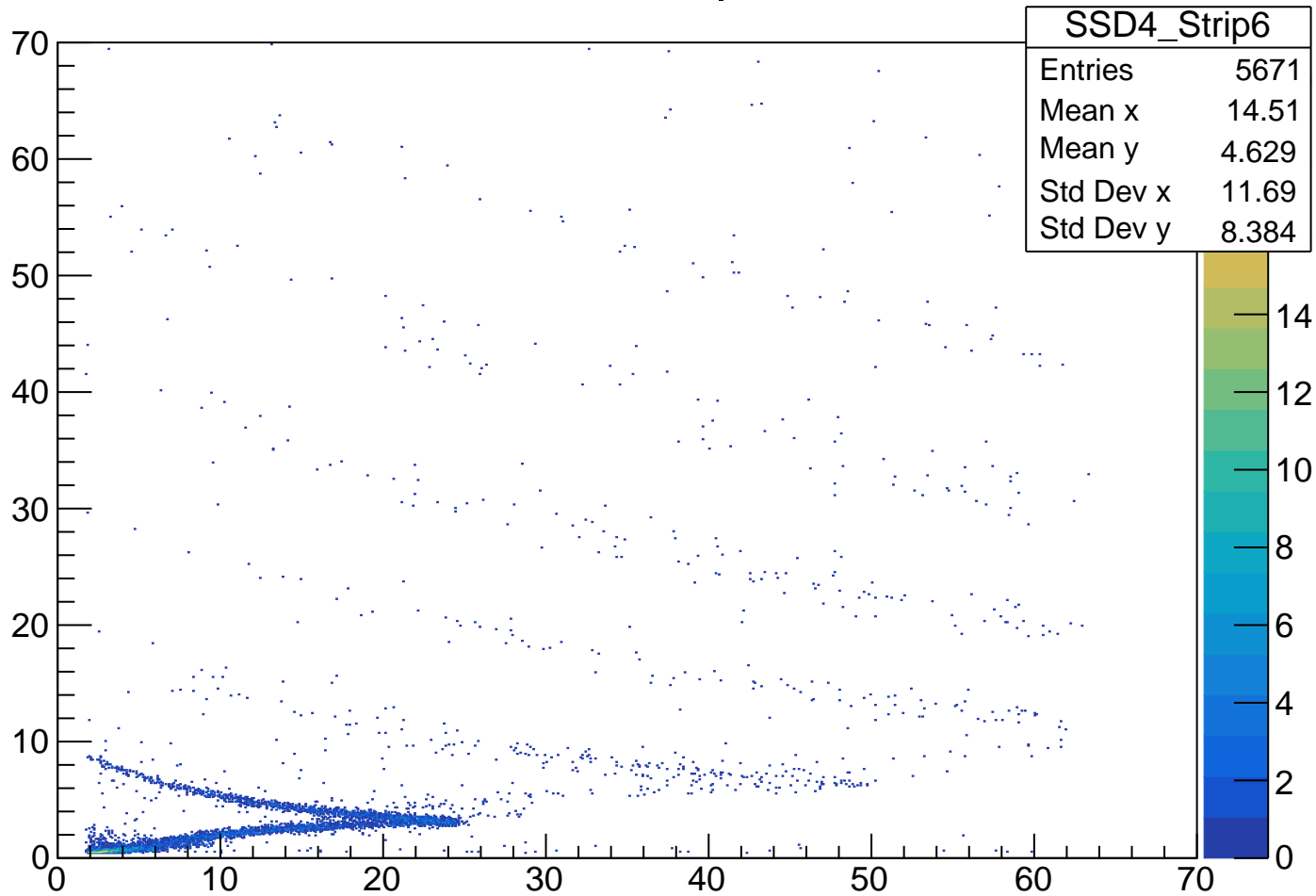
SSD4_Strip4



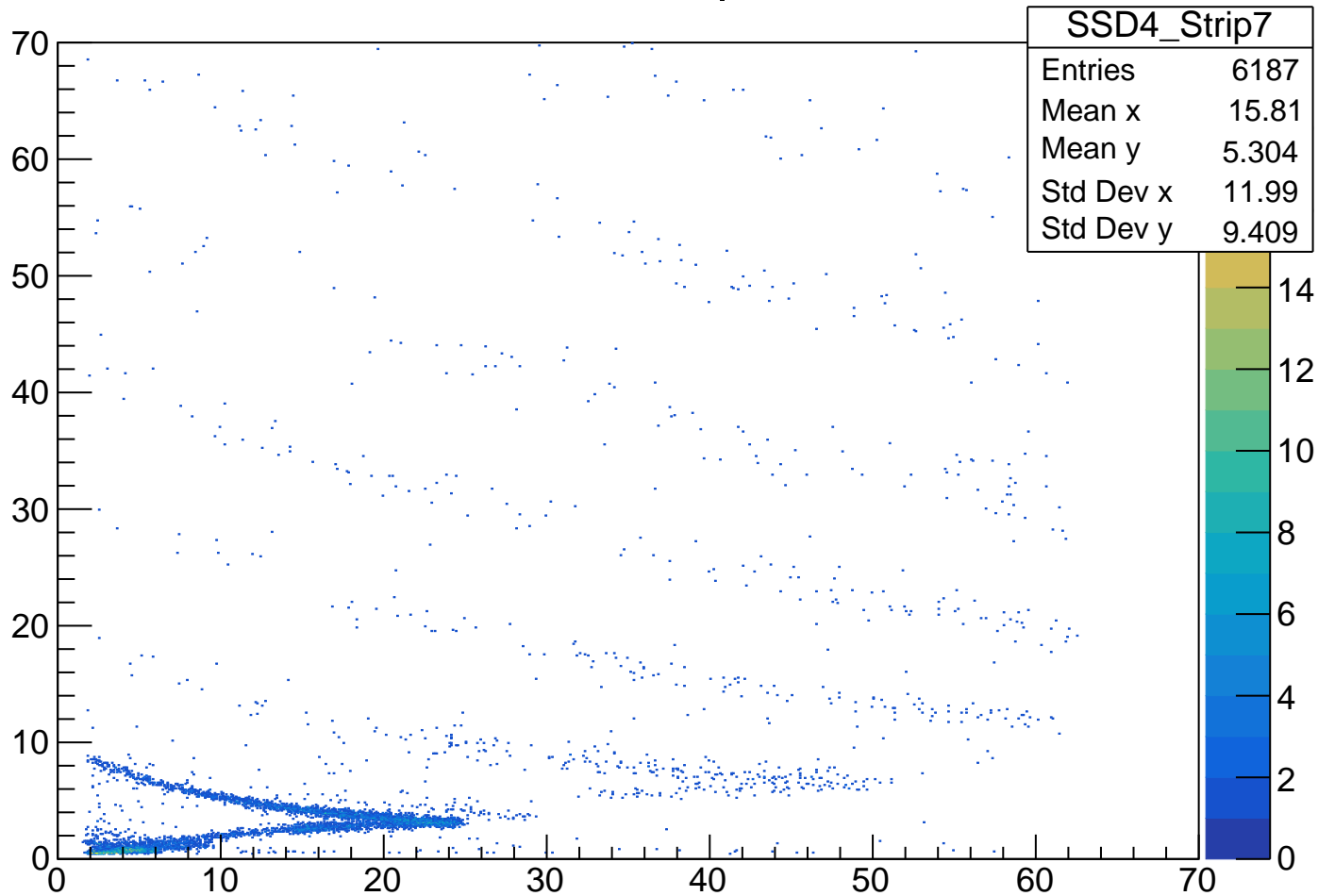
SSD4_Strip5



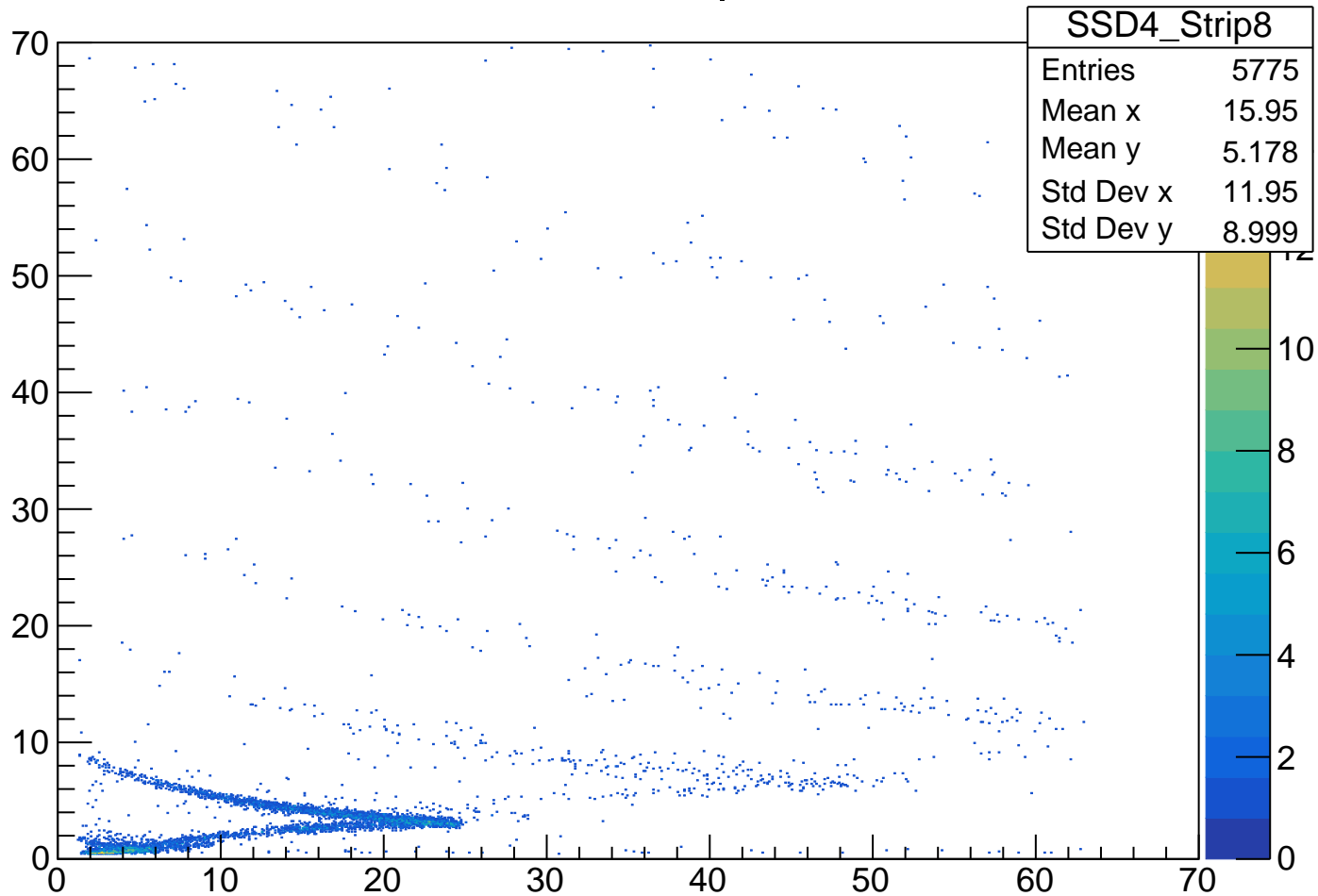
SSD4_Strip6



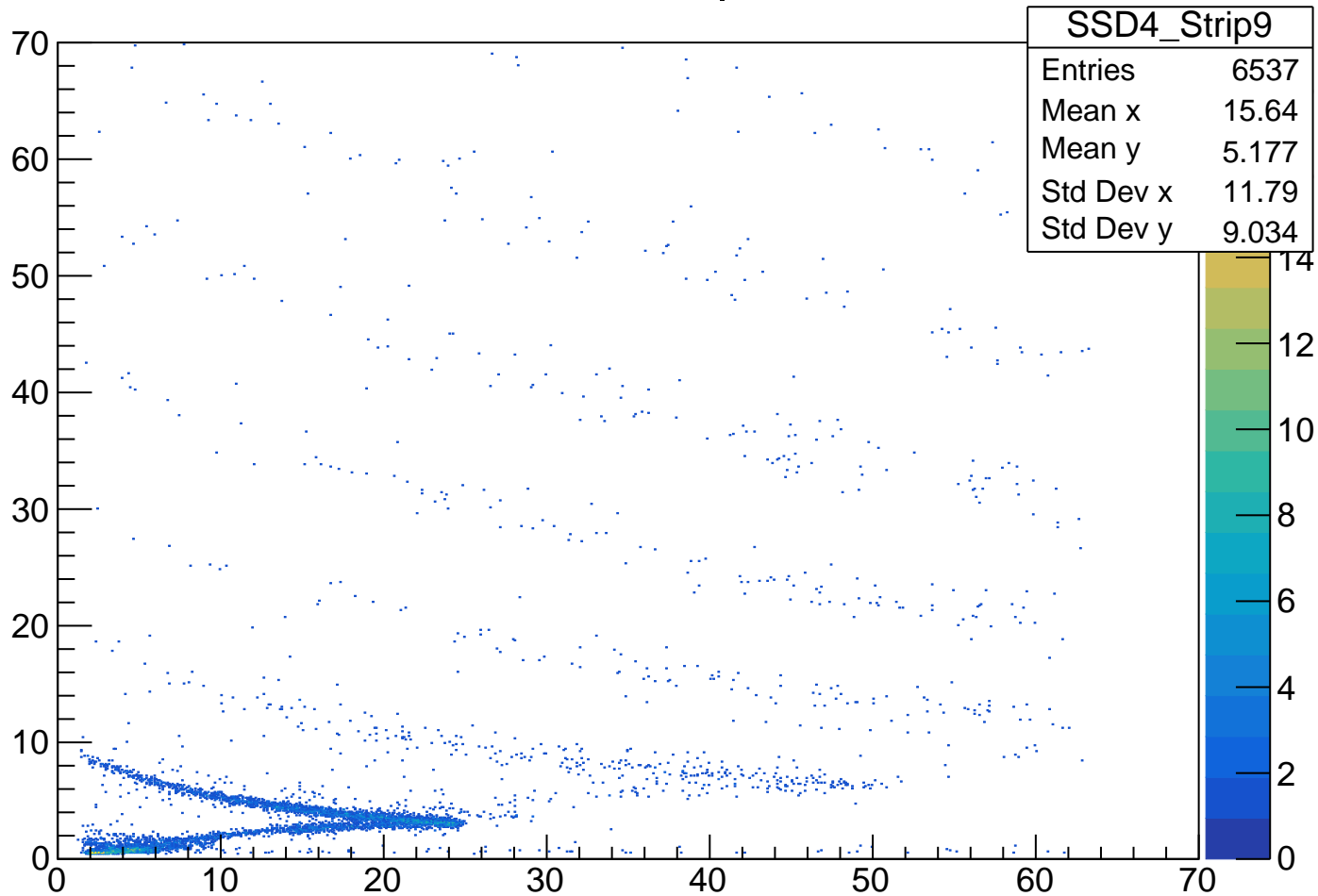
SSD4_Strip7



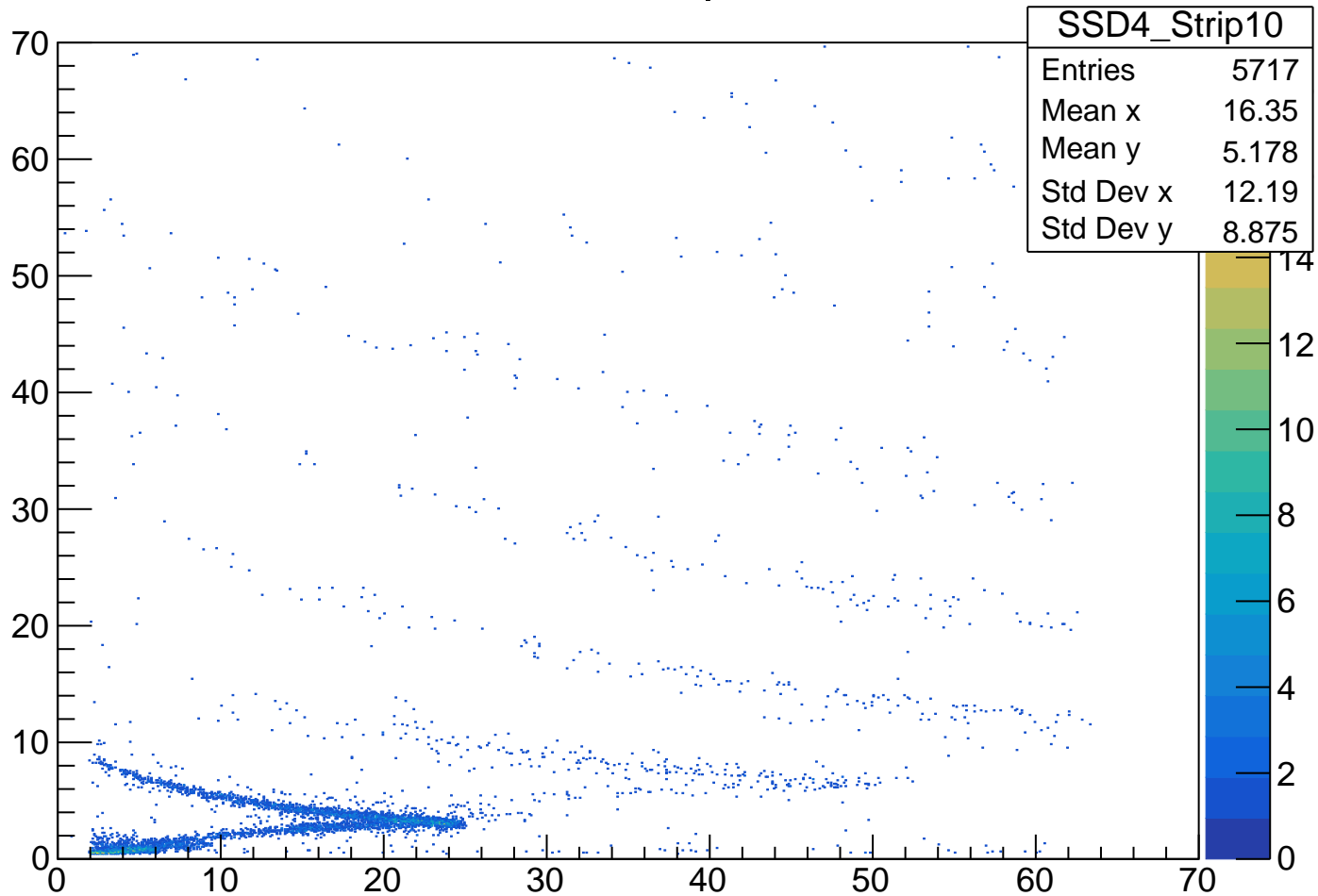
SSD4_Strip8



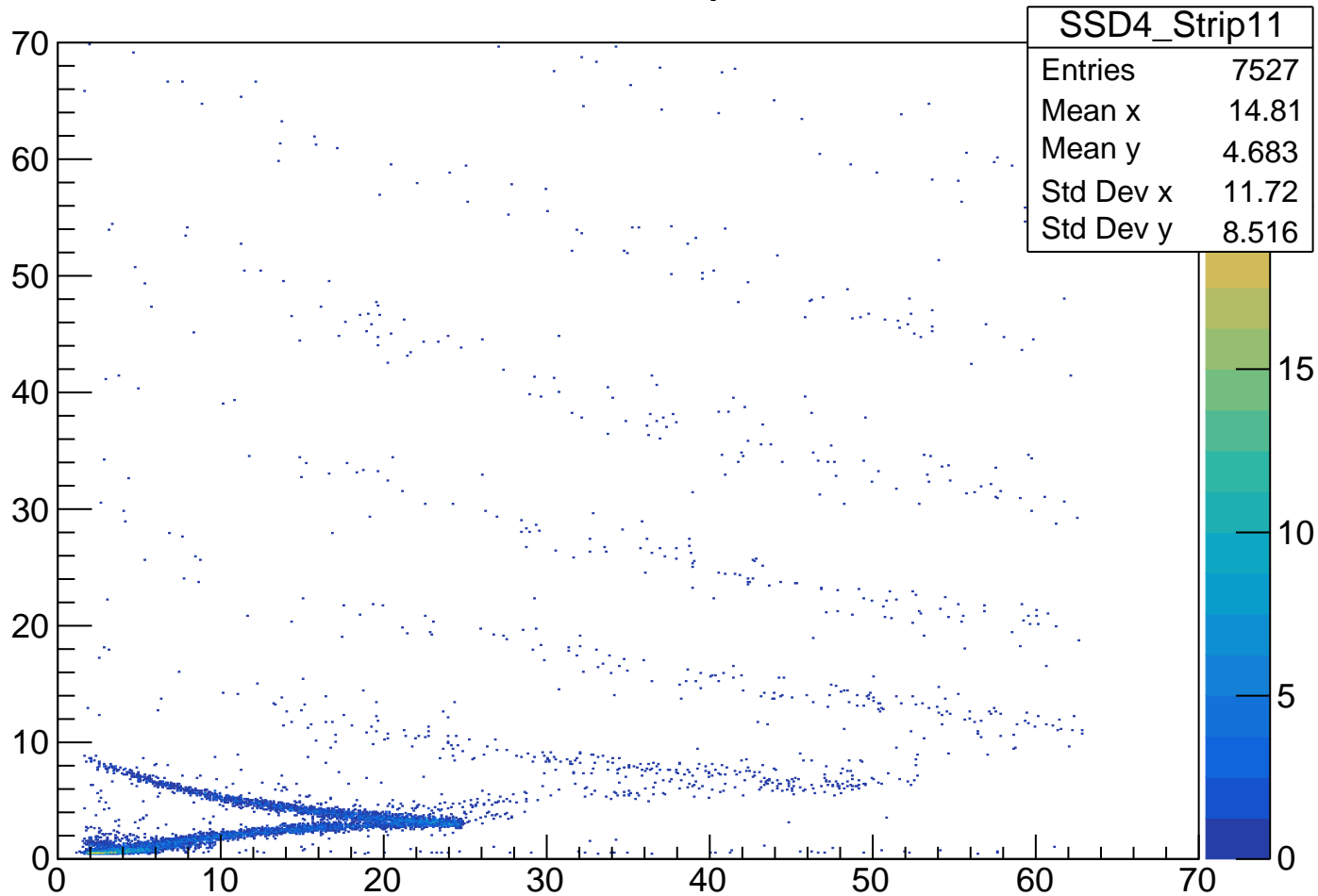
SSD4_Strip9



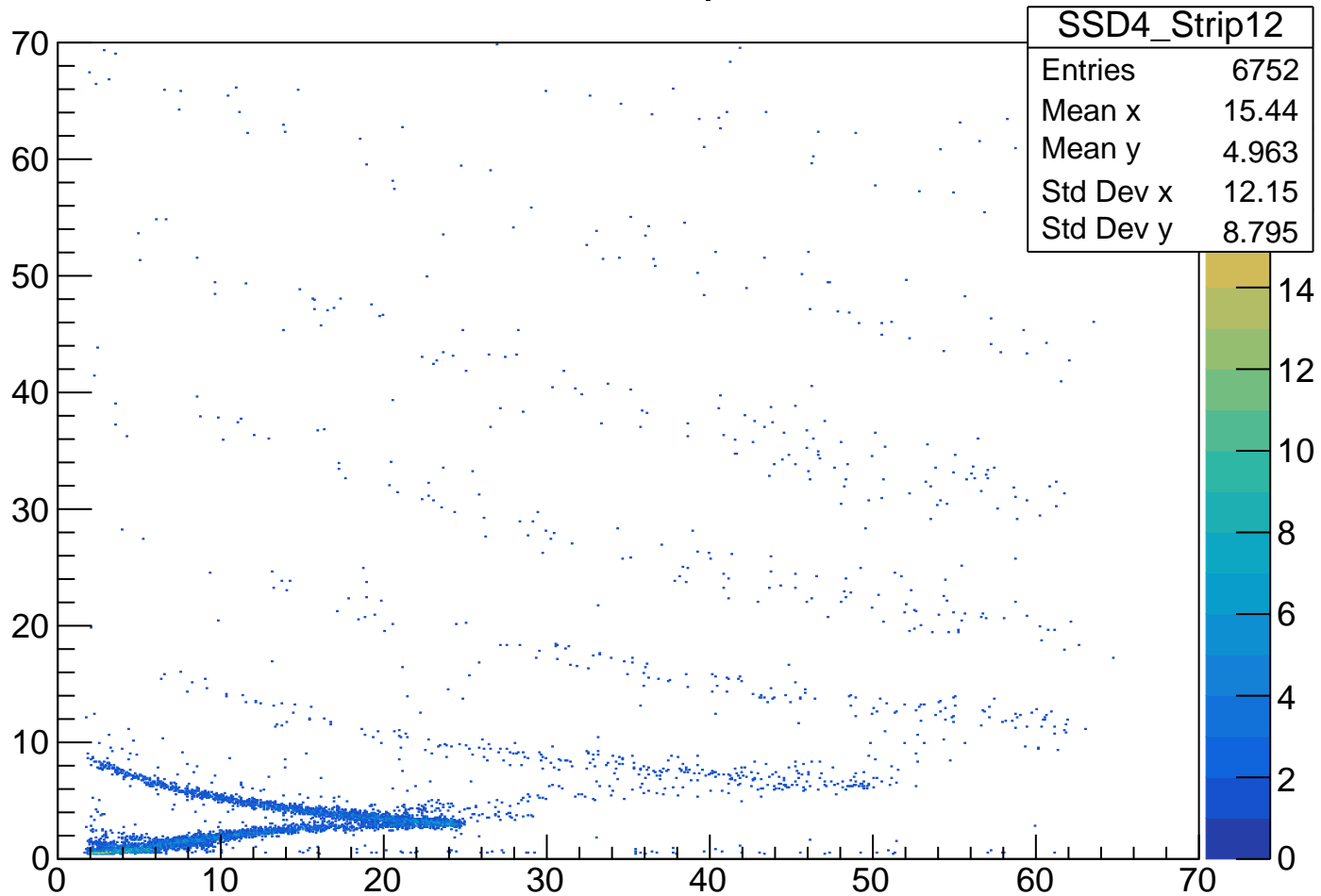
SSD4_Strip10



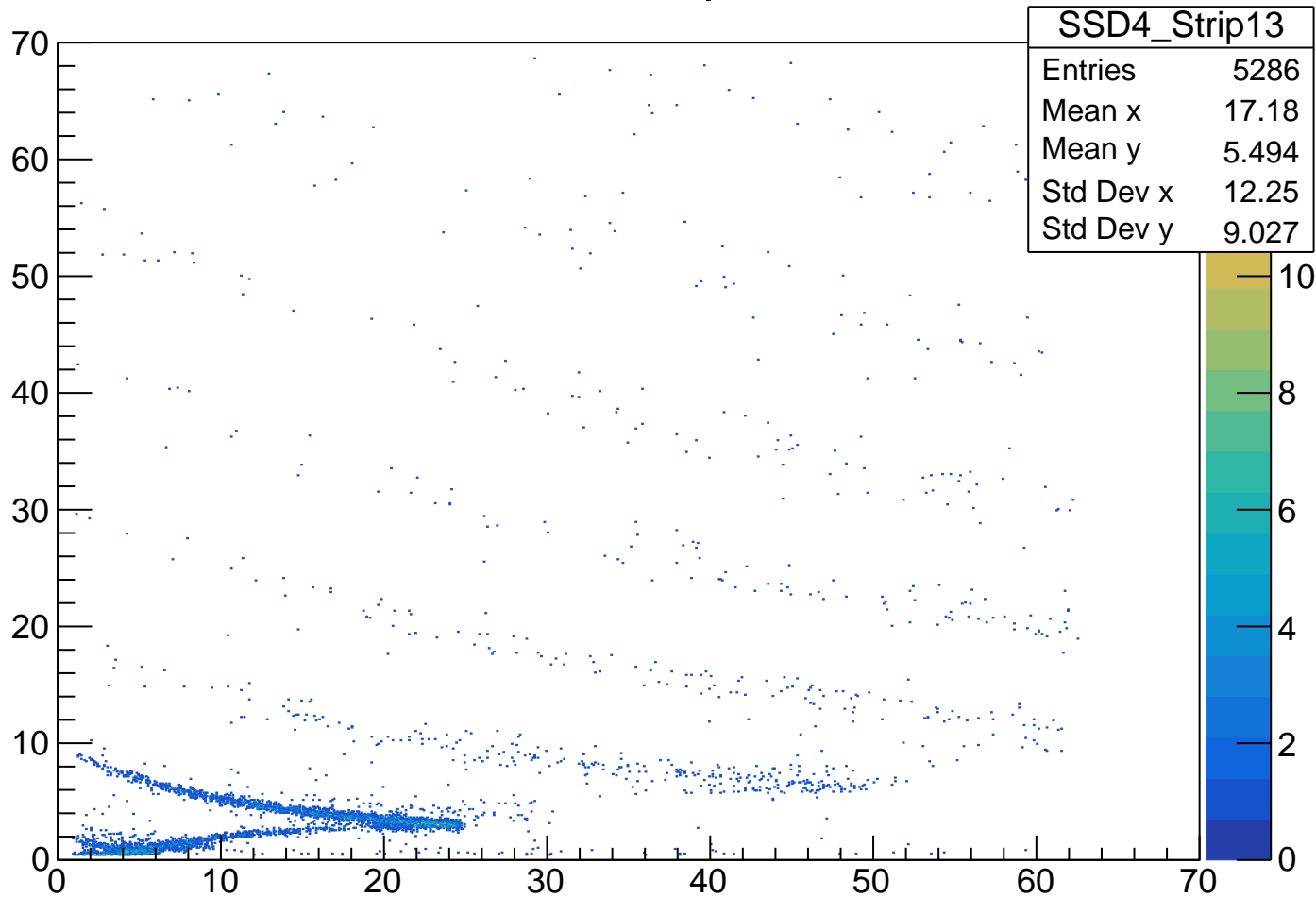
SSD4_Strip11



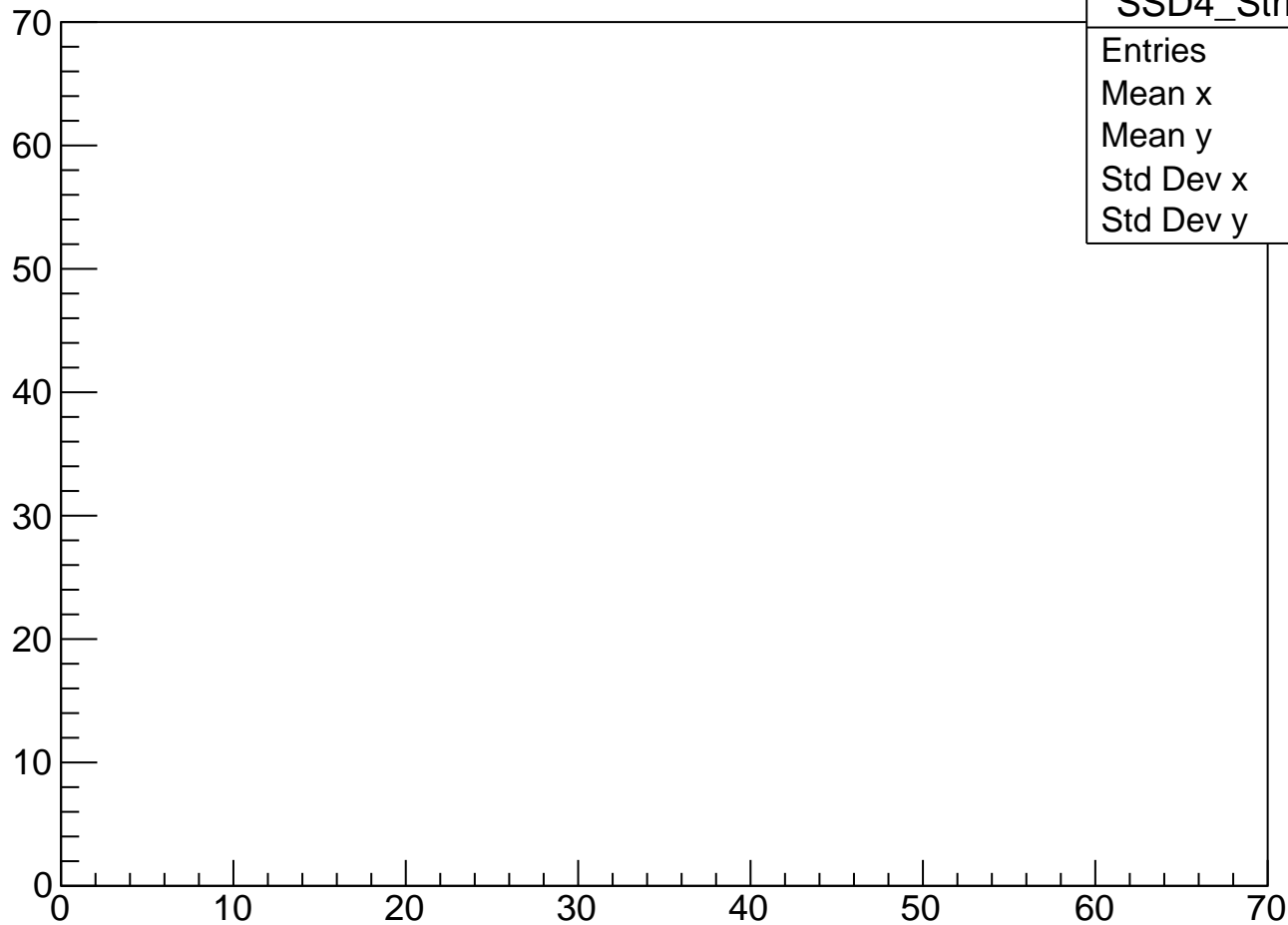
SSD4_Strip12



SSD4_Strip13

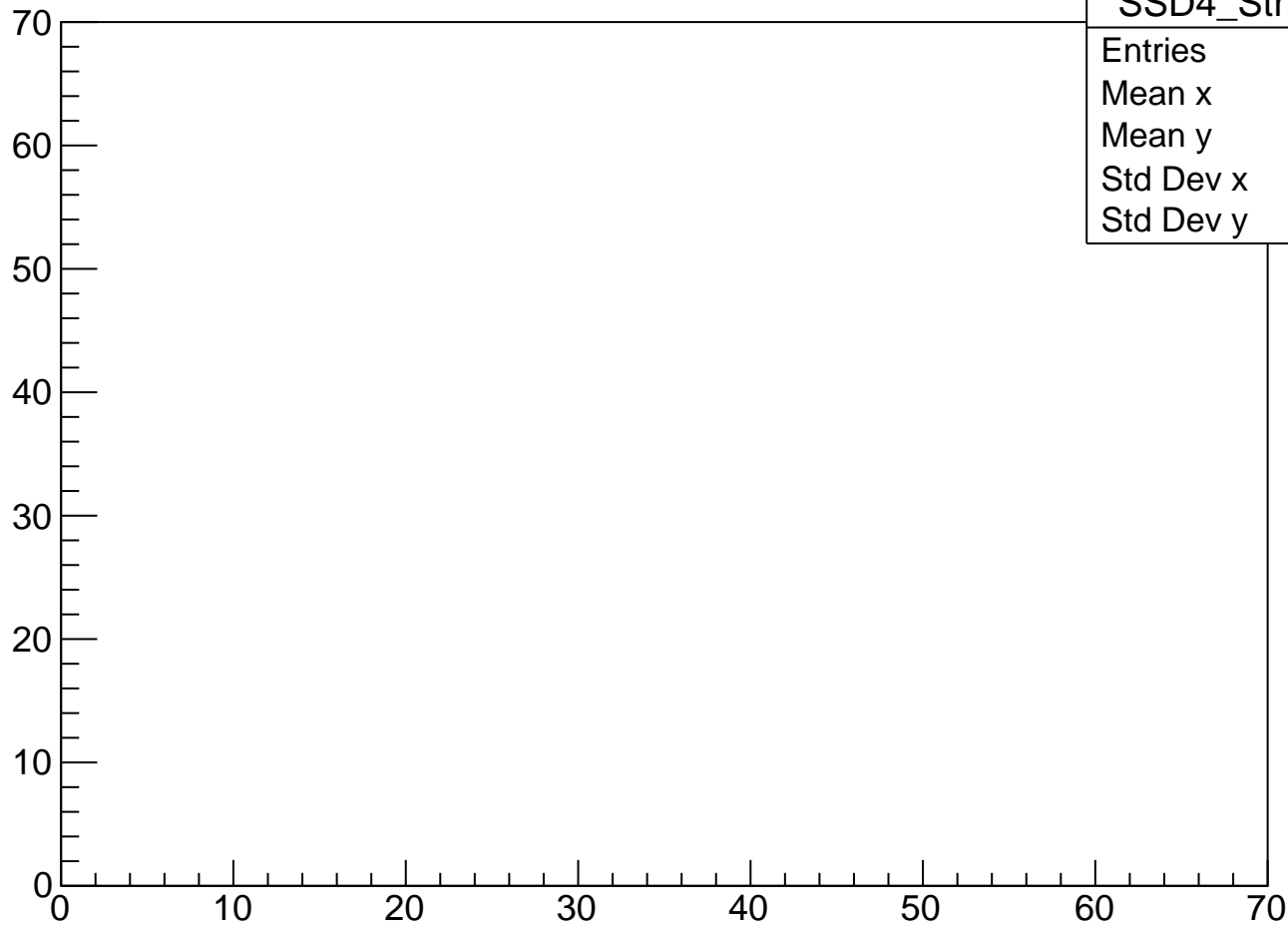


SSD4_Strip14



SSD4_Strip14	
Entries	0
Mean x	0
Mean y	0
Std Dev x	0
Std Dev y	0

SSD4_Strip15



SSD4_Strip15	
Entries	0
Mean x	0
Mean y	0
Std Dev x	0
Std Dev y	0

SSD4_Strip16

