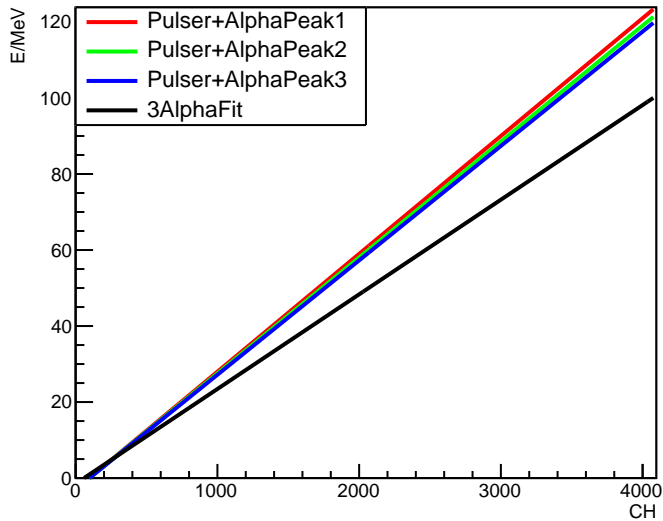
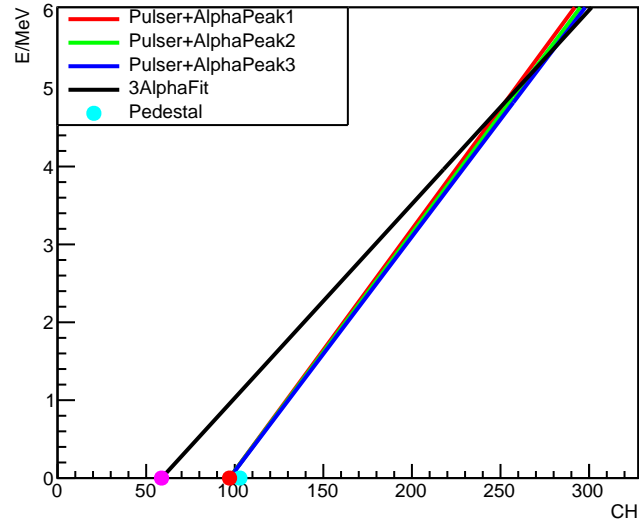


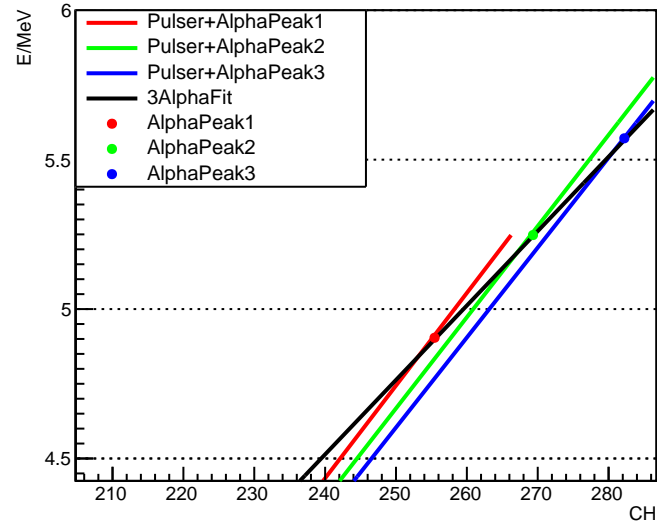
SSD1_L1S_Switch_Alpha00_04_CH00



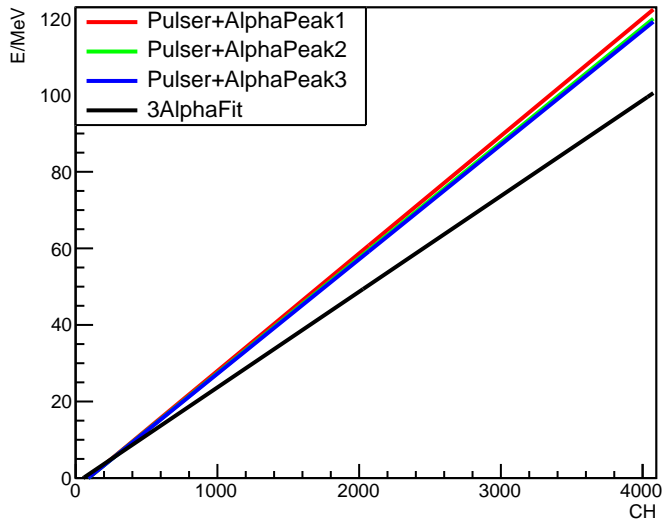
SSD1_L1S_Switch_Alpha00_04_CH00



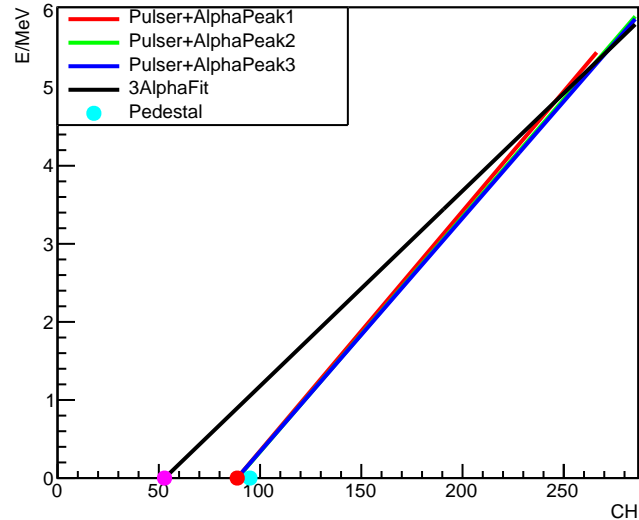
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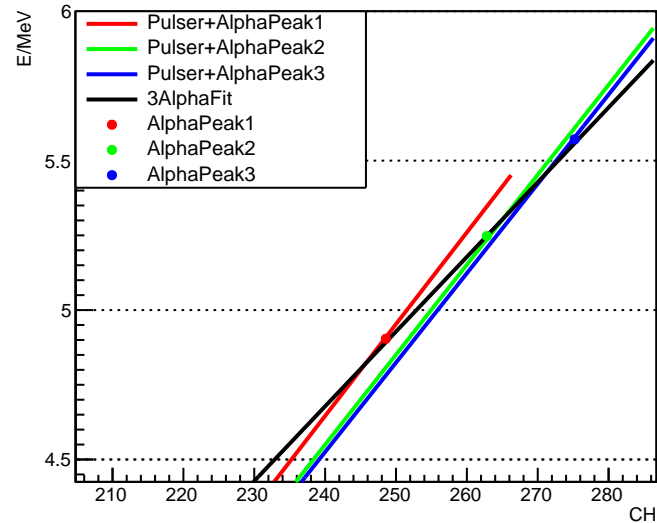
SSD1_L1S_Switch_Alpha00_04_CH01



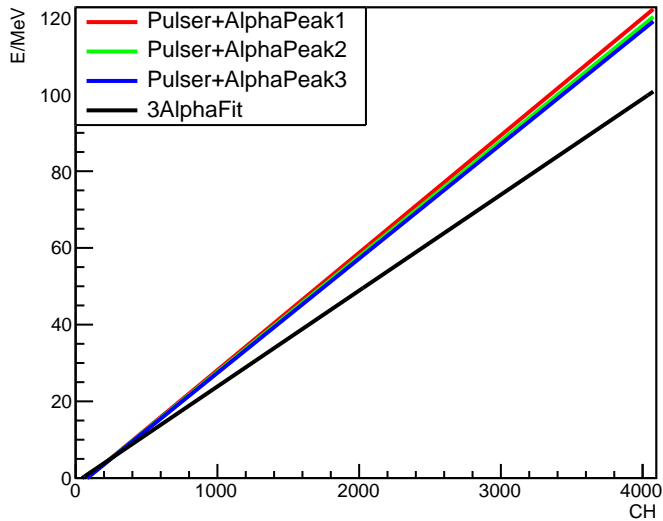
SSD1_L1S_Switch_Alpha00_04_CH01



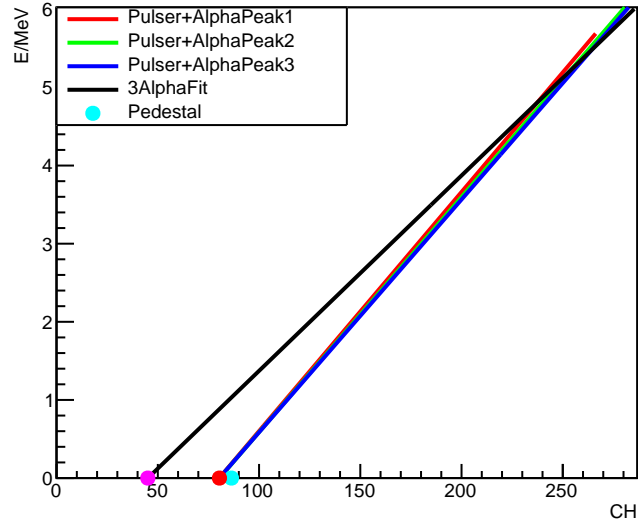
SSD1_L1S_Switch_Alpha00_04_CH01



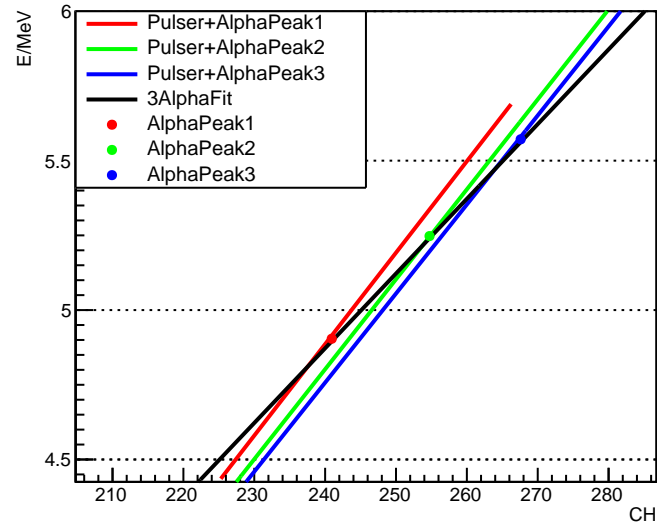
SSD1_L1S_Switch_Alpha00_04_CH02



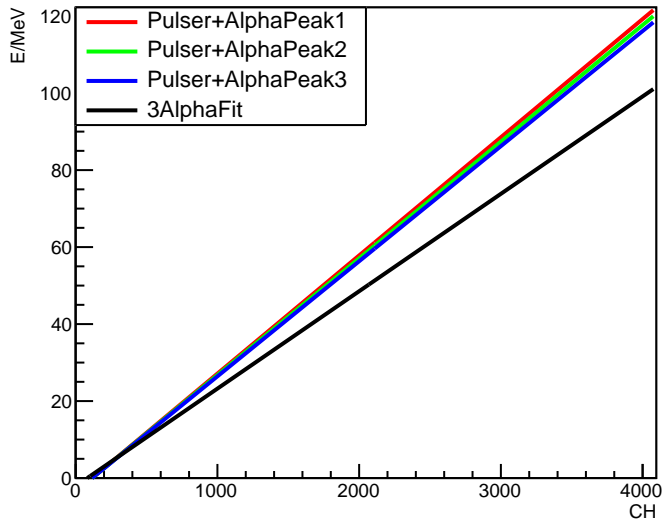
SSD1_L1S_Switch_Alpha00_04_CH02



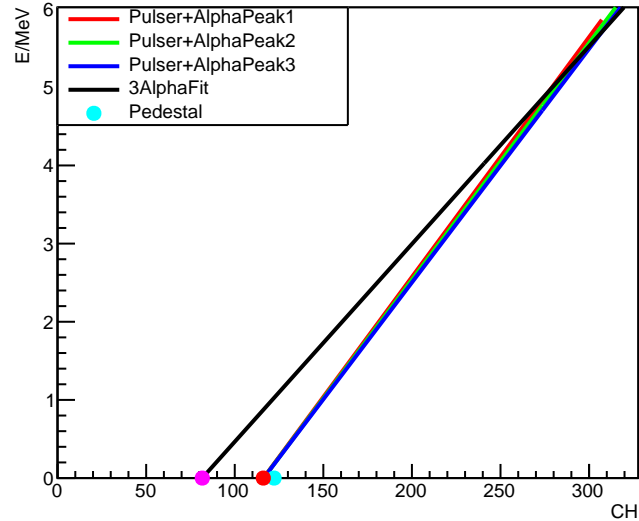
SSD1_L1S_Switch_Alpha00_04_CH02



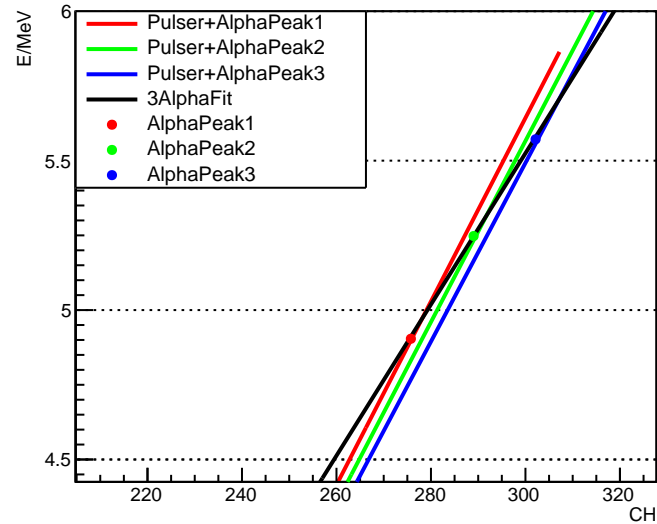
SSD1_L1S_Switch_Alpha00_04_CH03



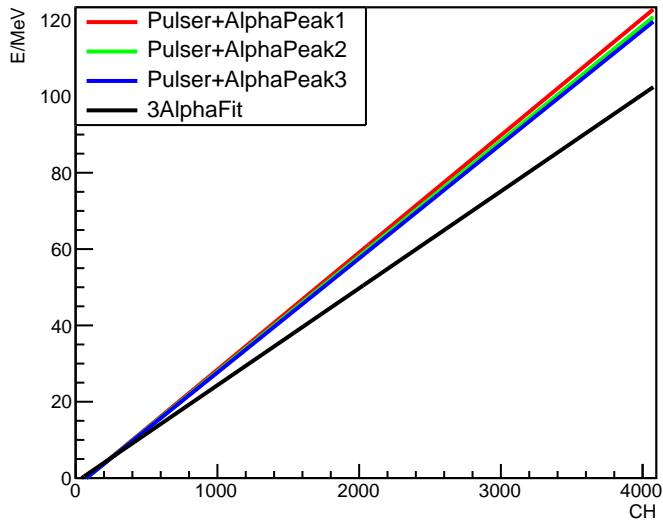
SSD1_L1S_Switch_Alpha00_04_CH03



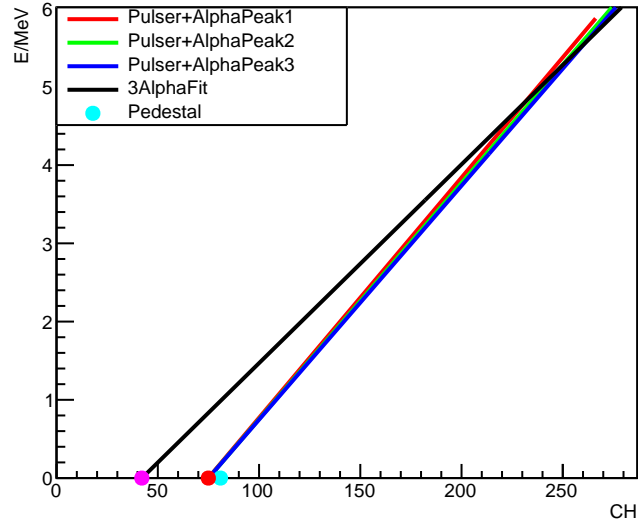
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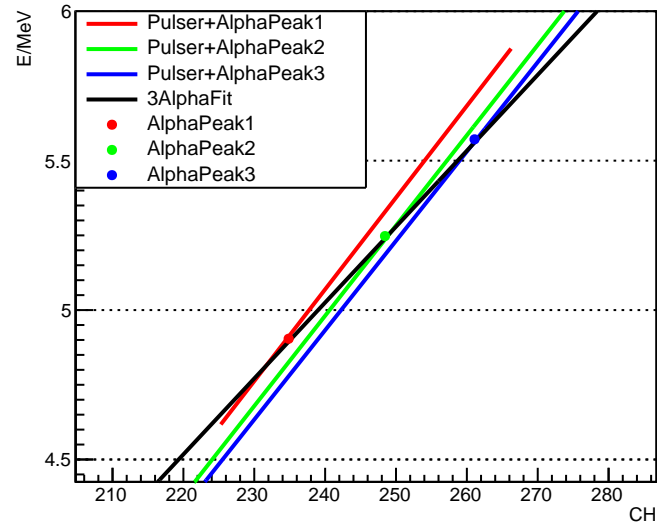
SSD1_L1S_Switch_Alpha00_04_CH04



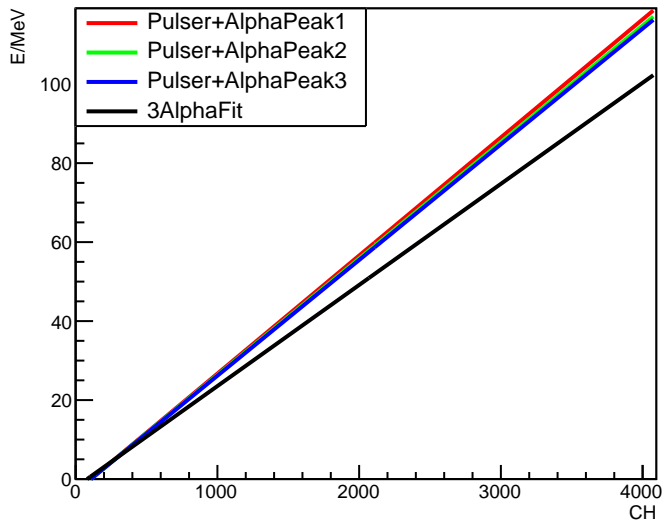
SSD1_L1S_Switch_Alpha00_04_CH04



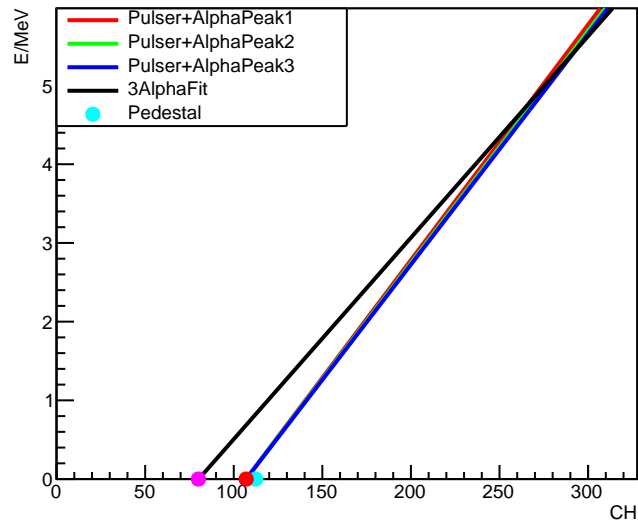
SSD1_L1S_Switch_Alpha00_04_CH04



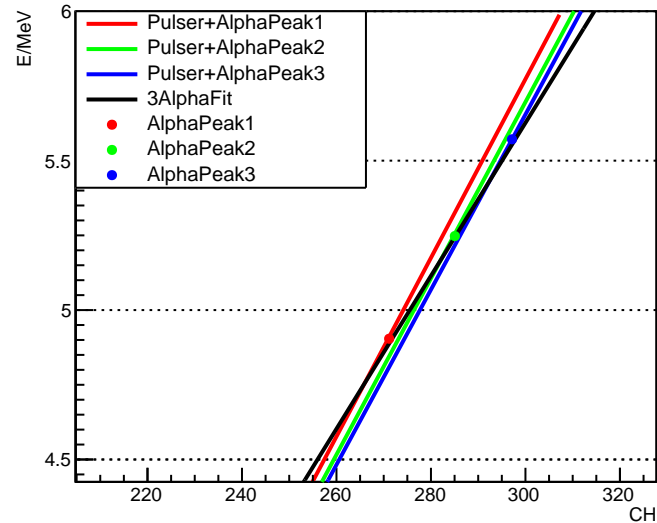
SSD1_L1S_Switch_Alpha00_04_CH05



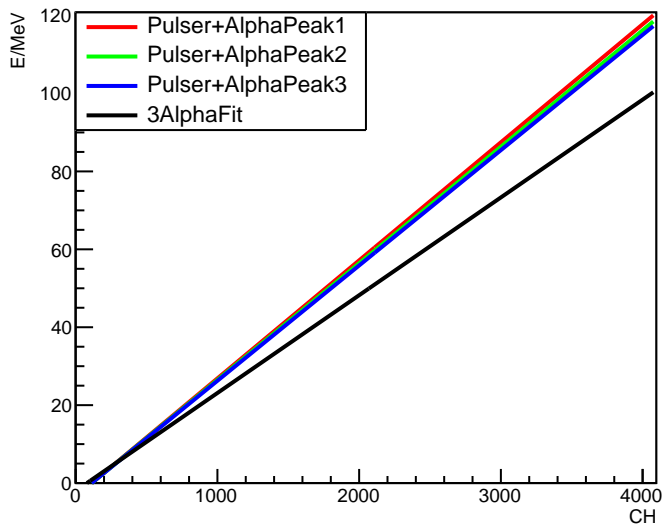
SSD1_L1S_Switch_Alpha00_04_CH05



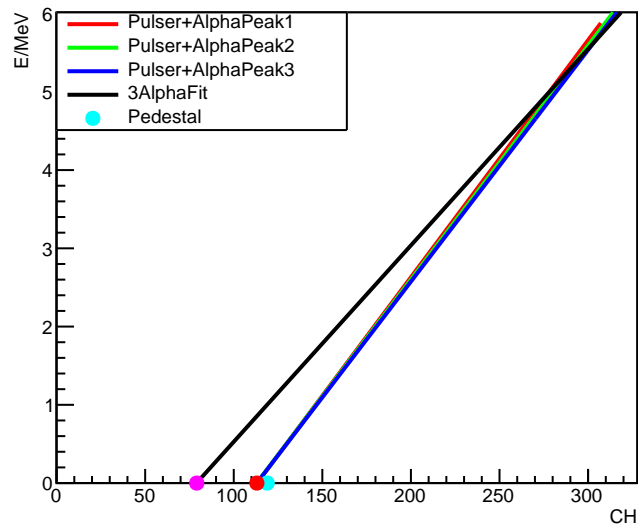
SSD1_L1S_Switch_Alpha00_04_CH05



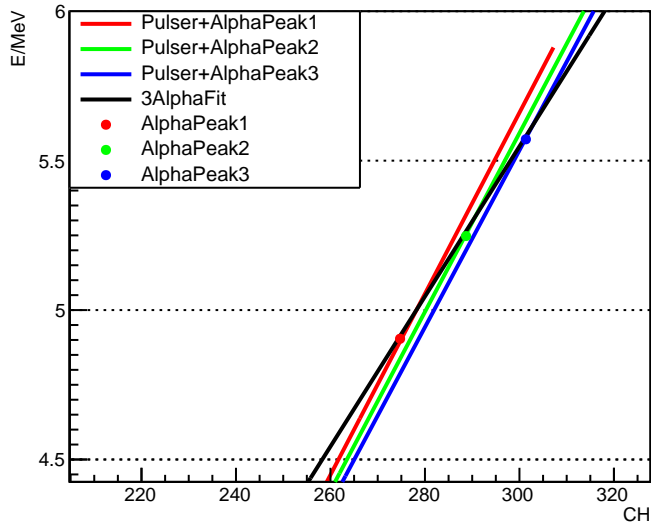
SSD1_L1S_Switch_Alpha00_04_CH06



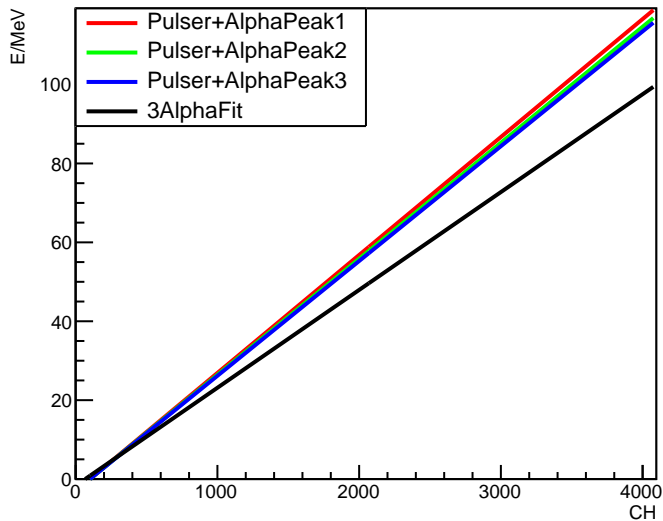
SSD1_L1S_Switch_Alpha00_04_CH06



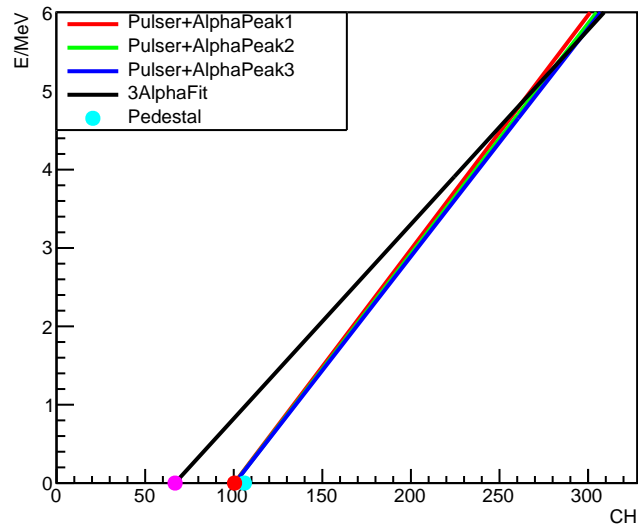
SSD1_L1S_Switch_Alpha00_04_CH06



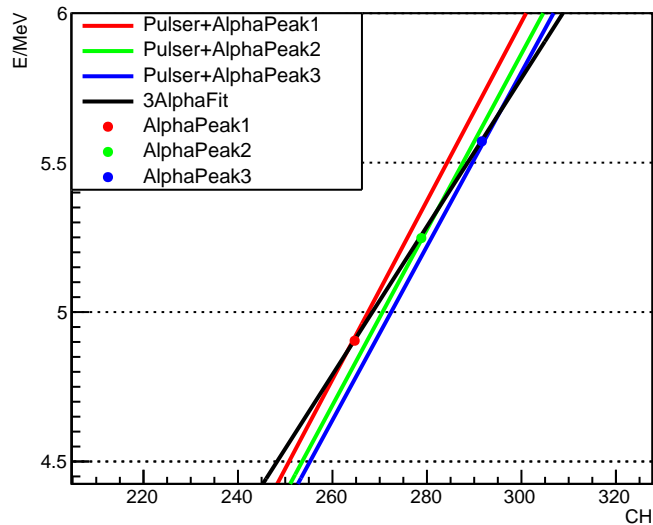
SSD1_L1S_Switch_Alpha00_04_CH07



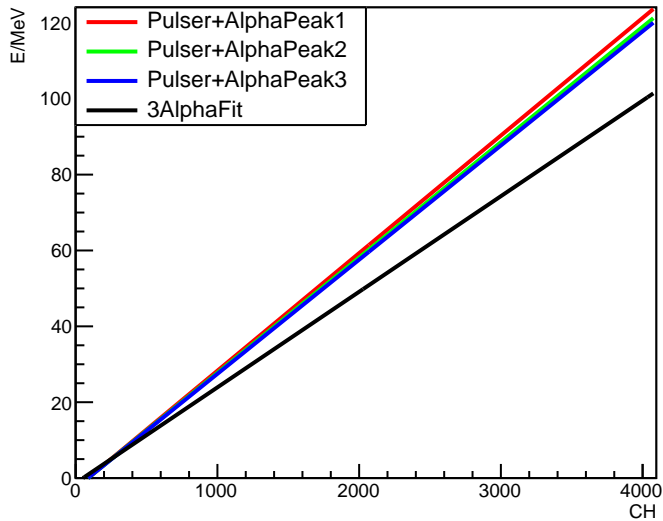
SSD1_L1S_Switch_Alpha00_04_CH07



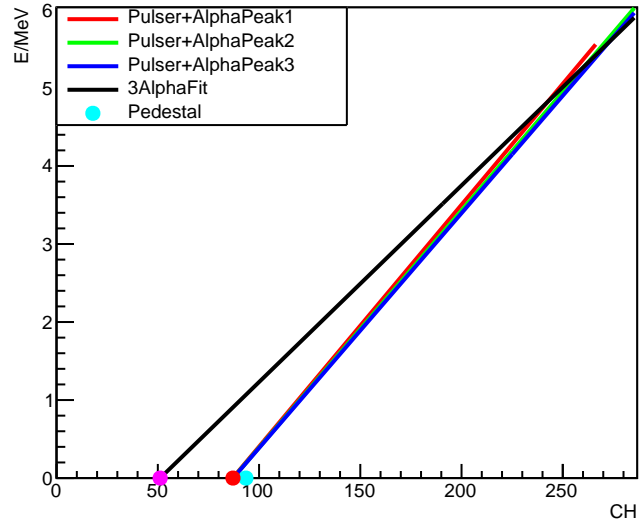
SSD1_L1S_Switch_Alpha00_04_CH07



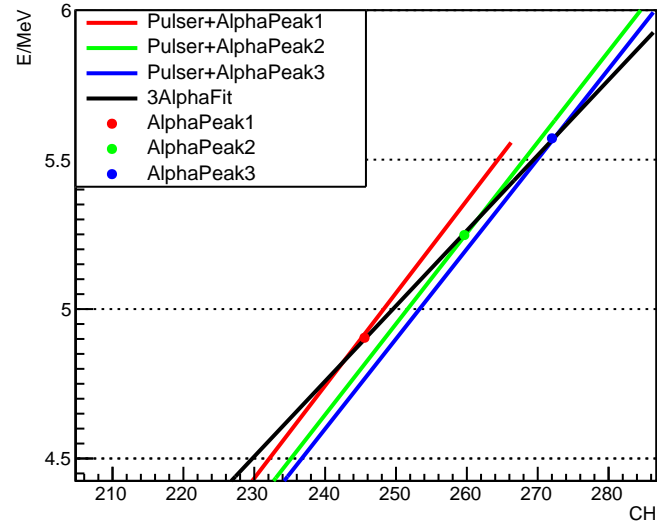
SSD1_L1S_Switch_Alpha00_04_CH08



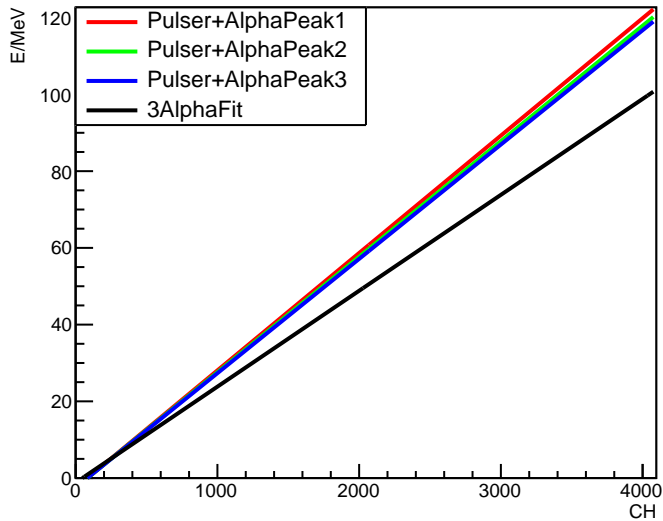
SSD1_L1S_Switch_Alpha00_04_CH08



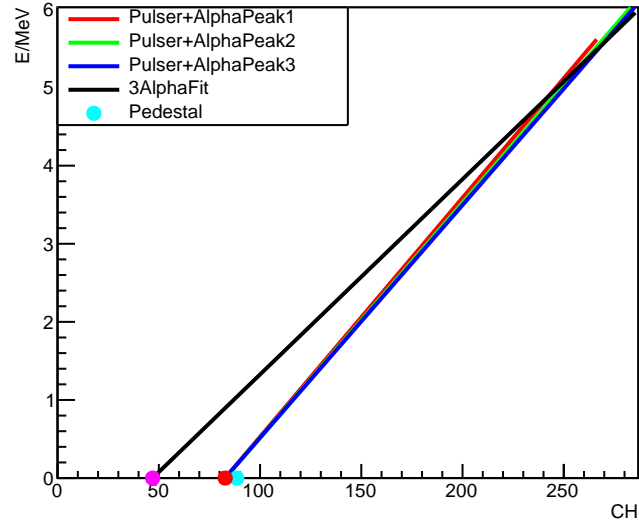
SSD1_L1S_Switch_Alpha00_04_CH08



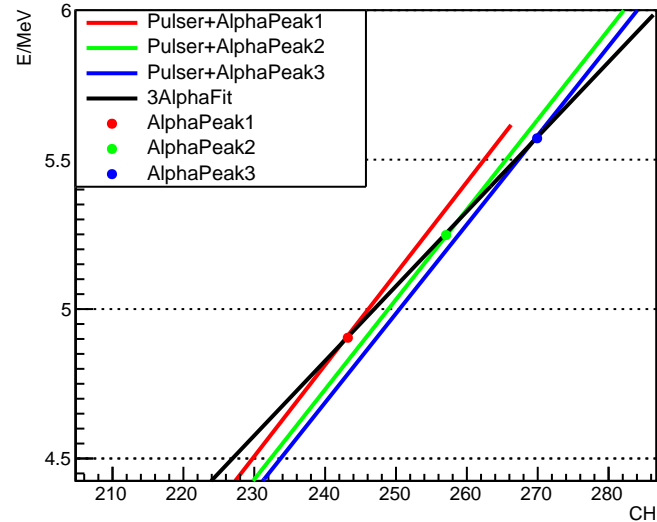
SSD1_L1S_Switch_Alpha00_04_CH09



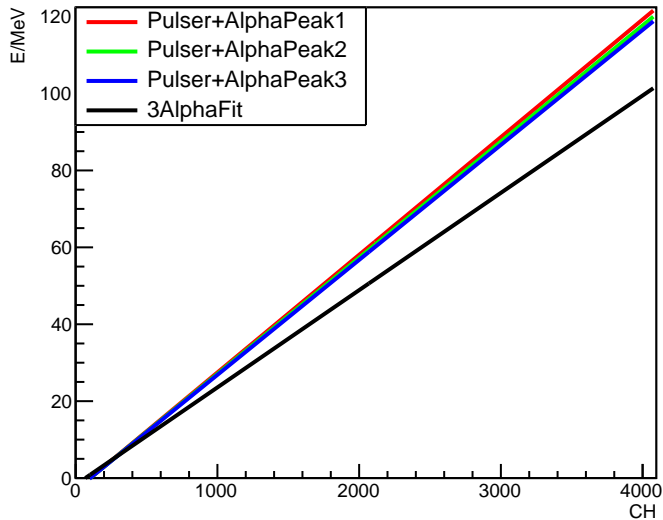
SSD1_L1S_Switch_Alpha00_04_CH09



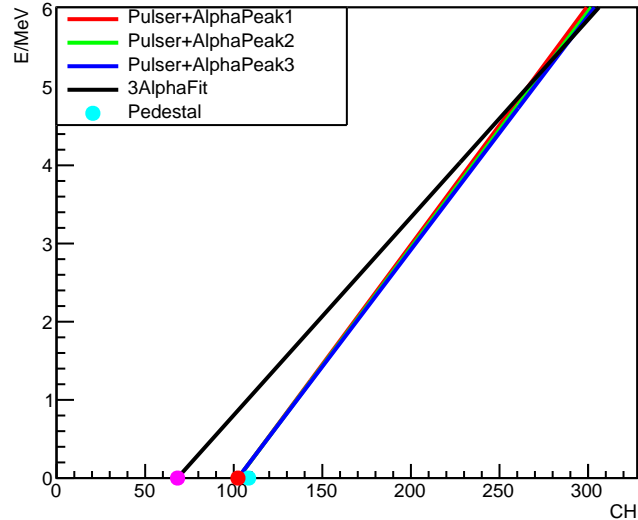
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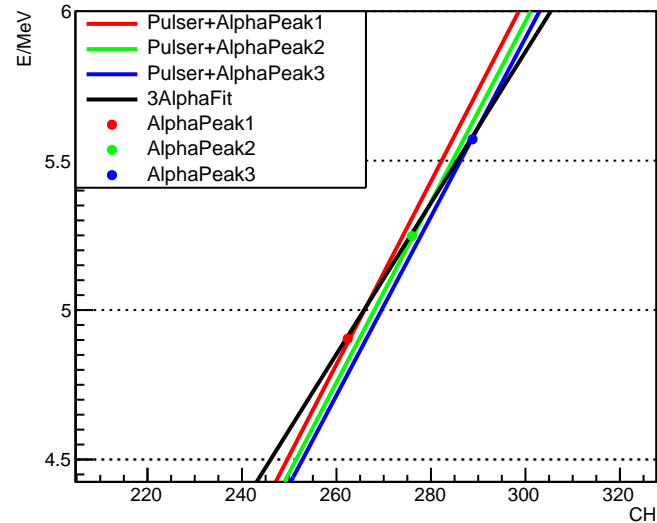
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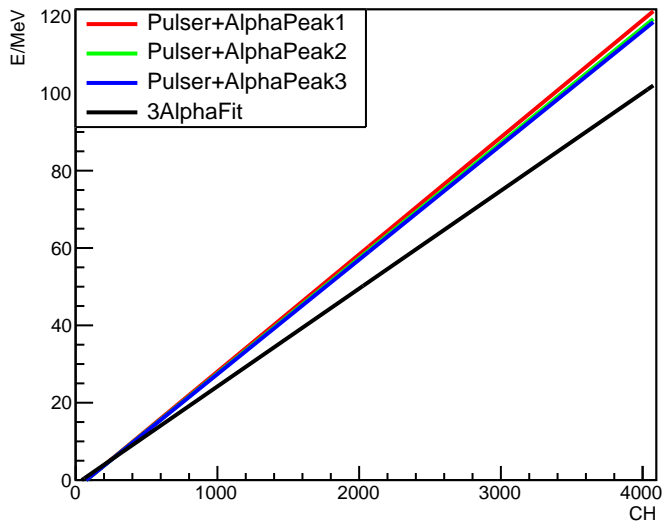
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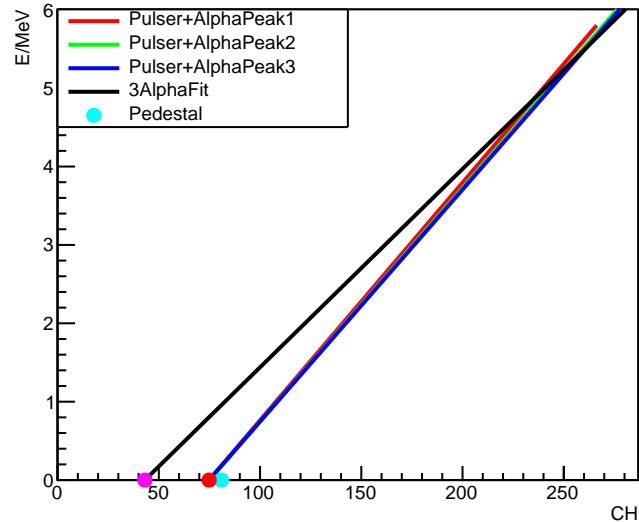
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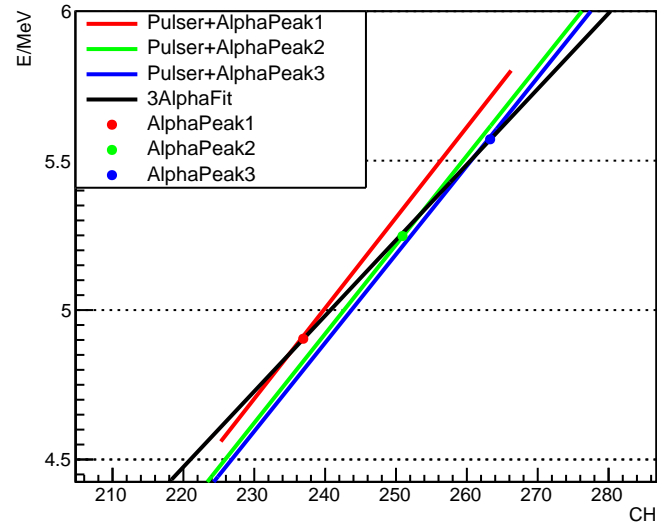
SSD1_L1S_Switch_Alpha00_04_CH11



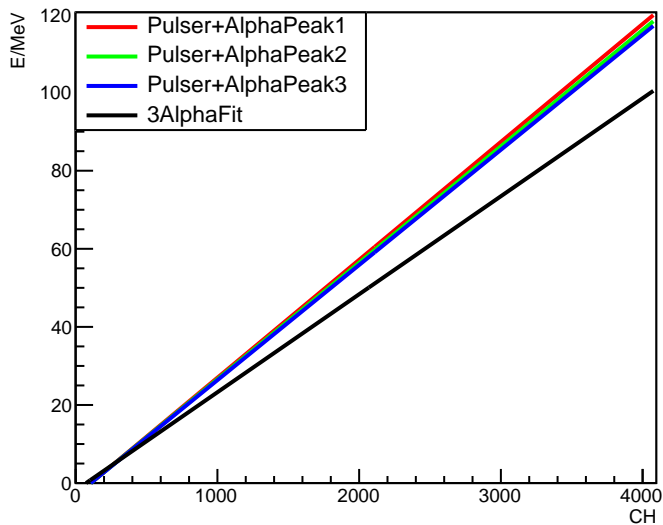
SSD1_L1S_Switch_Alpha00_04_CH11



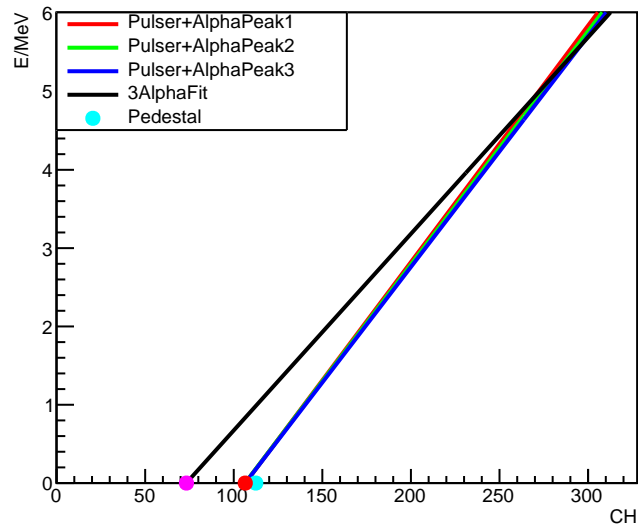
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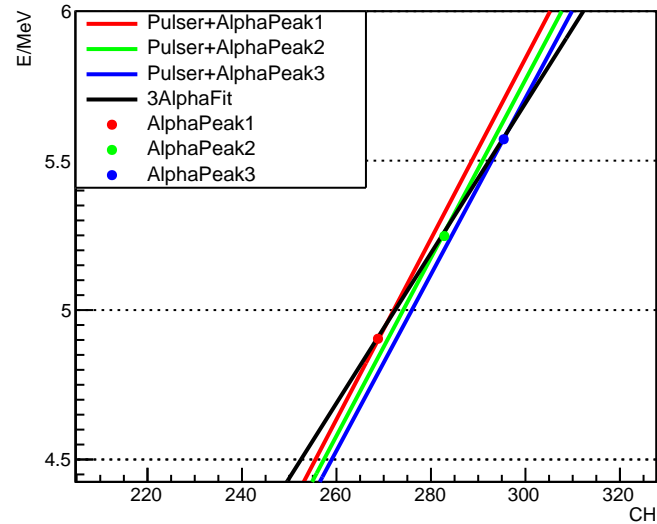
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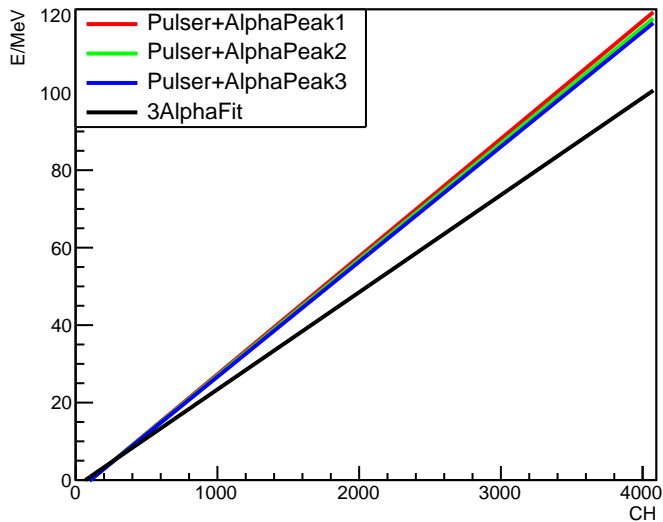
SSD1_L1S_Switch_Alpha00_04_CH12



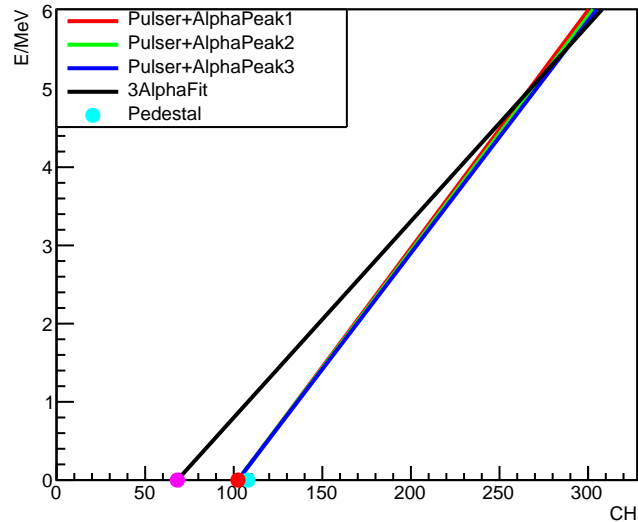
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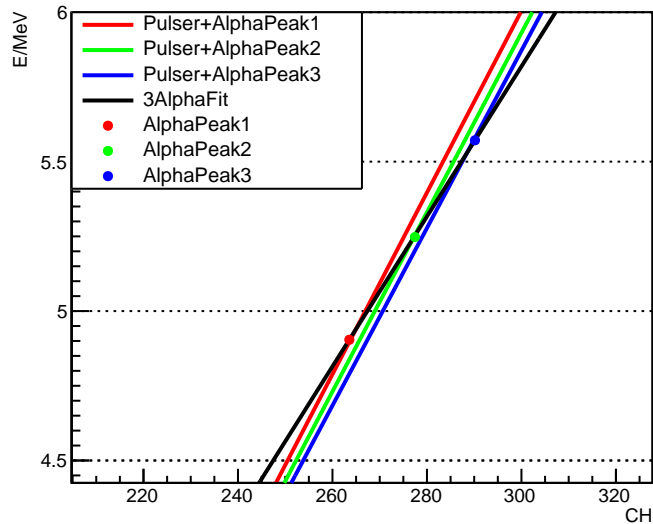
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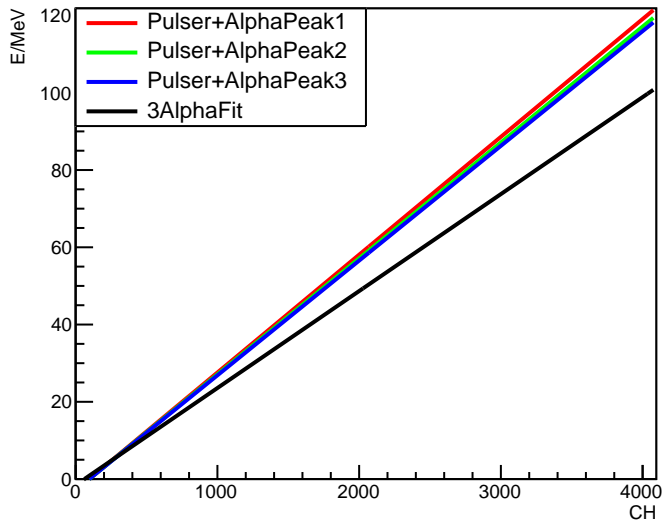
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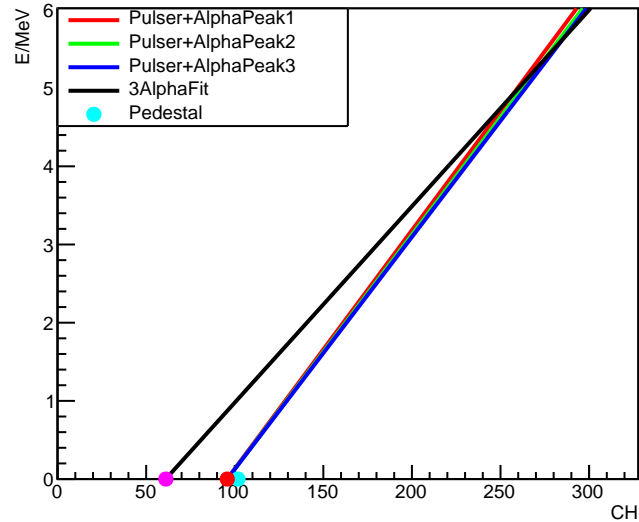
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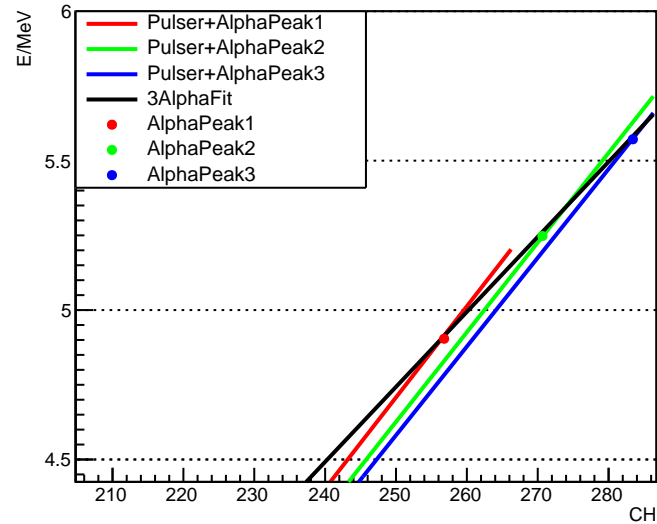
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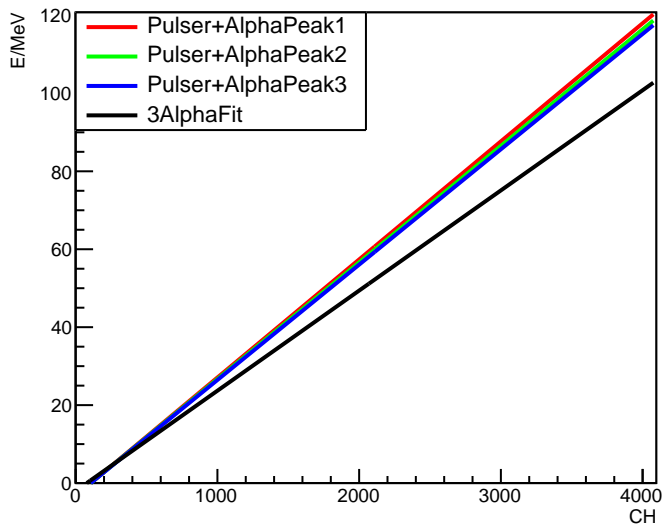
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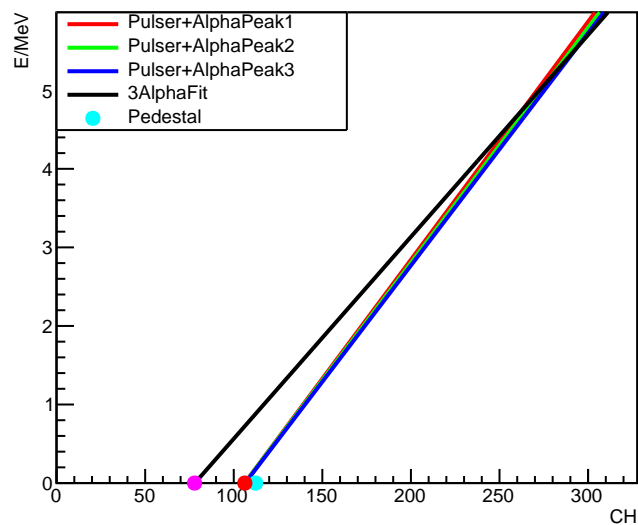
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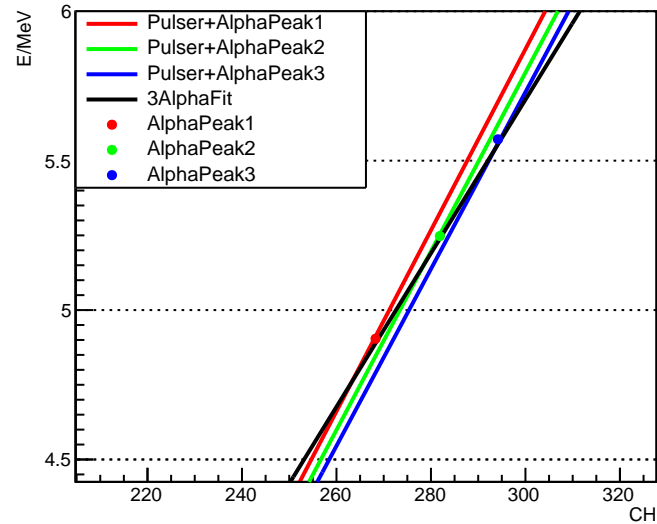
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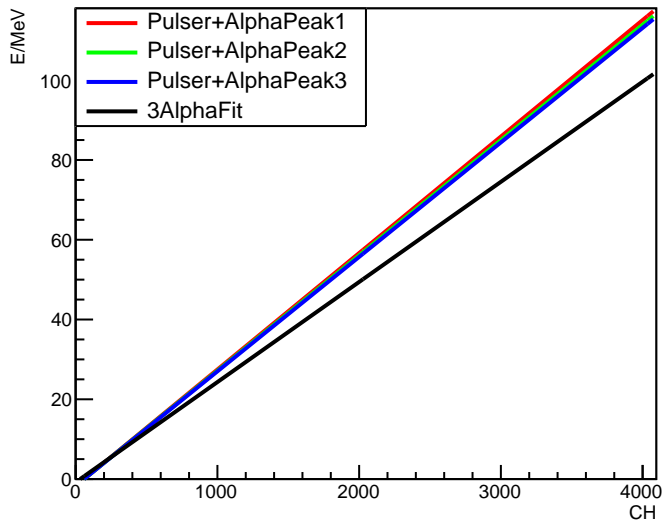
SSD1_L1S_Switch_Alpha00_04_CH15



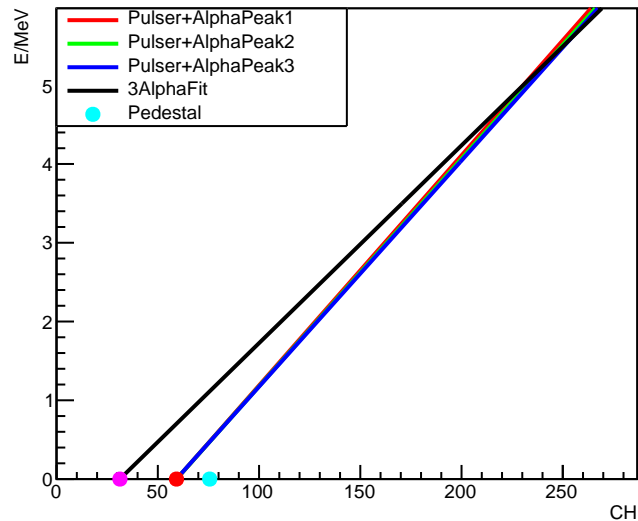
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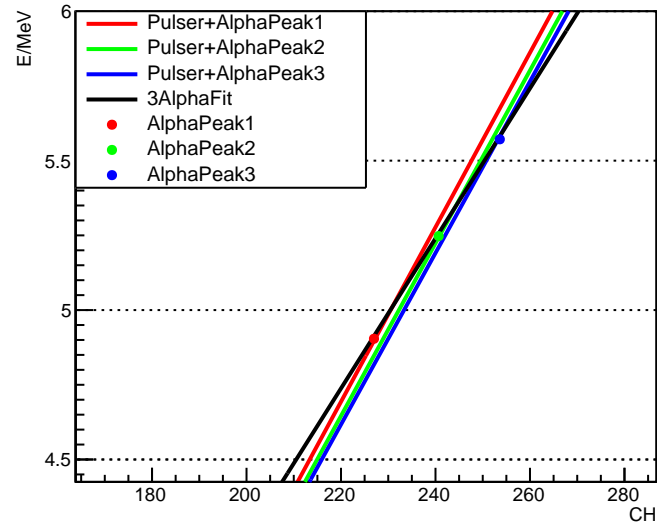
SSD2_L1S_Switch_Alpha00_04_CH00



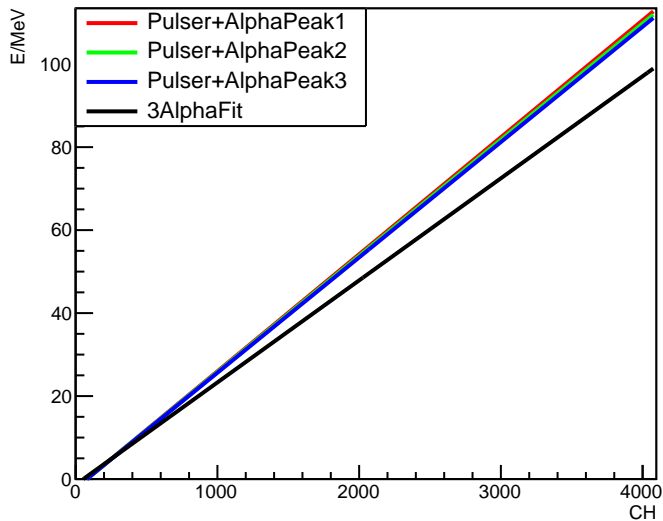
SSD2_L1S_Switch_Alpha00_04_CH00



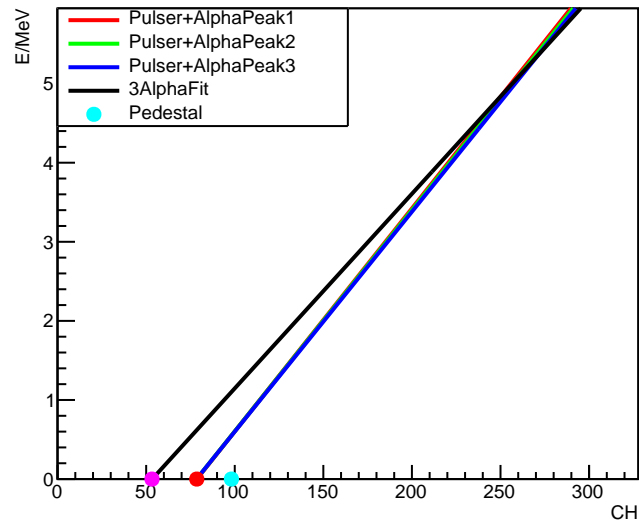
SSD2_L1S_Switch_Alpha00_04_CH00



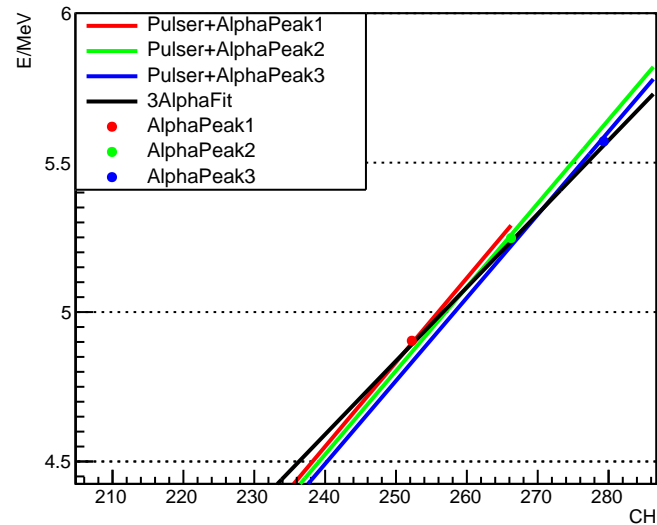
SSD2_L1S_Switch_Alpha00_04_CH01



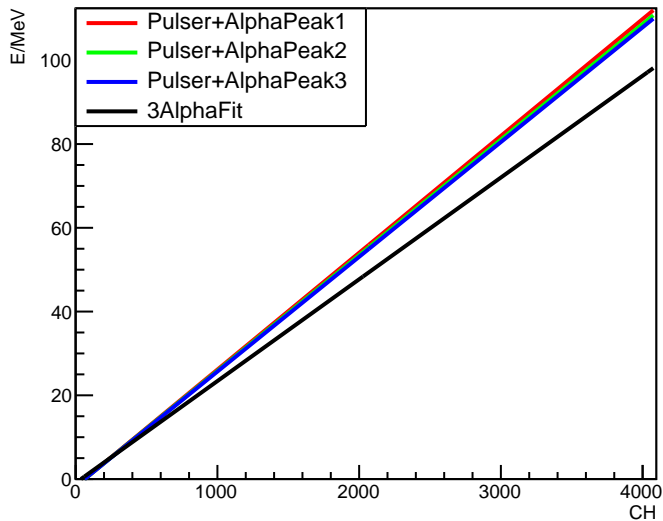
SSD2_L1S_Switch_Alpha00_04_CH01



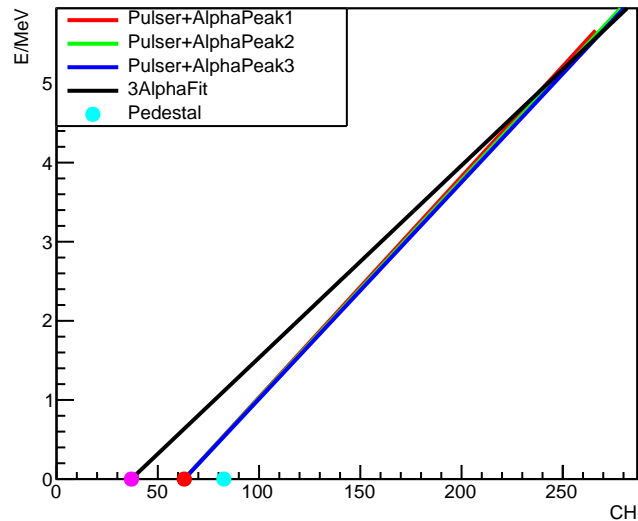
SSD2_L1S_Switch_Alpha00_04_CH01



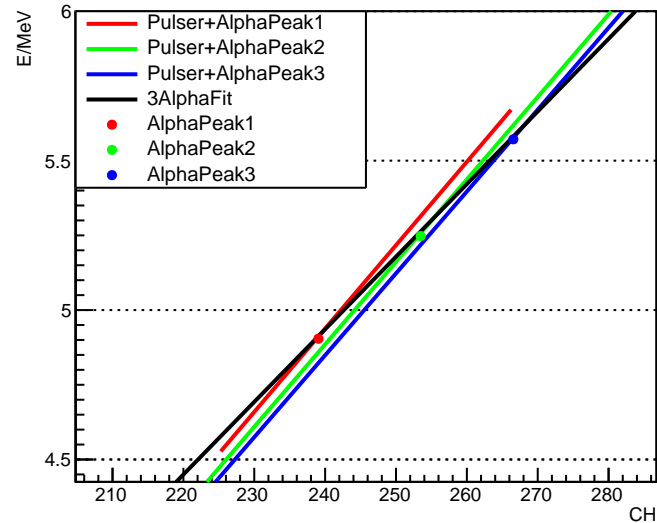
SSD2_L1S_Switch_Alpha00_04_CH02



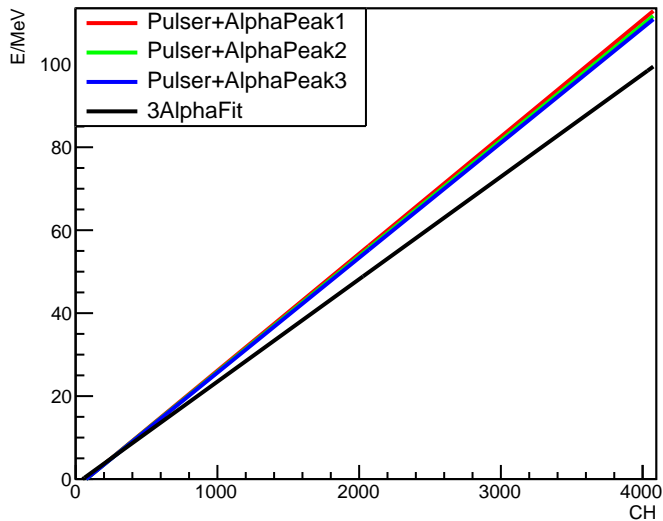
SSD2_L1S_Switch_Alpha00_04_CH02



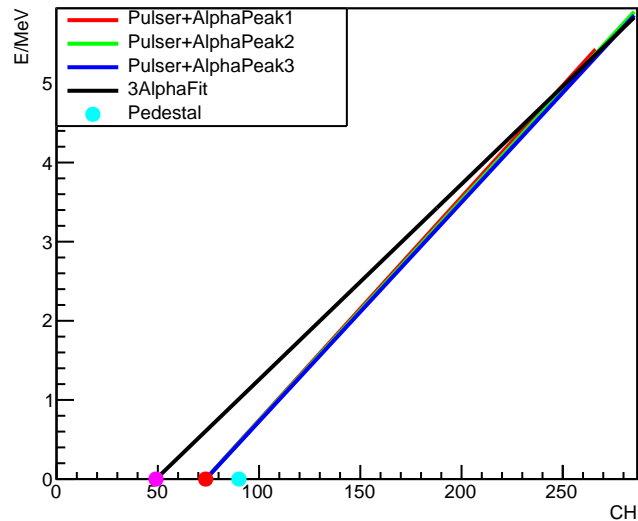
SSD2_L1S_Switch_Alpha00_04_CH02



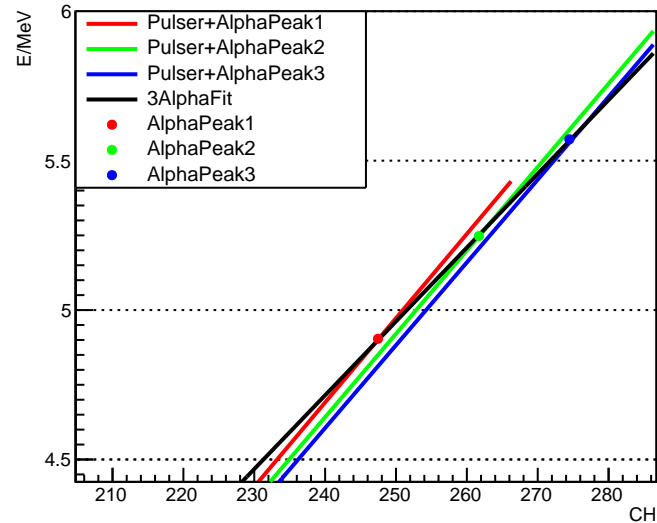
SSD2_L1S_Switch_Alpha00_04_CH03



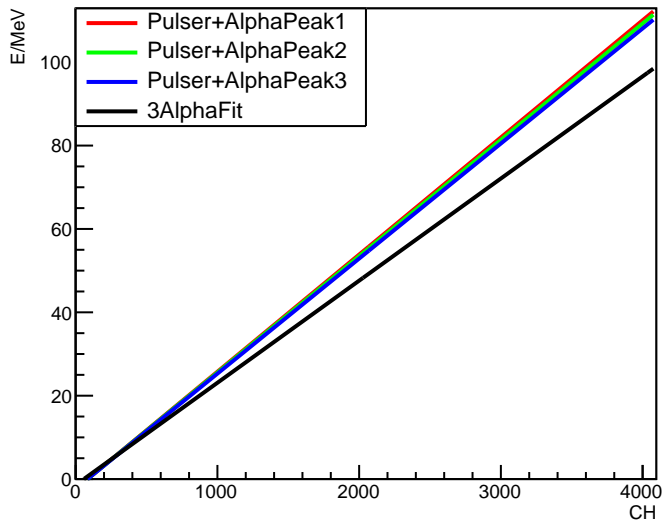
SSD2_L1S_Switch_Alpha00_04_CH03



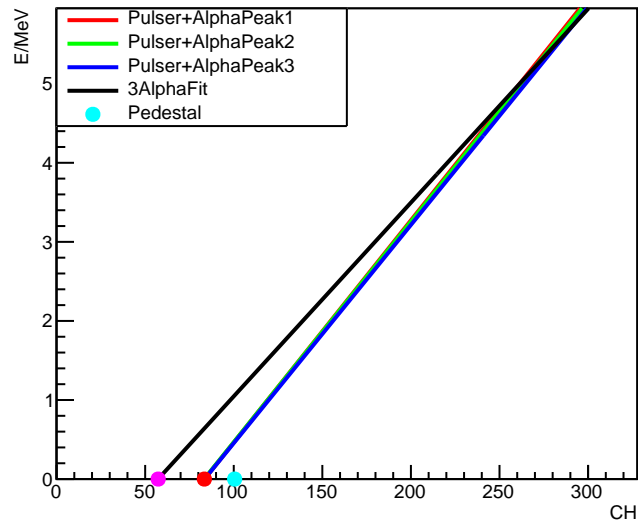
SSD2_L1S_Switch_Alpha00_04_CH03



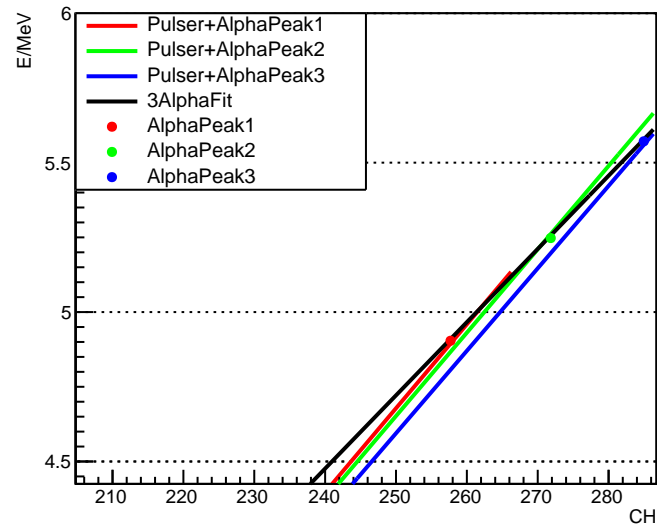
SSD2_L1S_Switch_Alpha00_04_CH04



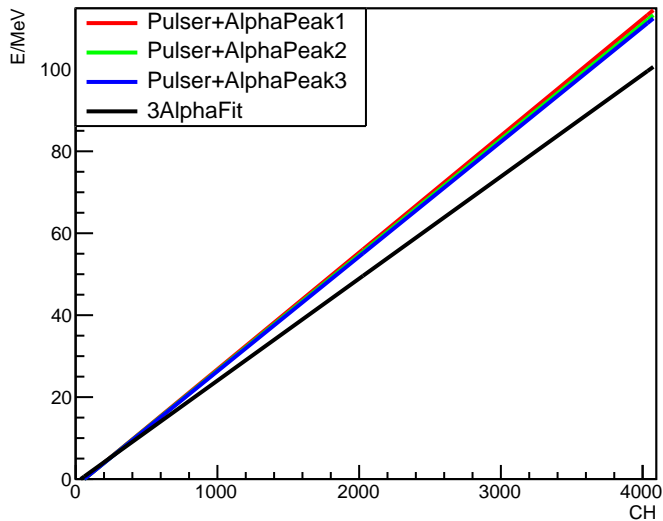
SSD2_L1S_Switch_Alpha00_04_CH04



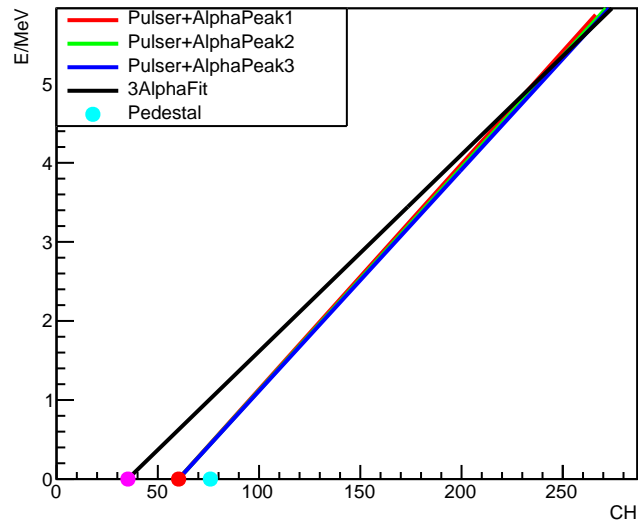
SSD2_L1S_Switch_Alpha00_04_CH04



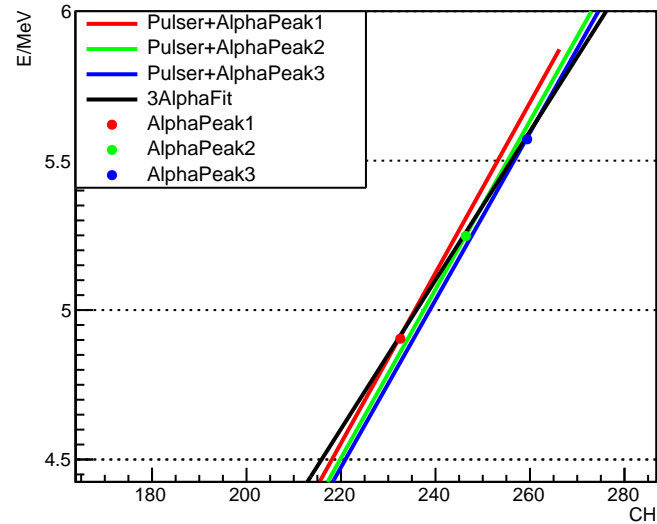
SSD2_L1S_Switch_Alpha00_04_CH05



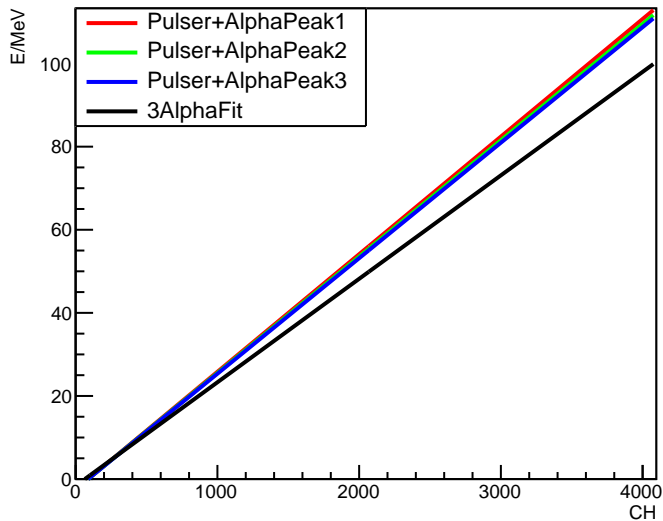
SSD2_L1S_Switch_Alpha00_04_CH05



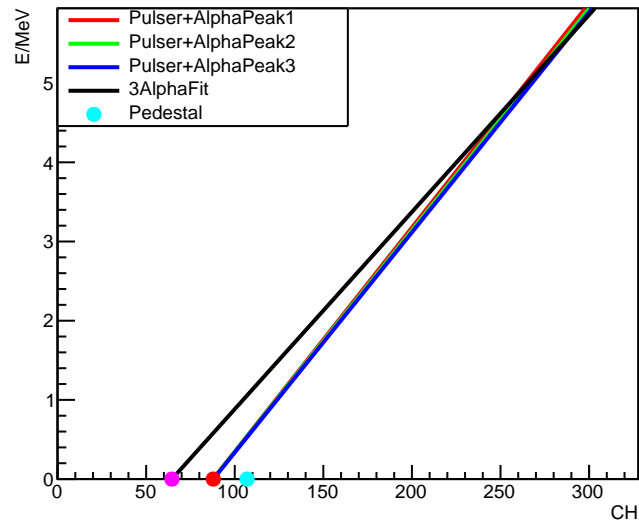
SSD2_L1S_Switch_Alpha00_04_CH05



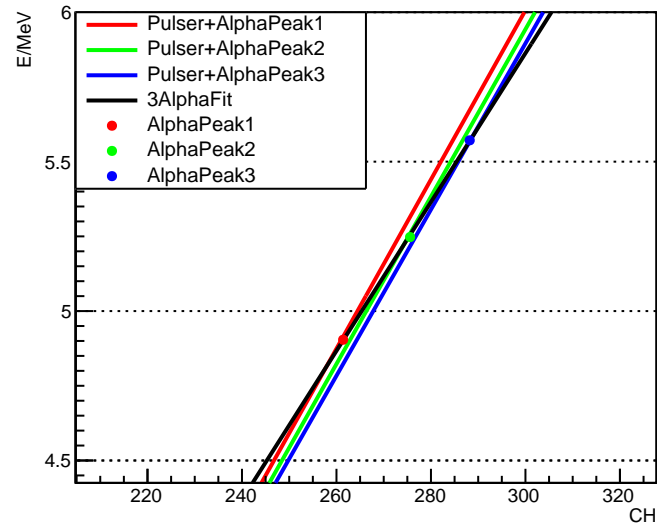
SSD2_L1S_Switch_Alpha00_04_CH06



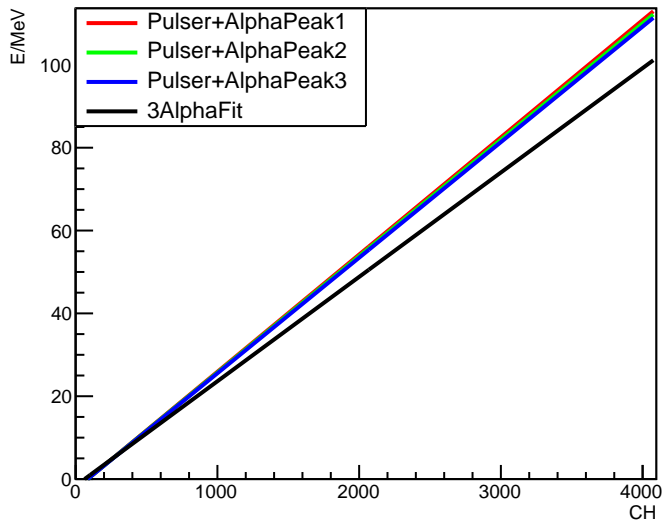
SSD2_L1S_Switch_Alpha00_04_CH06



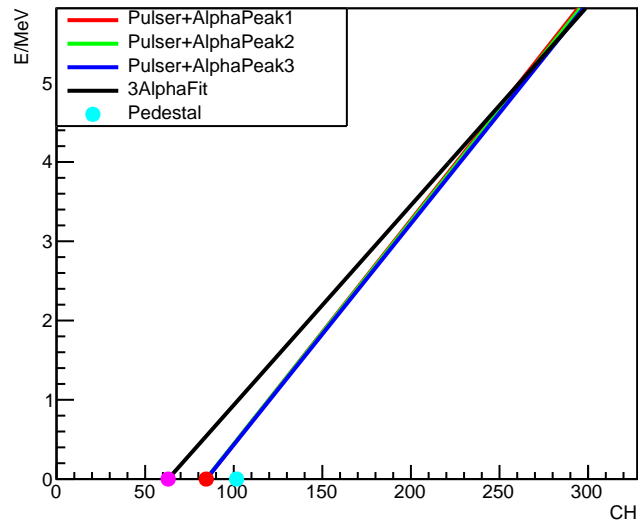
SSD2_L1S_Switch_Alpha00_04_CH06



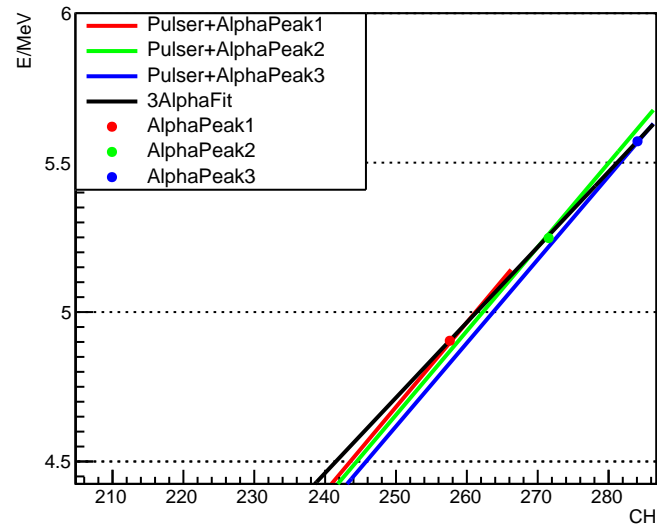
SSD2_L1S_Switch_Alpha00_04_CH07



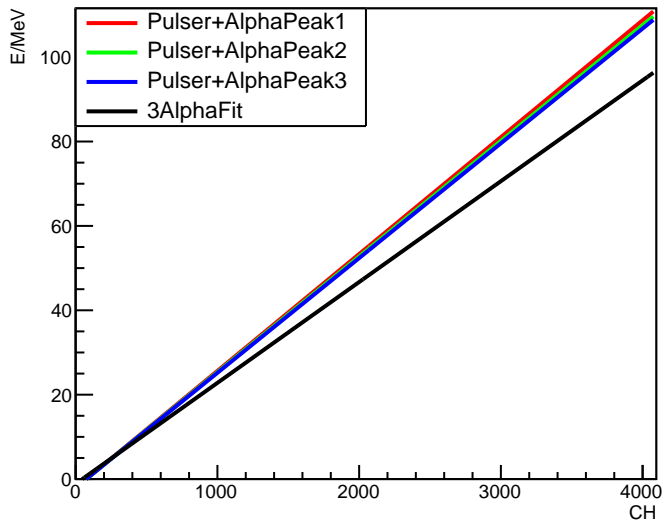
SSD2_L1S_Switch_Alpha00_04_CH07



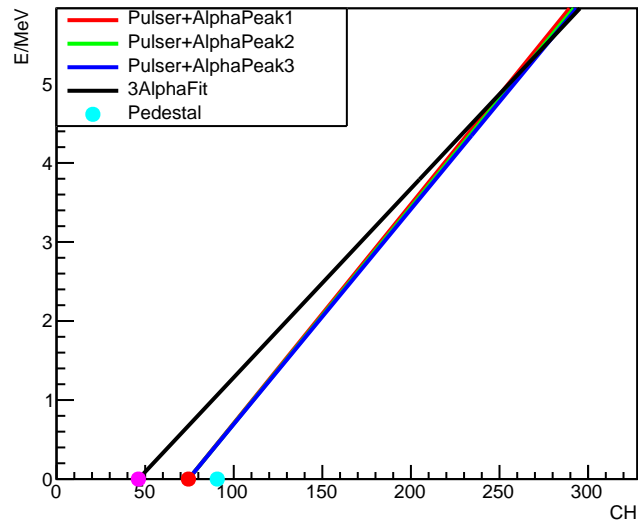
SSD2_L1S_Switch_Alpha00_04_CH07



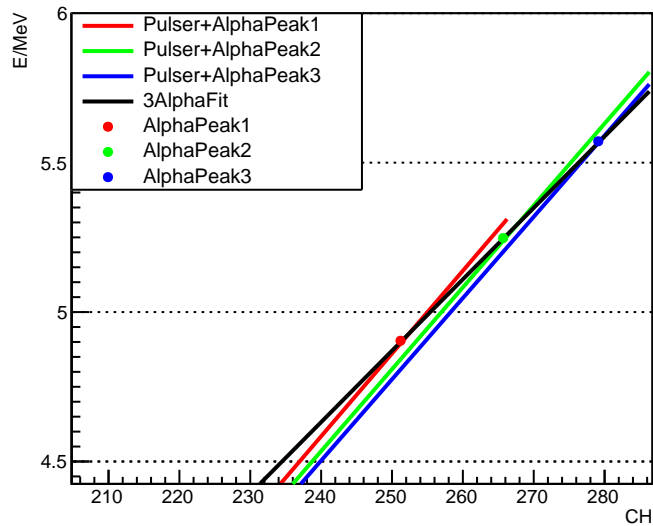
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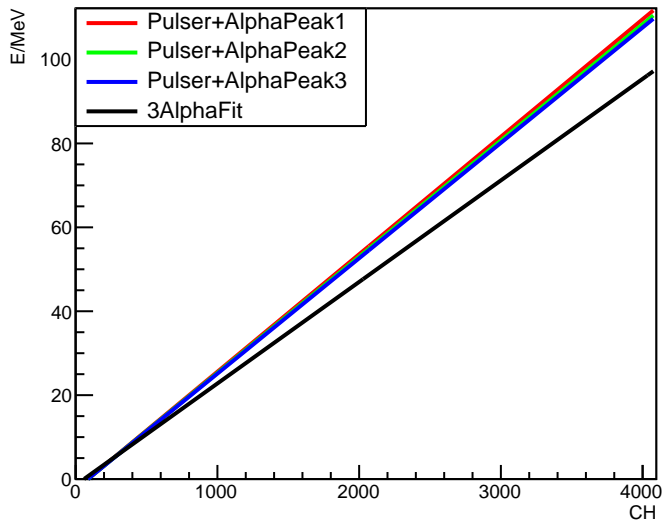
SSD2_L1S_Switch_Alpha00_04_CH08



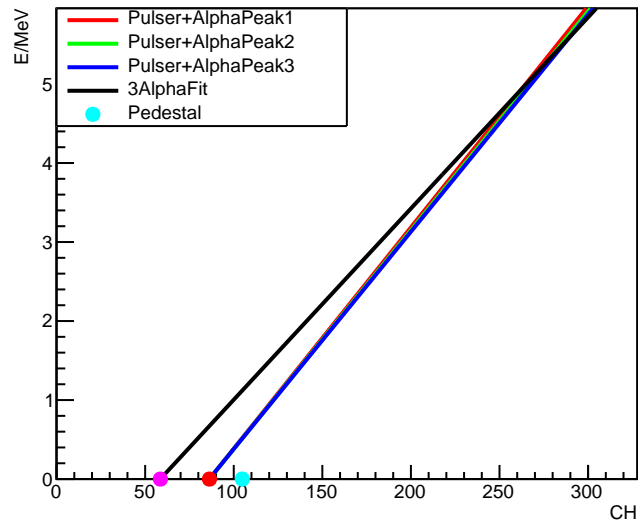
SSD2_L1S_Switch_Alpha00_04_CH08



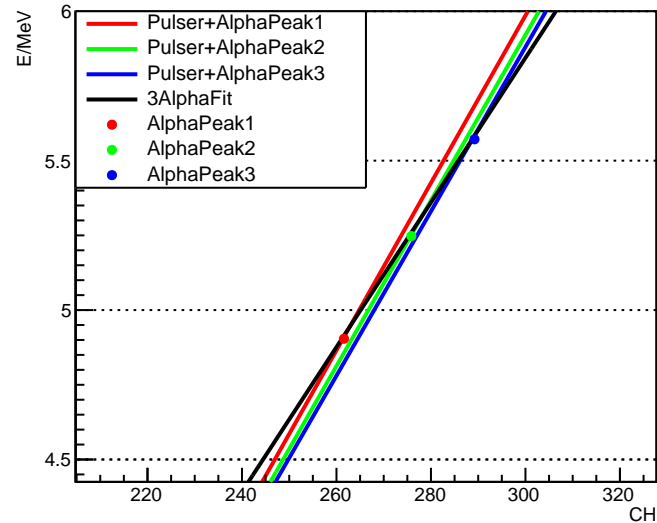
SSD2_L1S_Switch_Alpha00_04_CH09



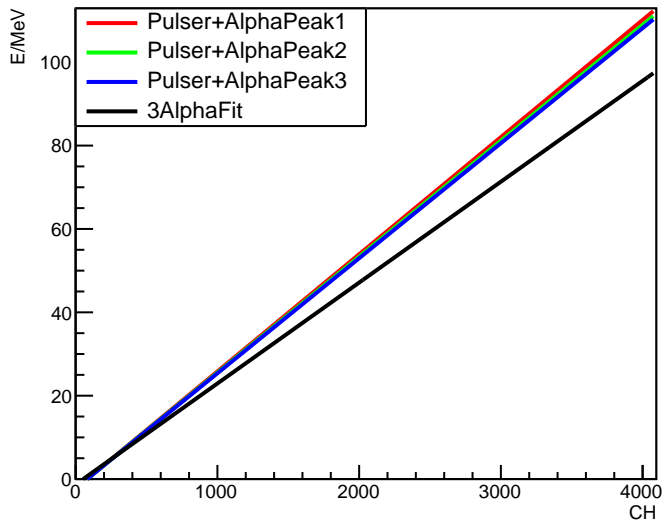
SSD2_L1S_Switch_Alpha00_04_CH09



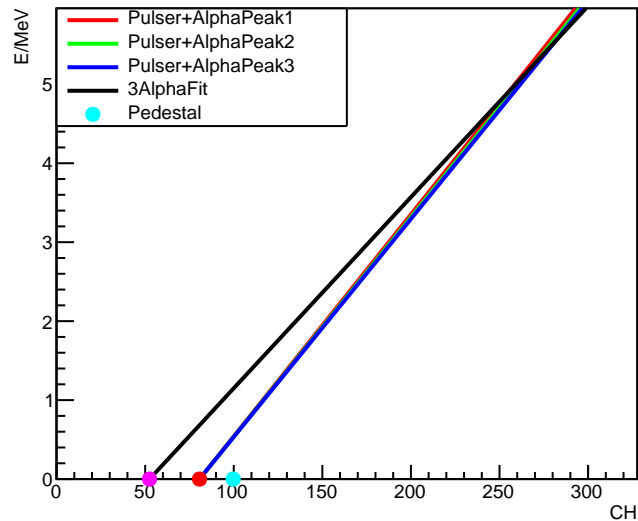
SSD2_L1S_Switch_Alpha00_04_CH09



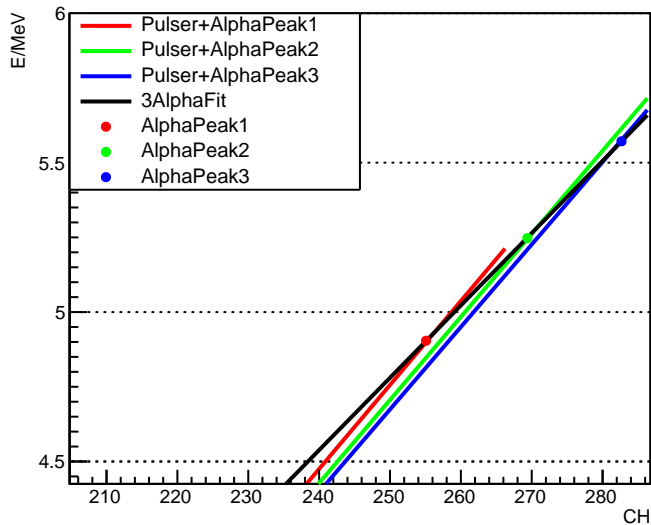
SSD2_L1S_Switch_Alpha00_04_CH10



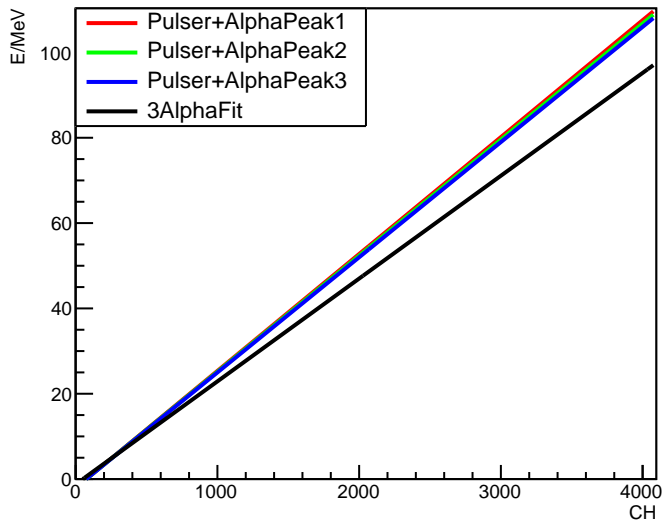
SSD2_L1S_Switch_Alpha00_04_CH10



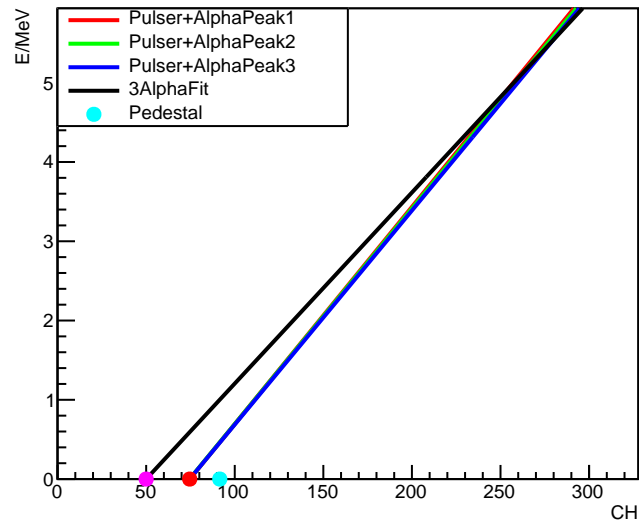
SSD2_L1S_Switch_Alpha00_04_CH10



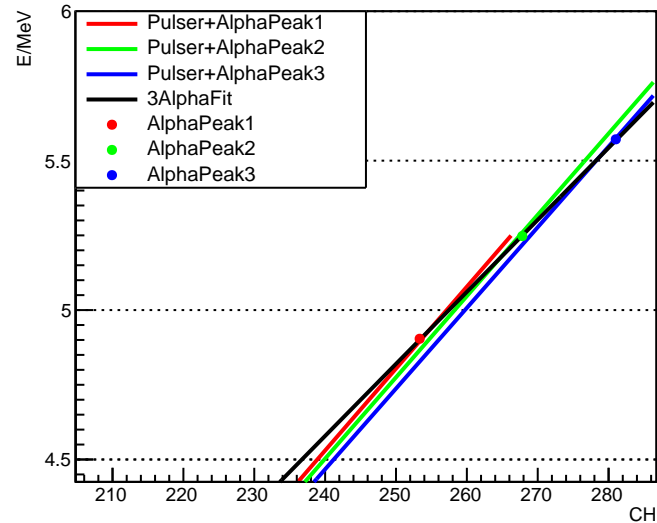
SSD2_L1S_Switch_Alpha00_04_CH11



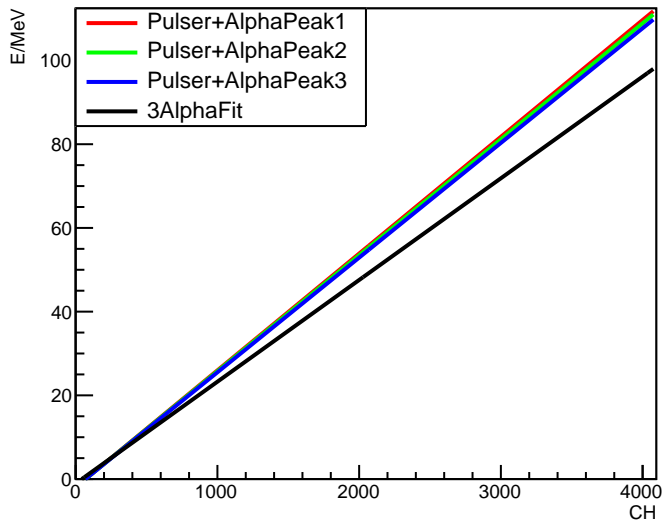
SSD2_L1S_Switch_Alpha00_04_CH11



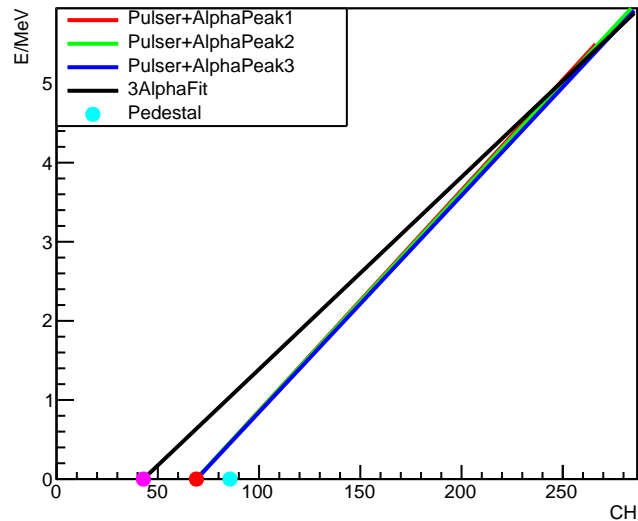
SSD2_L1S_Switch_Alpha00_04_CH11



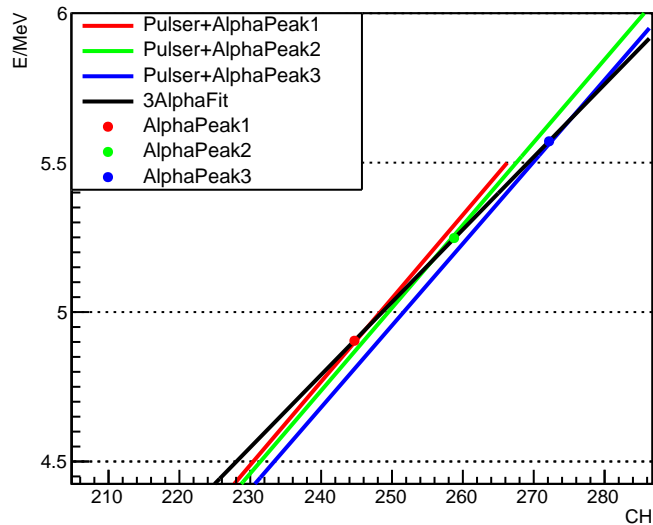
SSD2_L1S_Switch_Alpha00_04_CH12



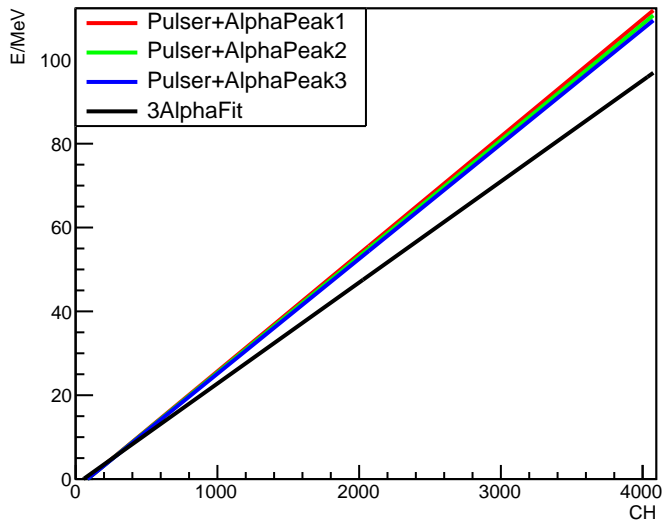
SSD2_L1S_Switch_Alpha00_04_CH12



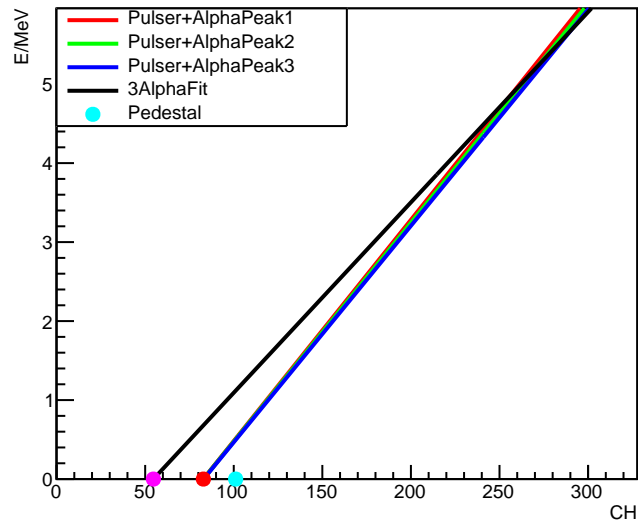
SSD2_L1S_Switch_Alpha00_04_CH12



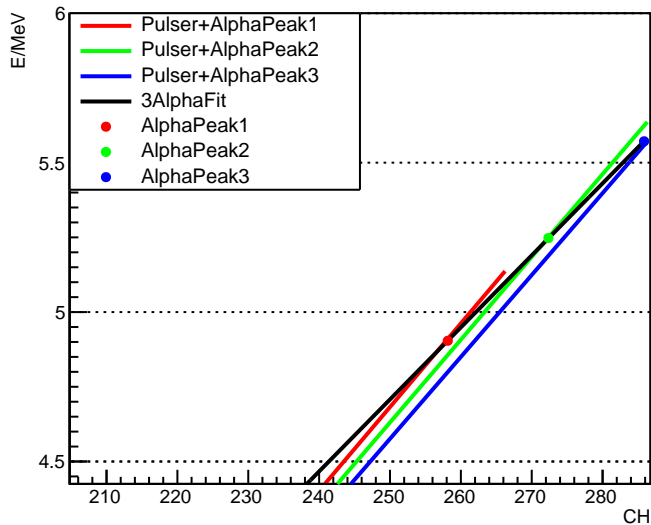
SSD2_L1S_Switch_Alpha00_04_CH13



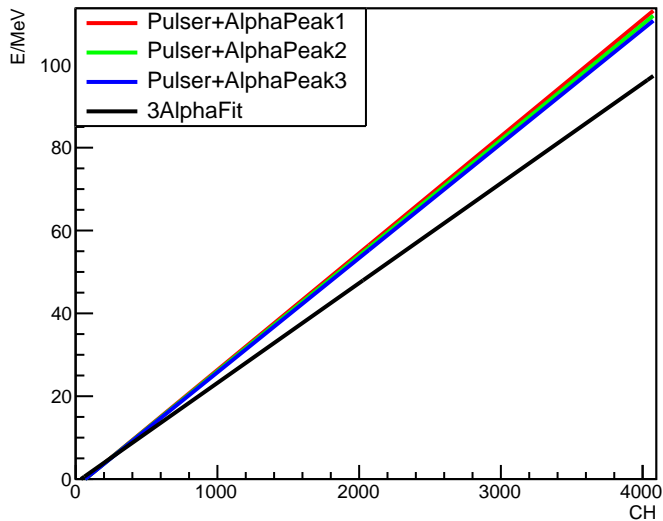
SSD2_L1S_Switch_Alpha00_04_CH13



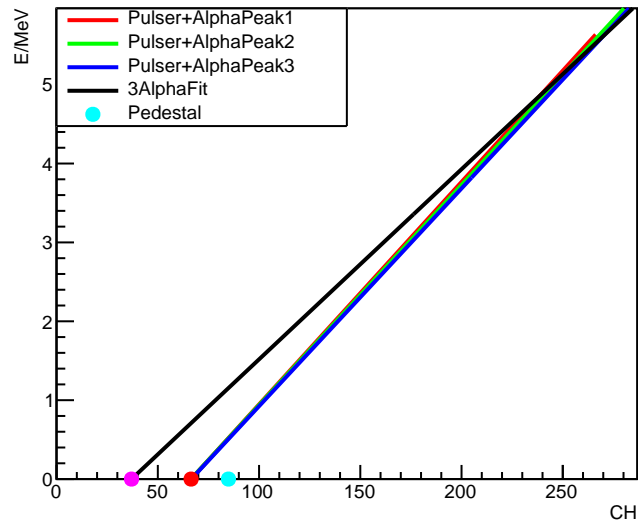
SSD2_L1S_Switch_Alpha00_04_CH13



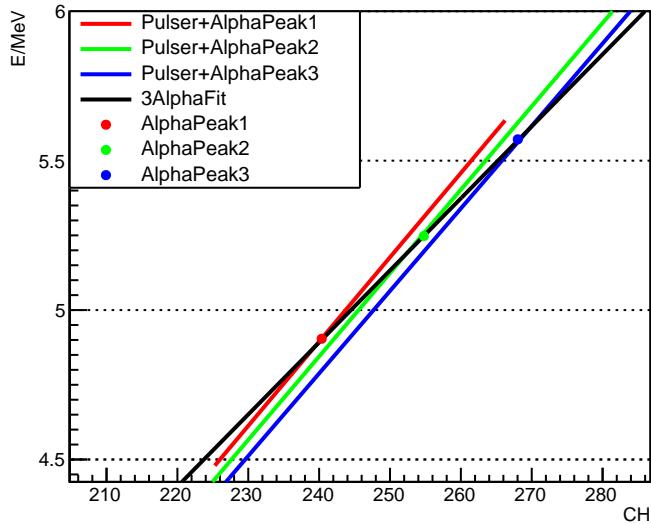
SSD2_L1S_Switch_Alpha00_04_CH14



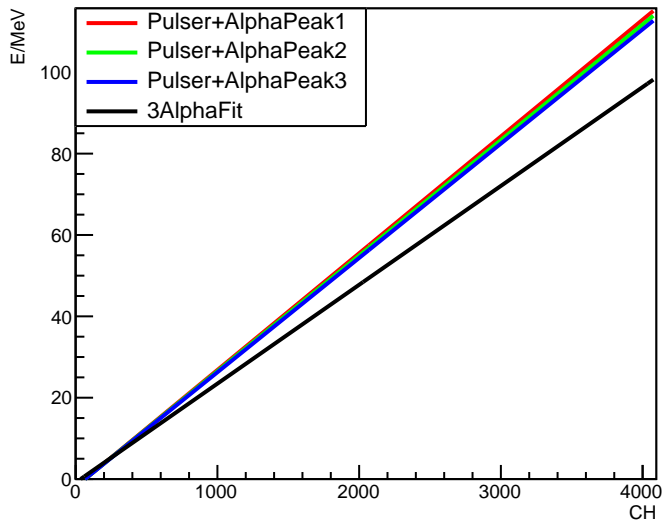
SSD2_L1S_Switch_Alpha00_04_CH14



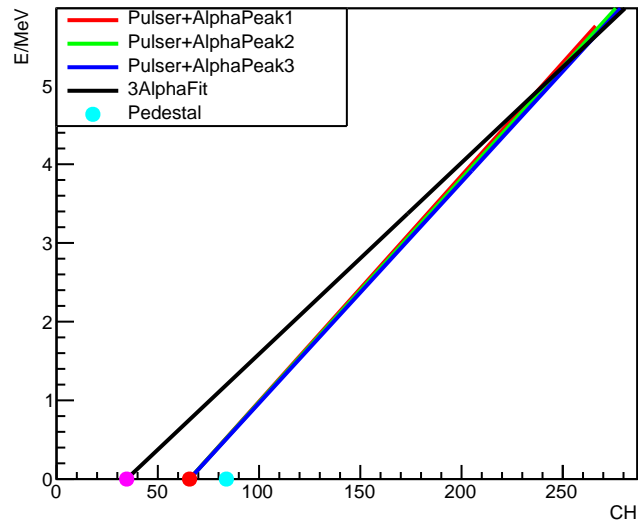
SSD2_L1S_Switch_Alpha00_04_CH14



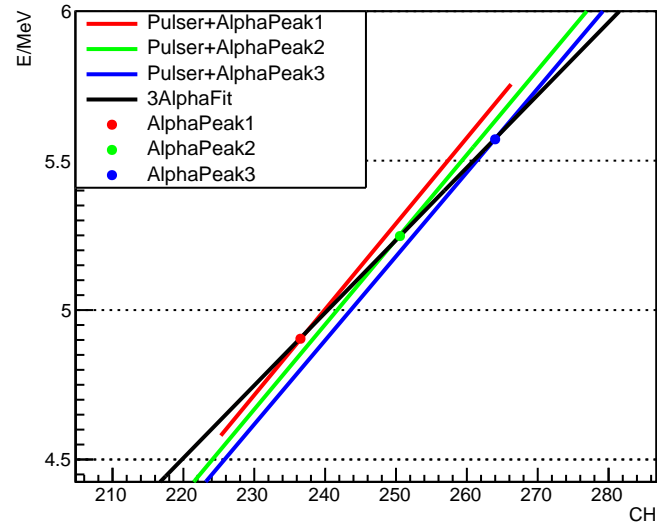
SSD2_L1S_Switch_Alpha00_04_CH15



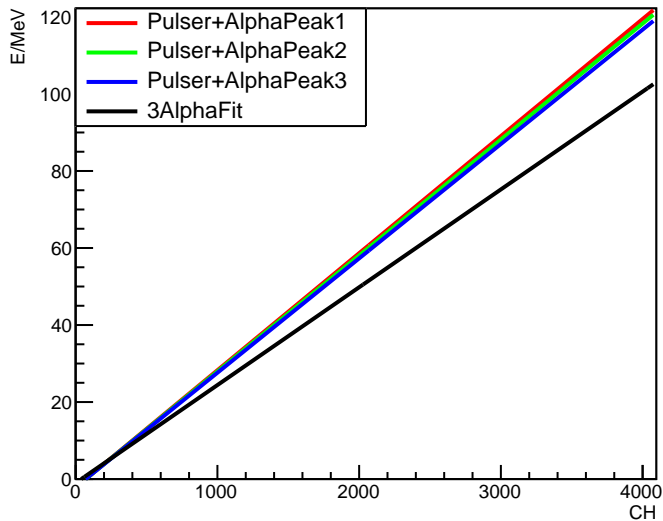
SSD2_L1S_Switch_Alpha00_04_CH15



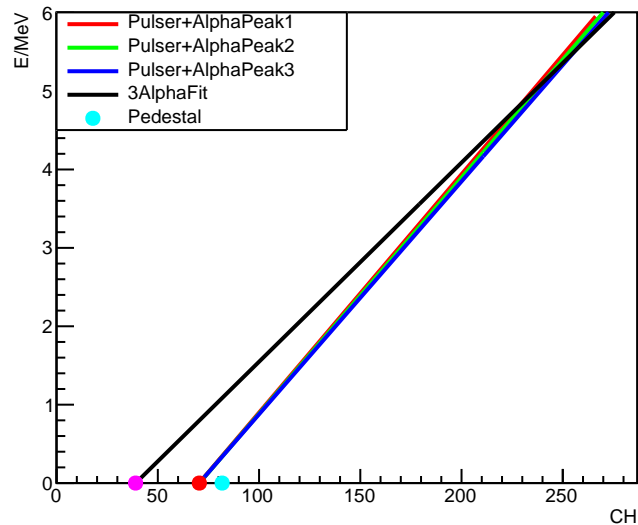
SSD2_L1S_Switch_Alpha00_04_CH15



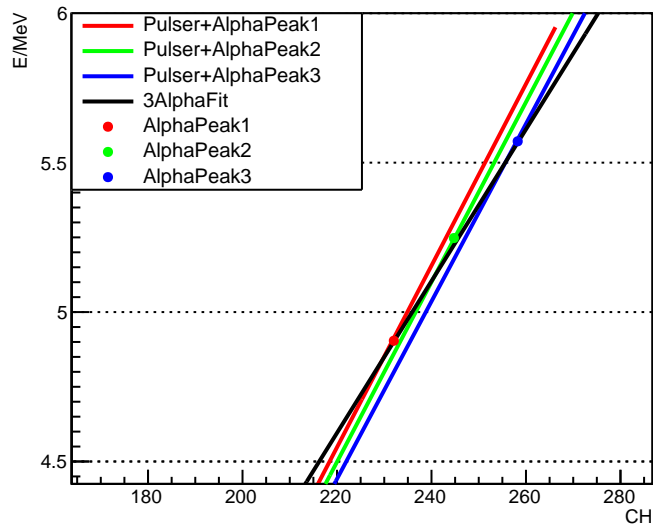
SSD3_L1S_Switch_Alpha00_04_CH00



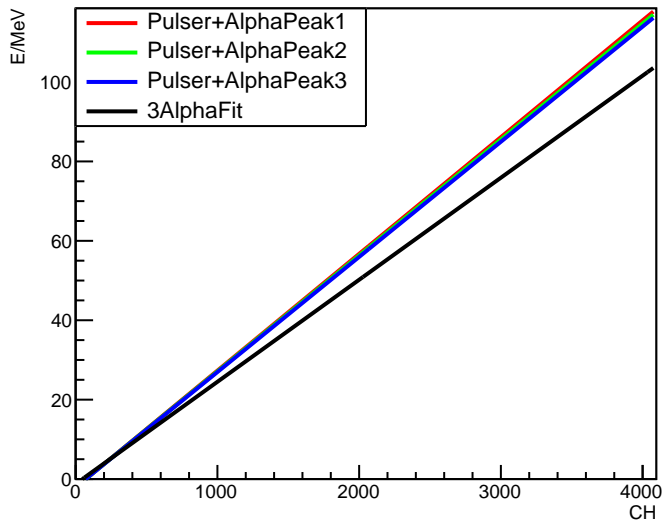
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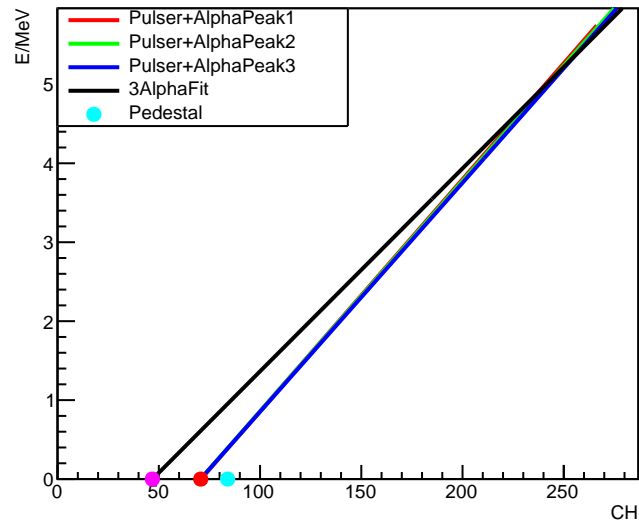
SSD3_L1S_Switch_Alpha00_04_CH00



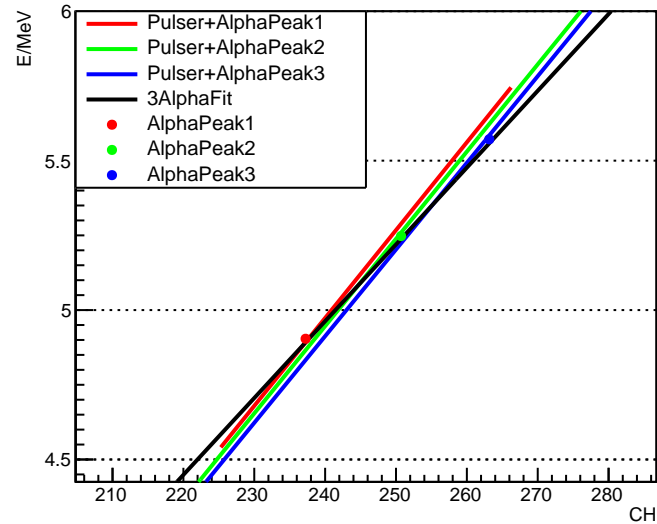
SSD3_L1S_Switch_Alpha00_04_CH01



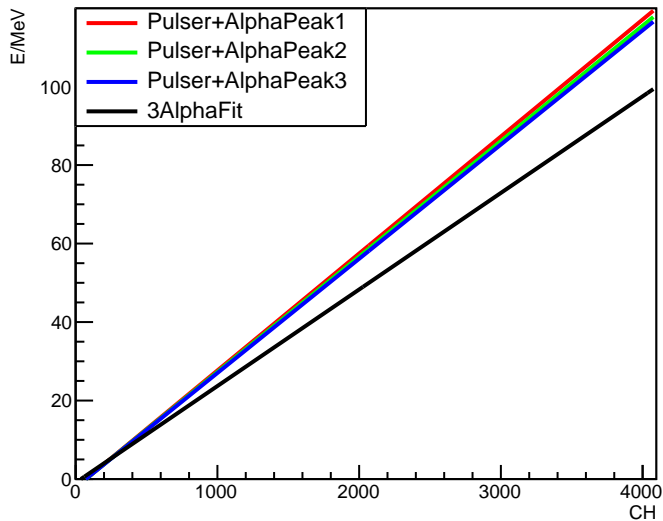
SSD3_L1S_Switch_Alpha00_04_CH01



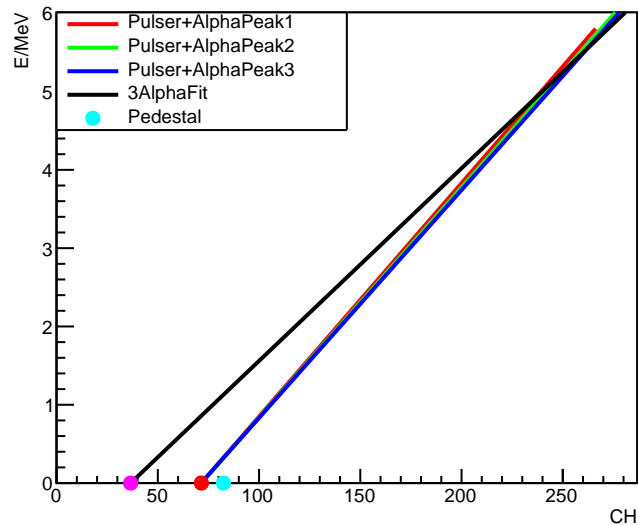
SSD3_L1S_Switch_Alpha00_04_CH01



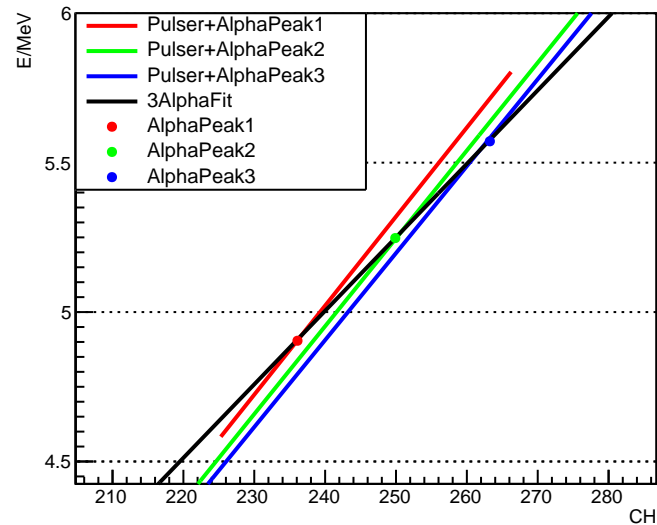
SSD3_L1S_Switch_Alpha00_04_CH02



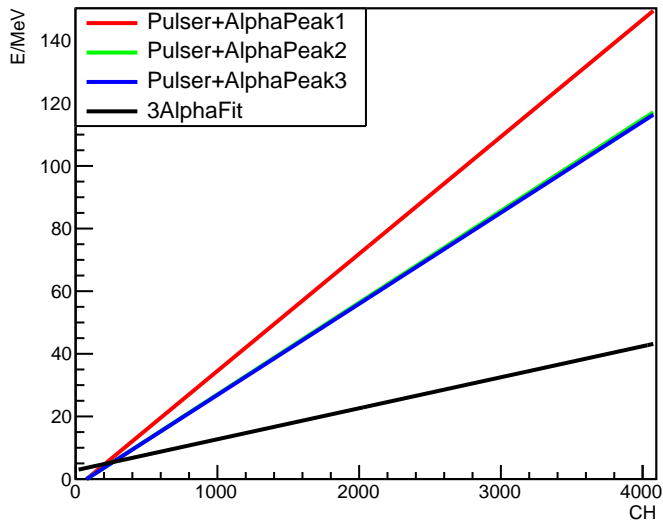
SSD3_L1S_Switch_Alpha00_04_CH02



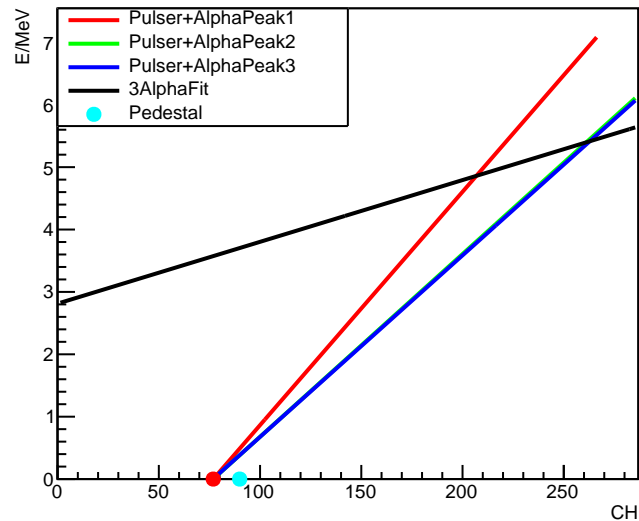
SSD3_L1S_Switch_Alpha00_04_CH02



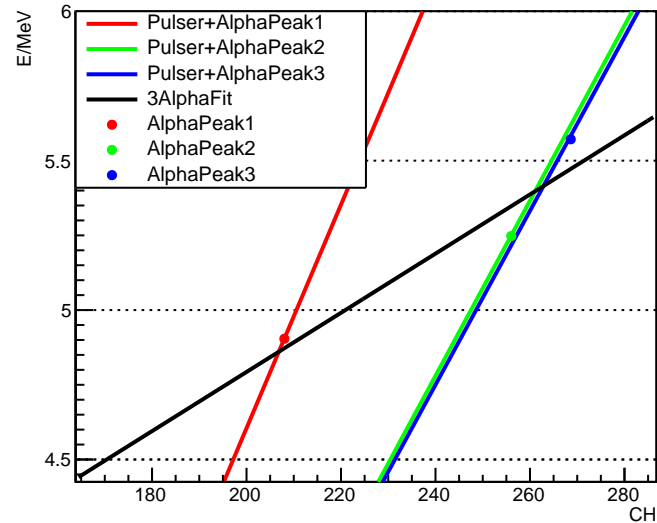
SSD3_L1S_Switch_Alpha00_04_CH03



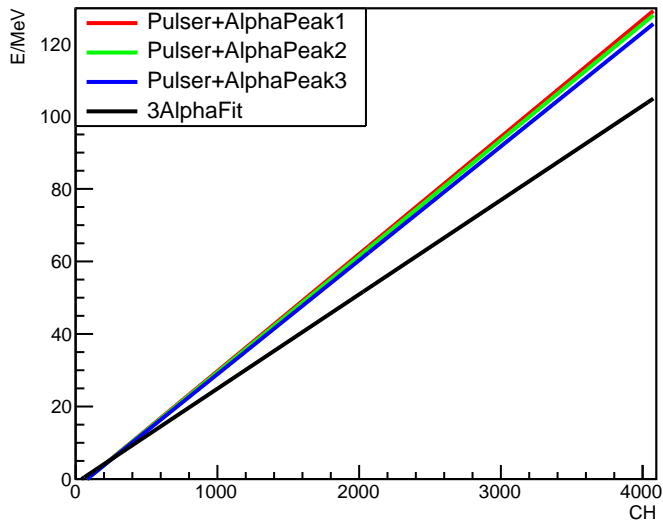
SSD3_L1S_Switch_Alpha00_04_CH03



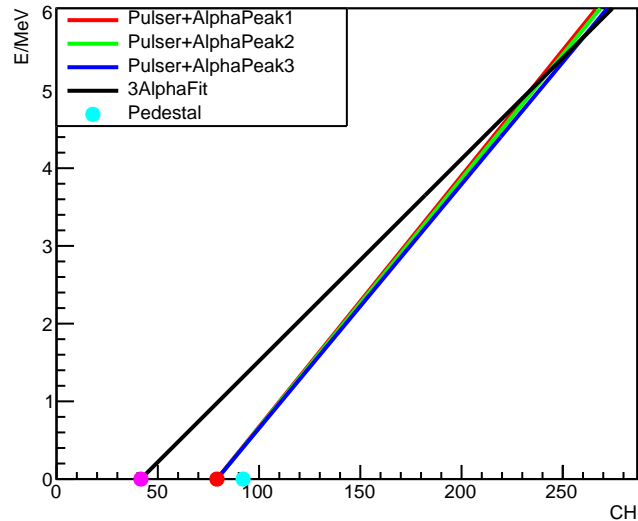
SSD3_L1S_Switch_Alpha00_04_CH03



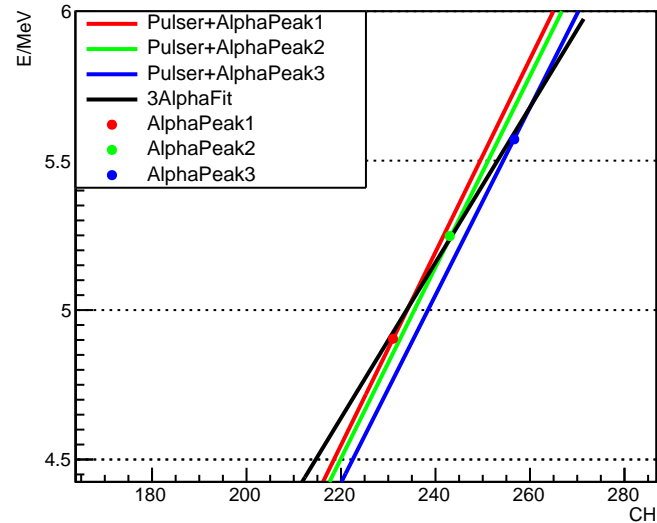
SSD3_L1S_Switch_Alpha00_04_CH04



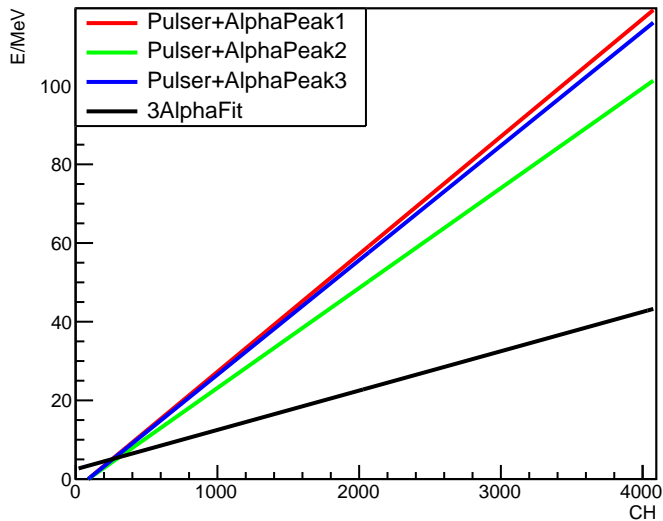
SSD3_L1S_Switch_Alpha00_04_CH04



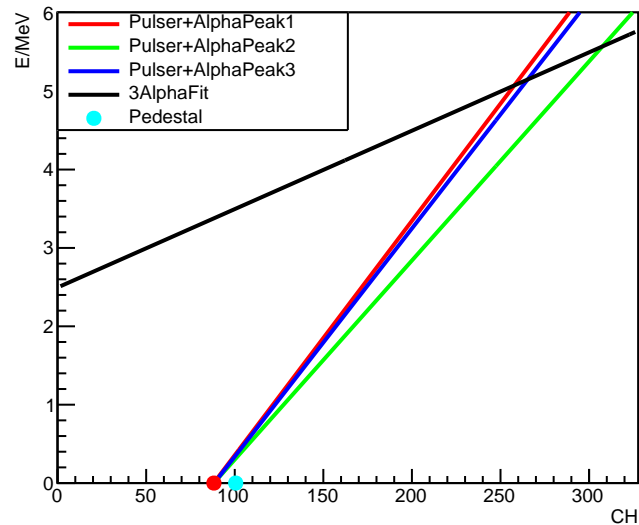
SSD3_L1S_Switch_Alpha00_04_CH04



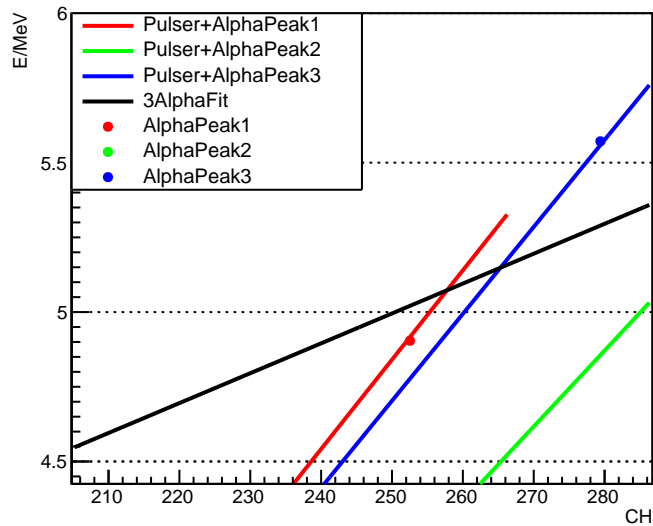
SSD3_L1S_Switch_Alpha00_04_CH05



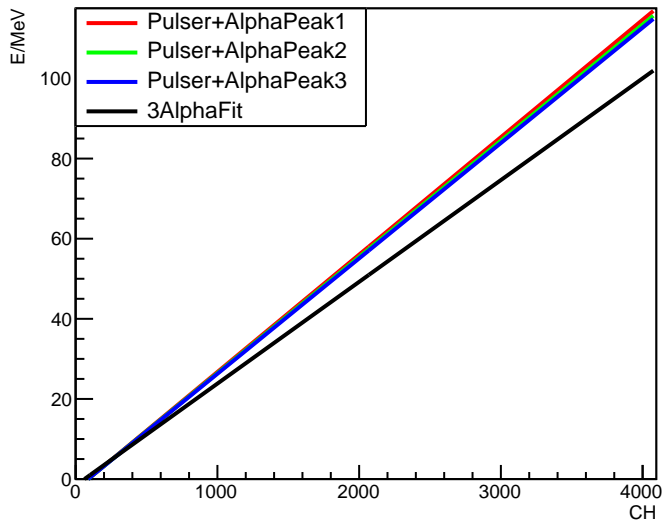
SSD3_L1S_Switch_Alpha00_04_CH05



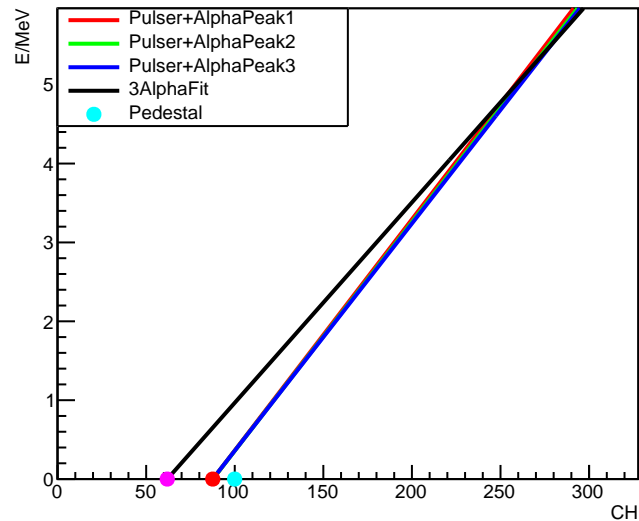
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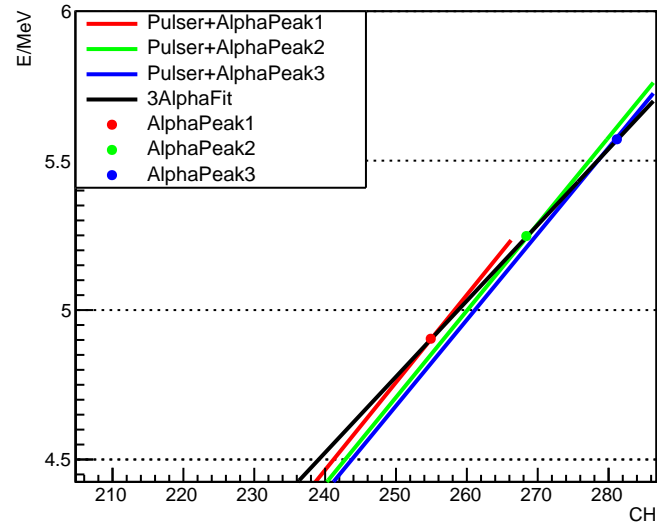
SSD3_L1S_Switch_Alpha00_04_CH06



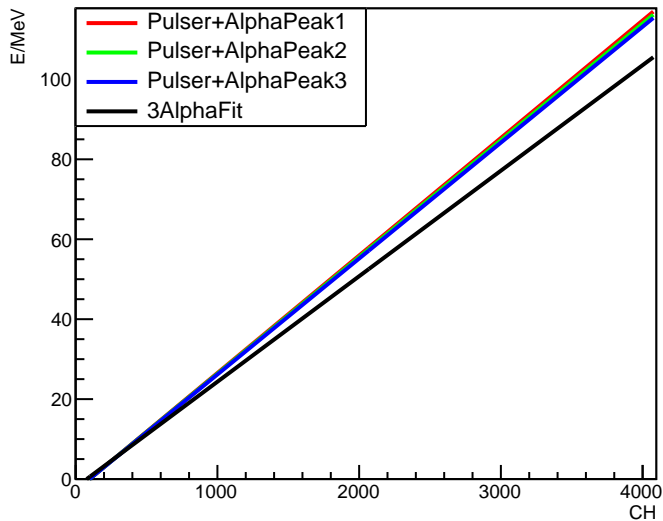
SSD3_L1S_Switch_Alpha00_04_CH06



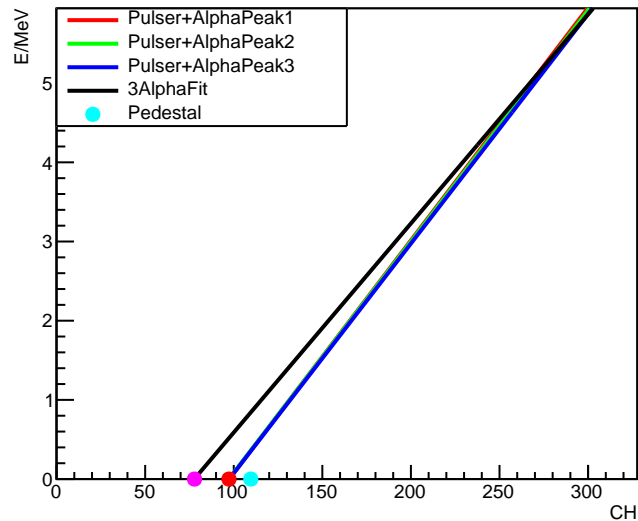
SSD3_L1S_Switch_Alpha00_04_CH06



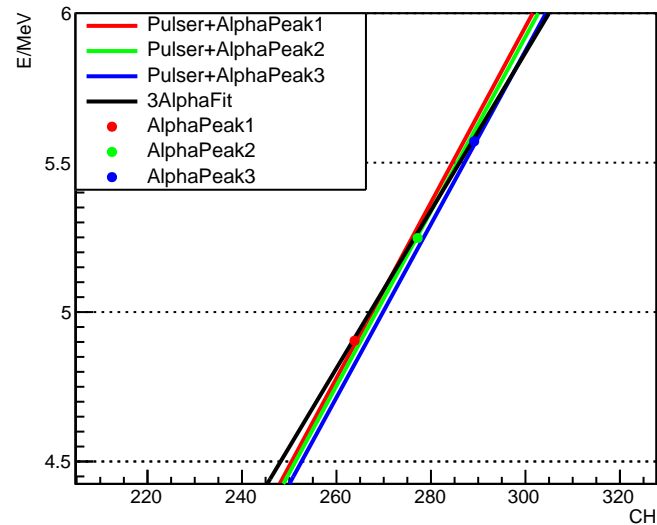
SSD3_L1S_Switch_Alpha00_04_CH07



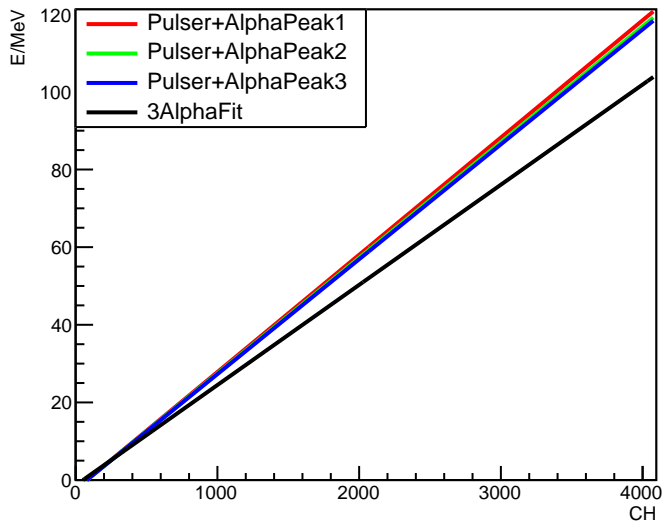
SSD3_L1S_Switch_Alpha00_04_CH07



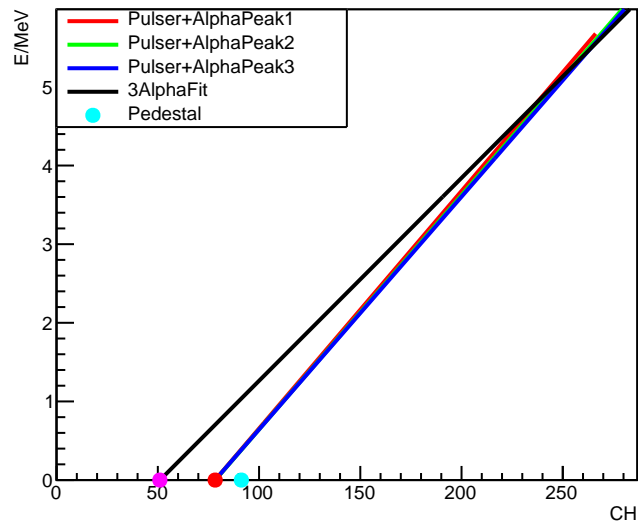
SSD3_L1S_Switch_Alpha00_04_CH07



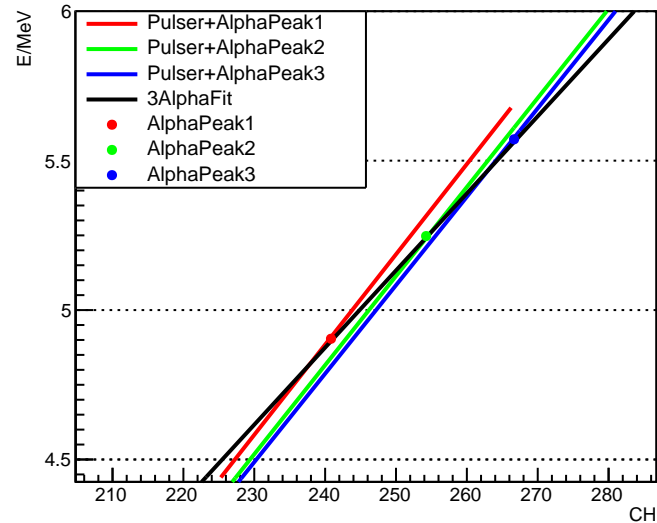
SSD3_L1S_Switch_Alpha00_04_CH08



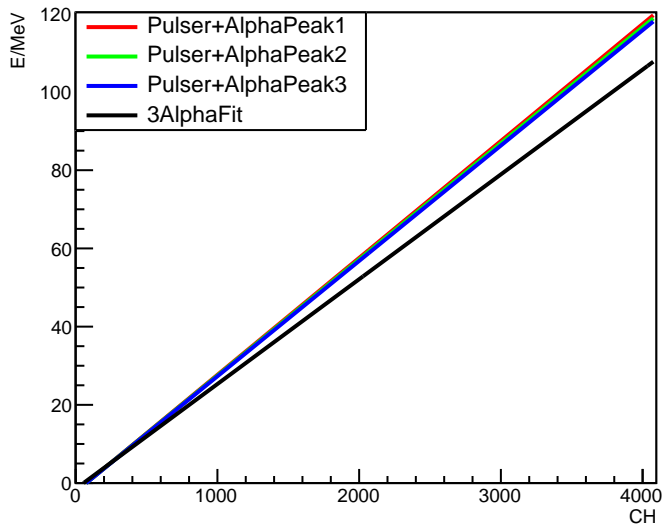
SSD3_L1S_Switch_Alpha00_04_CH08



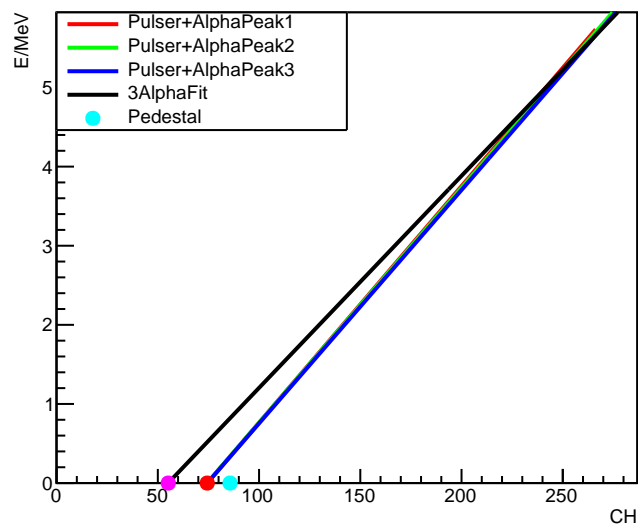
SSD3_L1S_Switch_Alpha00_04_CH08



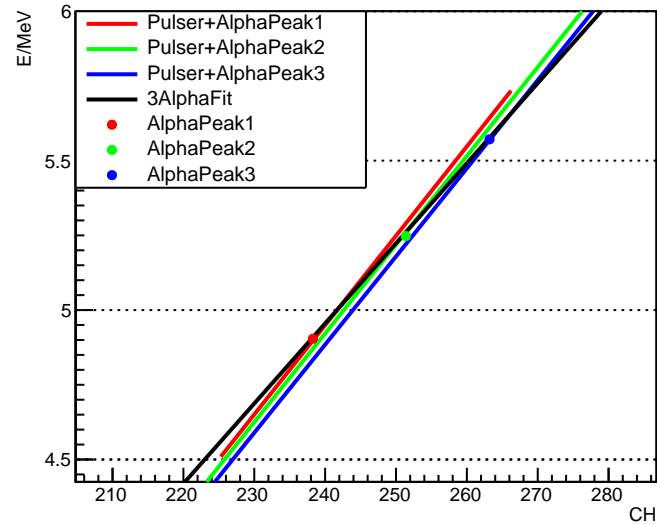
SSD3_L1S_Switch_Alpha00_04_CH09



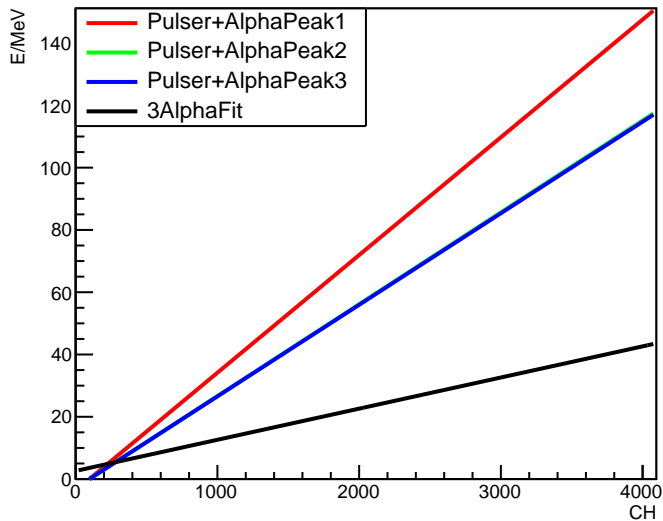
SSD3_L1S_Switch_Alpha00_04_CH09



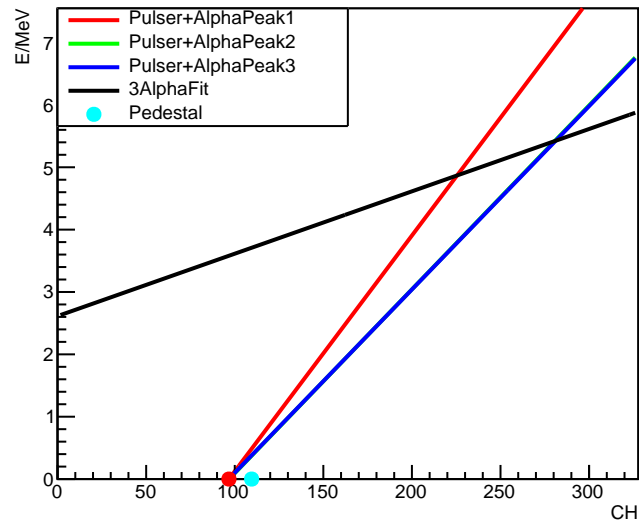
SSD3_L1S_Switch_Alpha00_04_CH09



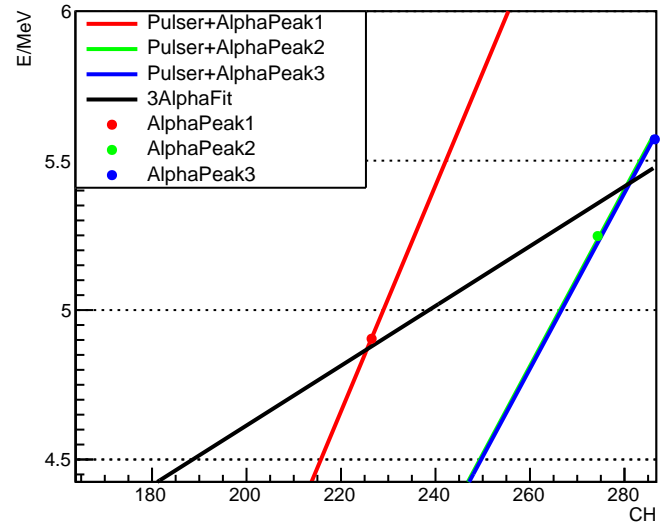
SSD3_L1S_Switch_Alpha00_04_CH10



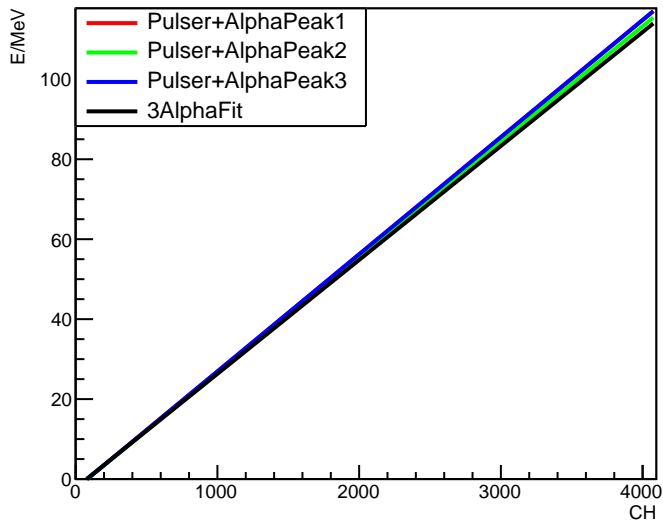
SSD3_L1S_Switch_Alpha00_04_CH10



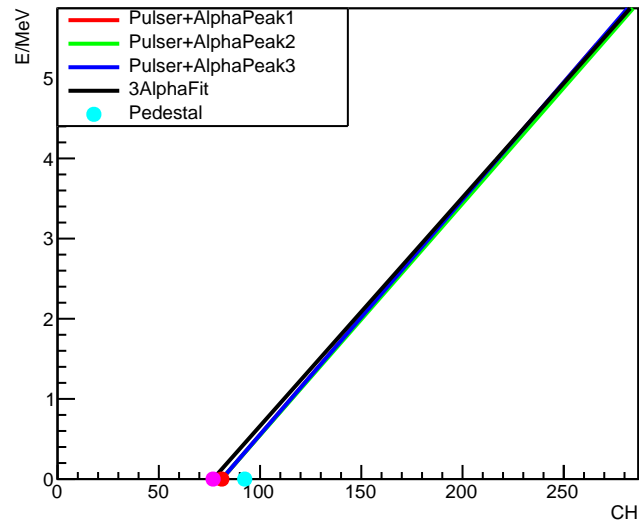
SSD3_L1S_Switch_Alpha00_04_CH10



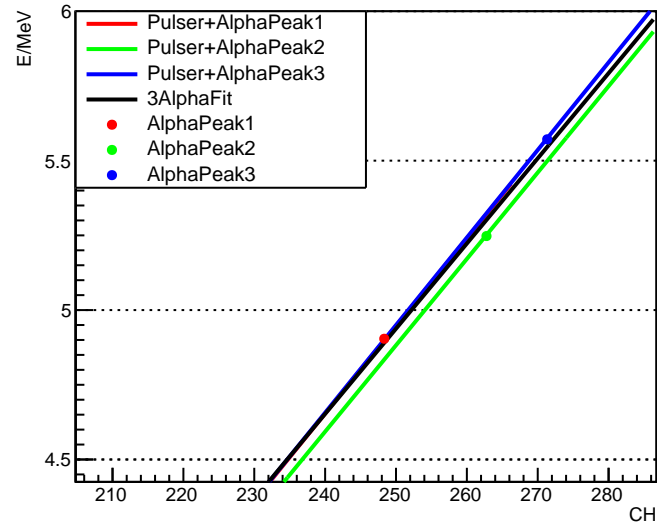
SSD3_L1S_Switch_Alpha00_04_CH11



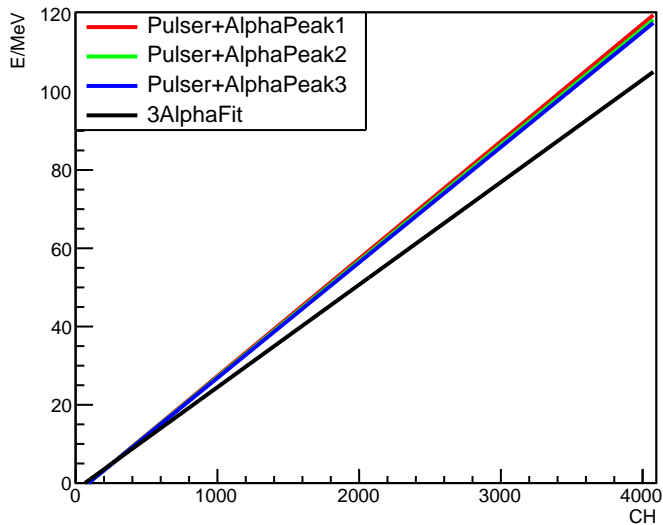
SSD3_L1S_Switch_Alpha00_04_CH11



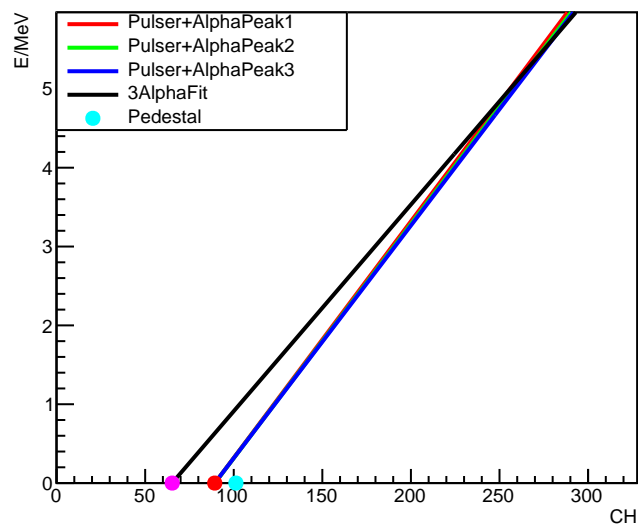
SSD3_L1S_Switch_Alpha00_04_CH11



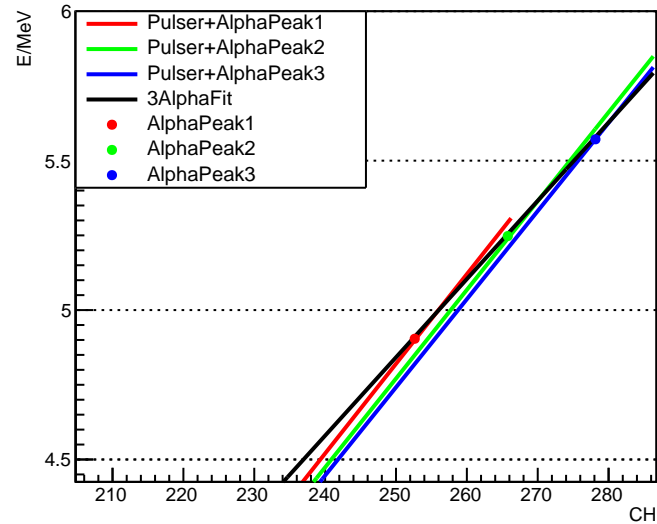
SSD3_L1S_Switch_Alpha00_04_CH12



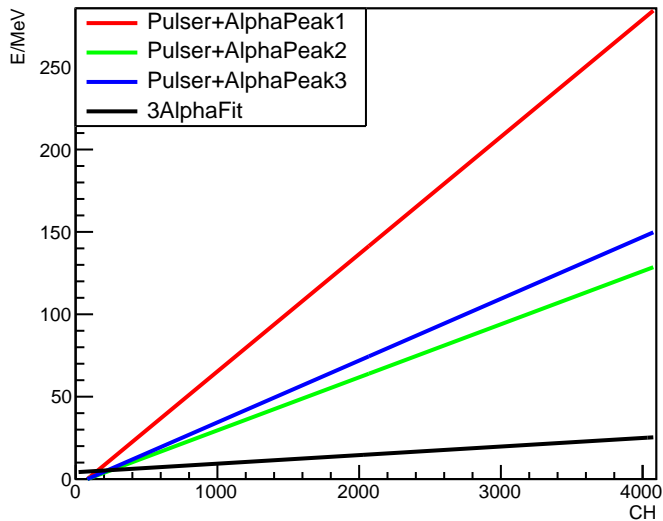
SSD3_L1S_Switch_Alpha00_04_CH12



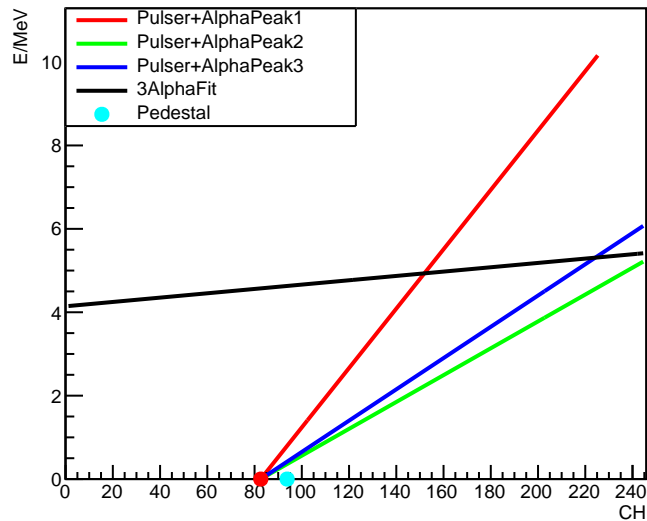
SSD3_L1S_Switch_Alpha00_04_CH12



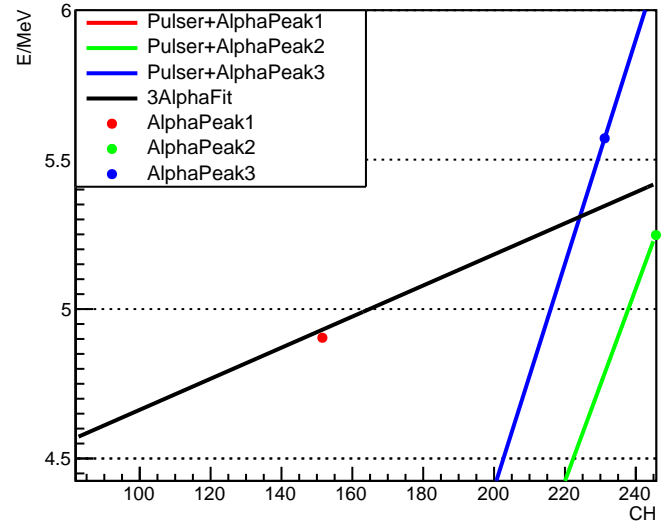
SSD3_L1S_Switch_Alpha00_04_CH13



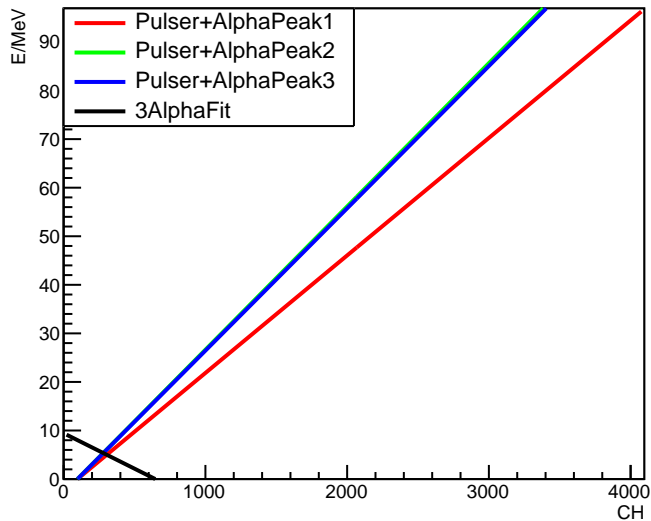
SSD3_L1S_Switch_Alpha00_04_CH13



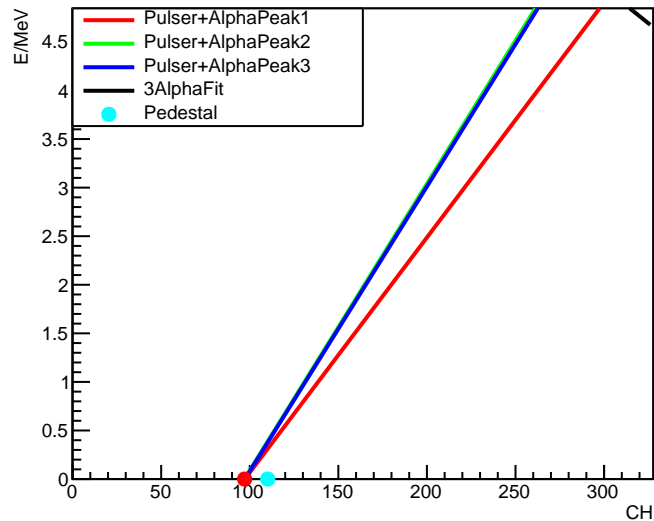
SSD3_L1S_Switch_Alpha00_04_CH13



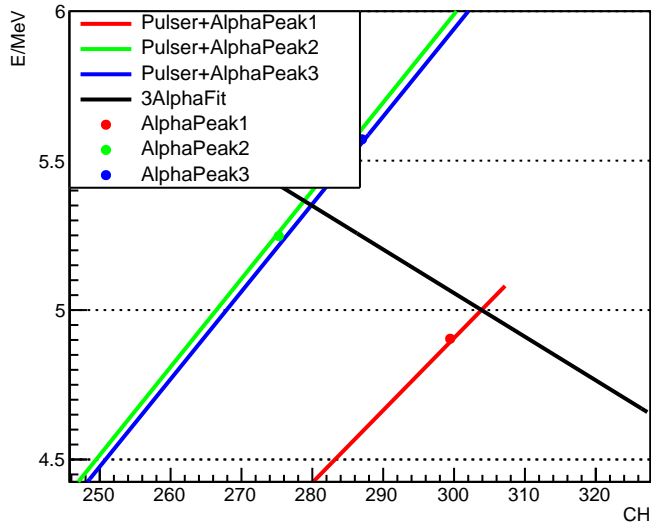
SSD3_L1S_Switch_Alpha00_04_CH14



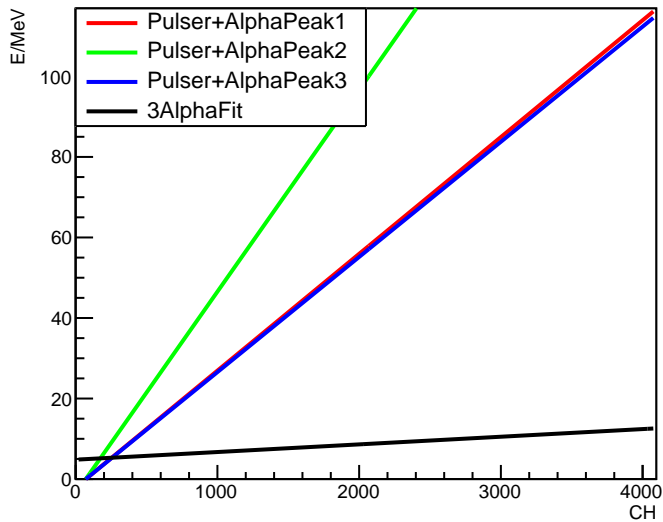
SSD3_L1S_Switch_Alpha00_04_CH14



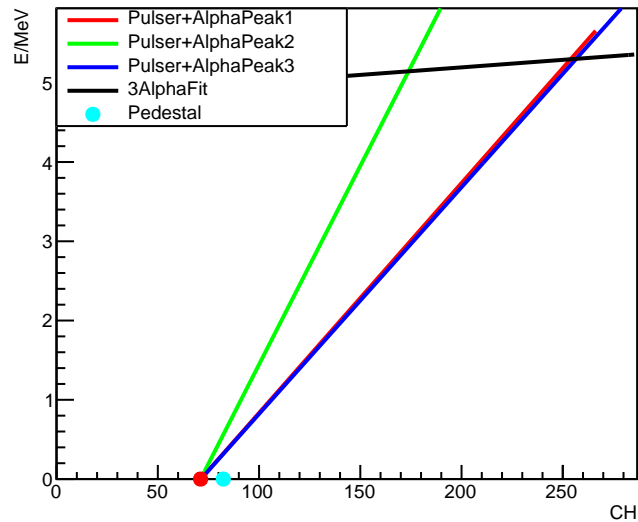
SSD3_L1S_Switch_Alpha00_04_CH14



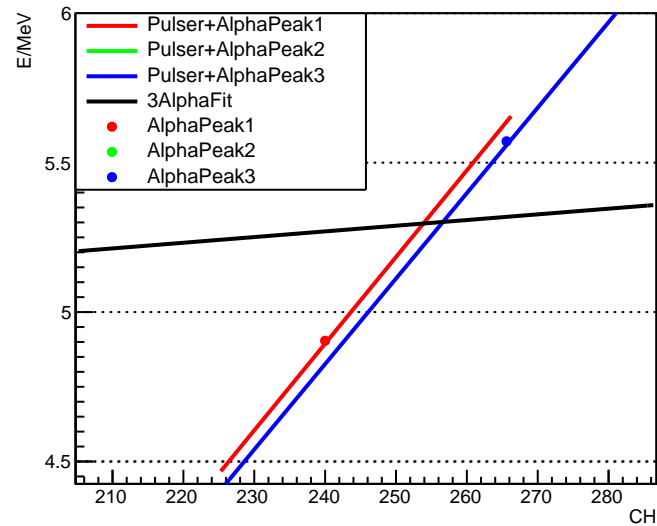
SSD3_L1S_Switch_Alpha00_04_CH15



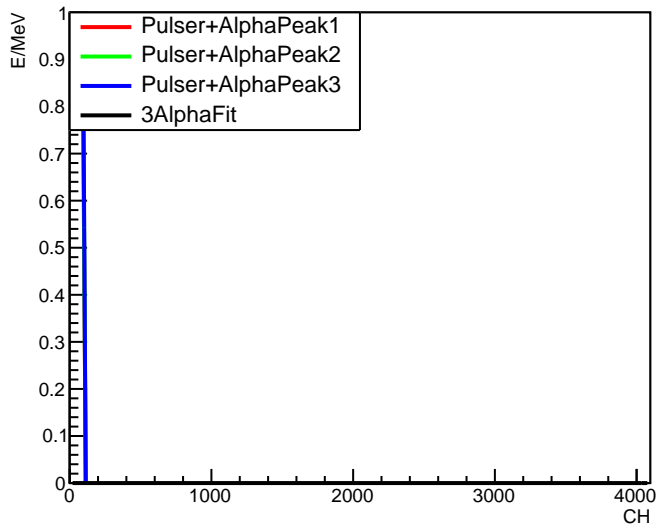
SSD3_L1S_Switch_Alpha00_04_CH15



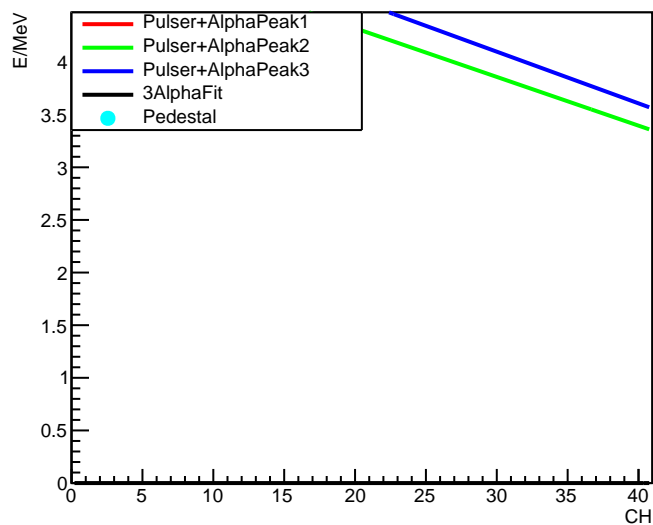
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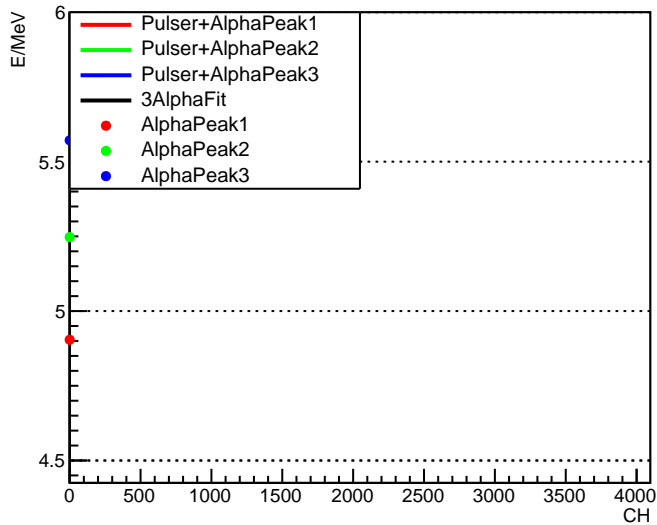
SSD4_L1S_Switch_Alpha00_04_CH00



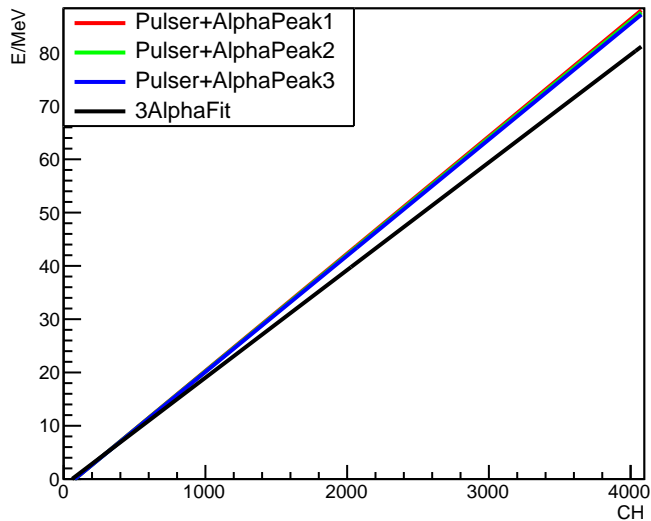
SSD4_L1S_Switch_Alpha00_04_CH00



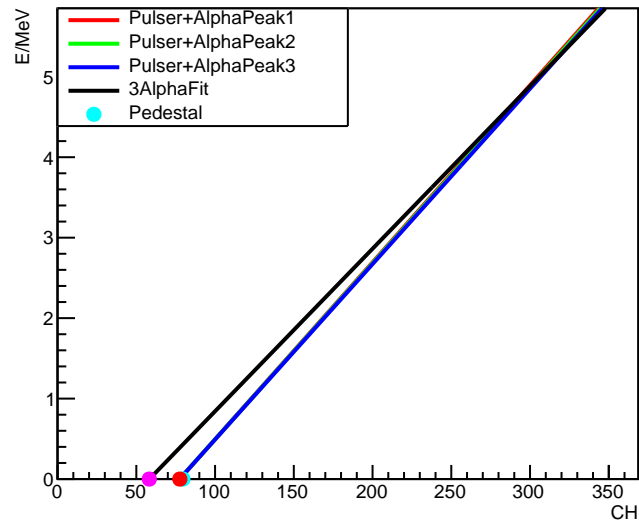
SSD4_L1S_Switch_Alpha00_04_CH00



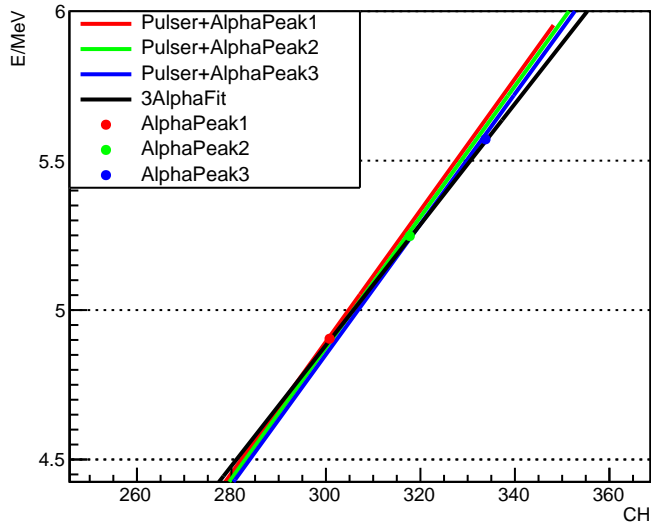
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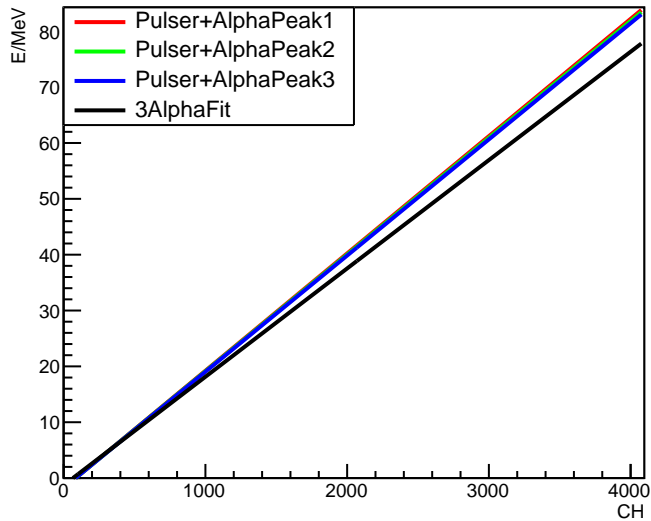
SSD4_L1S_Switch_Alpha00_04_CH01



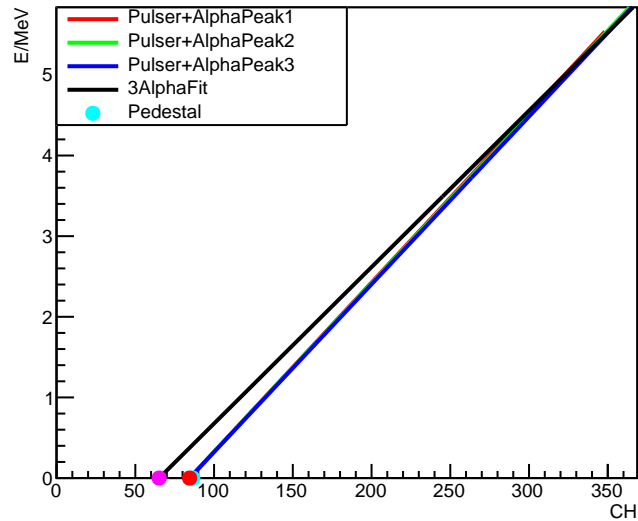
SSD4_L1S_Switch_Alpha00_04_CH01



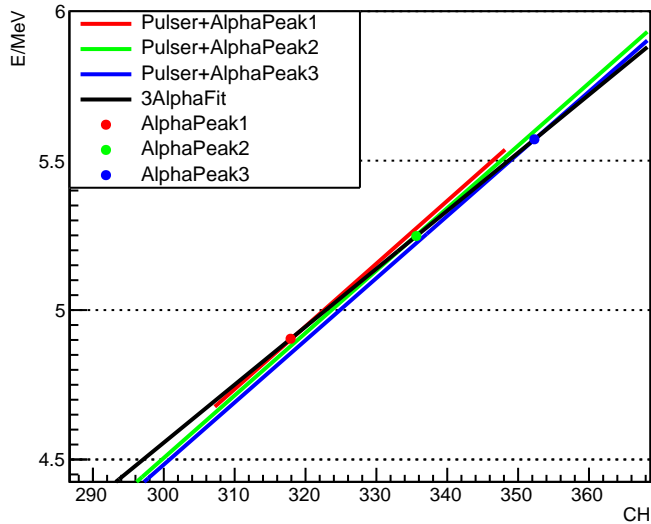
SSD4_L1S_Switch_Alpha00_04_CH02



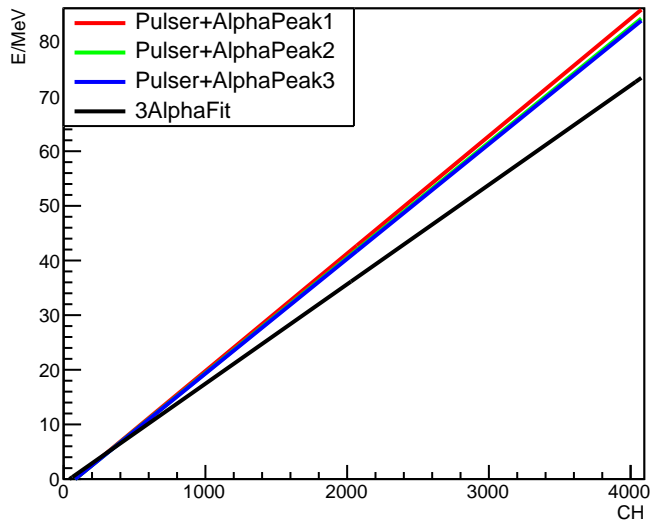
SSD4_L1S_Switch_Alpha00_04_CH02



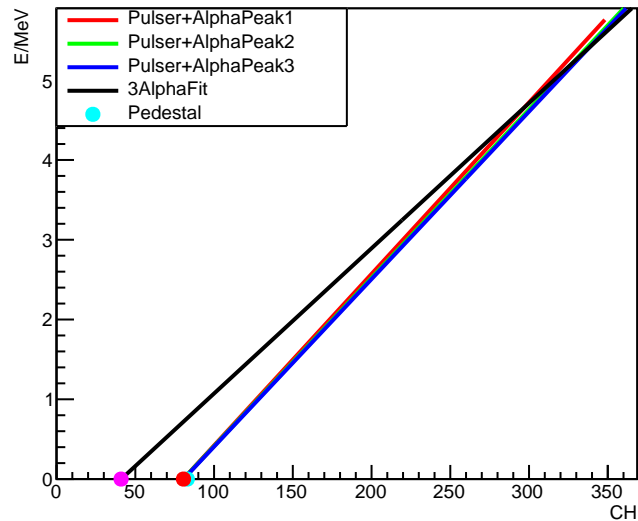
SSD4_L1S_Switch_Alpha00_04_CH02



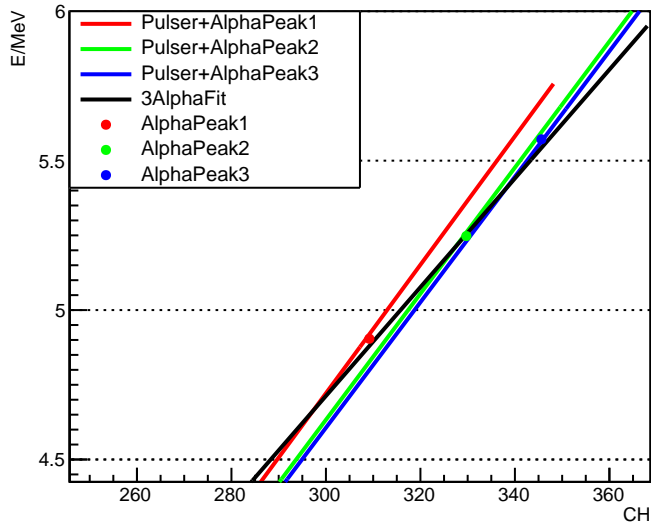
SSD4_L1S_Switch_Alpha00_04_CH03



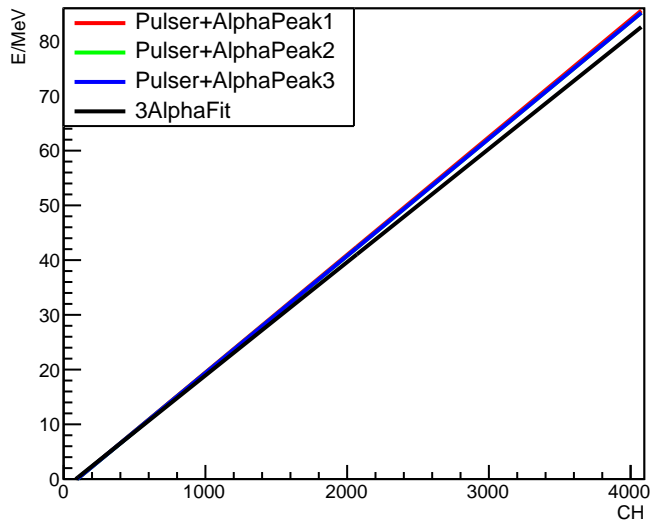
SSD4_L1S_Switch_Alpha00_04_CH03



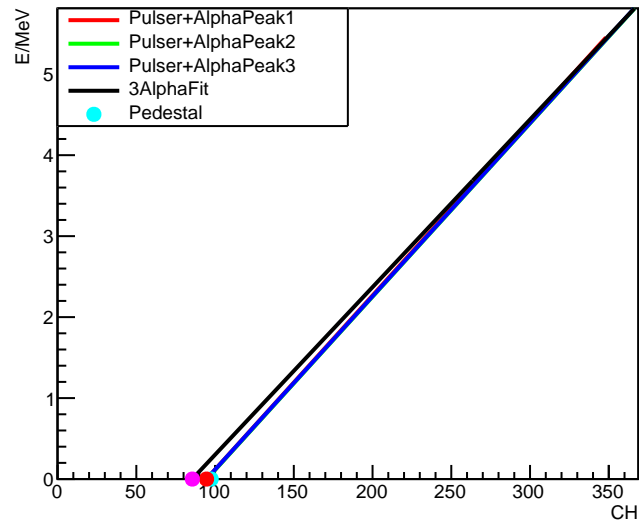
SSD4_L1S_Switch_Alpha00_04_CH03



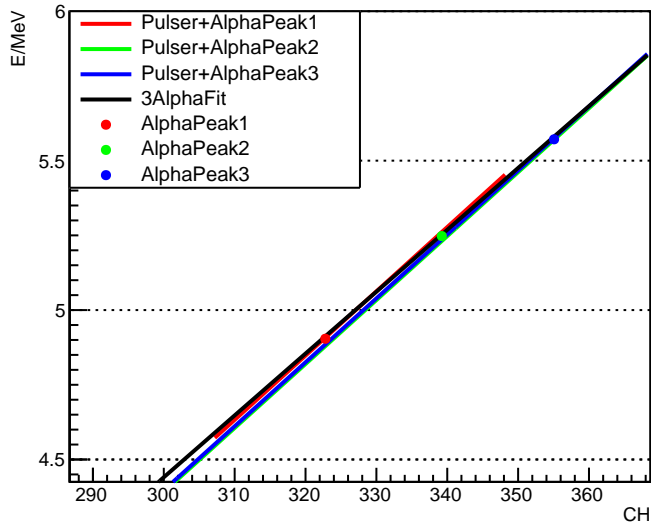
SSD4_L1S_Switch_Alpha00_04_CH04



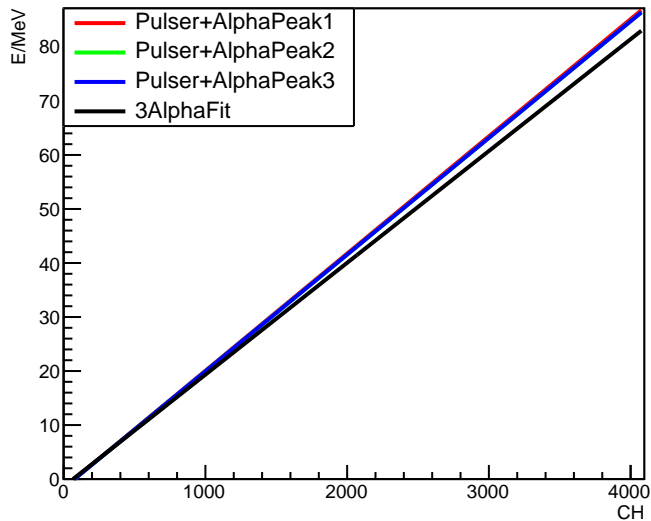
SSD4_L1S_Switch_Alpha00_04_CH04



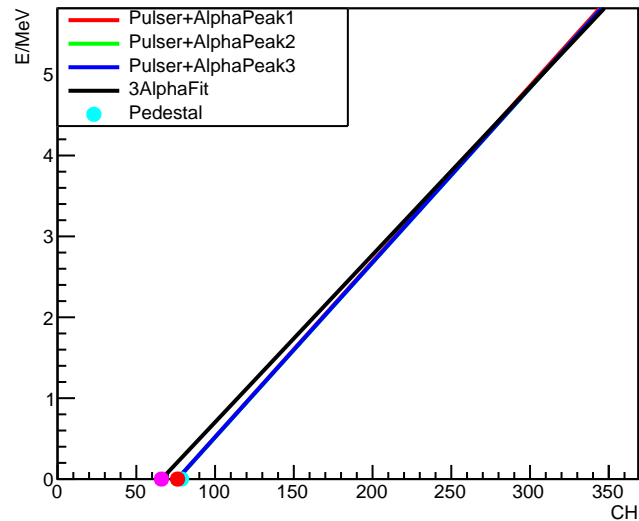
SSD4_L1S_Switch_Alpha00_04_CH04



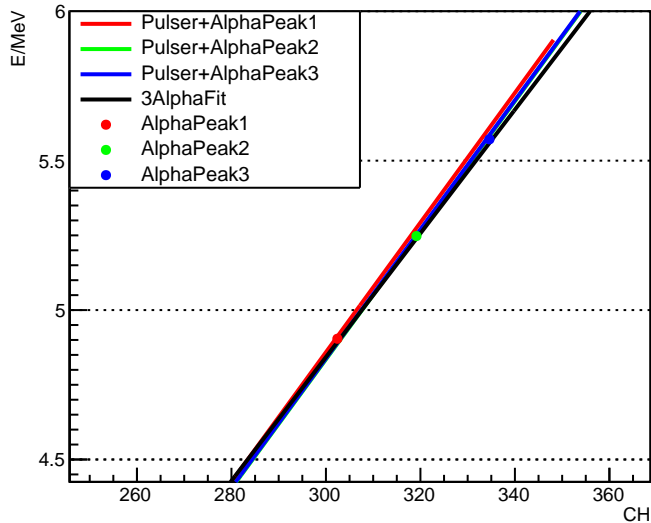
SSD4_L1S_Switch_Alpha00_04_CH05



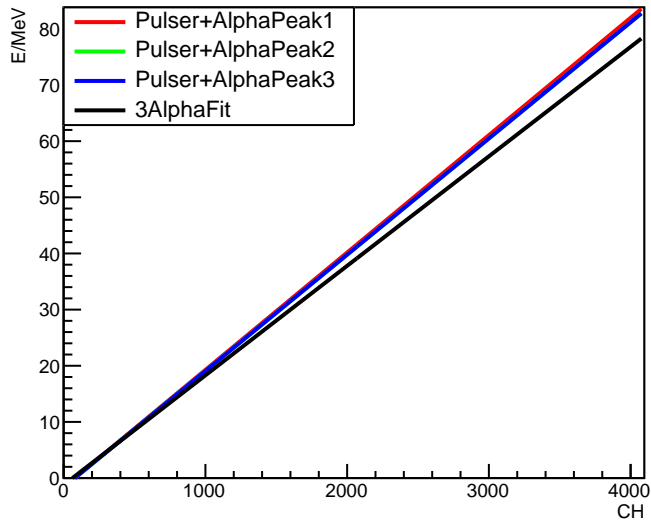
SSD4_L1S_Switch_Alpha00_04_CH05



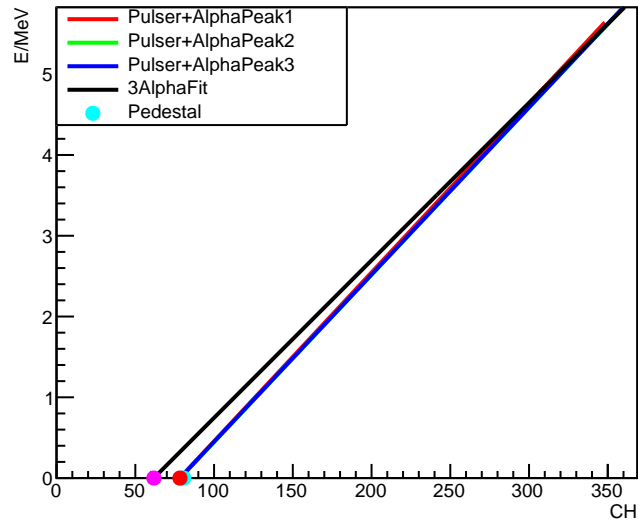
SSD4_L1S_Switch_Alpha00_04_CH05



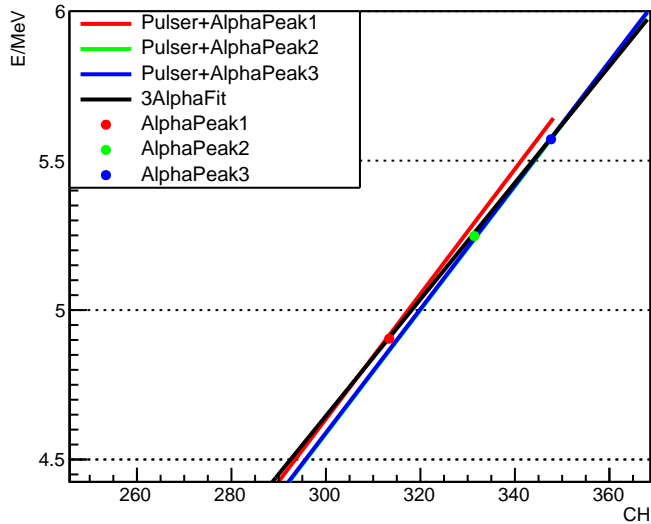
SSD4_L1S_Switch_Alpha00_04_CH06



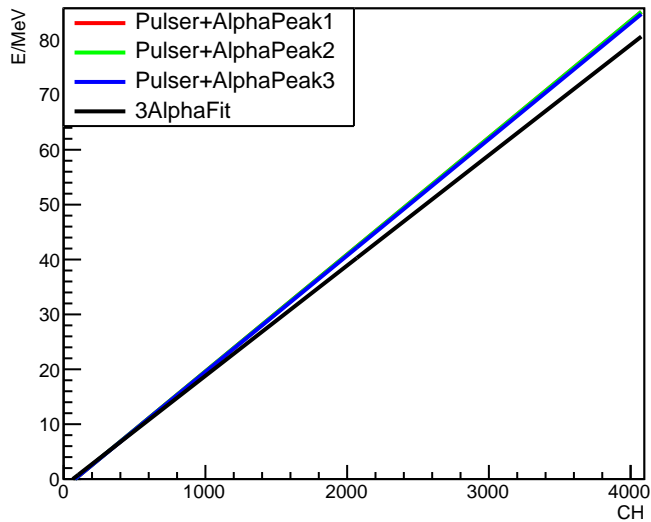
SSD4_L1S_Switch_Alpha00_04_CH06



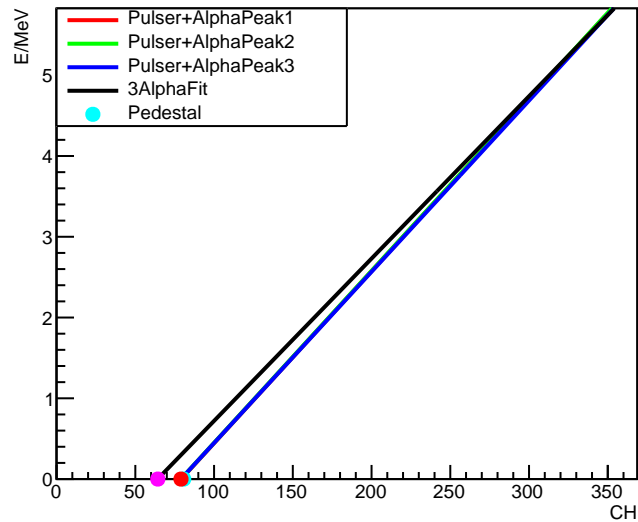
SSD4_L1S_Switch_Alpha00_04_CH06



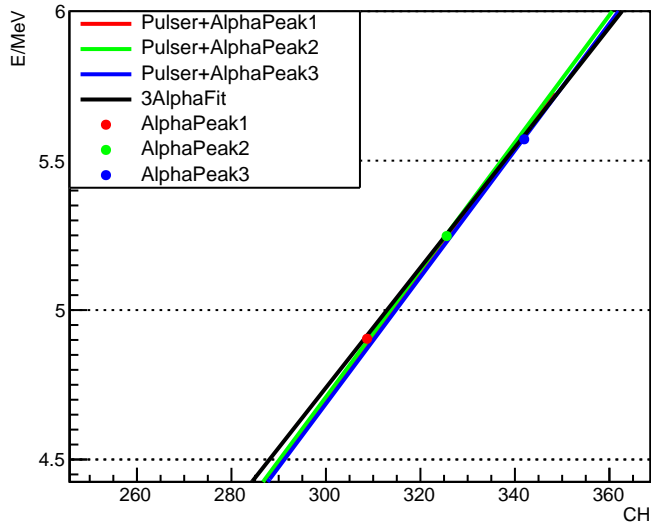
SSD4_L1S_Switch_Alpha00_04_CH07



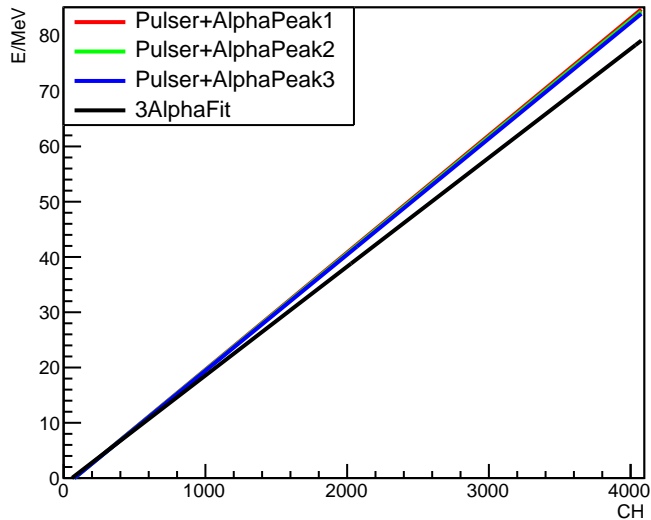
SSD4_L1S_Switch_Alpha00_04_CH07



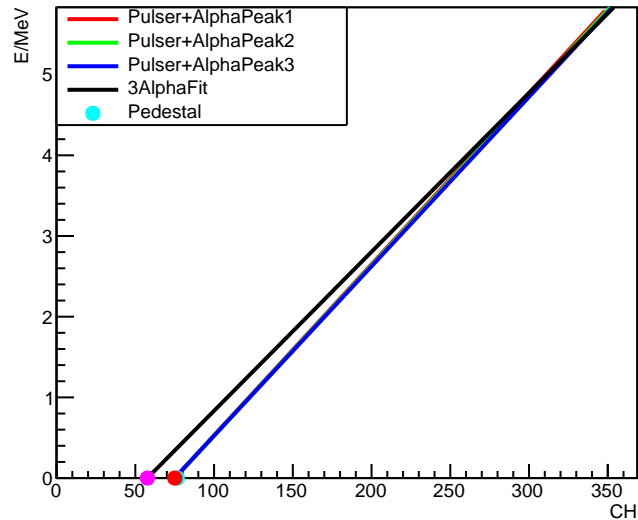
SSD4_L1S_Switch_Alpha00_04_CH07



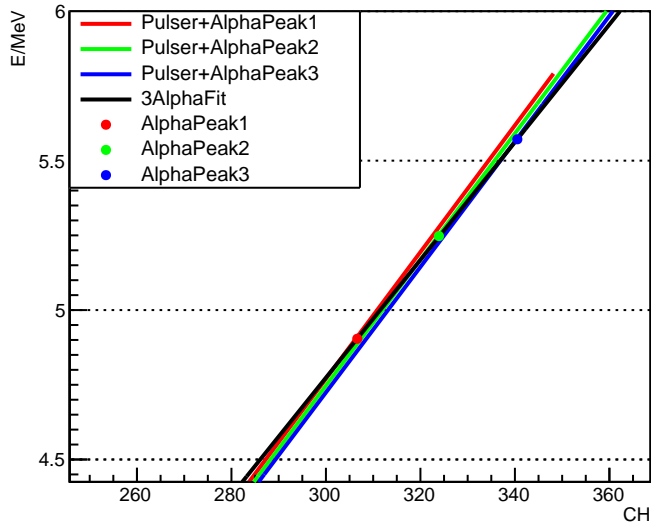
SSD4_L1S_Switch_Alpha00_04_CH08



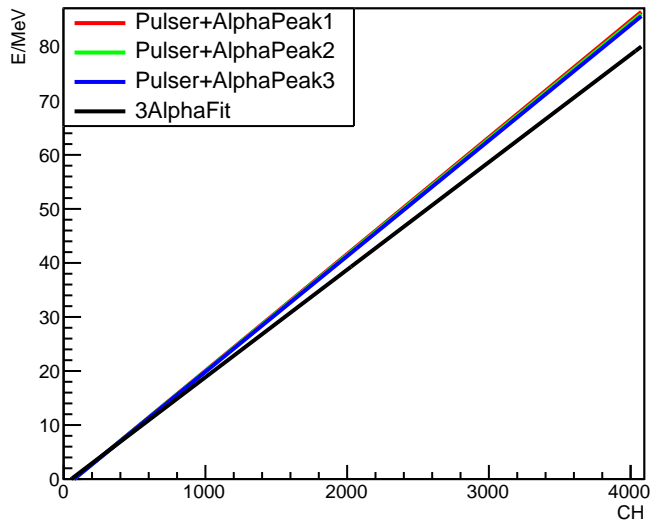
SSD4_L1S_Switch_Alpha00_04_CH08



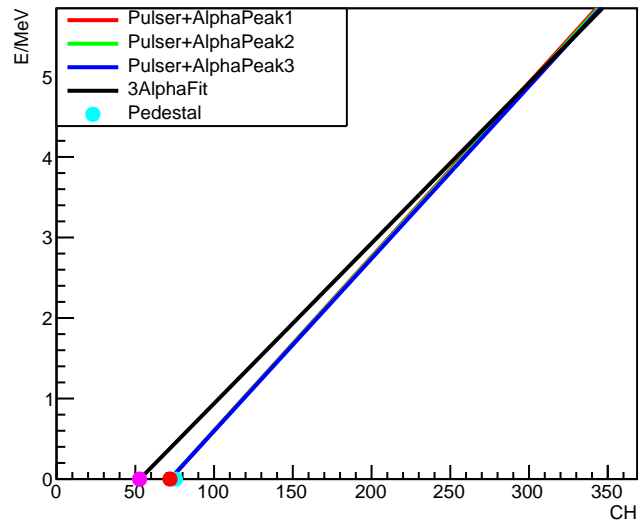
SSD4_L1S_Switch_Alpha00_04_CH08



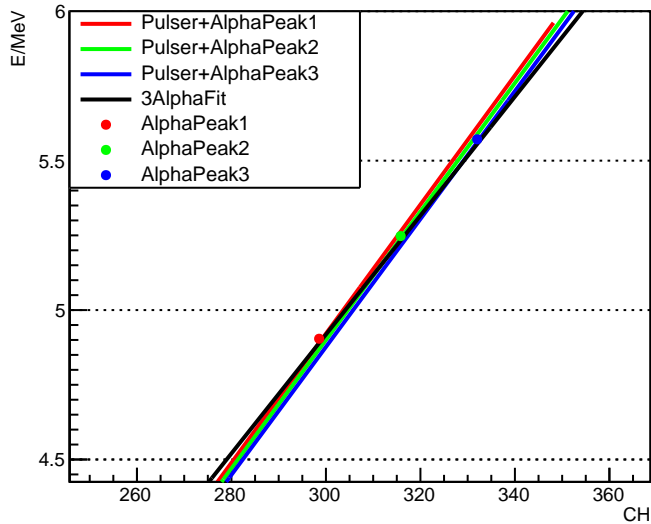
SSD4_L1S_Switch_Alpha00_04_CH09



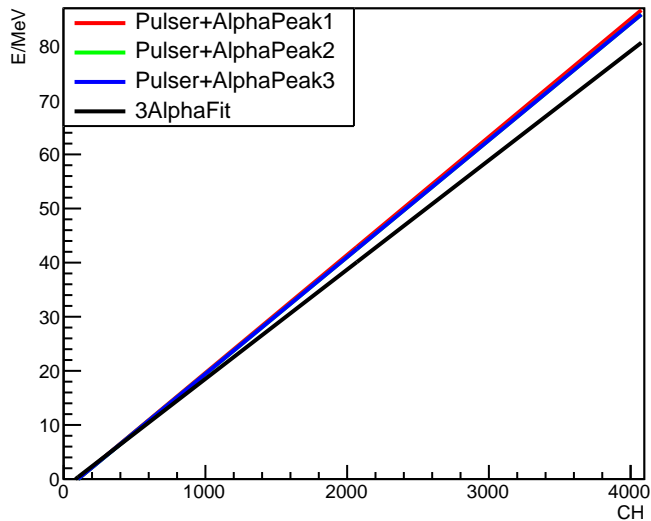
SSD4_L1S_Switch_Alpha00_04_CH09



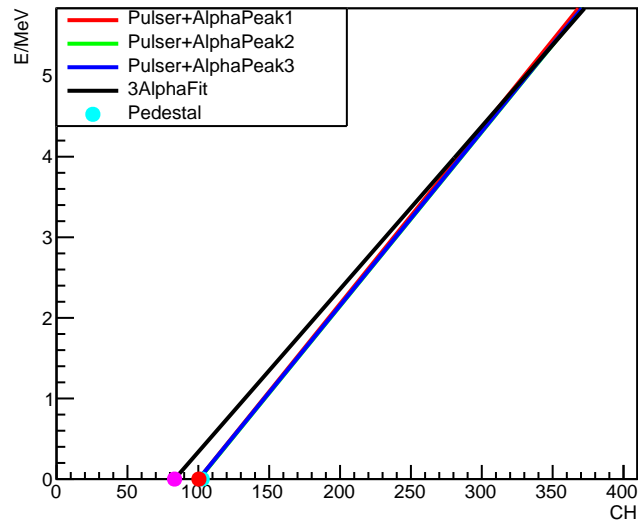
SSD4_L1S_Switch_Alpha00_04_CH09



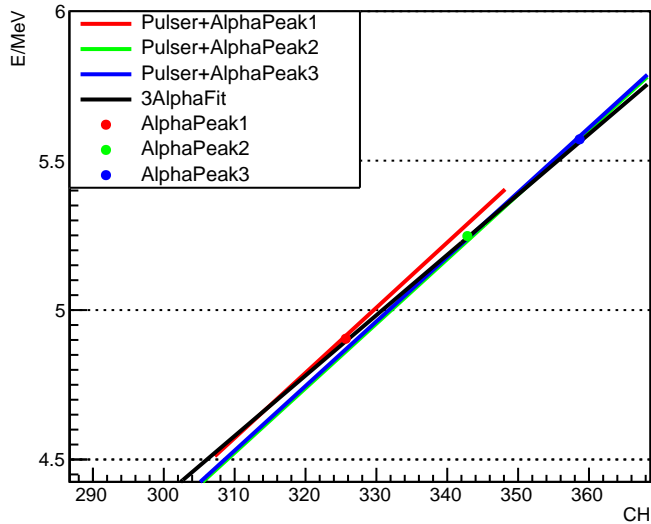
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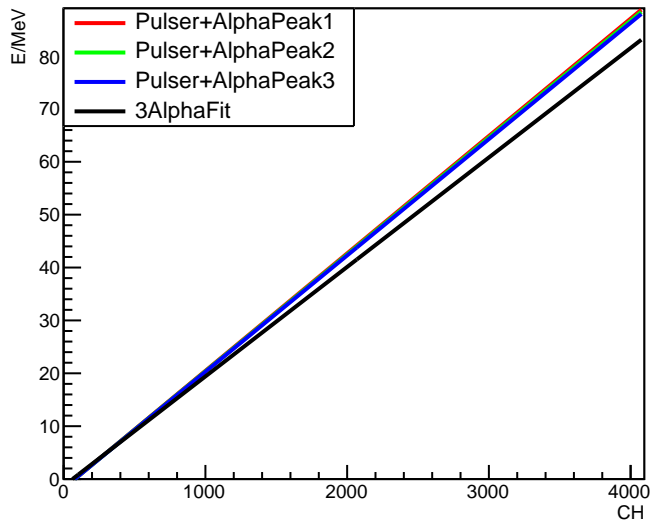
SSD4_L1S_Switch_Alpha00_04_CH10



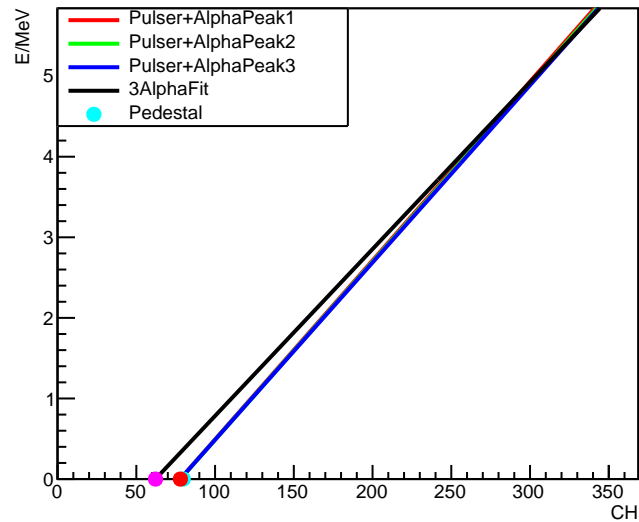
SSD4_L1S_Switch_Alpha00_04_CH10



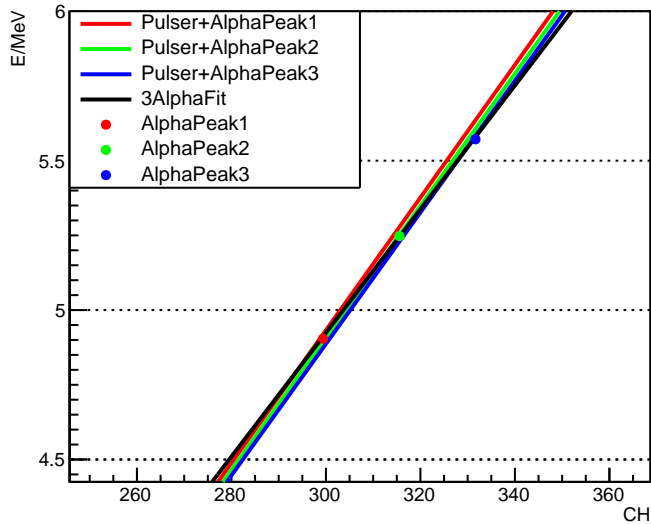
SSD4_L1S_Switch_Alpha00_04_CH11



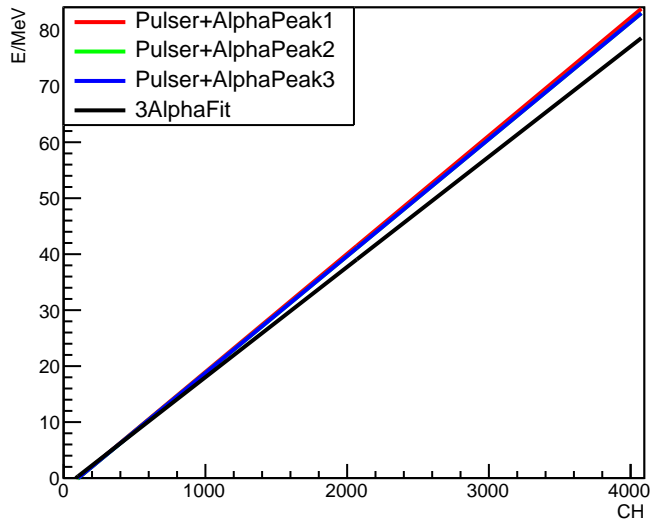
SSD4_L1S_Switch_Alpha00_04_CH11



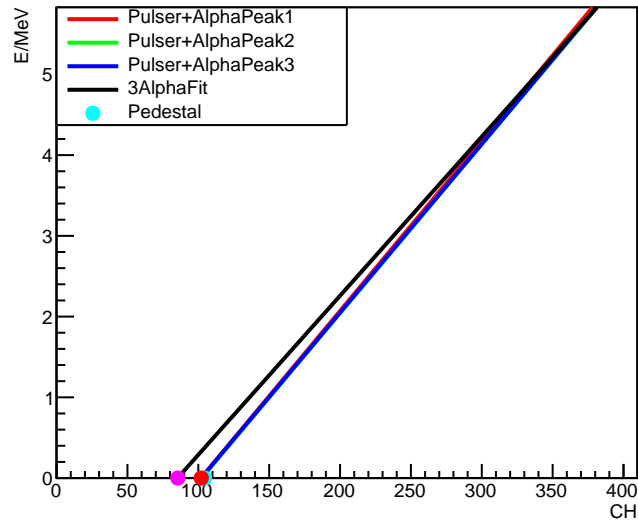
SSD4_L1S_Switch_Alpha00_04_CH11



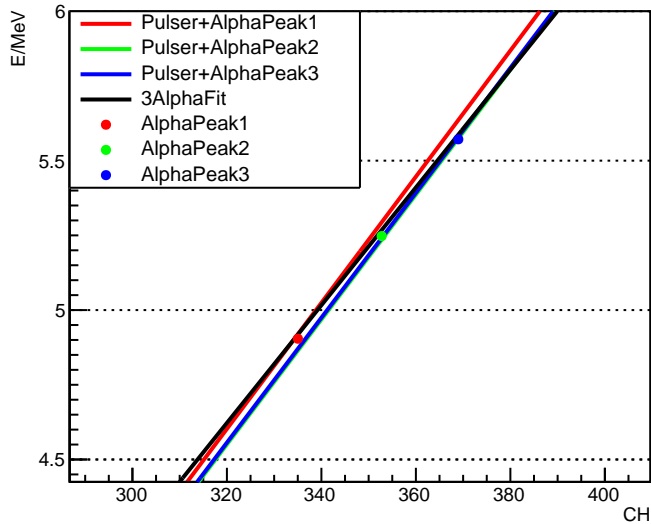
SSD4_L1S_Switch_Alpha00_04_CH12



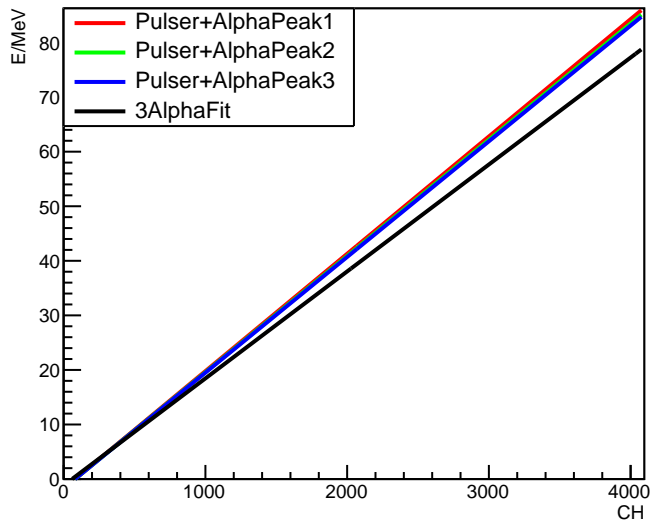
SSD4_L1S_Switch_Alpha00_04_CH12



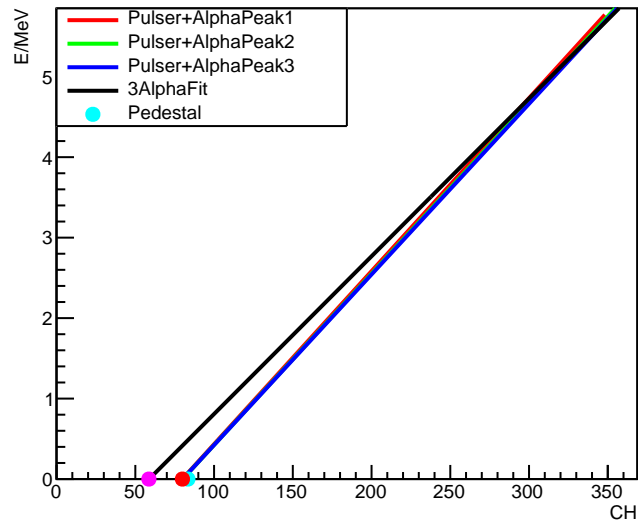
SSD4_L1S_Switch_Alpha00_04_CH12



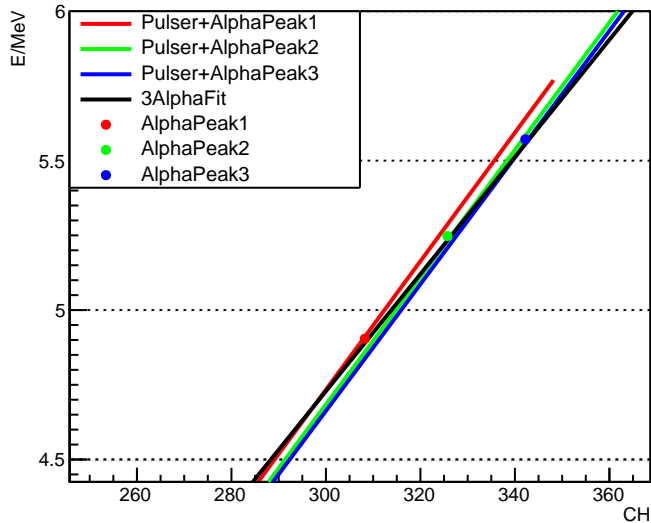
SSD4_L1S_Switch_Alpha00_04_CH13



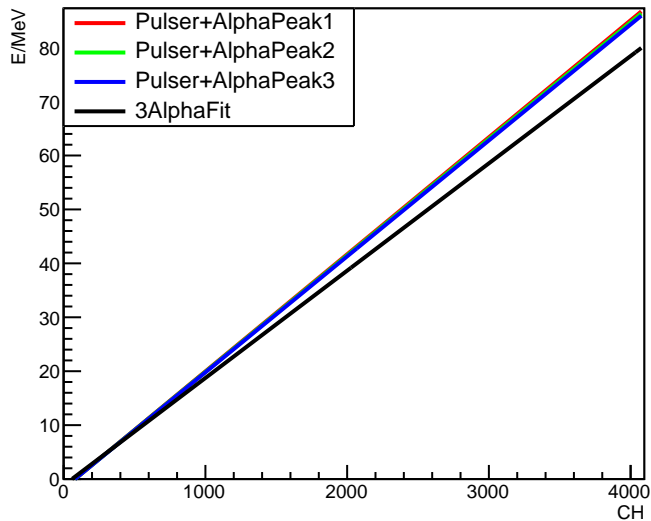
SSD4_L1S_Switch_Alpha00_04_CH13



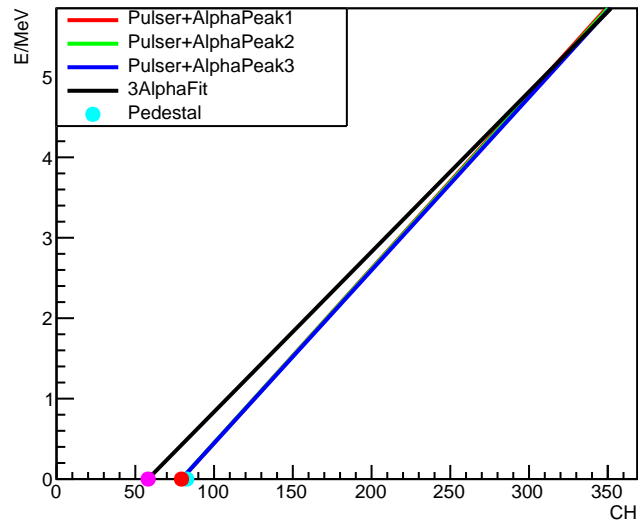
SSD4_L1S_Switch_Alpha00_04_CH13



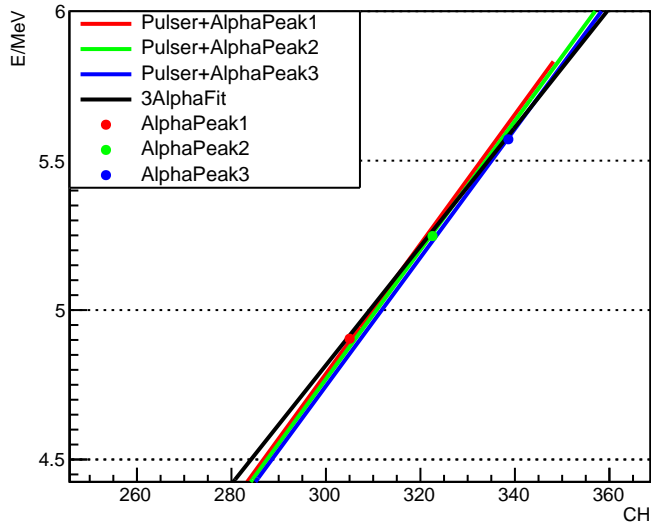
SSD4_L1S_Switch_Alpha00_04_CH14



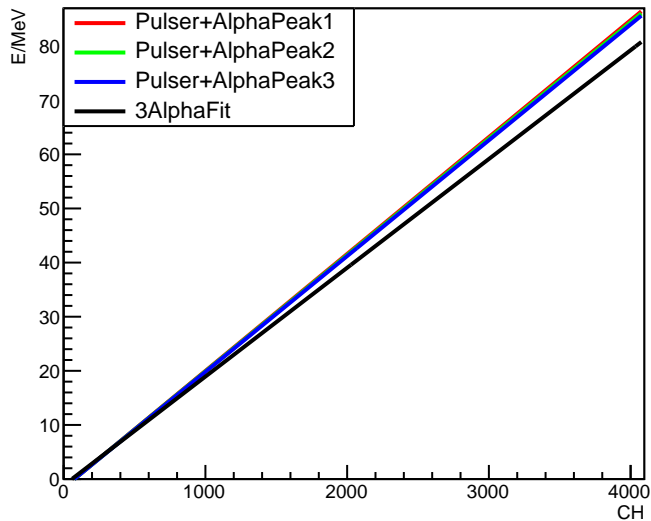
SSD4_L1S_Switch_Alpha00_04_CH14



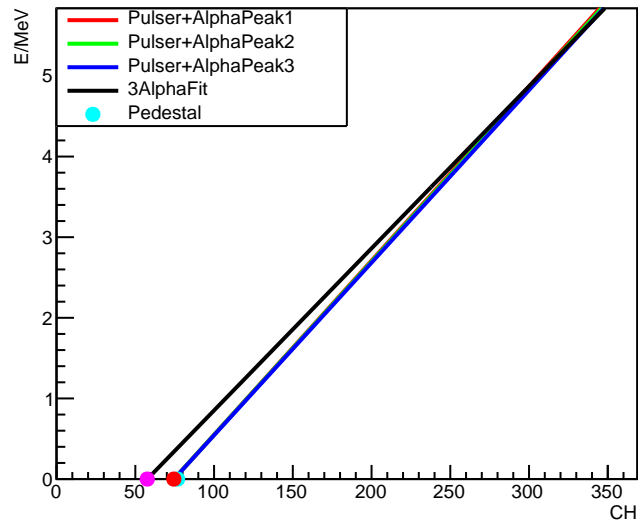
SSD4_L1S_Switch_Alpha00_04_CH14



SSD4_L1S_Switch_Alpha00_04_CH15



SSD4_L1S_Switch_Alpha00_04_CH15



SSD4_L1S_Switch_Alpha00_04_CH15

