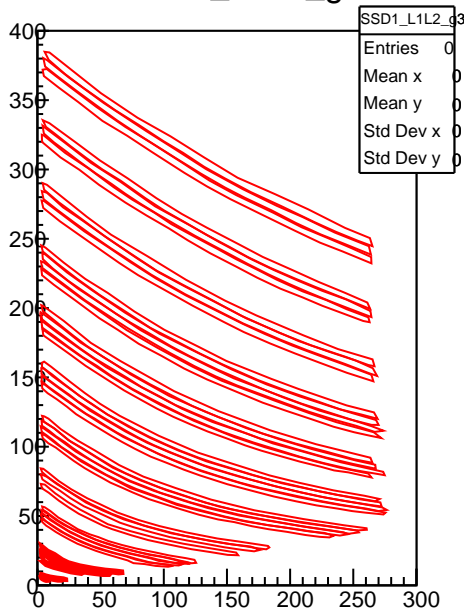
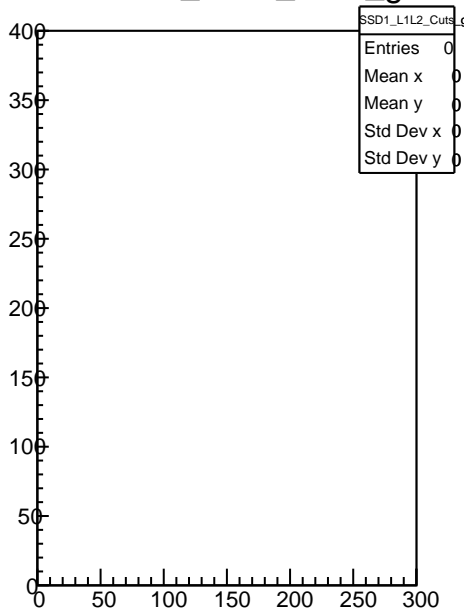


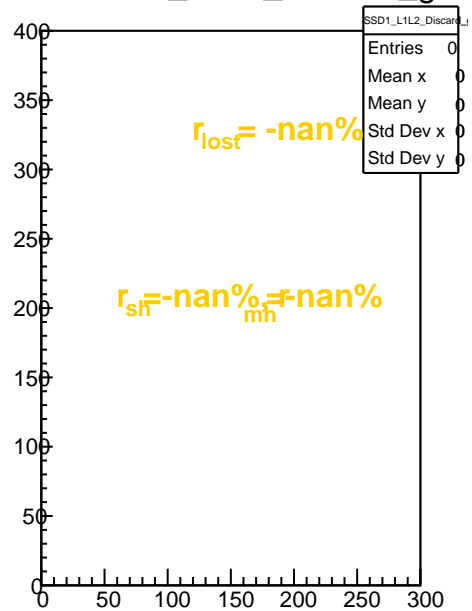
SSD1_L1L2_g3-202



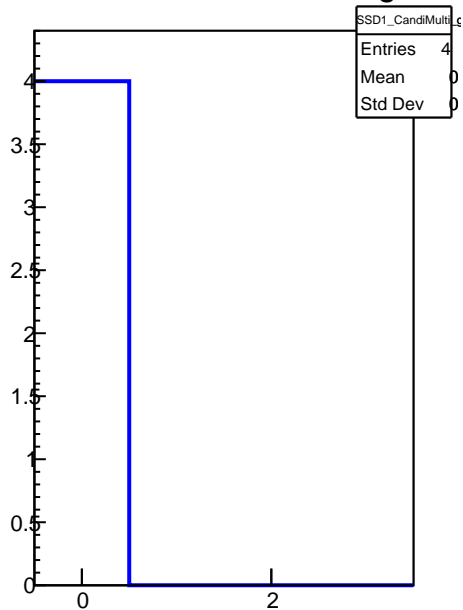
SSD1_L1L2_Cuts_g3-202



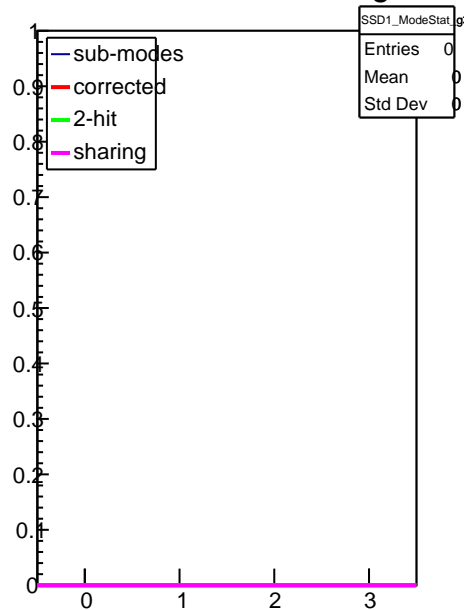
SSD1_L1L2_Discard_g3-202



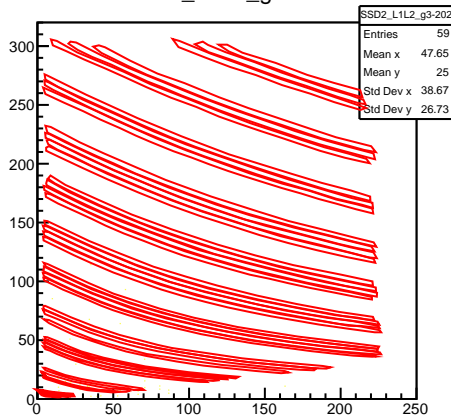
SSD1_CandiMulti_g3-202



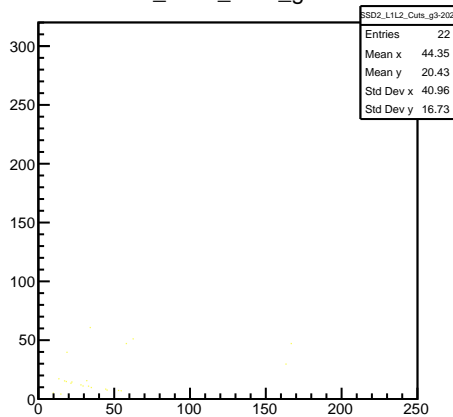
SSD1_ModeStat_g3-202



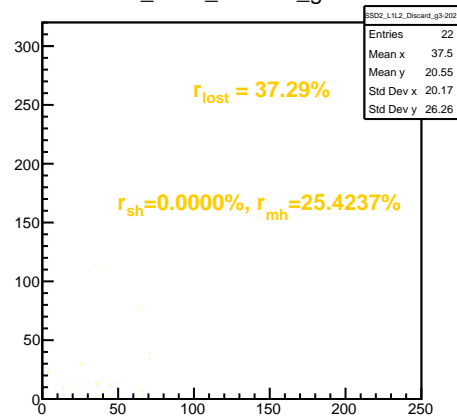
SSD2_L1L2_g3-202



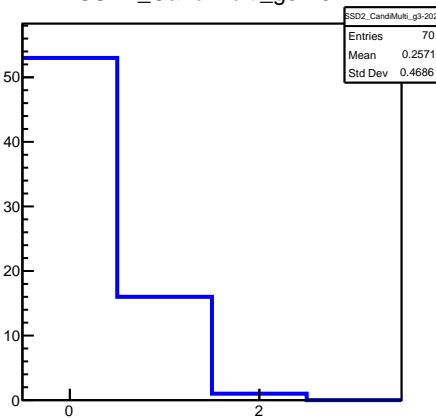
SSD2_L1L2_Cuts_g3-202



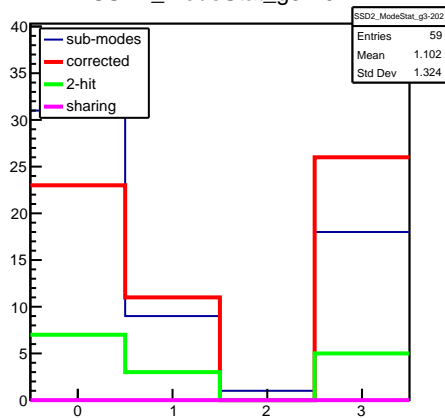
SSD2_L1L2_Discard_g3-202



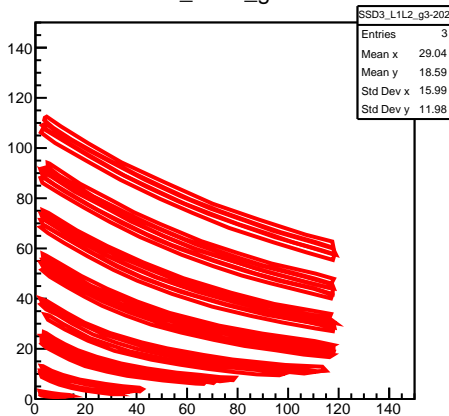
SSD2_CandiMulti_g3-202



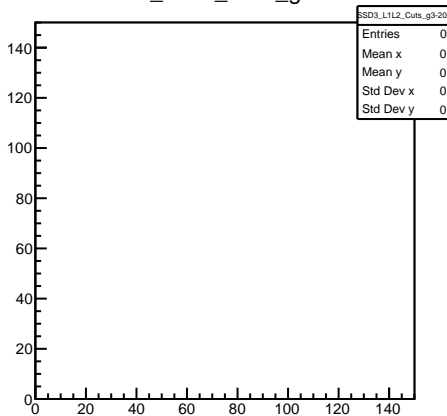
SSD2_ModeStat_g3-202



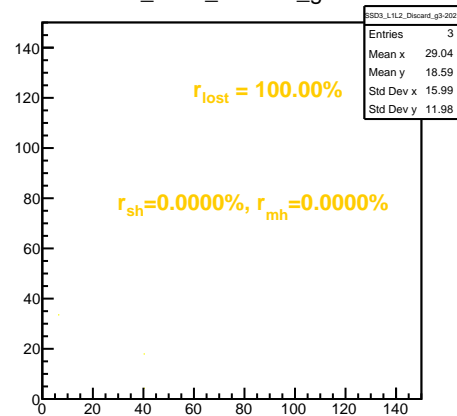
SSD3_L1L2_g3-202



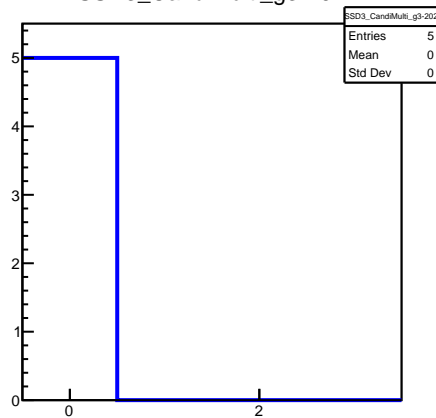
SSD3_L1L2_Cuts_g3-202



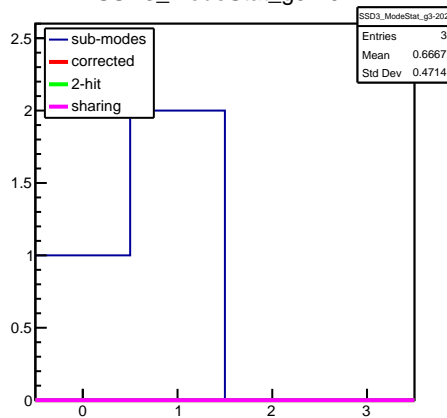
SSD3_L1L2_Discard_g3-202



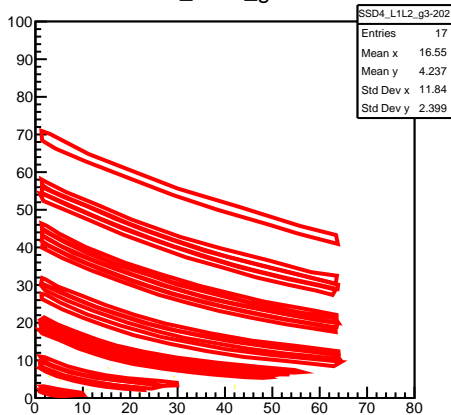
SSD3_CandiMulti_g3-202



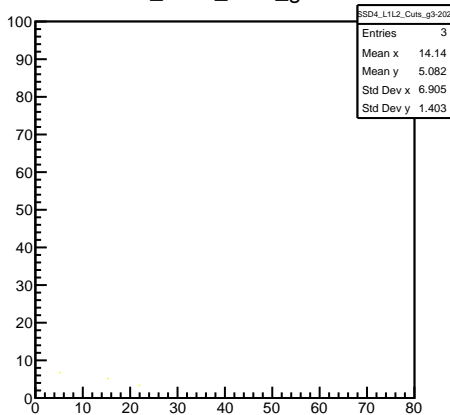
SSD3_ModeStat_g3-202



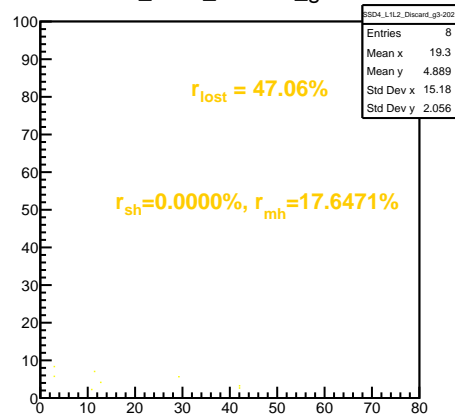
SSD4_L1L2_g3-202



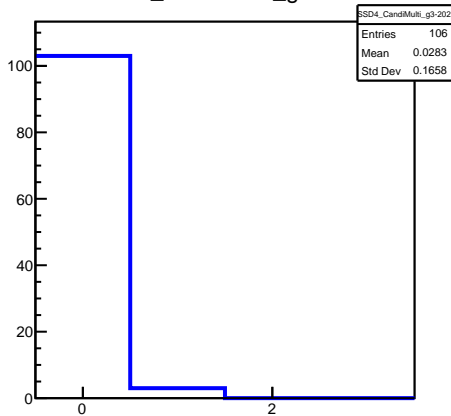
SSD4_L1L2_Cuts_g3-202



SSD4_L1L2_Discard_g3-202



SSD4_CandiMulti_g3-202



SSD4_ModeStat_g3-202

