SSD1_L2B_Switch_AlphaCali33_48_CH00

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

140
120
100
80
60
40

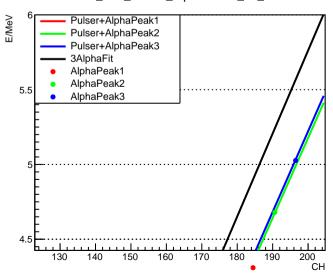
SSD1_L2B_Switch_AlphaCali33_48_CH00

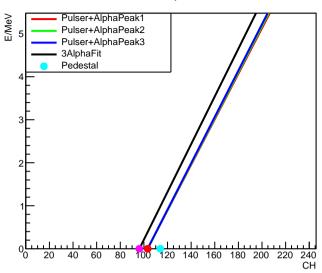
3000

4000 CH

20

1000

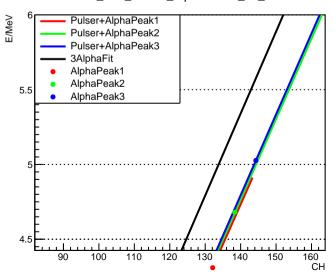




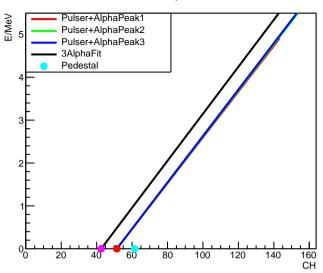
SSD1_L2B_Switch_AlphaCali33_48_CH01 E/MeV Pulser+AlphaPeak1 Pulser+AlphaPeak2 Pulser+AlphaPeak3 3AlphaFit **|**-

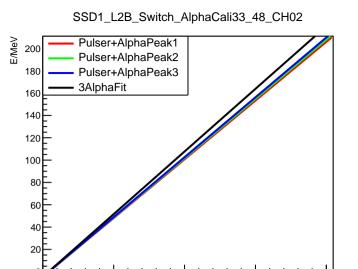
SSD1_L2B_Switch_AlphaCali33_48_CH01

CH



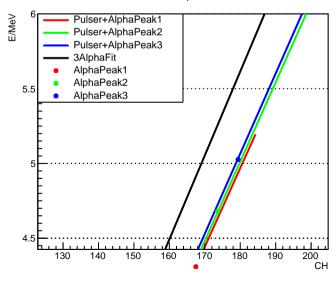


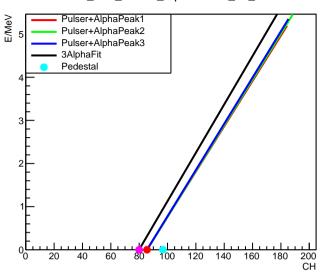






CH





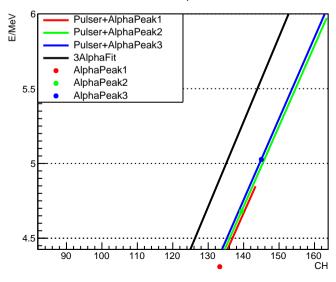
SSD1_L2B_Switch_AlphaCali33_48_CH03

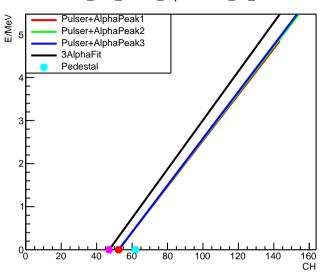
Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

140
120
100
80
40

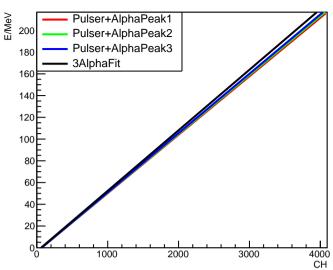


CH

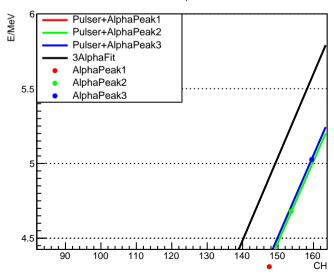


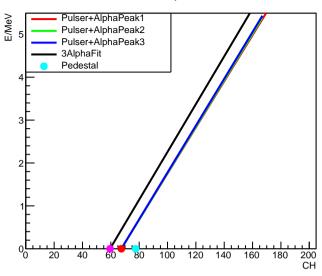


SSD1_L2B_Switch_AlphaCali33_48_CH04

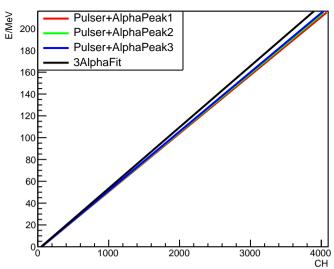


SSD1_L2B_Switch_AlphaCali33_48_CH04

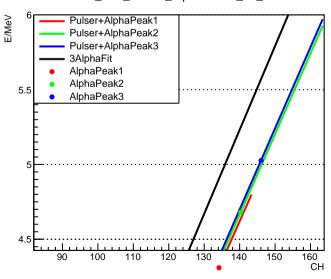




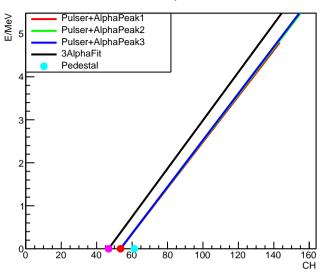
SSD1_L2B_Switch_AlphaCali33_48_CH05



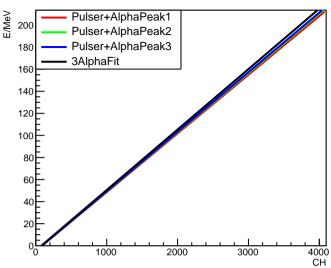
SSD1_L2B_Switch_AlphaCali33_48_CH05



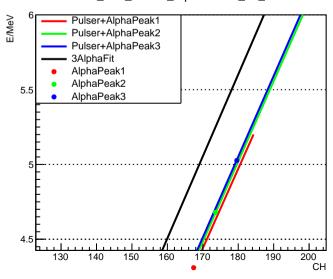


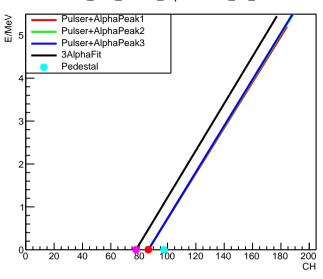


SSD1_L2B_Switch_AlphaCali33_48_CH06



SSD1_L2B_Switch_AlphaCali33_48_CH06





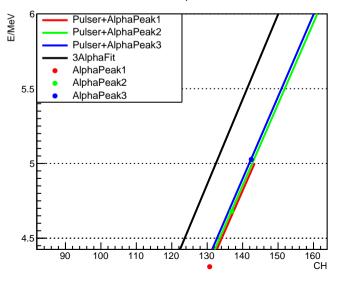
SSD1_L2B_Switch_AlphaCali33_48_CH07 E/MeV Pulser+AlphaPeak1 200 Pulser+AlphaPeak2 Pulser+AlphaPeak3 180 3AlphaFit 160 140 120 100 80 60[⁴⁰E 20

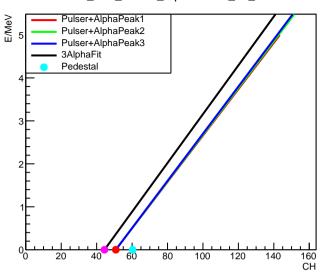


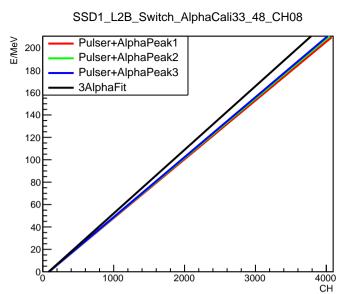
3000

1000

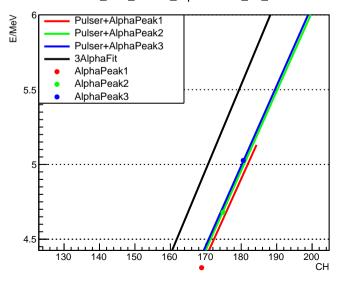
4000 CH

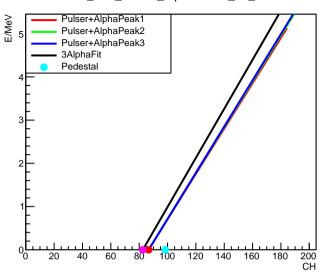


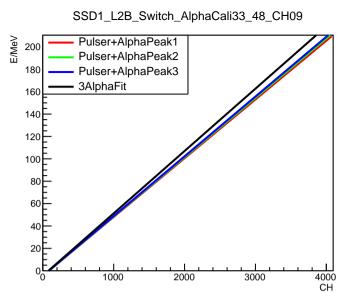




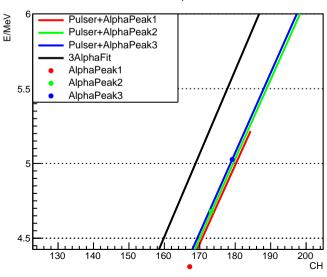
SSD1_L2B_Switch_AlphaCali33_48_CH08



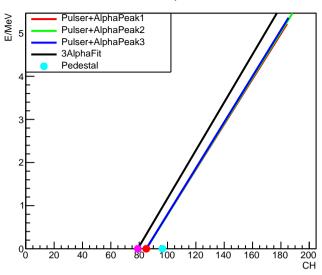




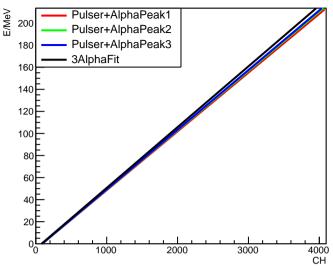




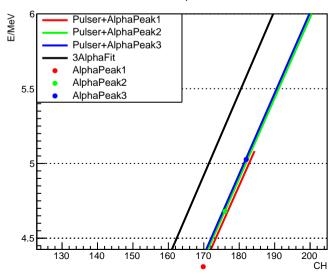




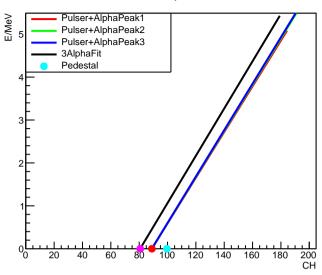
SSD1_L2B_Switch_AlphaCali33_48_CH10

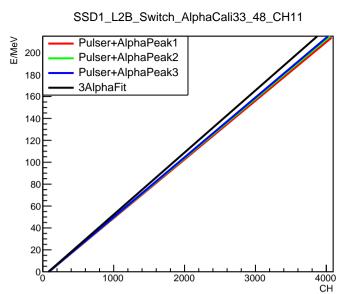


SSD1_L2B_Switch_AlphaCali33_48_CH10

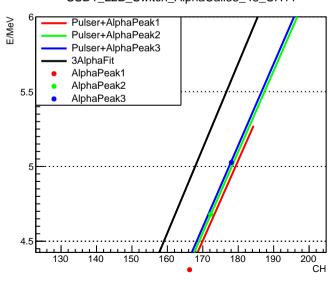


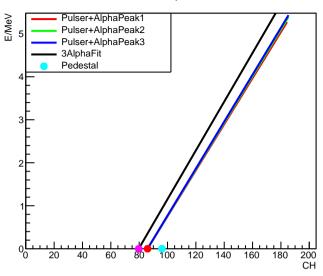




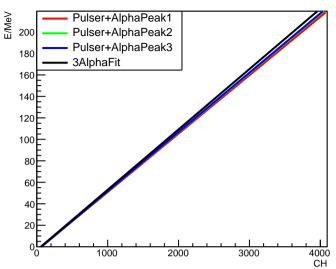




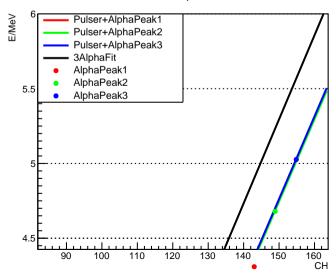




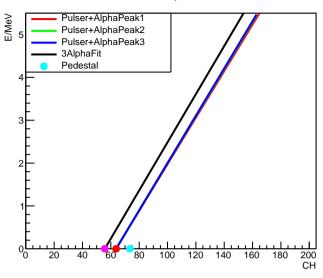
SSD1_L2B_Switch_AlphaCali33_48_CH12



SSD1_L2B_Switch_AlphaCali33_48_CH12



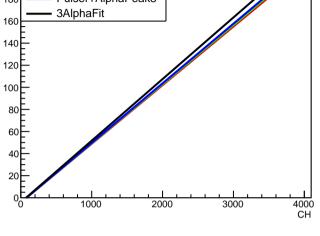




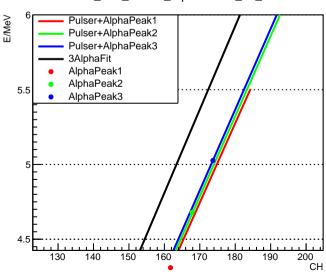
SSD1_L2B_Switch_AlphaCali33_48_CH13

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
AlphaFit

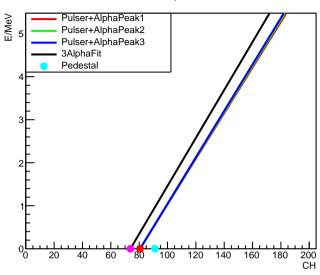
160
140



SSD1_L2B_Switch_AlphaCali33_48_CH13







SSD1_L2B_Switch_AlphaCali33_48_CH14

Pulser+AlphaPeak1
Pulser+AlphaPeak2
Pulser+AlphaPeak3
3AlphaFit

140
120
100
80
60
40

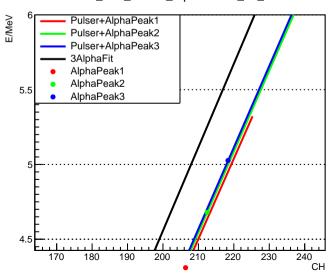
SSD1_L2B_Switch_AlphaCali33_48_CH14

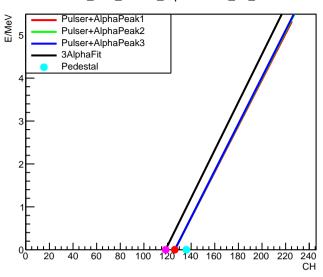
3000

4000 CH

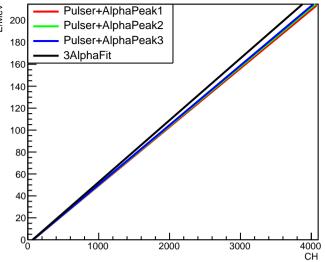
20

1000

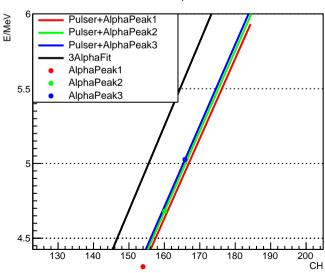




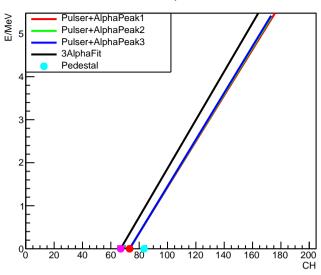
SSD1_L2B_Switch_AlphaCali33_48_CH15 E/MeV Pulser+AlphaPeak1 200 Pulser+AlphaPeak2 Pulser+AlphaPeak3 180



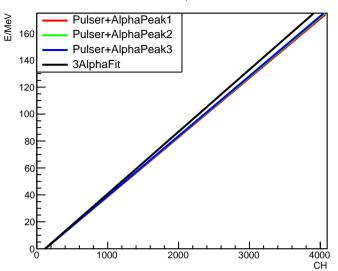
SSD1_L2B_Switch_AlphaCali33_48_CH15



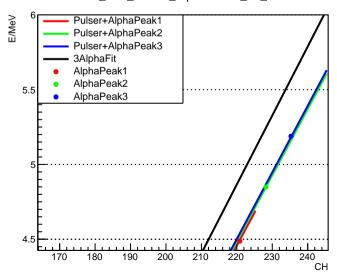


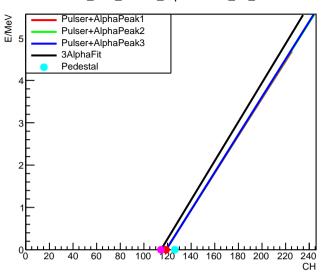


SSD2_L2B_Switch_AlphaCali33_48_CH00

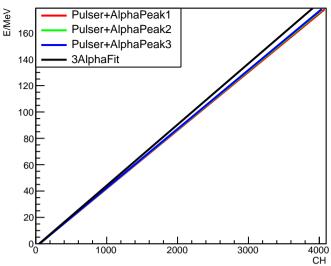


SSD2_L2B_Switch_AlphaCali33_48_CH00

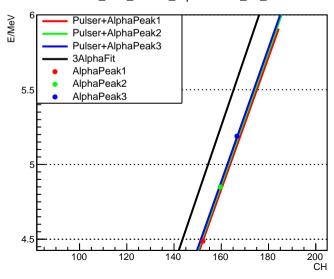


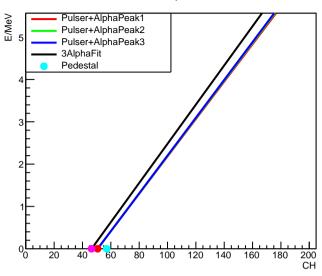


SSD2_L2B_Switch_AlphaCali33_48_CH01

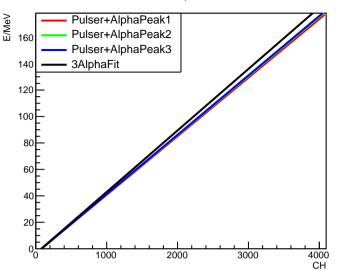


SSD2_L2B_Switch_AlphaCali33_48_CH01

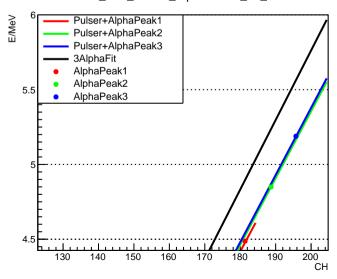


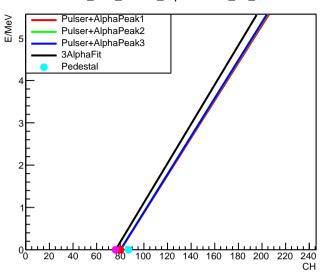


SSD2_L2B_Switch_AlphaCali33_48_CH02

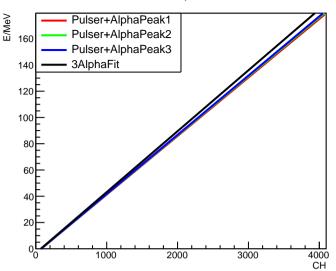


SSD2_L2B_Switch_AlphaCali33_48_CH02

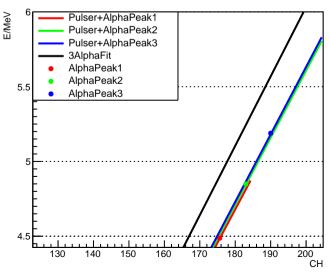


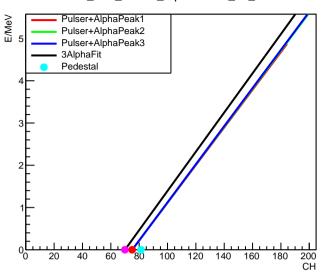


SSD2_L2B_Switch_AlphaCali33_48_CH03

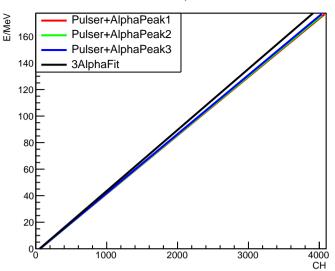


SSD2_L2B_Switch_AlphaCali33_48_CH03

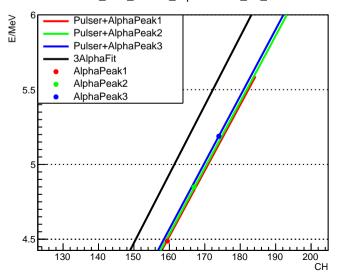


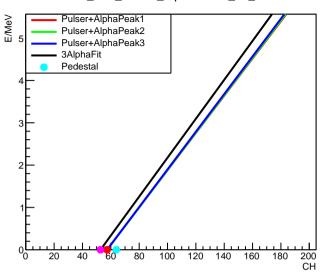


SSD2_L2B_Switch_AlphaCali33_48_CH04

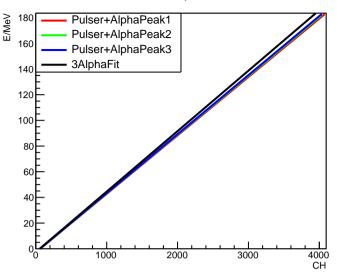


SSD2_L2B_Switch_AlphaCali33_48_CH04

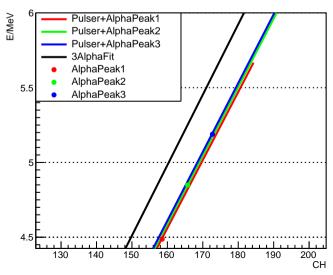


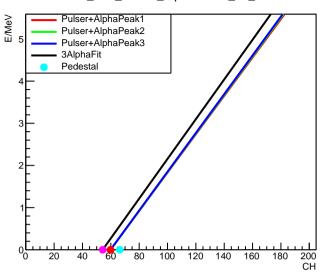


SSD2_L2B_Switch_AlphaCali33_48_CH05

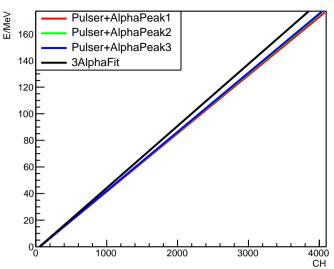


SSD2_L2B_Switch_AlphaCali33_48_CH05

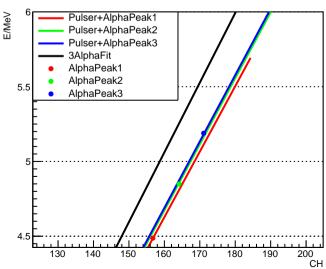


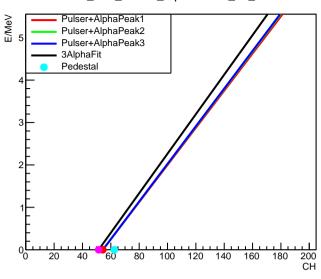


SSD2_L2B_Switch_AlphaCali33_48_CH06

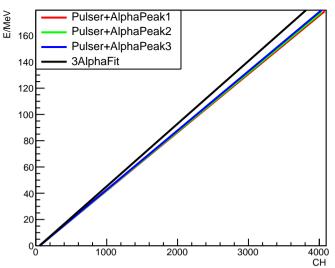


SSD2_L2B_Switch_AlphaCali33_48_CH06

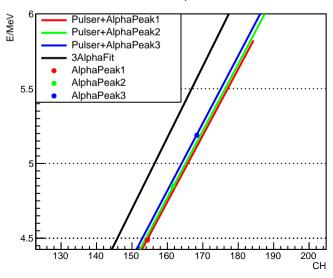




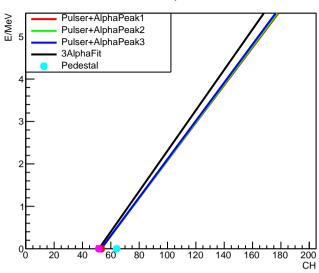
SSD2_L2B_Switch_AlphaCali33_48_CH07 Pulser+AlphaPeak1



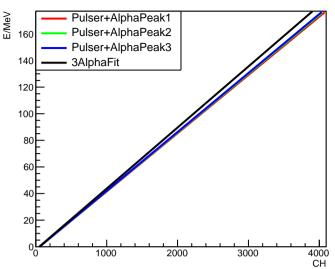
SSD2_L2B_Switch_AlphaCali33_48_CH07



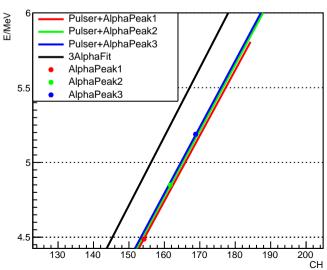


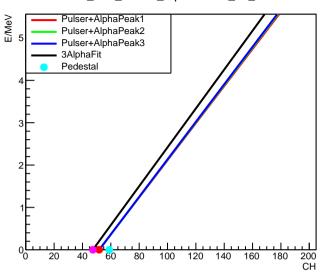


 $SSD2_L2B_Switch_AlphaCali33_48_CH08$

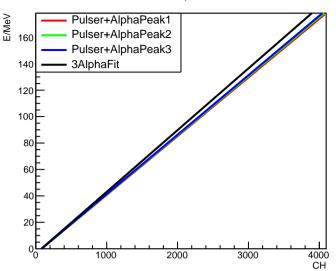


SSD2_L2B_Switch_AlphaCali33_48_CH08

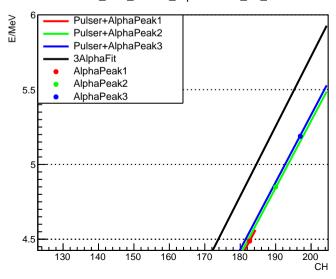


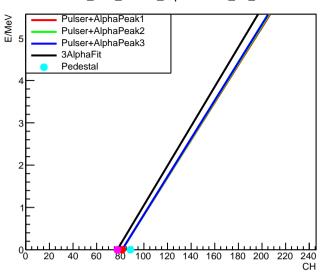


SSD2_L2B_Switch_AlphaCali33_48_CH09

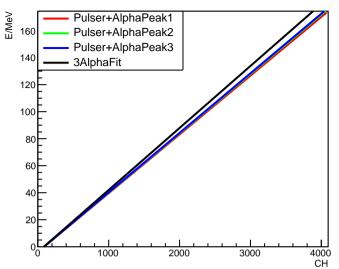


SSD2_L2B_Switch_AlphaCali33_48_CH09

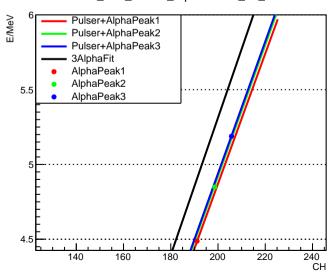


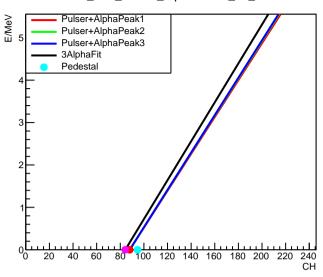


SSD2_L2B_Switch_AlphaCali33_48_CH10

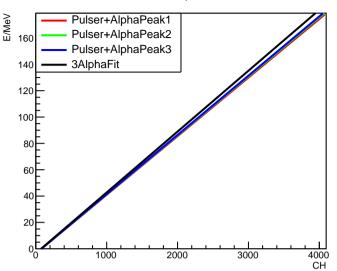


SSD2_L2B_Switch_AlphaCali33_48_CH10

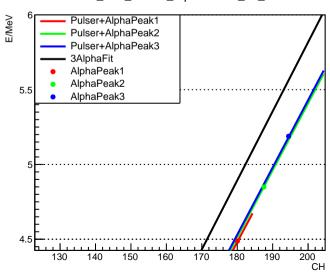


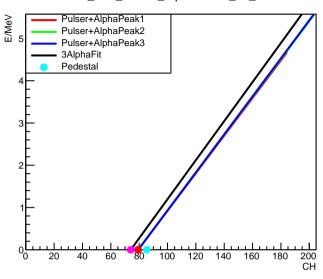


SSD2_L2B_Switch_AlphaCali33_48_CH11

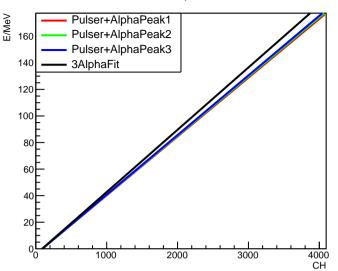


SSD2_L2B_Switch_AlphaCali33_48_CH11

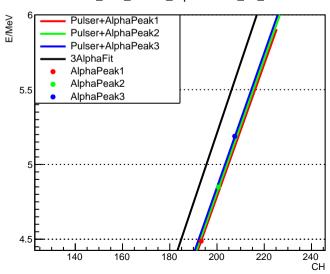


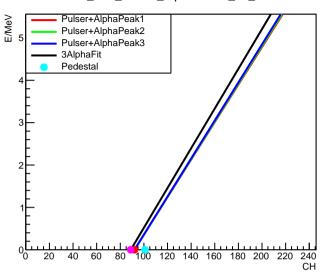


SSD2_L2B_Switch_AlphaCali33_48_CH12

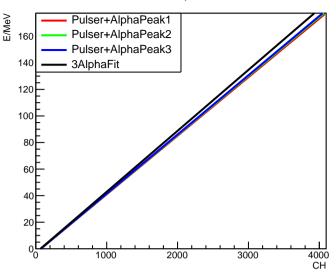


SSD2_L2B_Switch_AlphaCali33_48_CH12

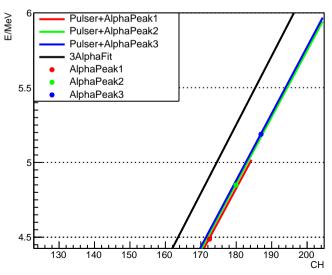


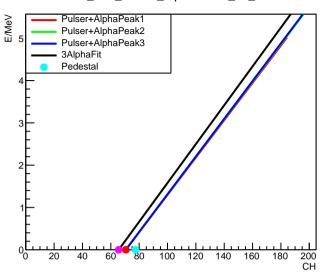


SSD2_L2B_Switch_AlphaCali33_48_CH13

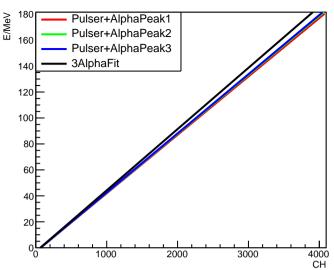


SSD2_L2B_Switch_AlphaCali33_48_CH13

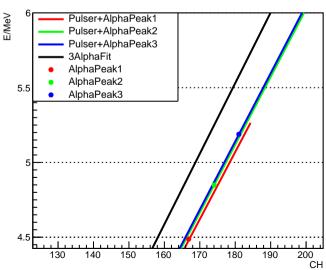


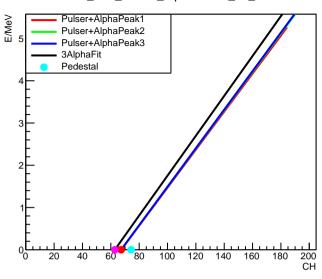


SSD2_L2B_Switch_AlphaCali33_48_CH14

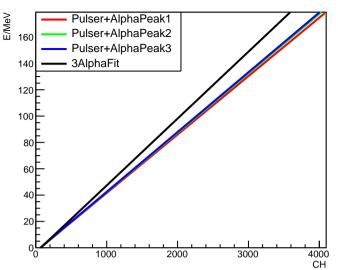


SSD2_L2B_Switch_AlphaCali33_48_CH14

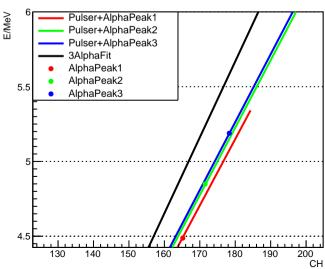


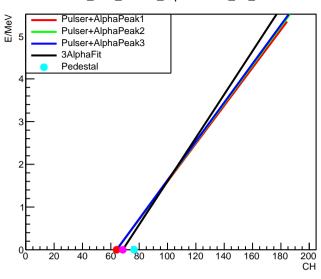


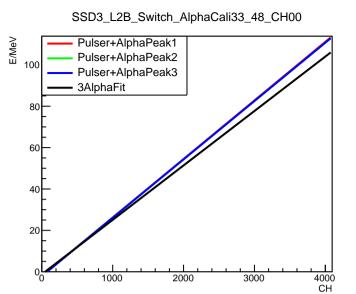
SSD2_L2B_Switch_AlphaCali33_48_CH15



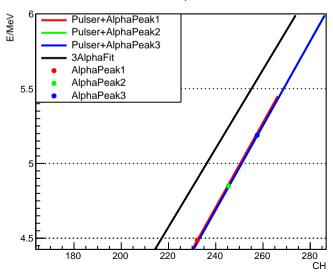
SSD2_L2B_Switch_AlphaCali33_48_CH15

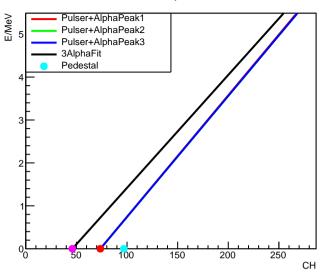


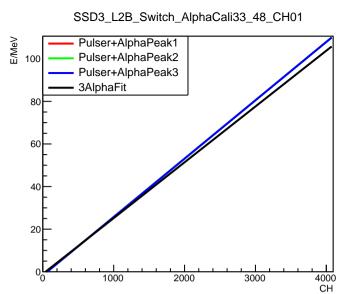




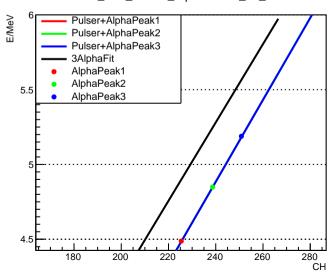




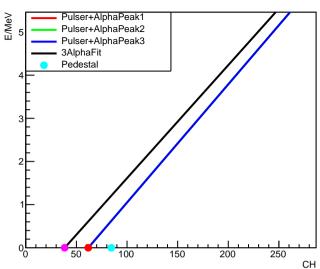


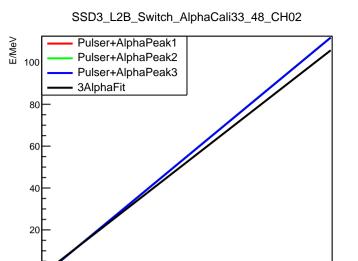


SSD3_L2B_Switch_AlphaCali33_48_CH01



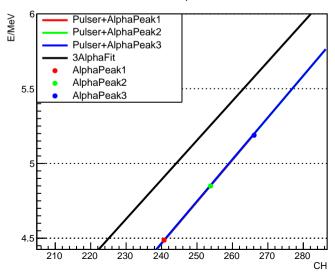


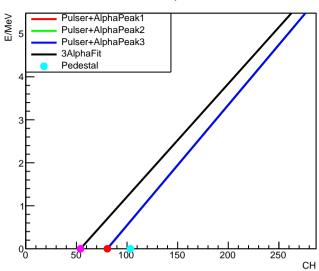


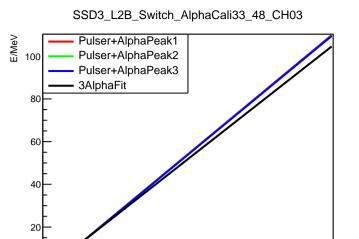




CH

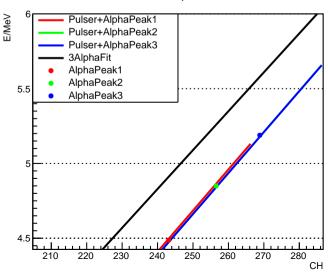


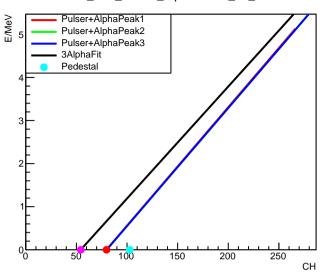


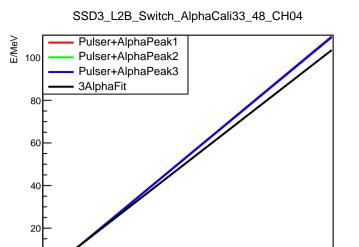




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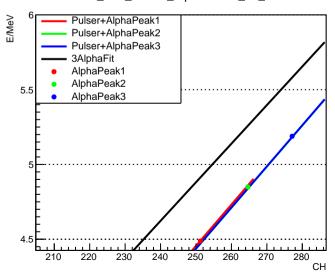




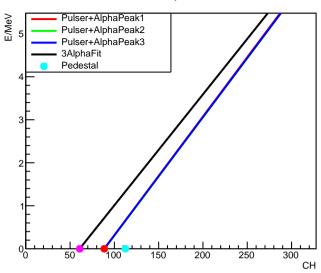


SSD3_L2B_Switch_AlphaCali33_48_CH04

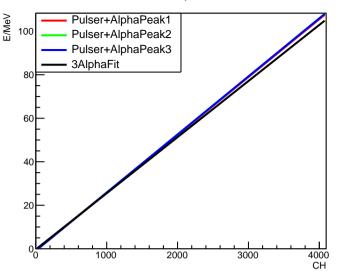
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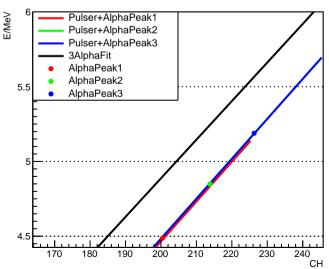


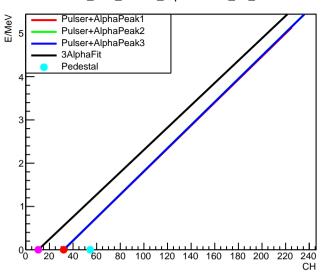


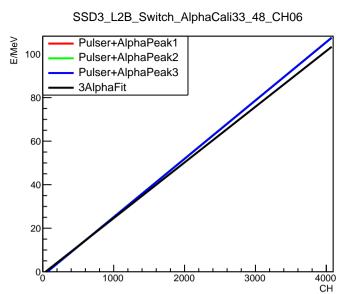
SSD3_L2B_Switch_AlphaCali33_48_CH05



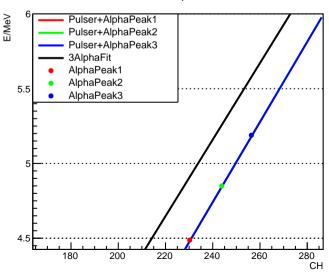
SSD3_L2B_Switch_AlphaCali33_48_CH05

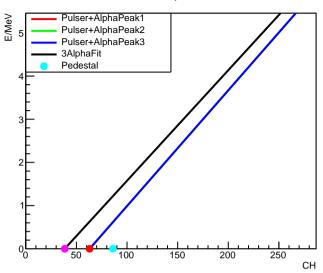




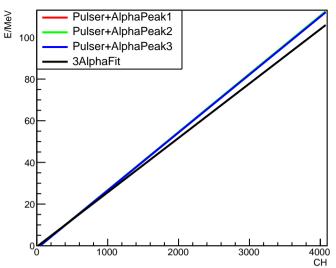




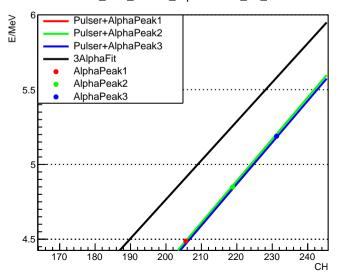


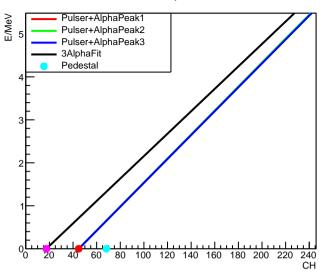


 $SSD3_L2B_Switch_AlphaCali33_48_CH07$

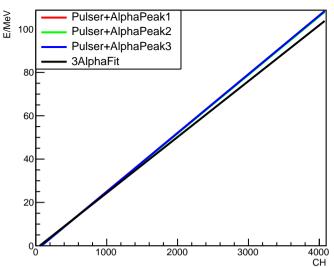


SSD3_L2B_Switch_AlphaCali33_48_CH07

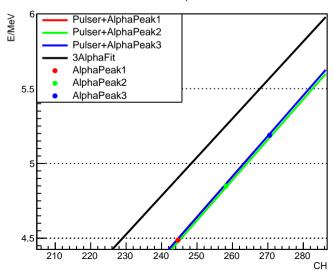




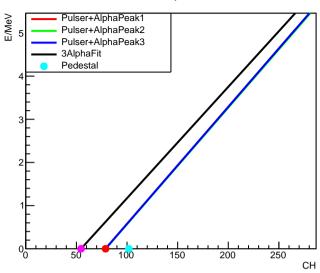
SSD3_L2B_Switch_AlphaCali33_48_CH08 Pulser+AlphaPeak1



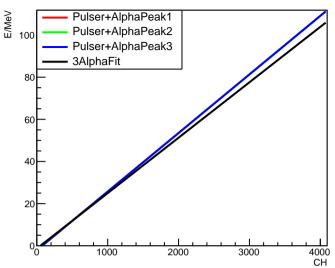
SSD3_L2B_Switch_AlphaCali33_48_CH08



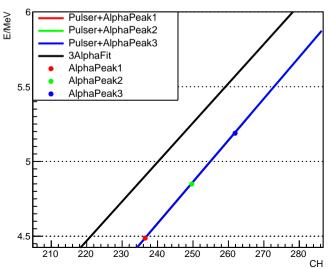


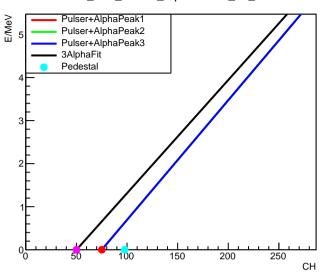


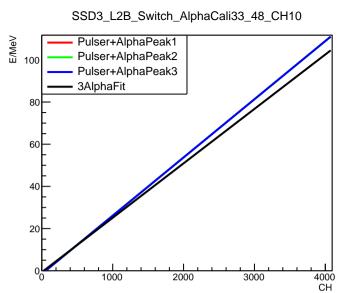
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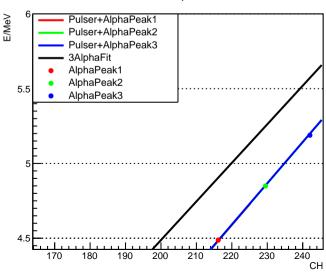
SSD3_L2B_Switch_AlphaCali33_48_CH09

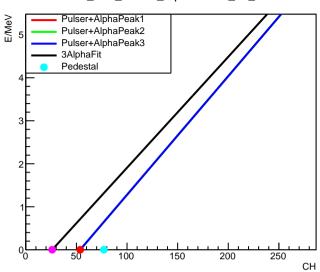


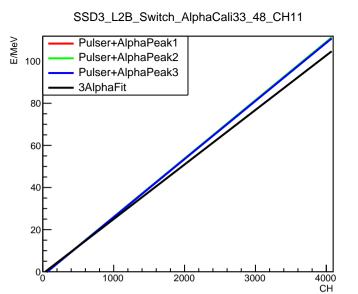




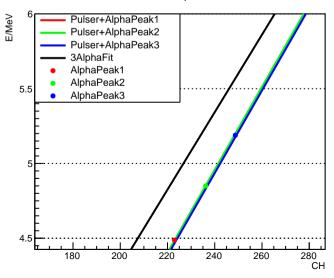




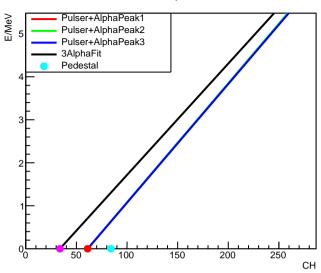




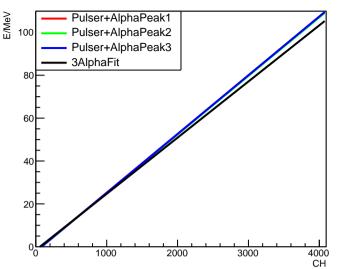




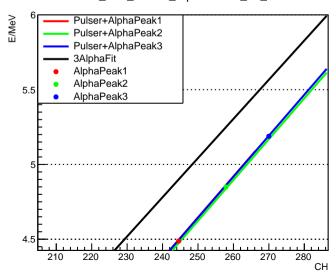


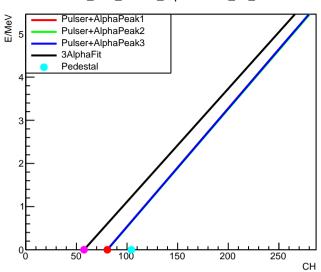


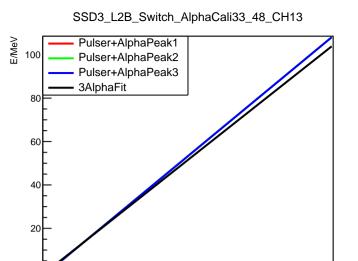
SSD3_L2B_Switch_AlphaCali33_48_CH12



SSD3_L2B_Switch_AlphaCali33_48_CH12

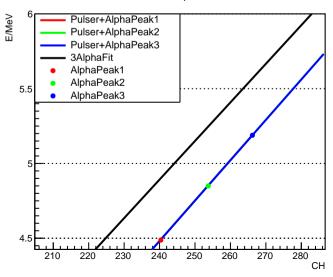


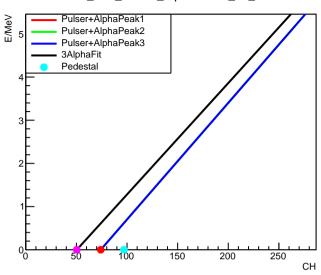






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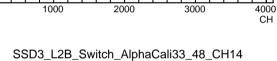




SSD3_L2B_Switch_AlphaCali33_48_CH14 E/MeV Pulser+AlphaPeak1 100 Pulser+AlphaPeak2 Pulser+AlphaPeak3 3AlphaFit 80 60

40

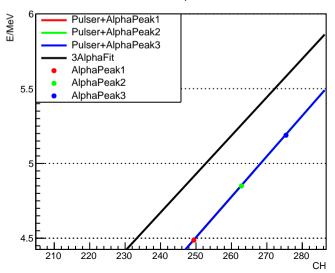
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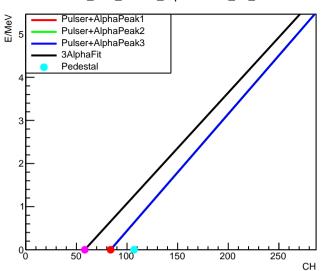


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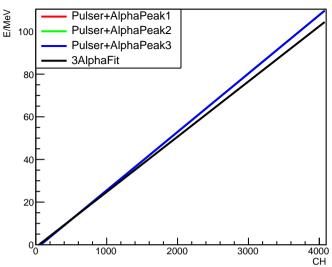
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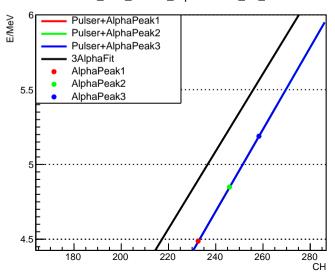


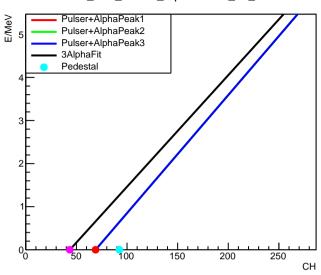


SSD3_L2B_Switch_AlphaCali33_48_CH15

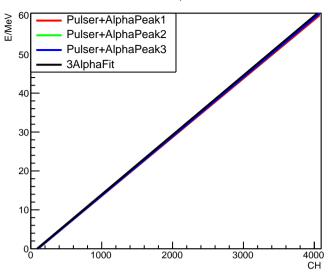


SSD3_L2B_Switch_AlphaCali33_48_CH15

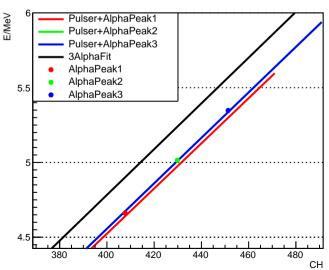




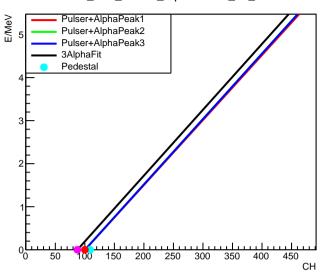
SSD4_L2B_Switch_AlphaCali33_48_CH00



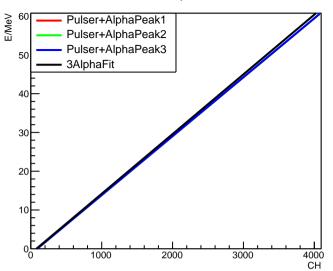
SSD4_L2B_Switch_AlphaCali33_48_CH00



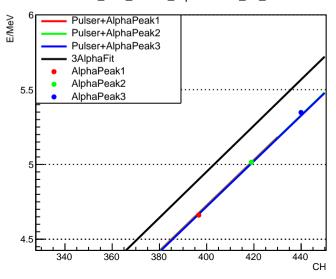
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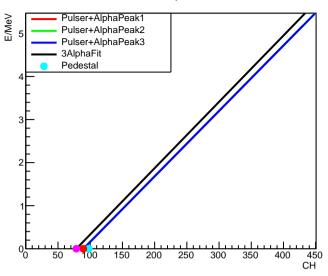


SSD4_L2B_Switch_AlphaCali33_48_CH01

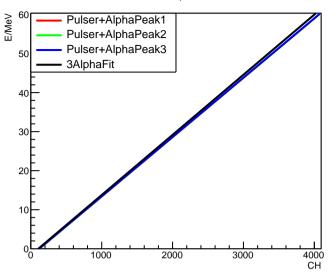


SSD4_L2B_Switch_AlphaCali33_48_CH01

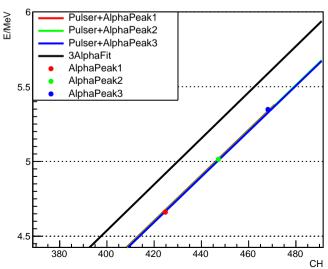


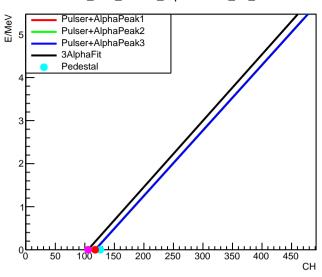


SSD4_L2B_Switch_AlphaCali33_48_CH02

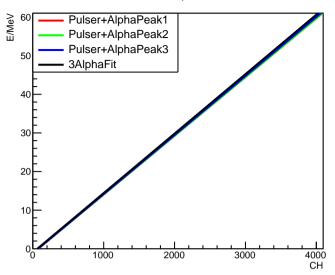


SSD4_L2B_Switch_AlphaCali33_48_CH02

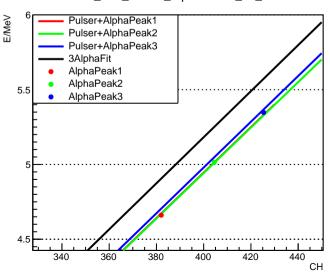


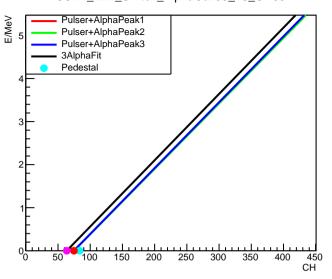


SSD4_L2B_Switch_AlphaCali33_48_CH03

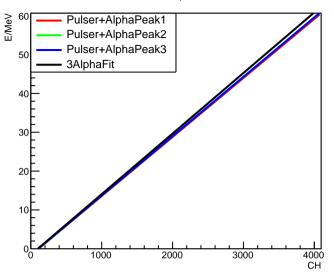


SSD4_L2B_Switch_AlphaCali33_48_CH03

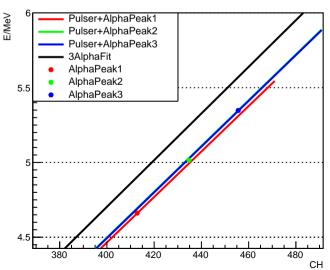


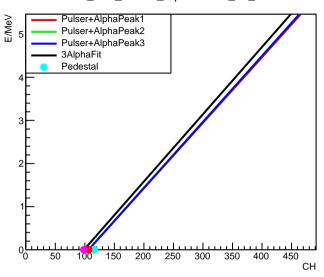


SSD4_L2B_Switch_AlphaCali33_48_CH04

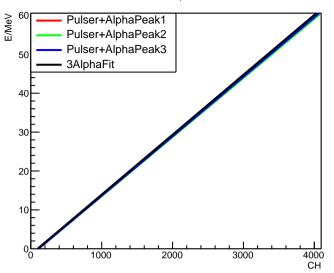


SSD4_L2B_Switch_AlphaCali33_48_CH04

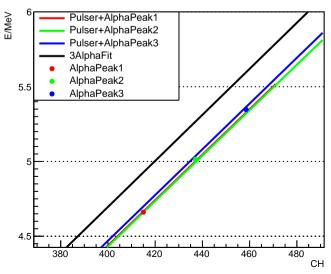


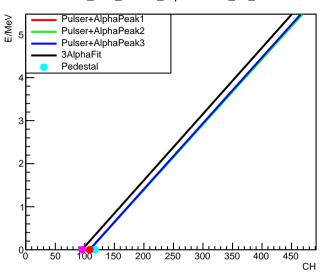


SSD4_L2B_Switch_AlphaCali33_48_CH05

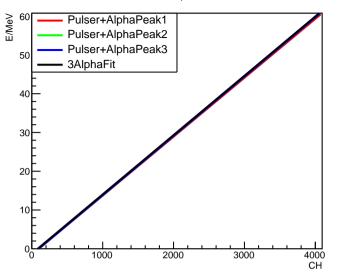


SSD4_L2B_Switch_AlphaCali33_48_CH05

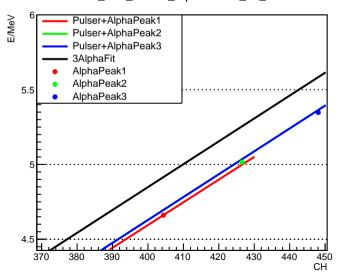


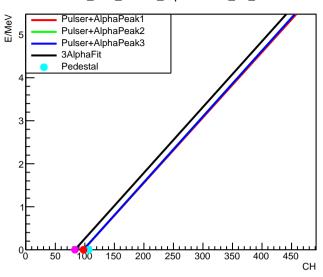


SSD4_L2B_Switch_AlphaCali33_48_CH06

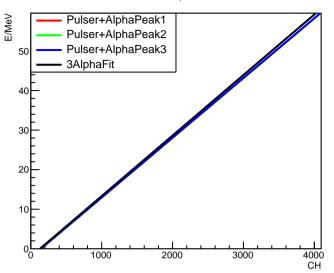


SSD4_L2B_Switch_AlphaCali33_48_CH06

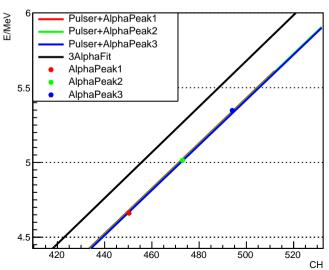




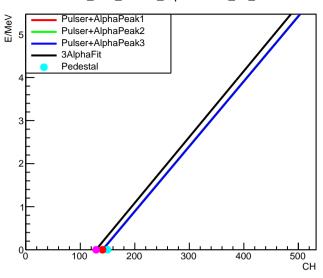
SSD4_L2B_Switch_AlphaCali33_48_CH07



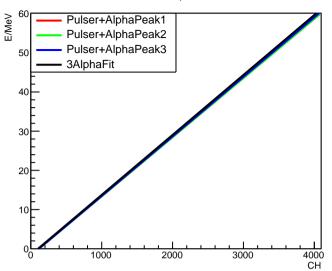
SSD4_L2B_Switch_AlphaCali33_48_CH07



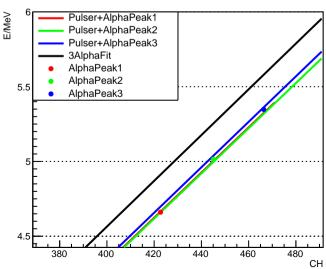
SSD4_L2B_Switch_AlphaCali33_48_CH07

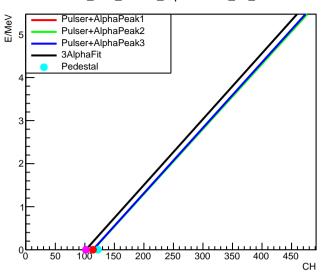


SSD4_L2B_Switch_AlphaCali33_48_CH08

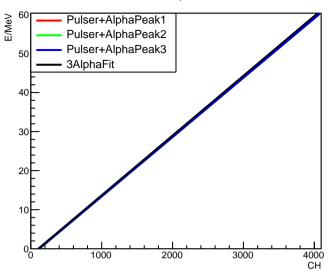


SSD4_L2B_Switch_AlphaCali33_48_CH08

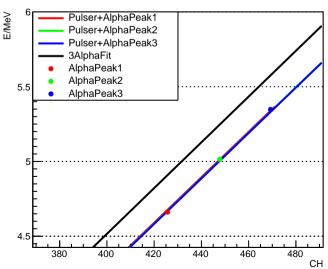


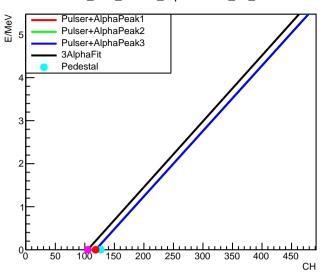


SSD4_L2B_Switch_AlphaCali33_48_CH09

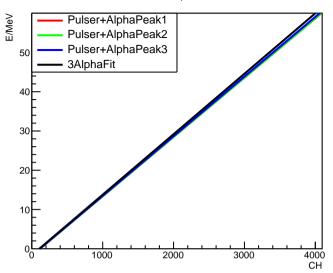


SSD4_L2B_Switch_AlphaCali33_48_CH09

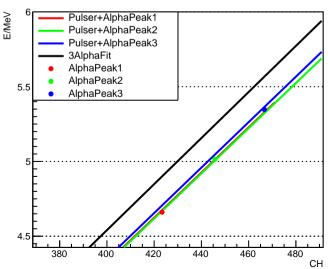


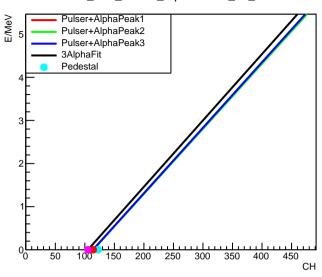


SSD4_L2B_Switch_AlphaCali33_48_CH10

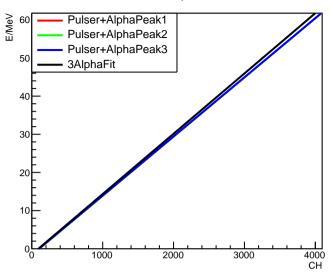


SSD4_L2B_Switch_AlphaCali33_48_CH10

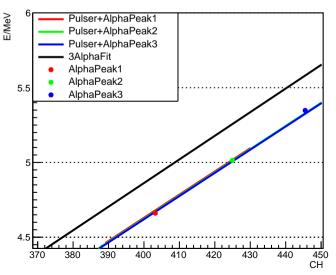


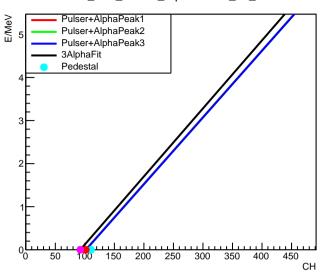


SSD4_L2B_Switch_AlphaCali33_48_CH11

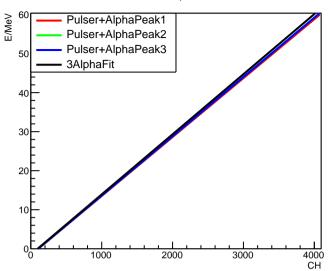


SSD4_L2B_Switch_AlphaCali33_48_CH11

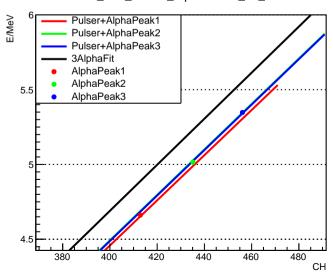


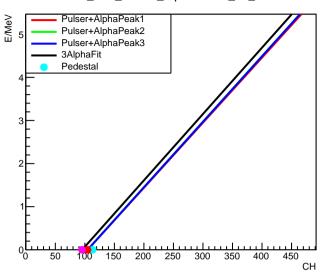


SSD4_L2B_Switch_AlphaCali33_48_CH12

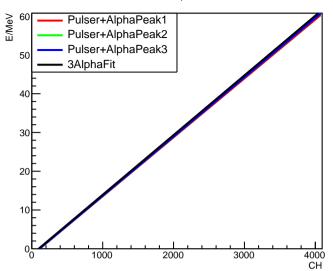


SSD4_L2B_Switch_AlphaCali33_48_CH12

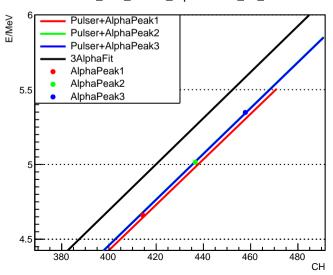


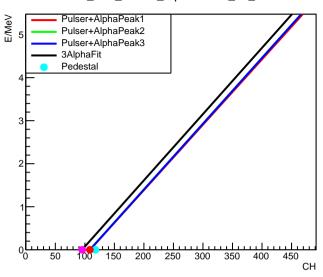


SSD4_L2B_Switch_AlphaCali33_48_CH13

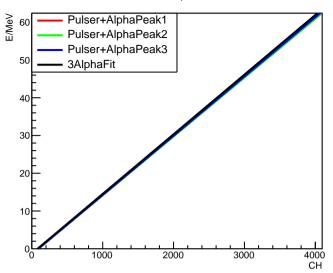


SSD4_L2B_Switch_AlphaCali33_48_CH13

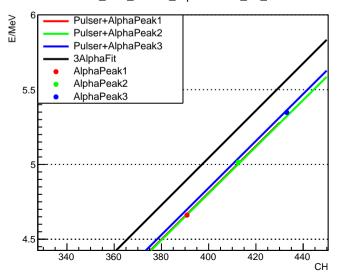


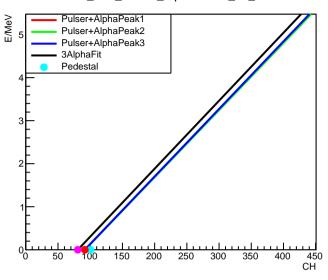


SSD4_L2B_Switch_AlphaCali33_48_CH14

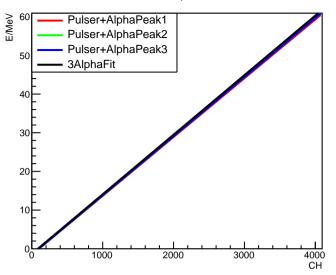


SSD4_L2B_Switch_AlphaCali33_48_CH14





SSD4_L2B_Switch_AlphaCali33_48_CH15



SSD4_L2B_Switch_AlphaCali33_48_CH15

