



2000

ADC Channel

2500

3000

3500

Relative_Pulser_Height

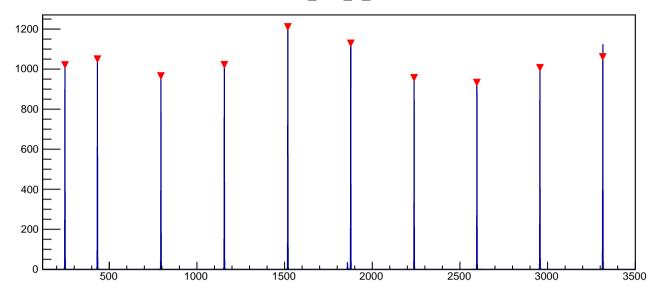
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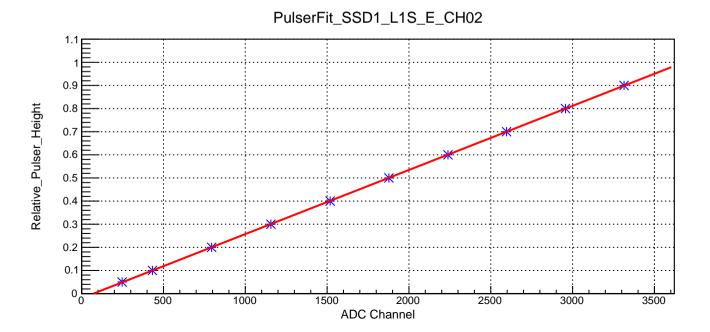
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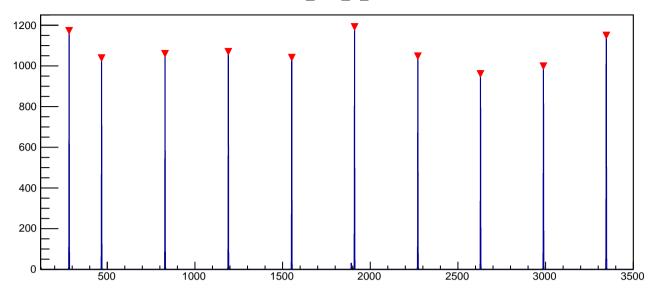
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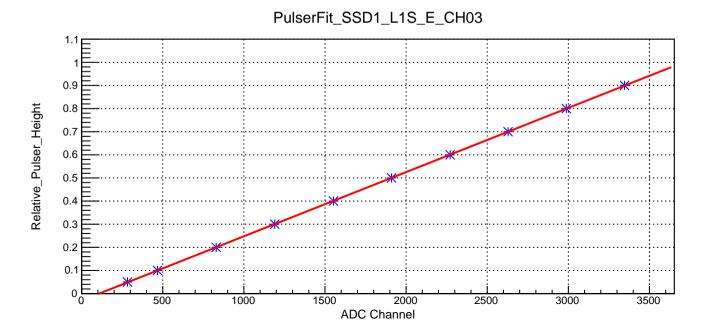
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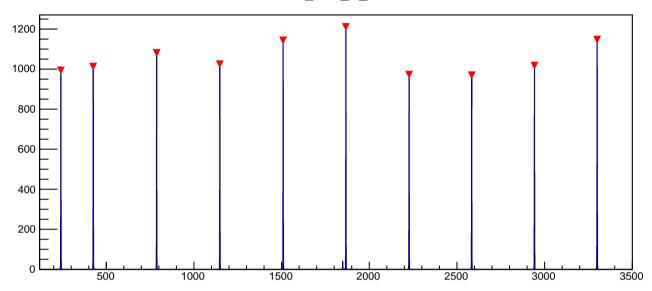
PulserFit_SSD1_L1S_E_CH01

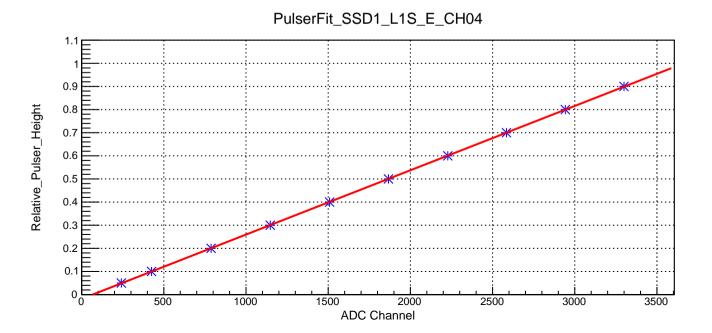


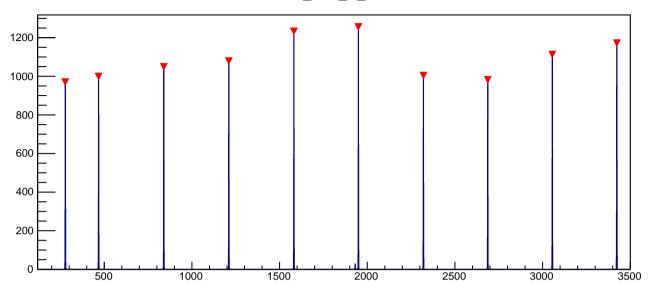


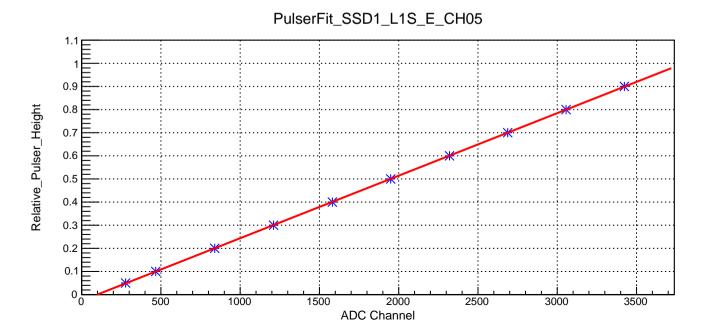


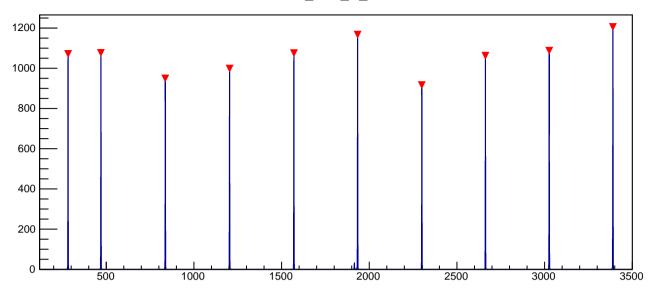


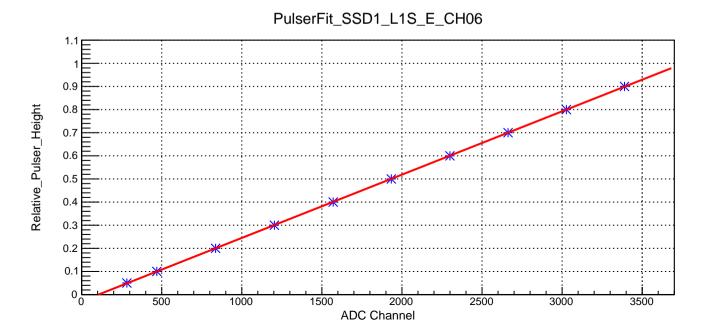


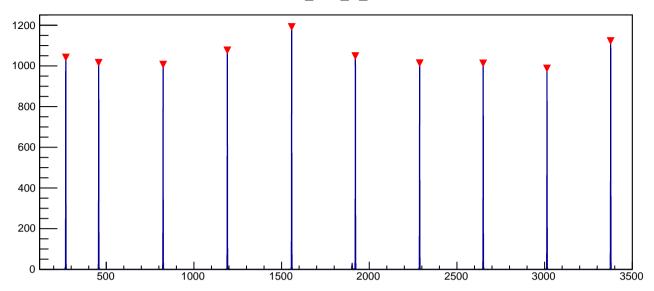


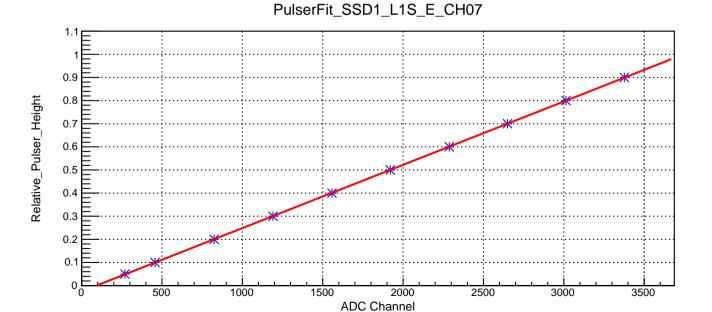


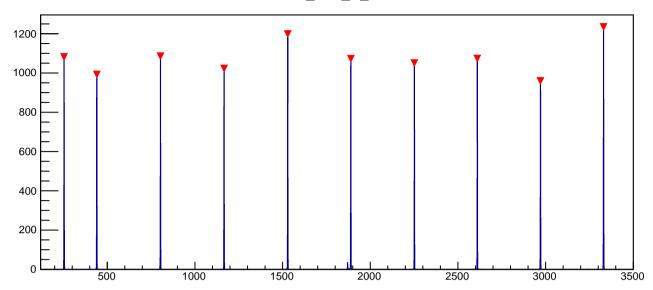


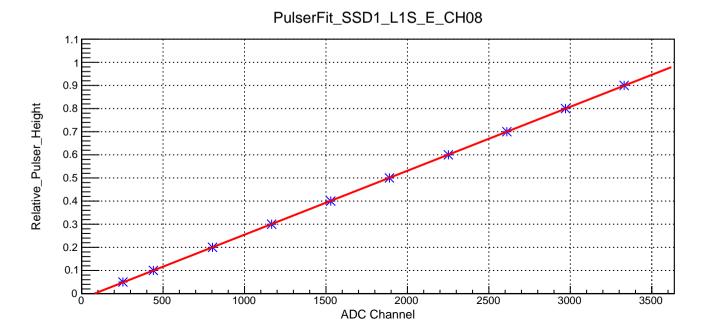


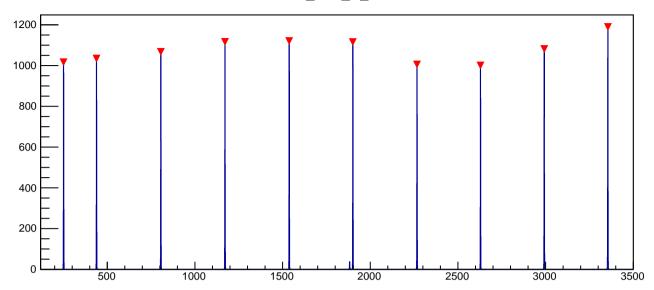


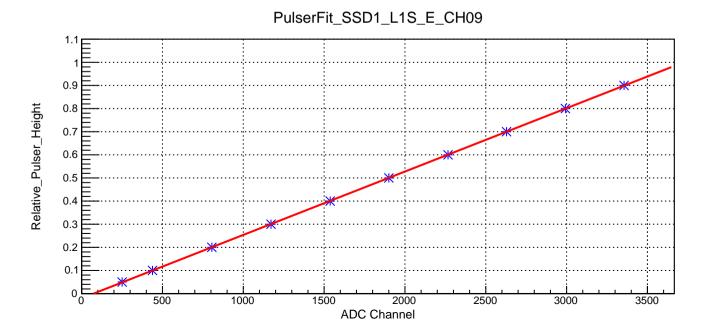


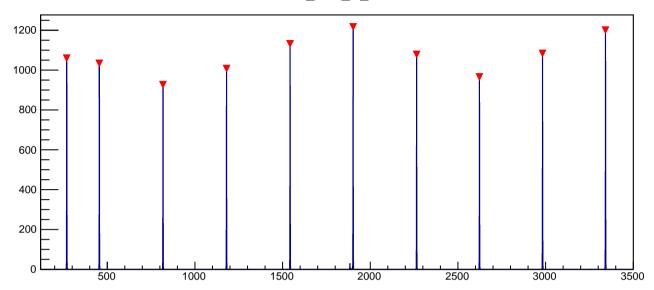


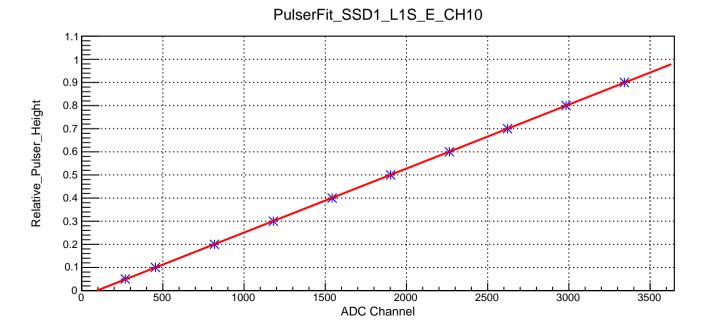


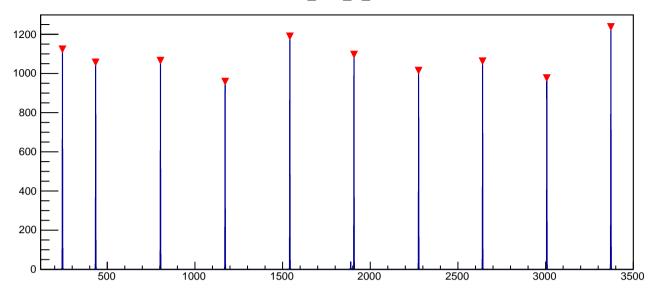


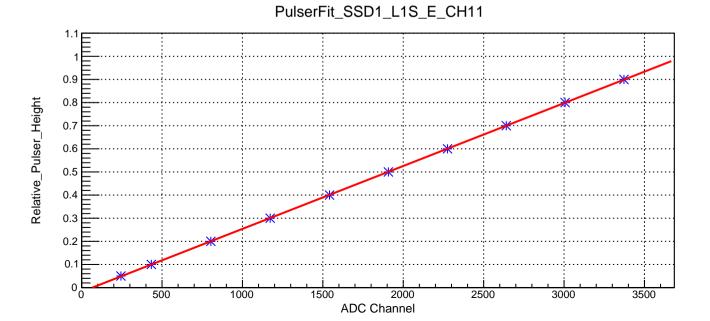


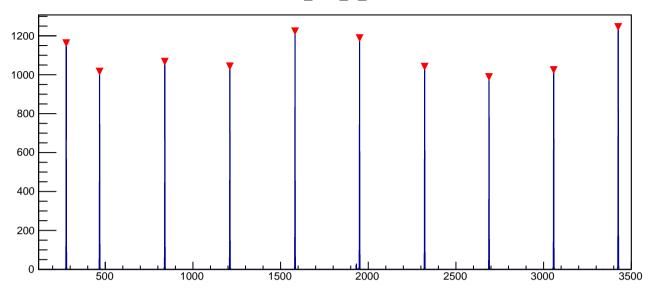


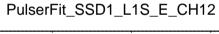


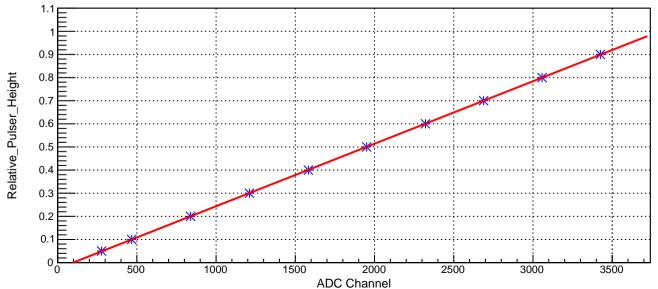


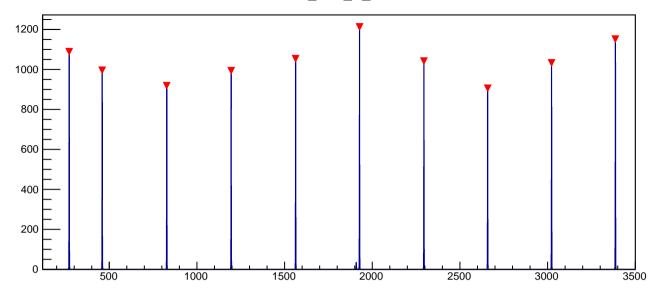


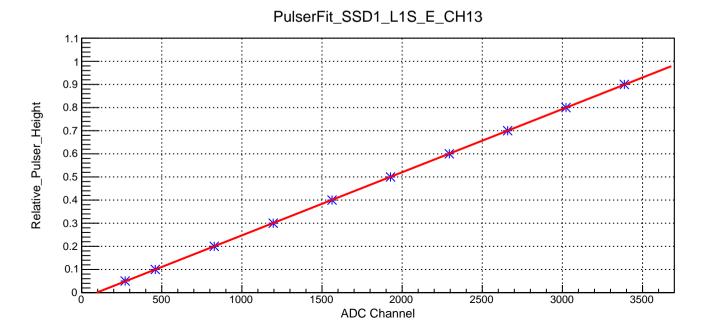


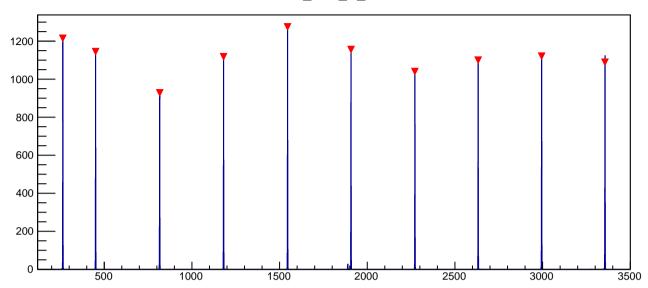


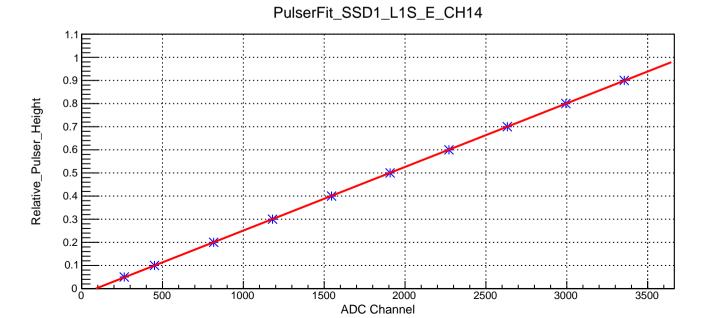


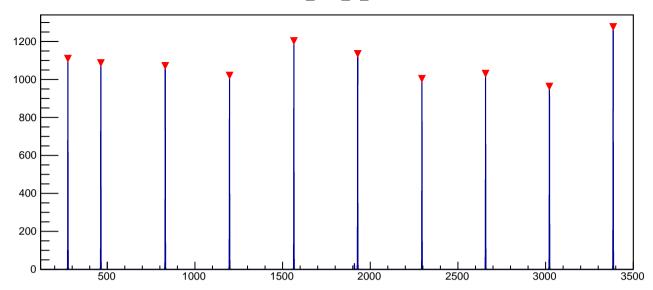


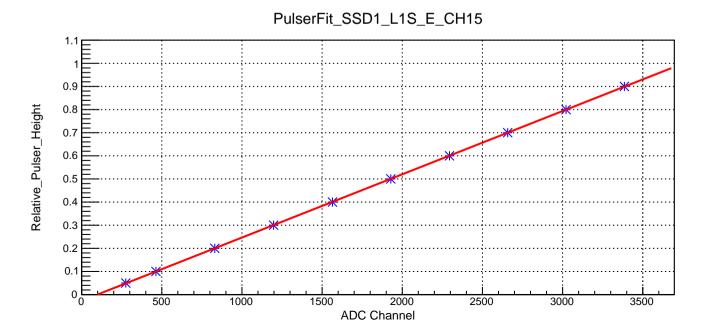


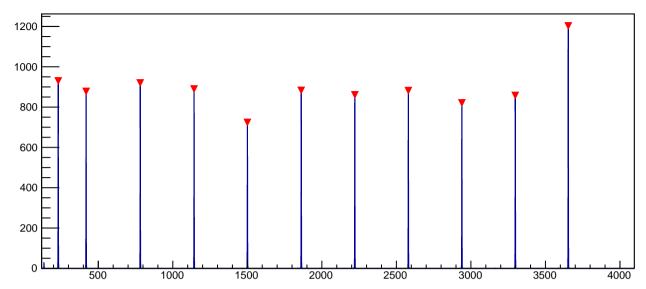


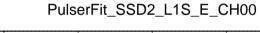


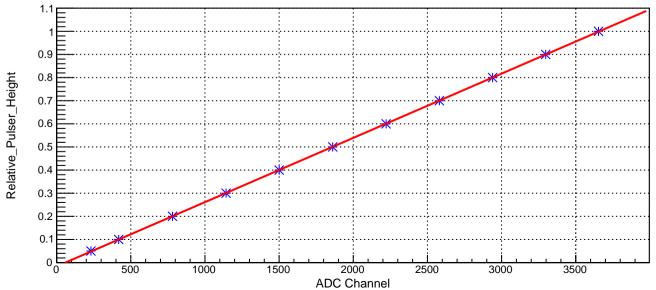


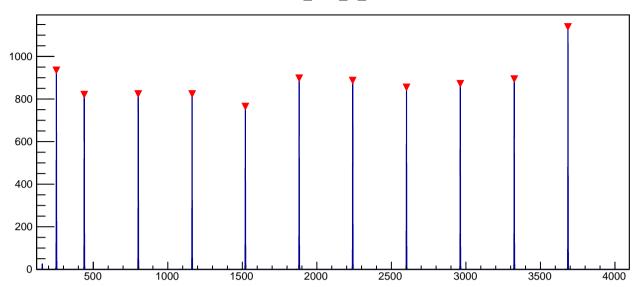


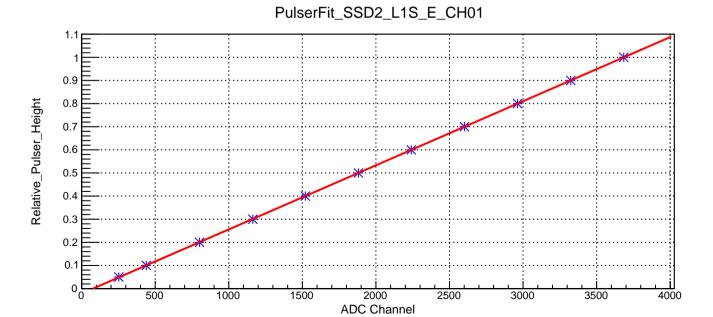


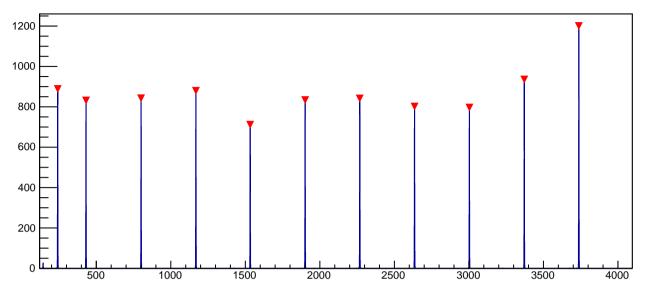




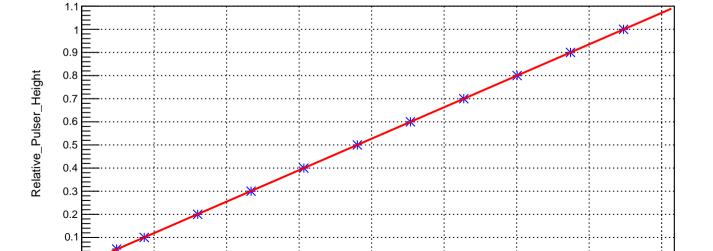




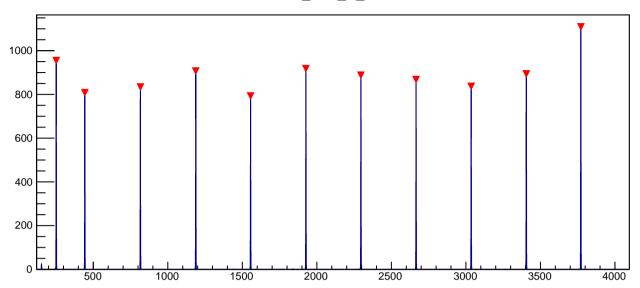


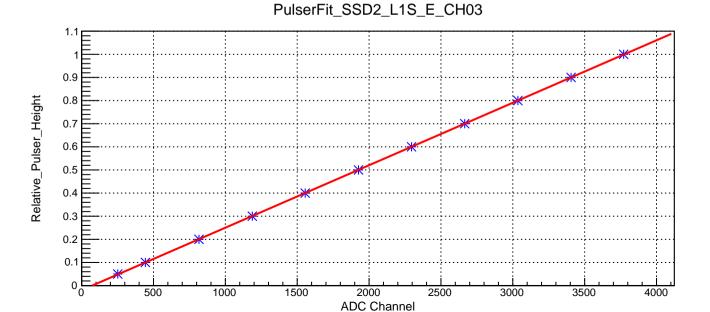


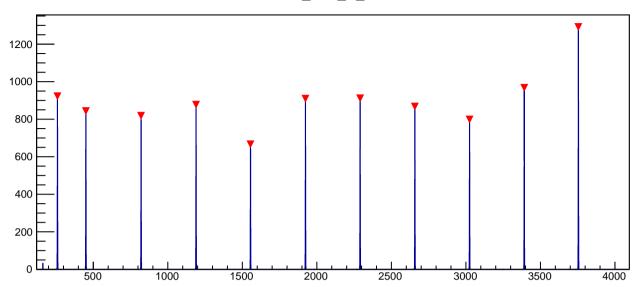
PulserFit_SSD2_L1S_E_CH02

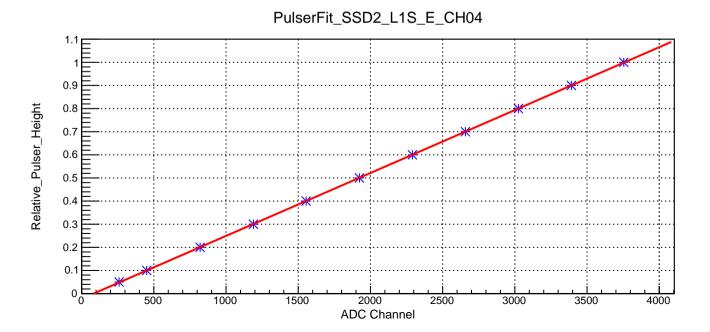


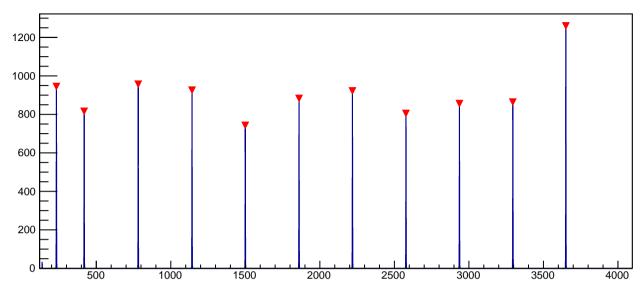
ADC Channel



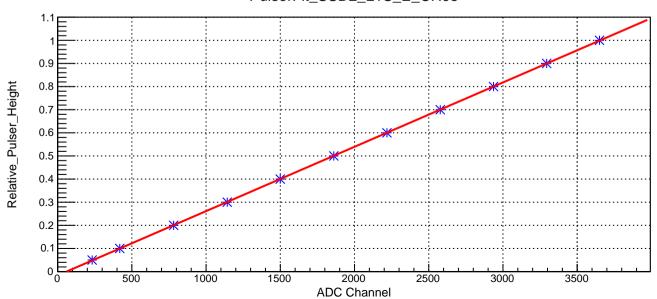


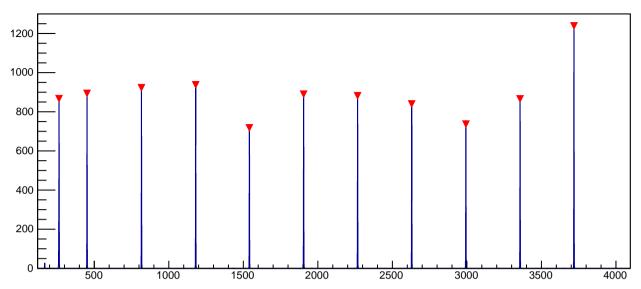


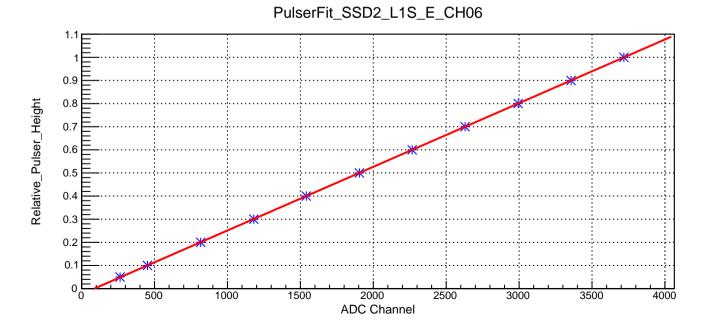


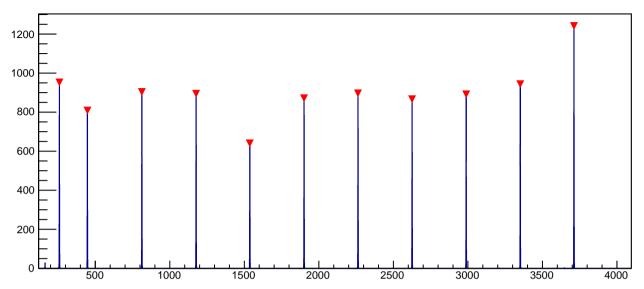


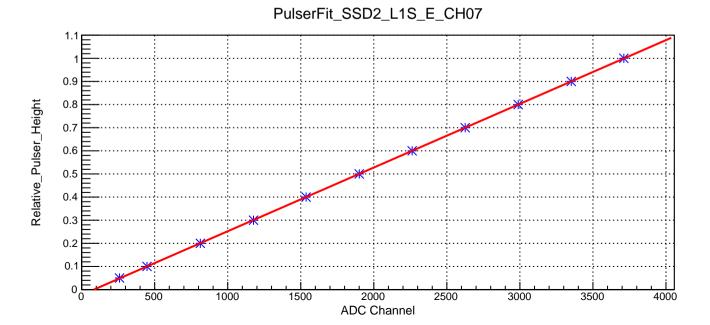


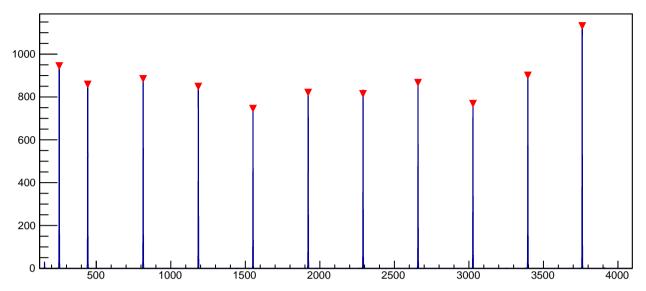


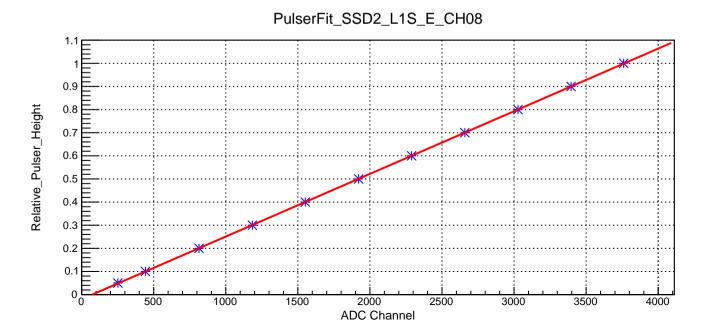


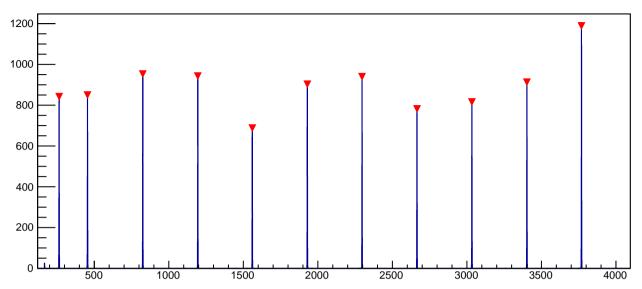




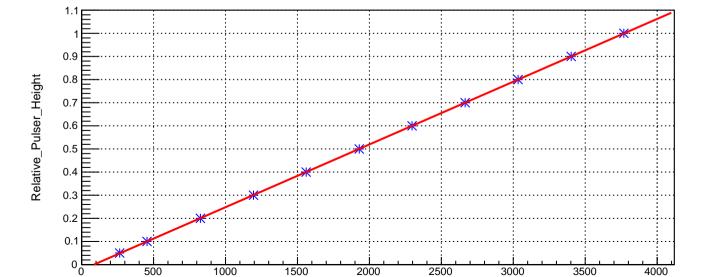








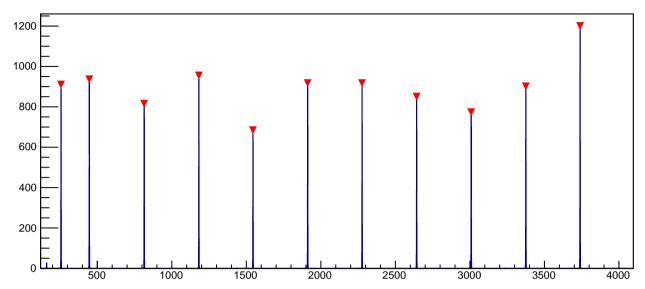
PulserFit_SSD2_L1S_E_CH09



ADC Channel

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500



PulserFit_SSD2_L1S_E_CH10

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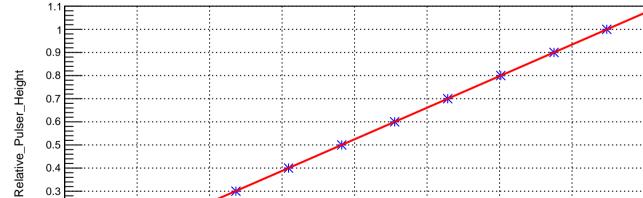
ADC Channel

2500

3000

3500

4000

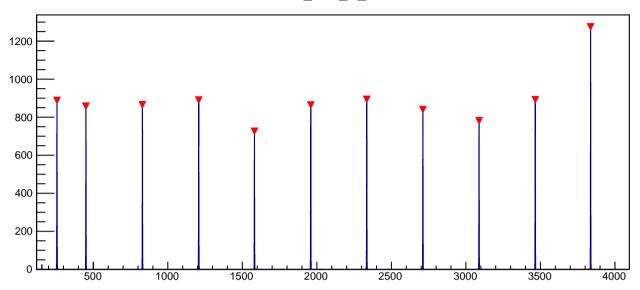


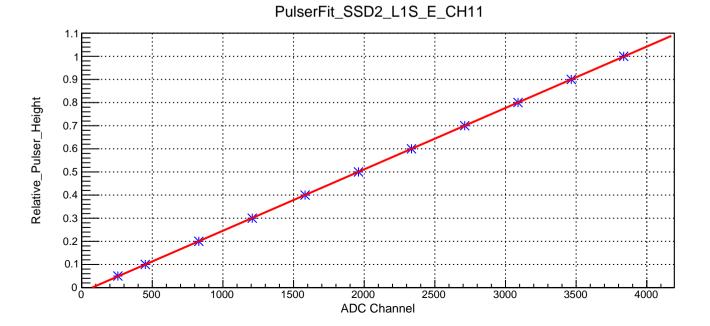
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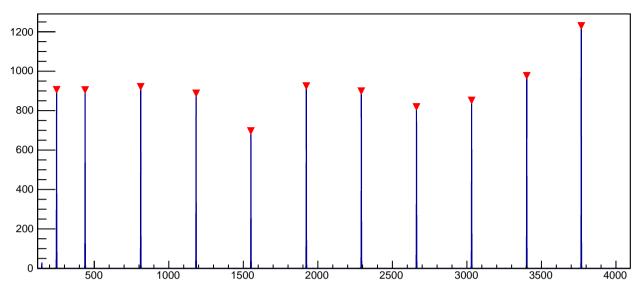
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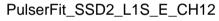
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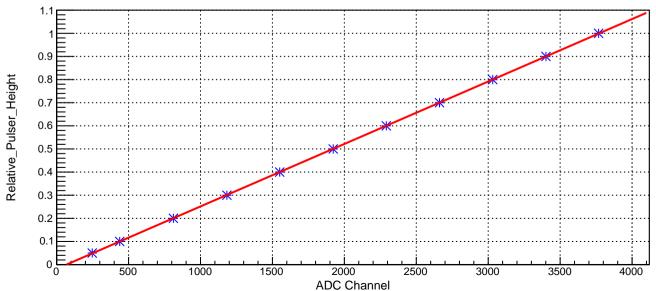
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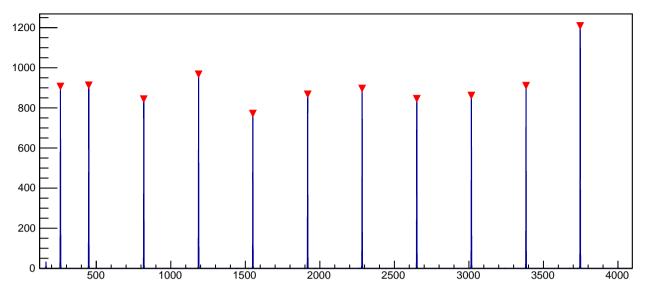


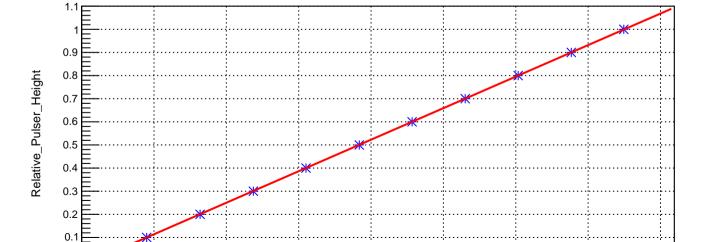






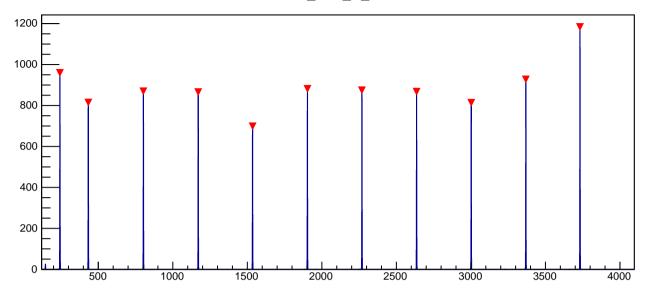


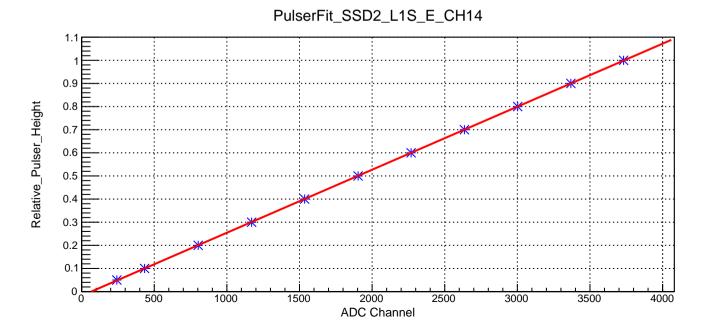


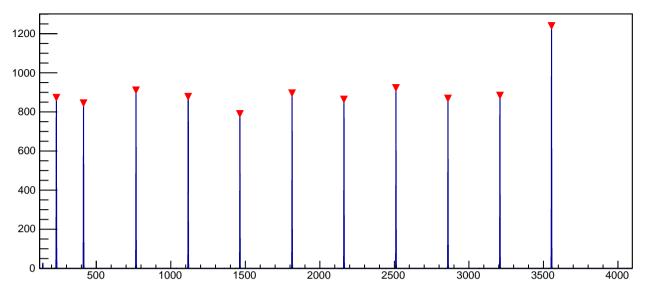


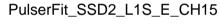
ADC Channel

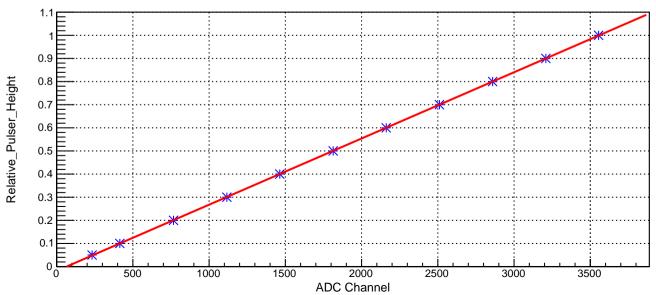
PulserFit_SSD2_L1S_E_CH13

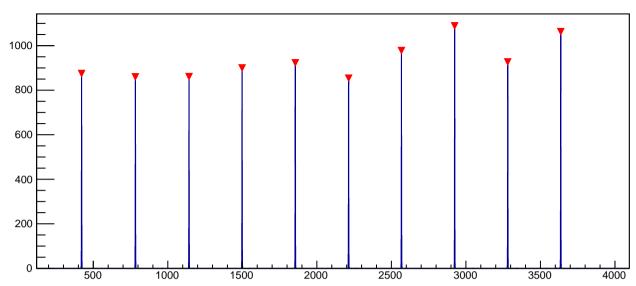


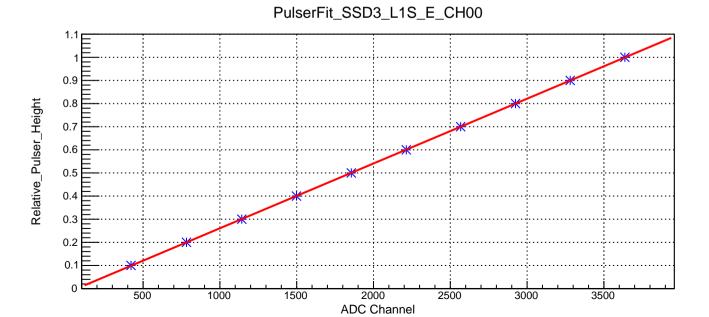


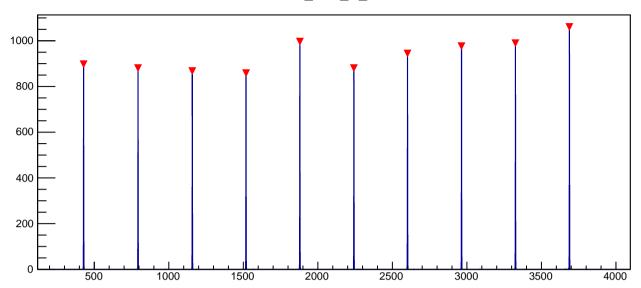




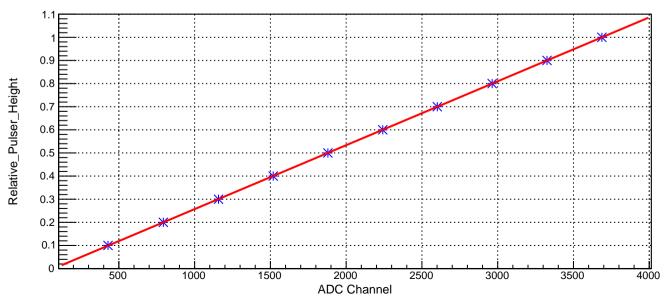


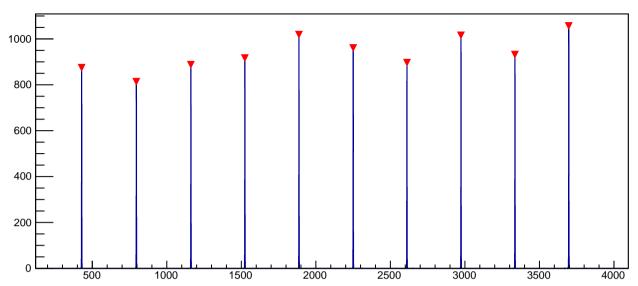


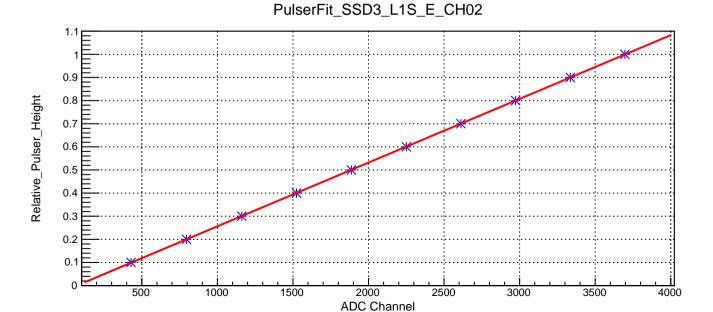


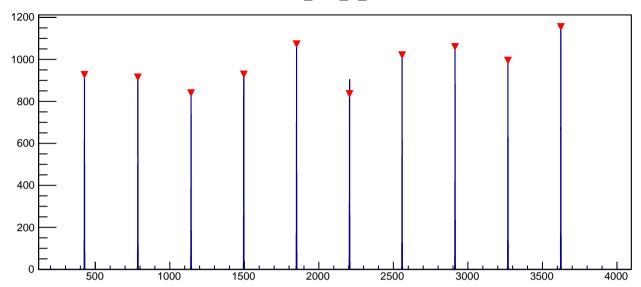


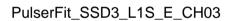


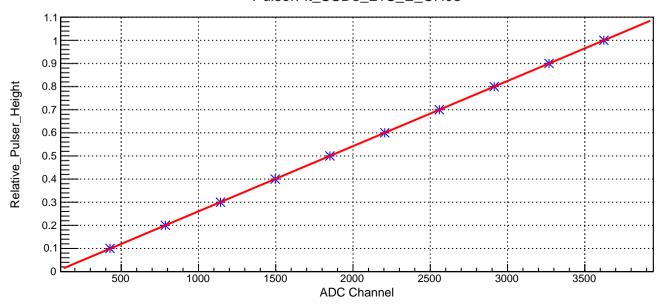


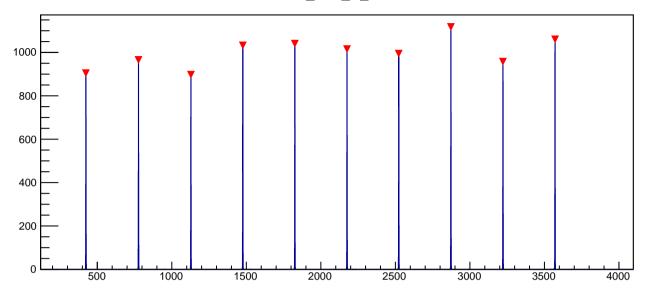


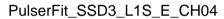


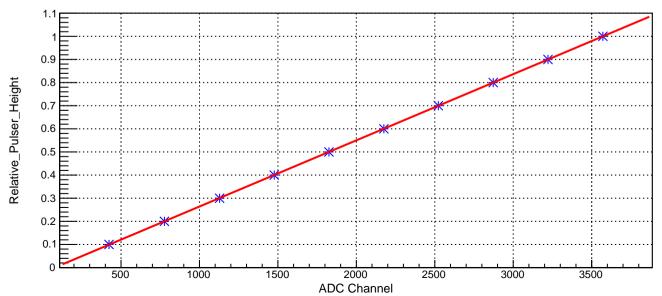


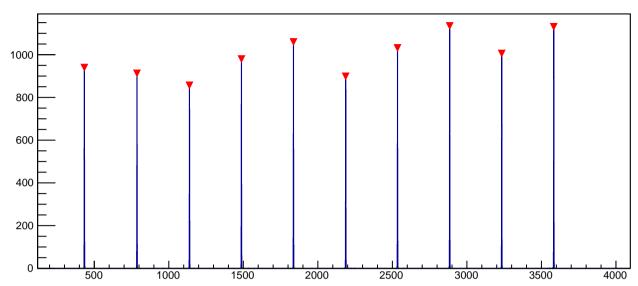


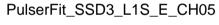


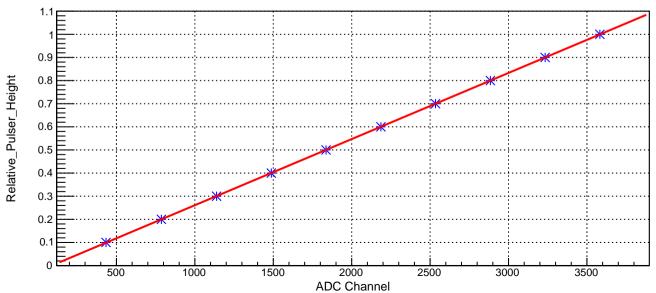


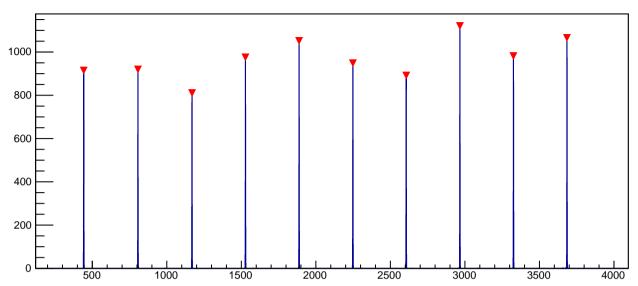


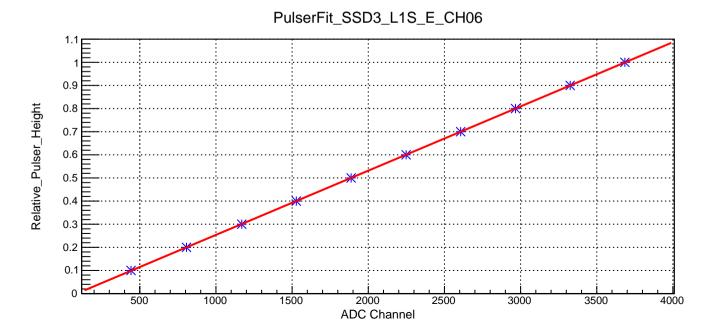


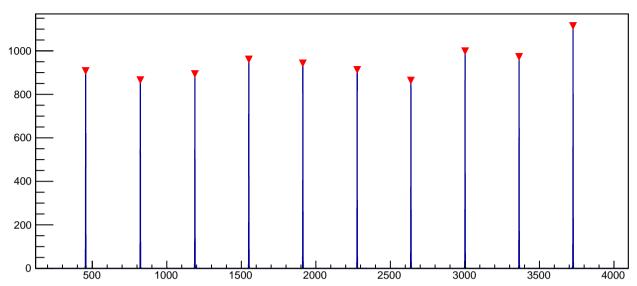


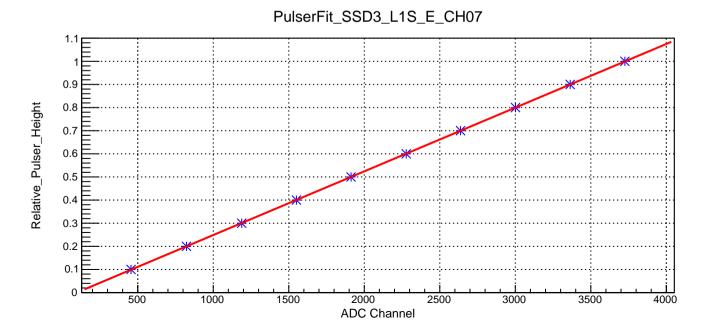


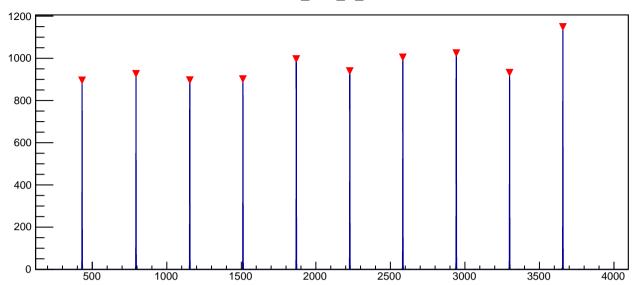


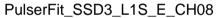


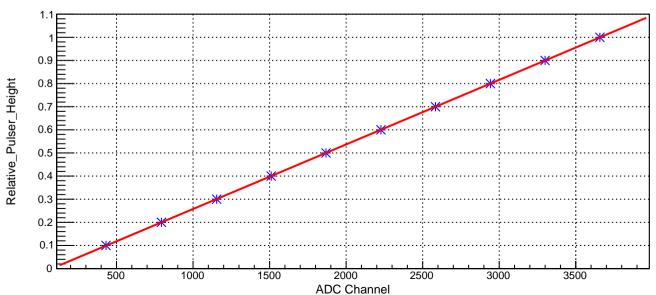


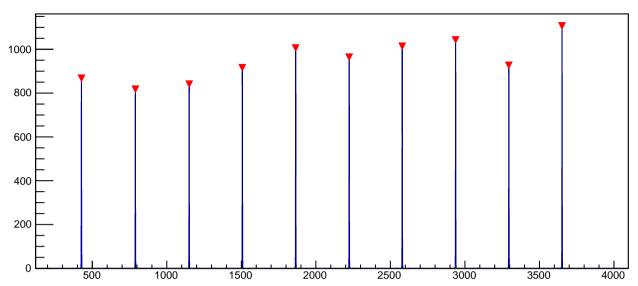


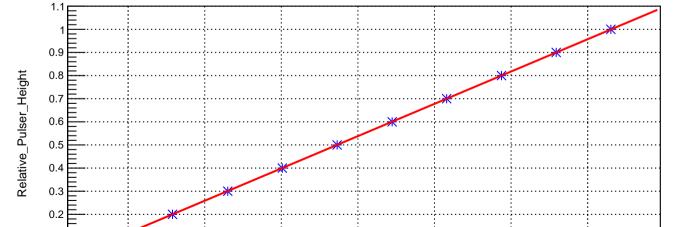








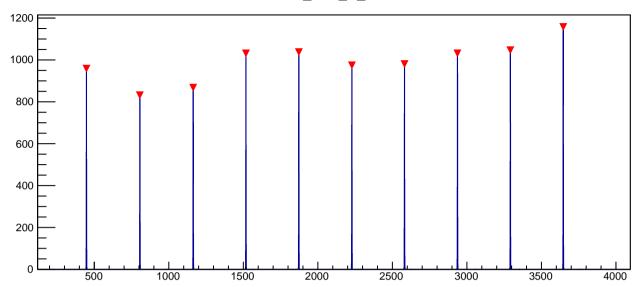


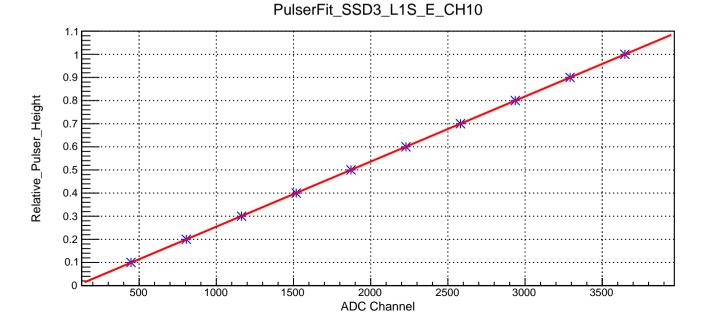


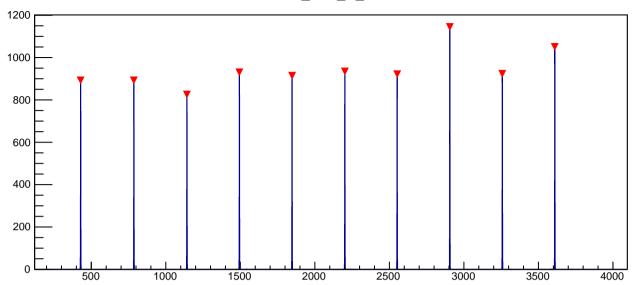
ADC Channel

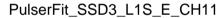
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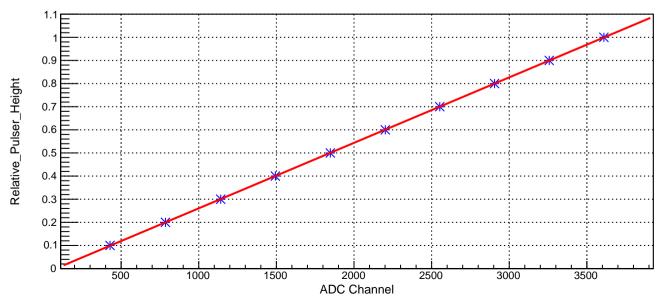
PulserFit_SSD3_L1S_E_CH09

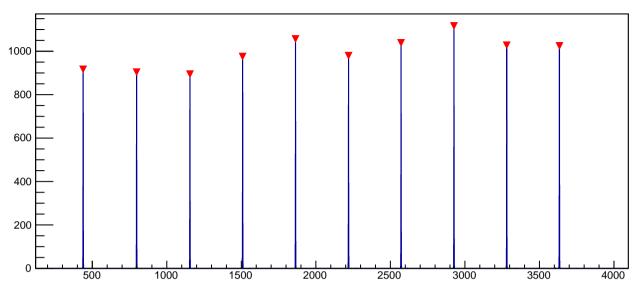


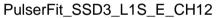


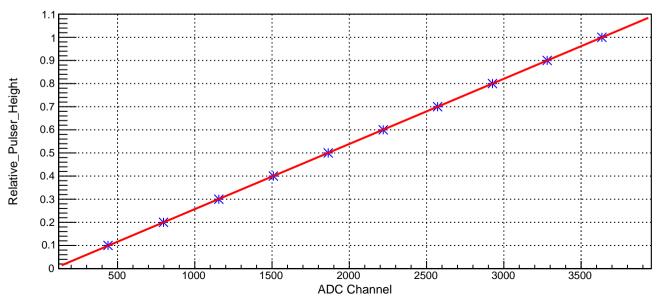


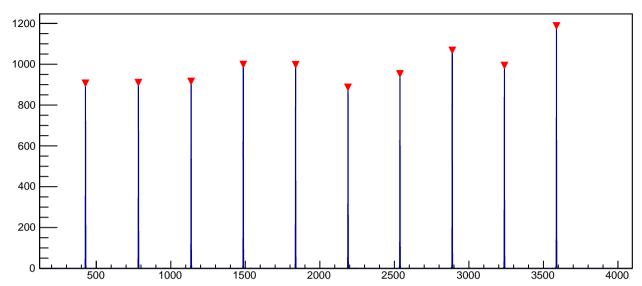




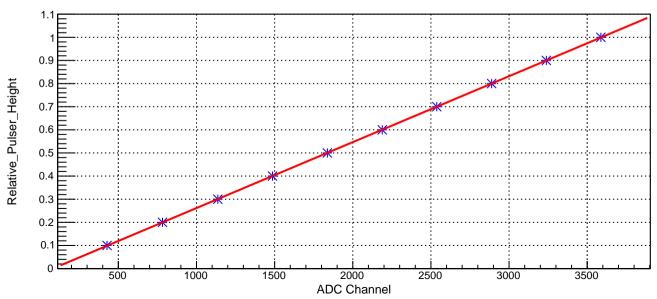


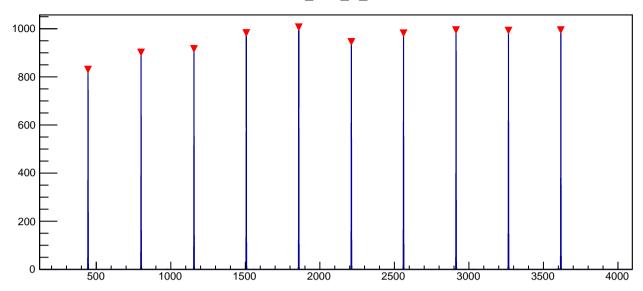


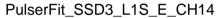


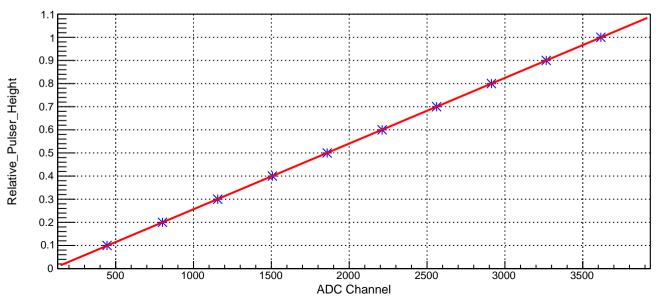


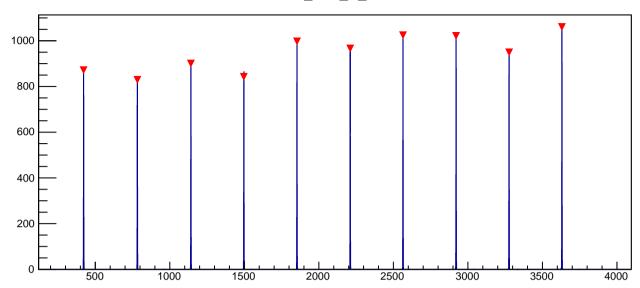


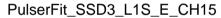


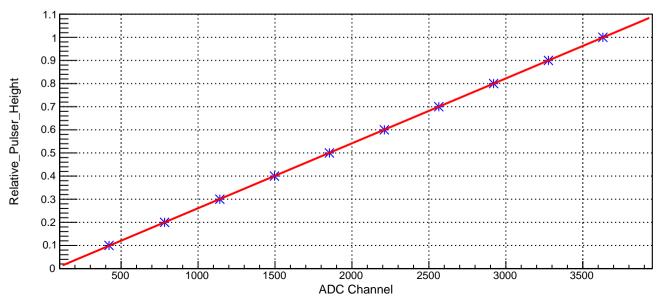


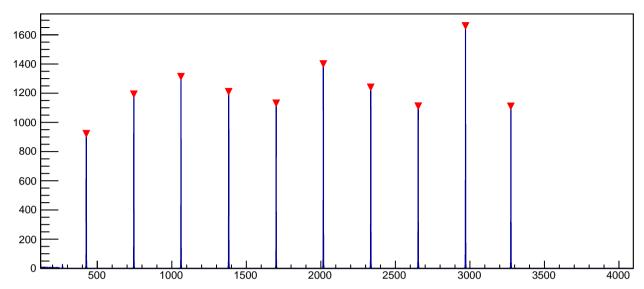


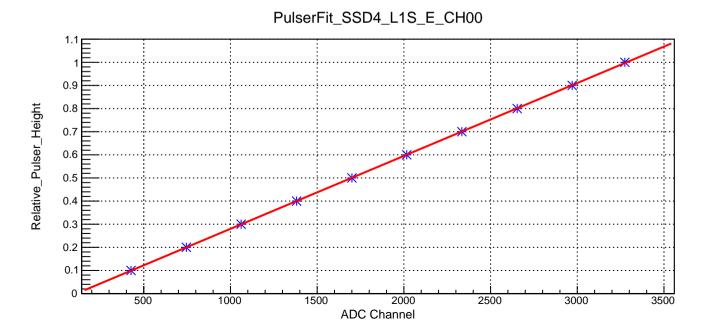


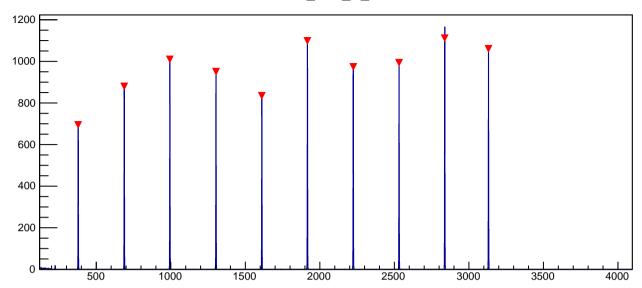


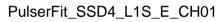


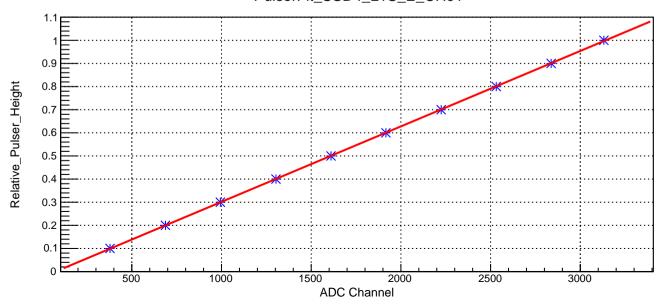


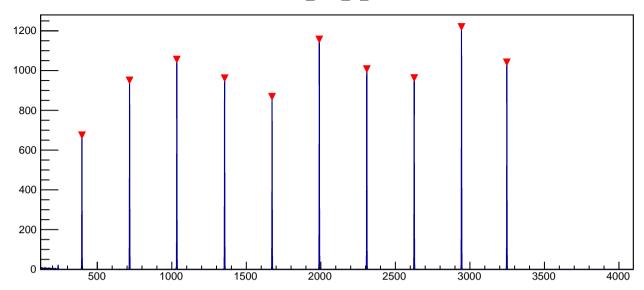


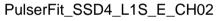


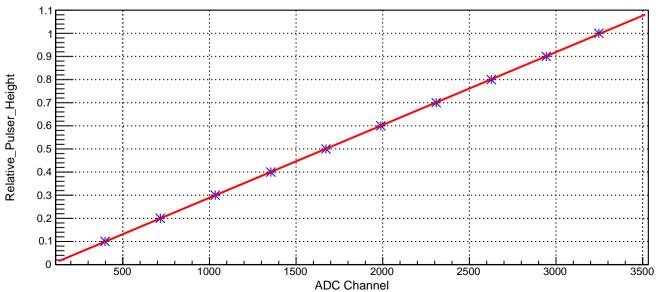


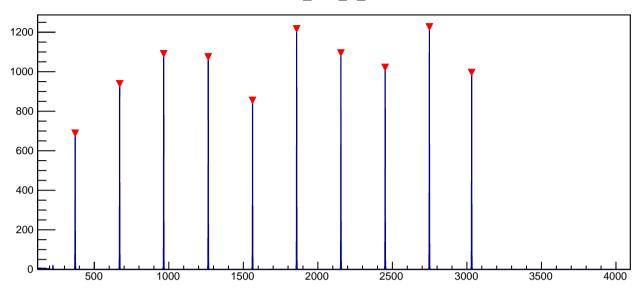


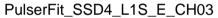


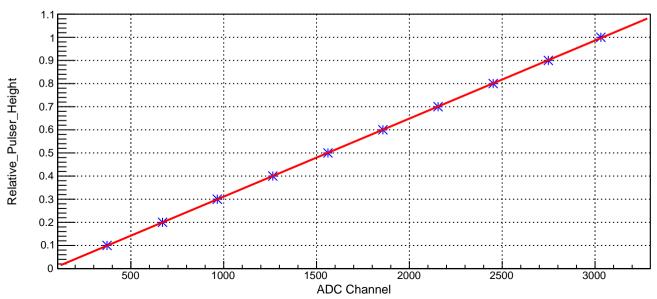


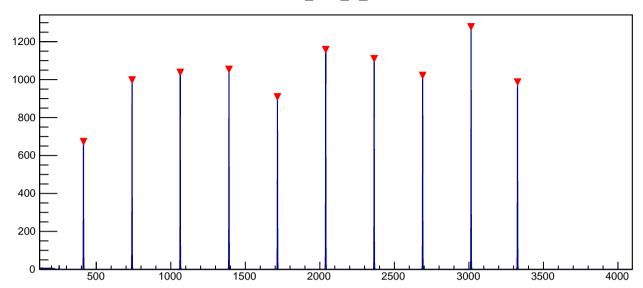


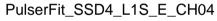


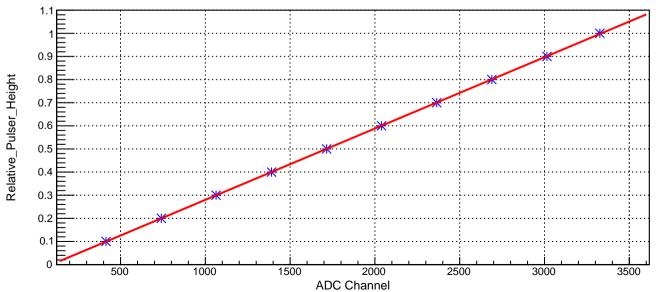


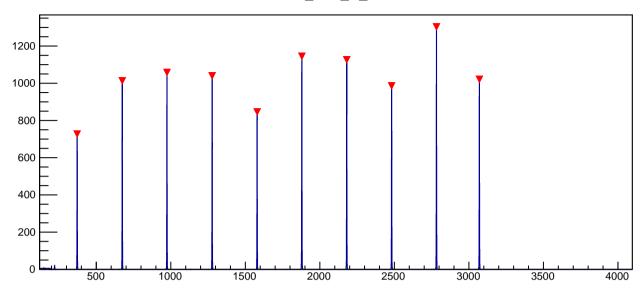


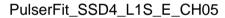


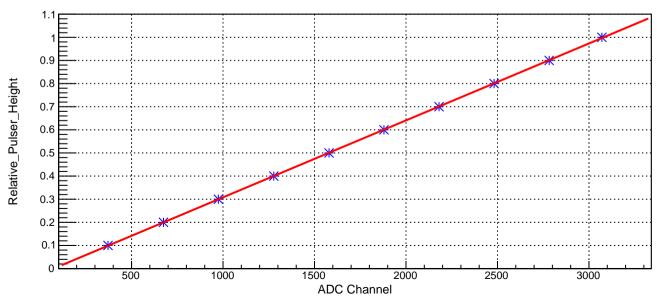


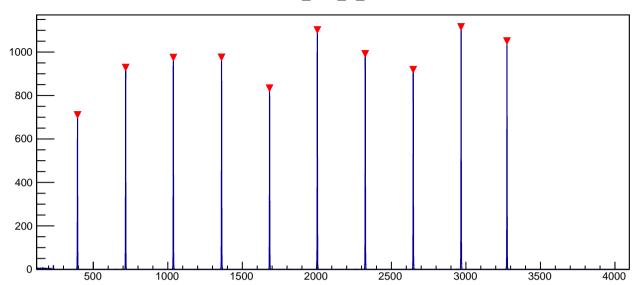


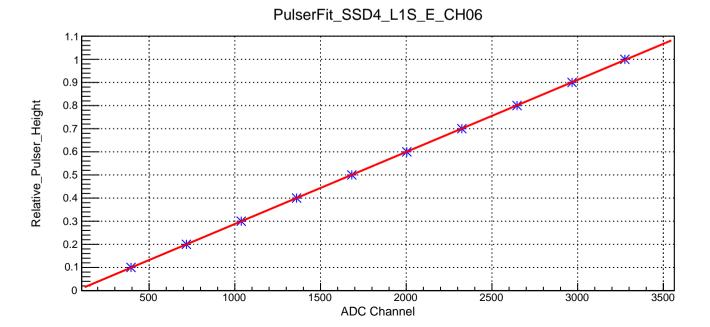


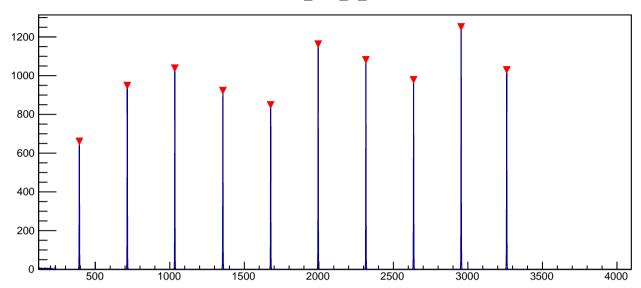


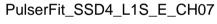


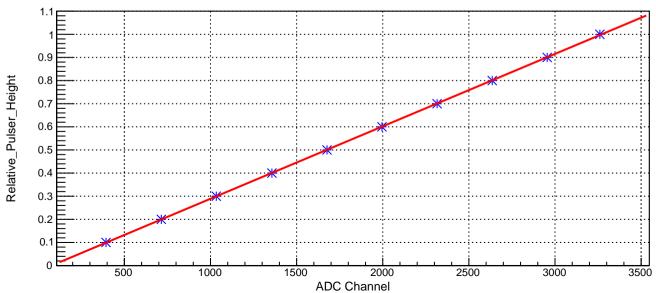


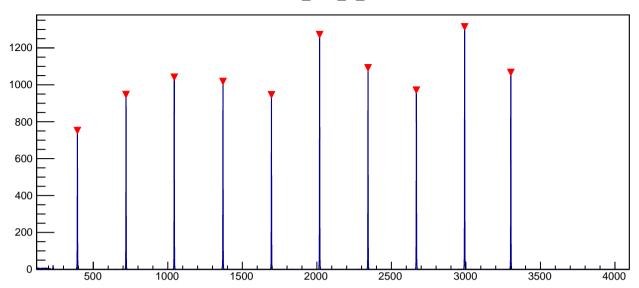


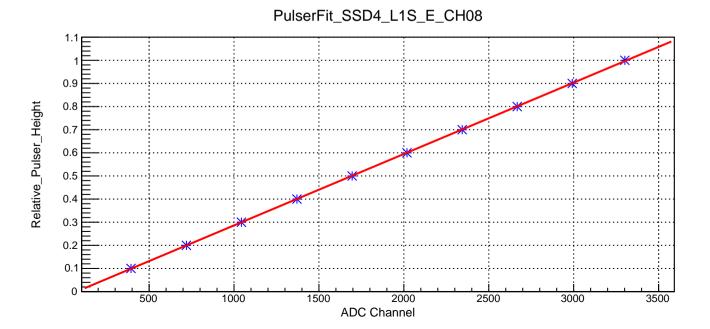


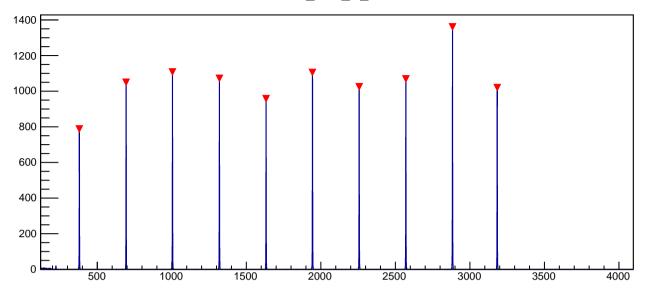


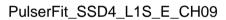


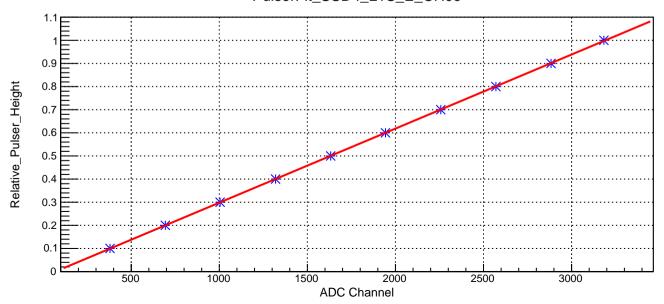


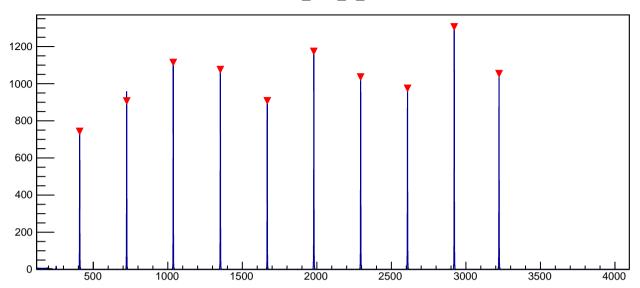


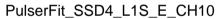


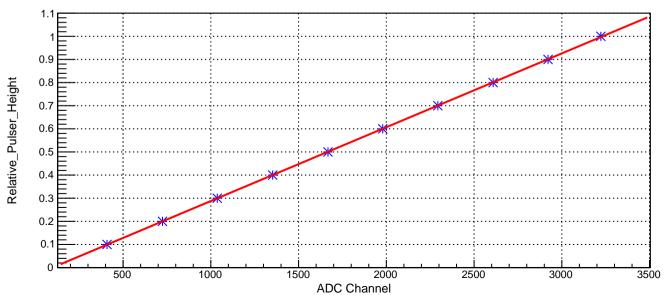


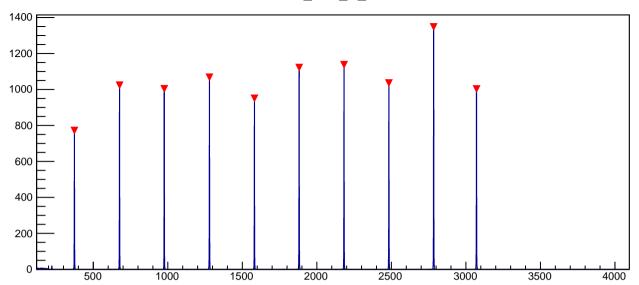




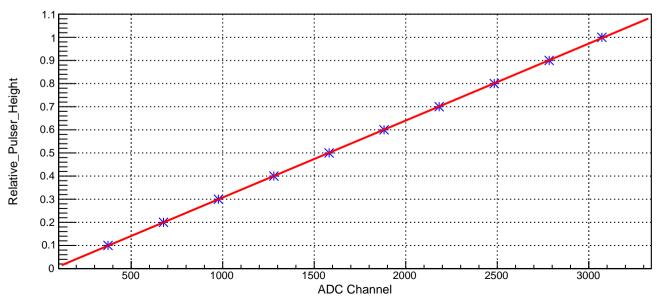


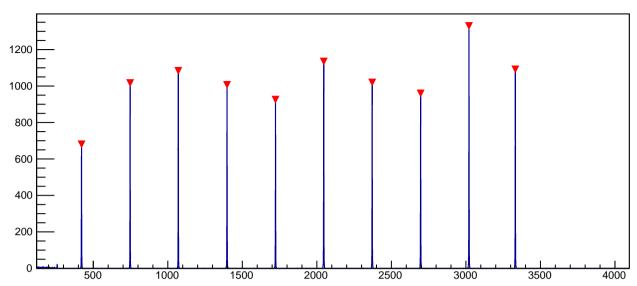


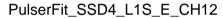


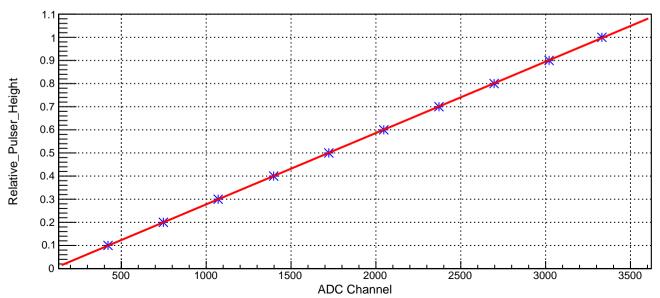


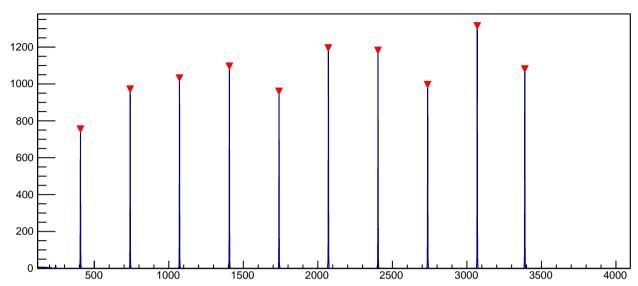




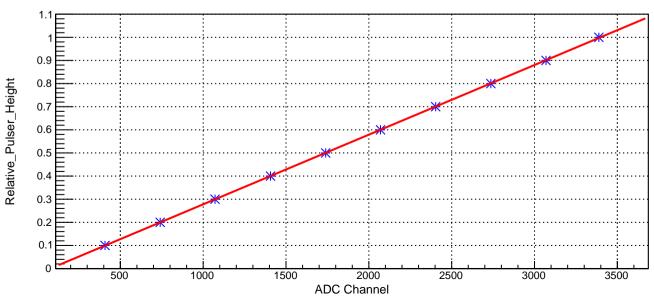


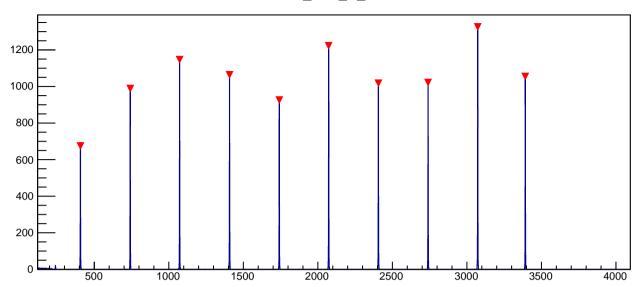


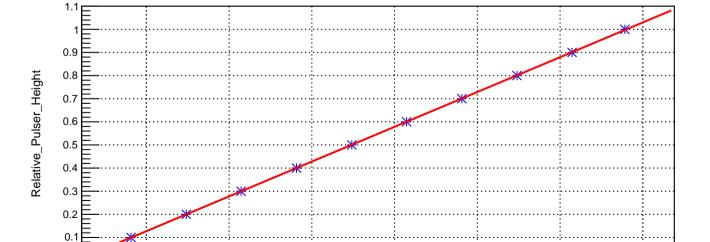












ADC Channel

PulserFit_SSD4_L1S_E_CH14

