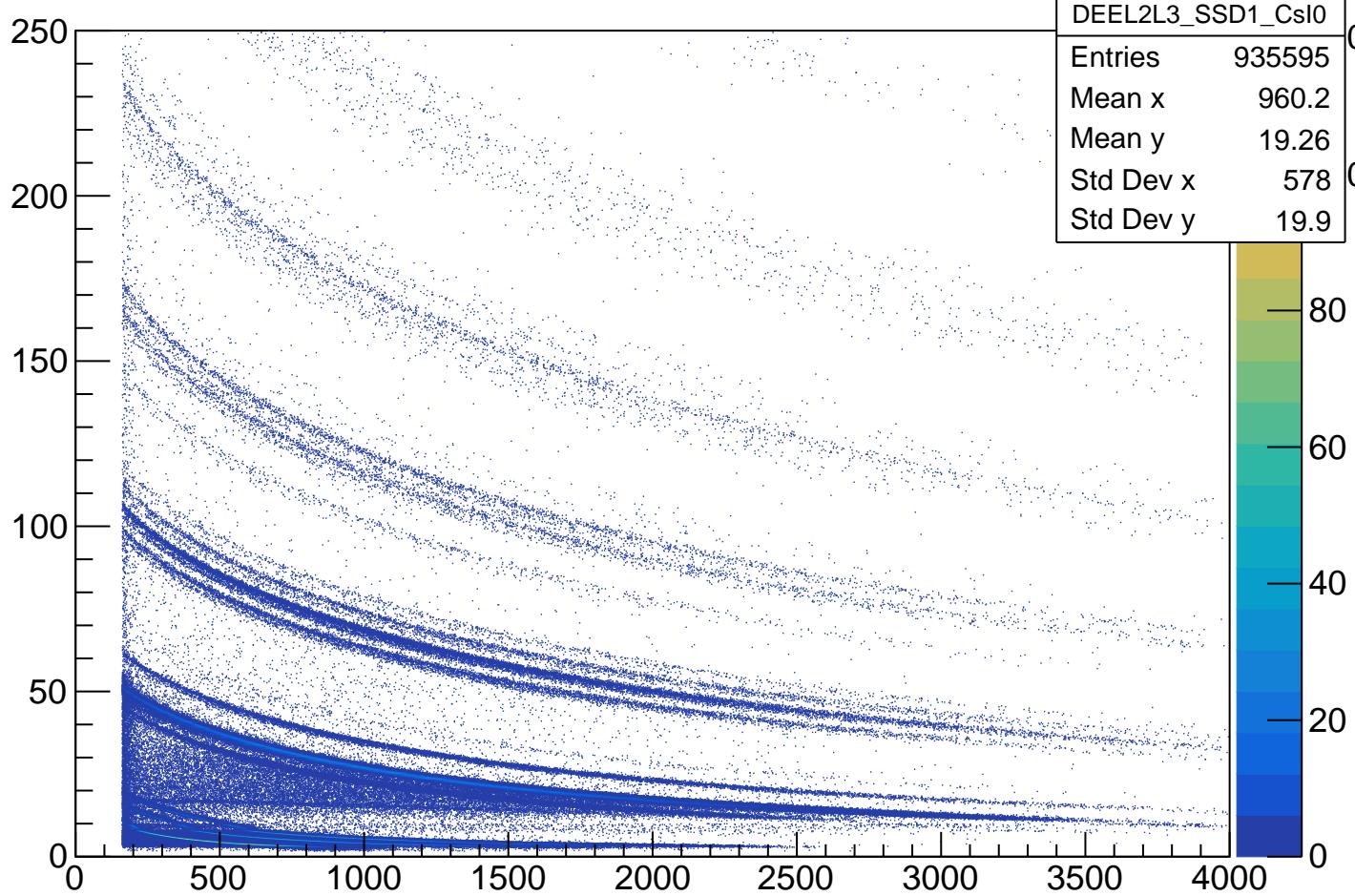
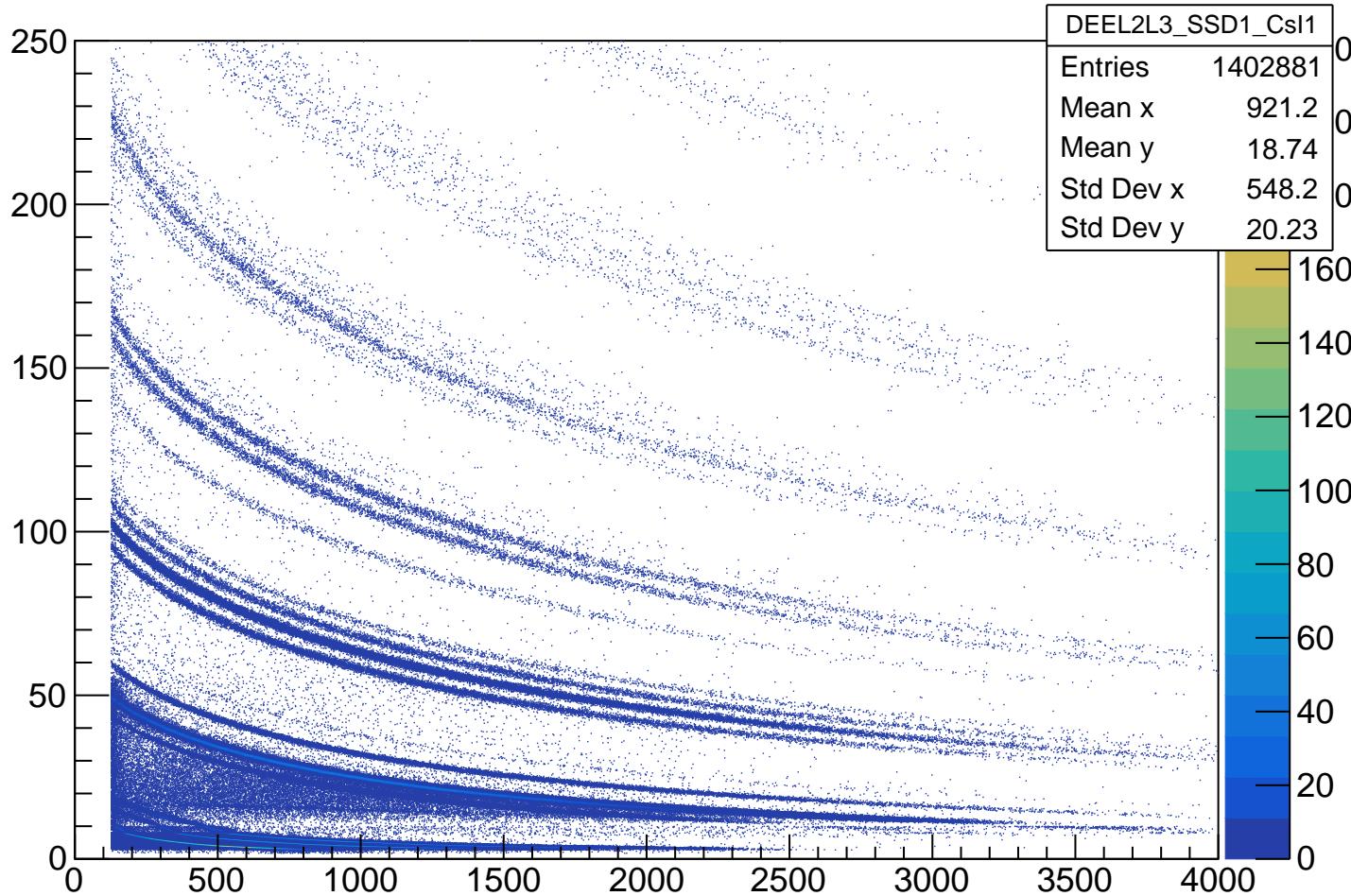


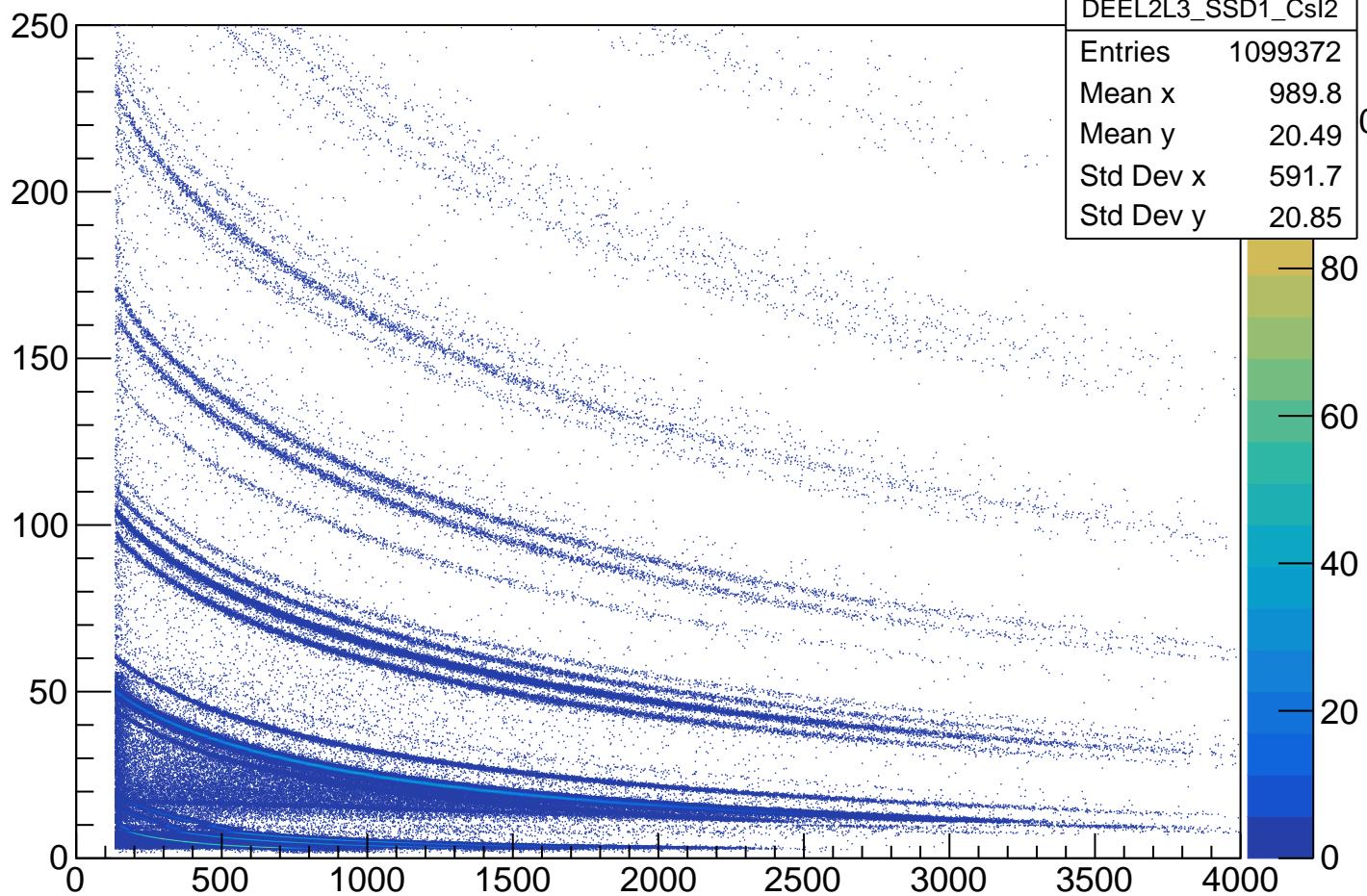
# DEEL2L3\_SSD1\_CsI0



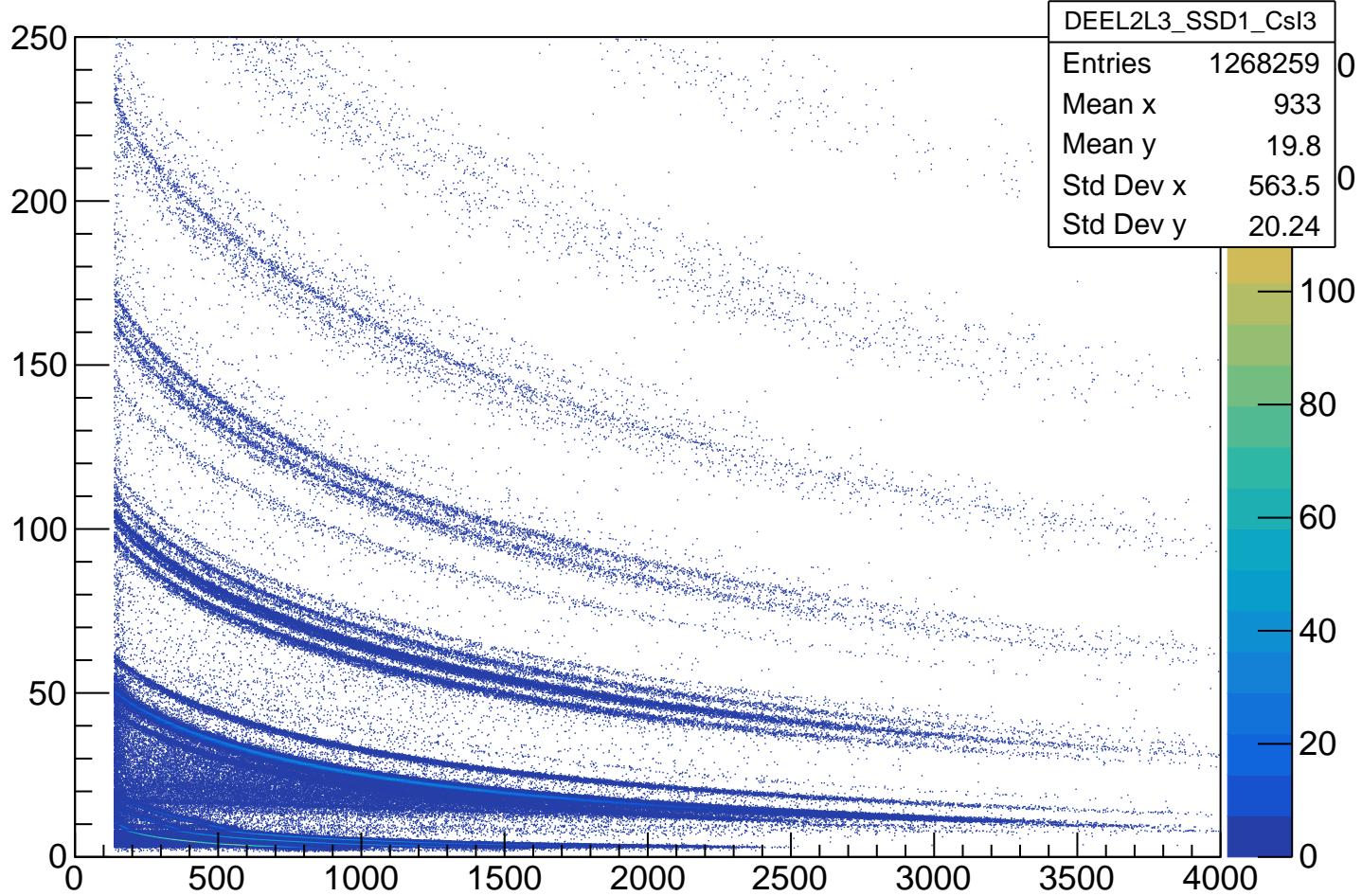
# DEEL2L3\_SSD1\_CsI1



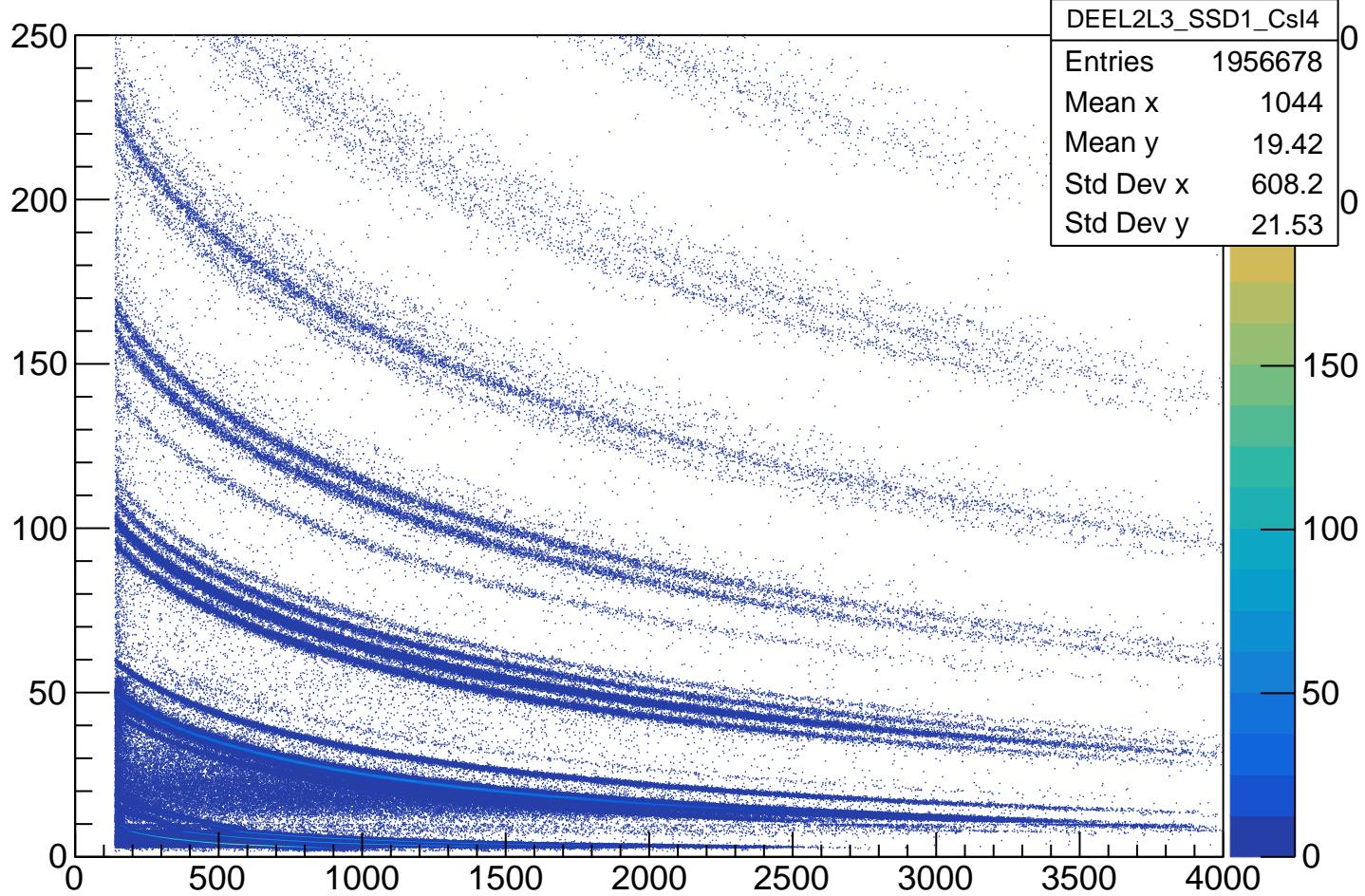
# DEEL2L3\_SSD1\_Csl2



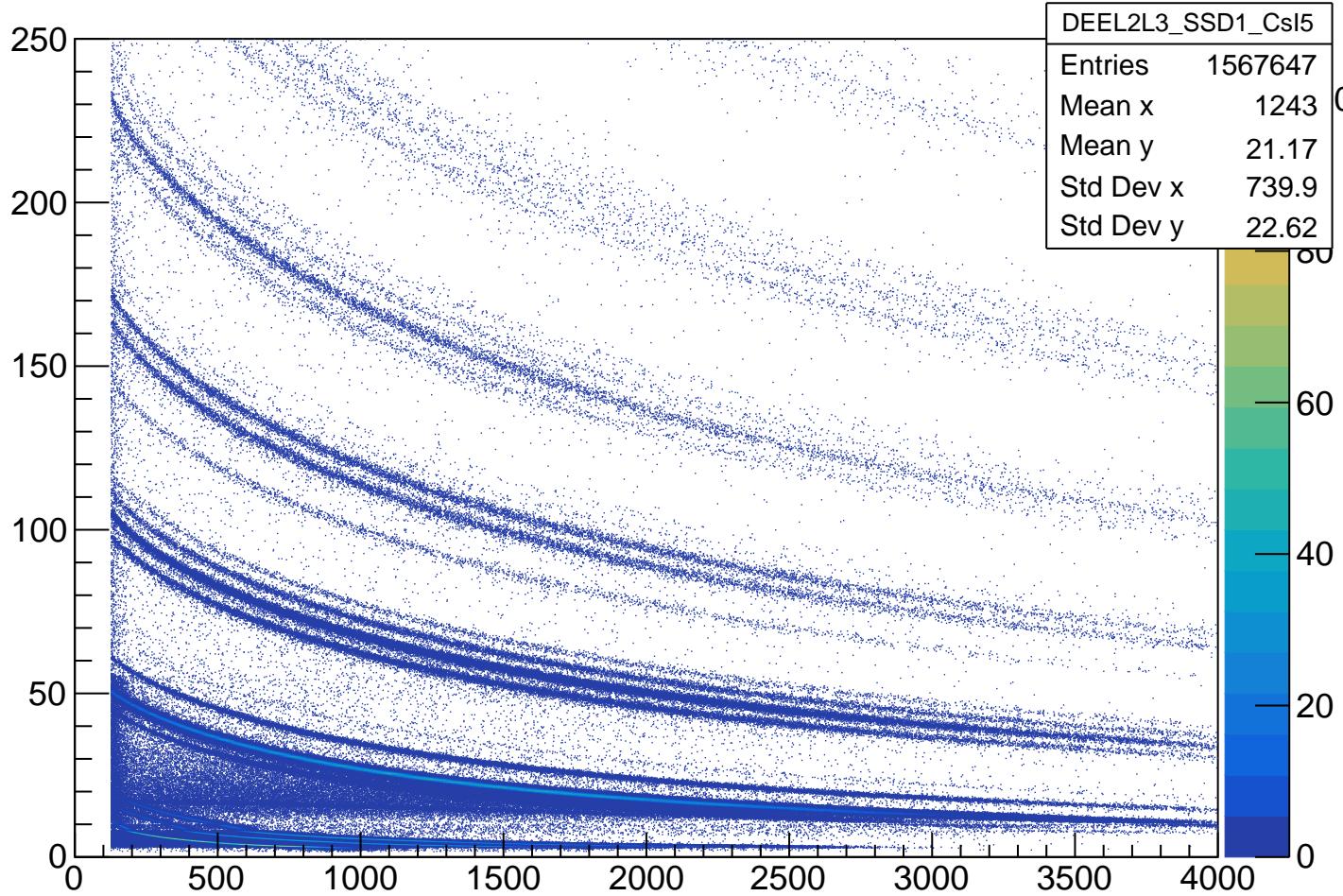
# DEEL2L3\_SSD1\_Csl3



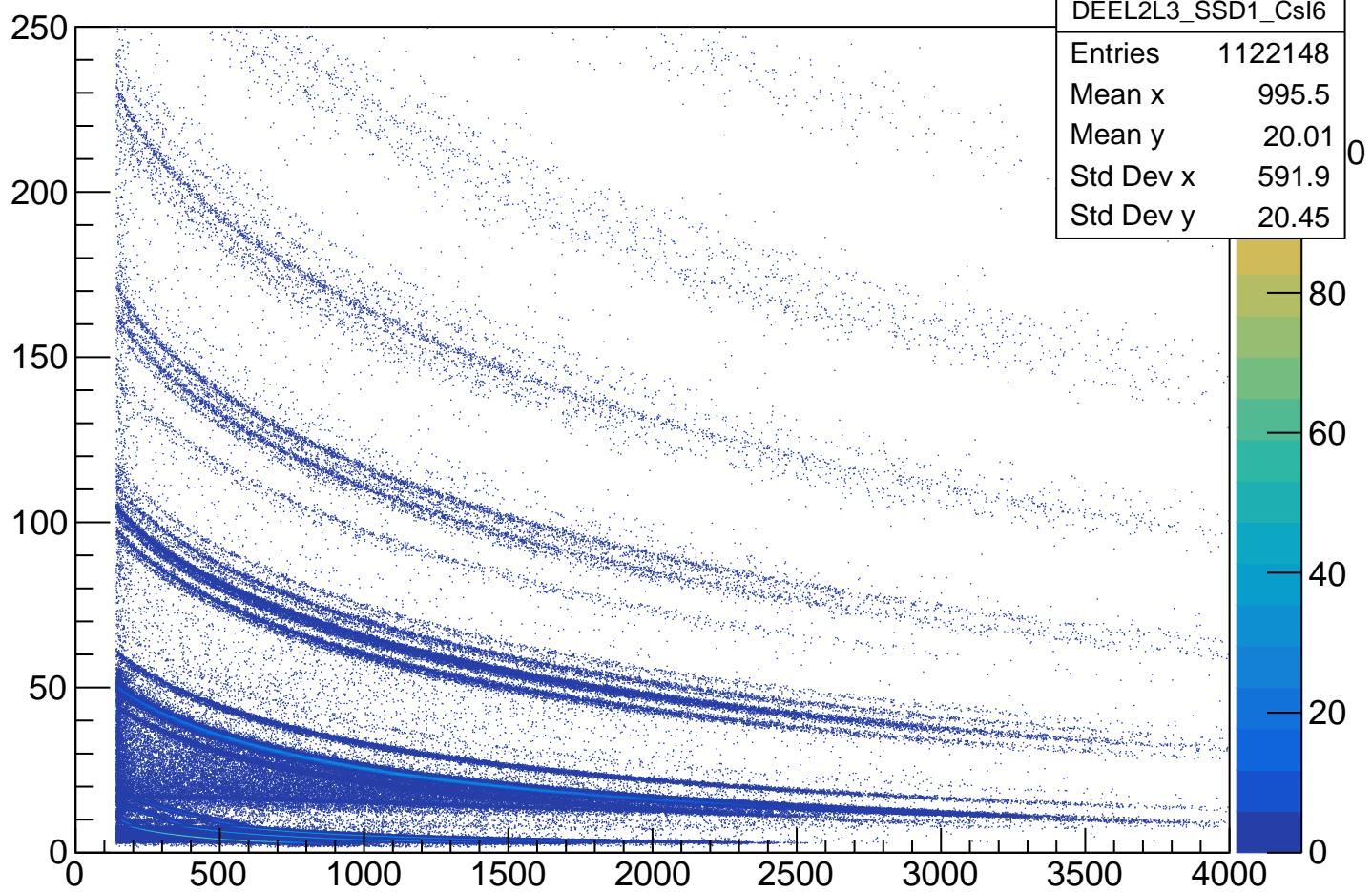
# DEEL2L3\_SSD1\_CsI4



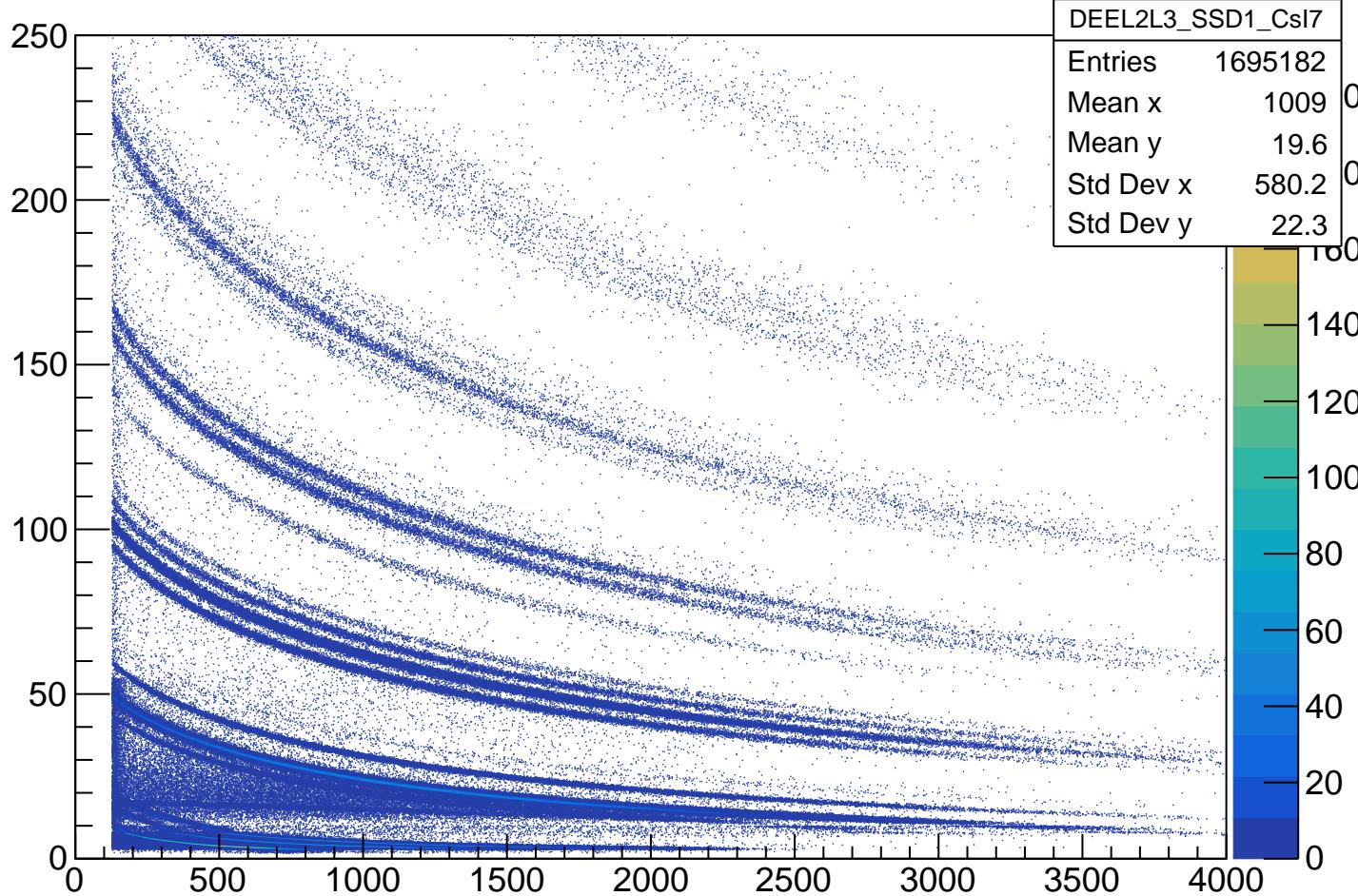
# DEEL2L3\_SSD1\_Csl5



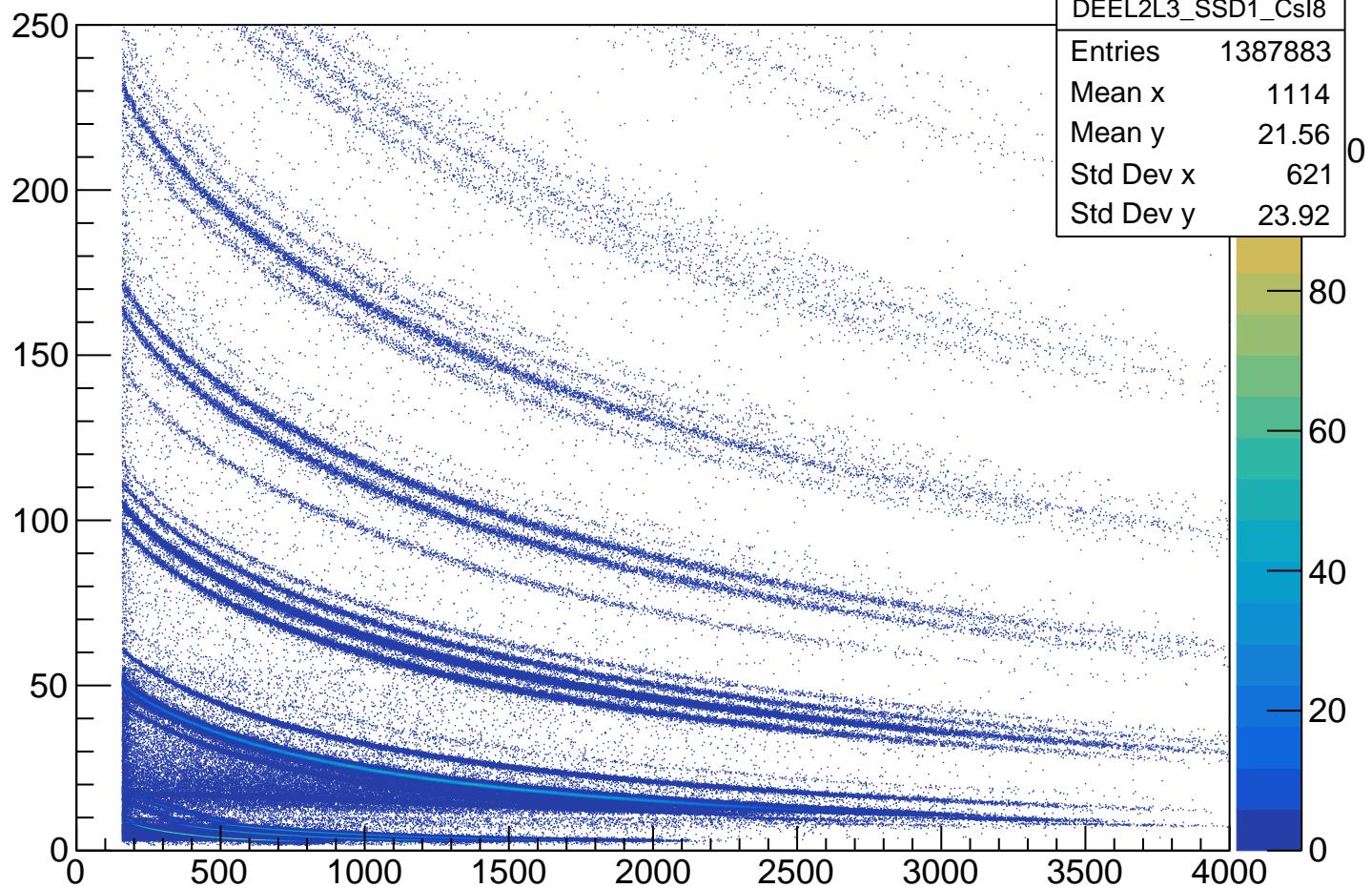
# DEEL2L3\_SSD1\_Csl6



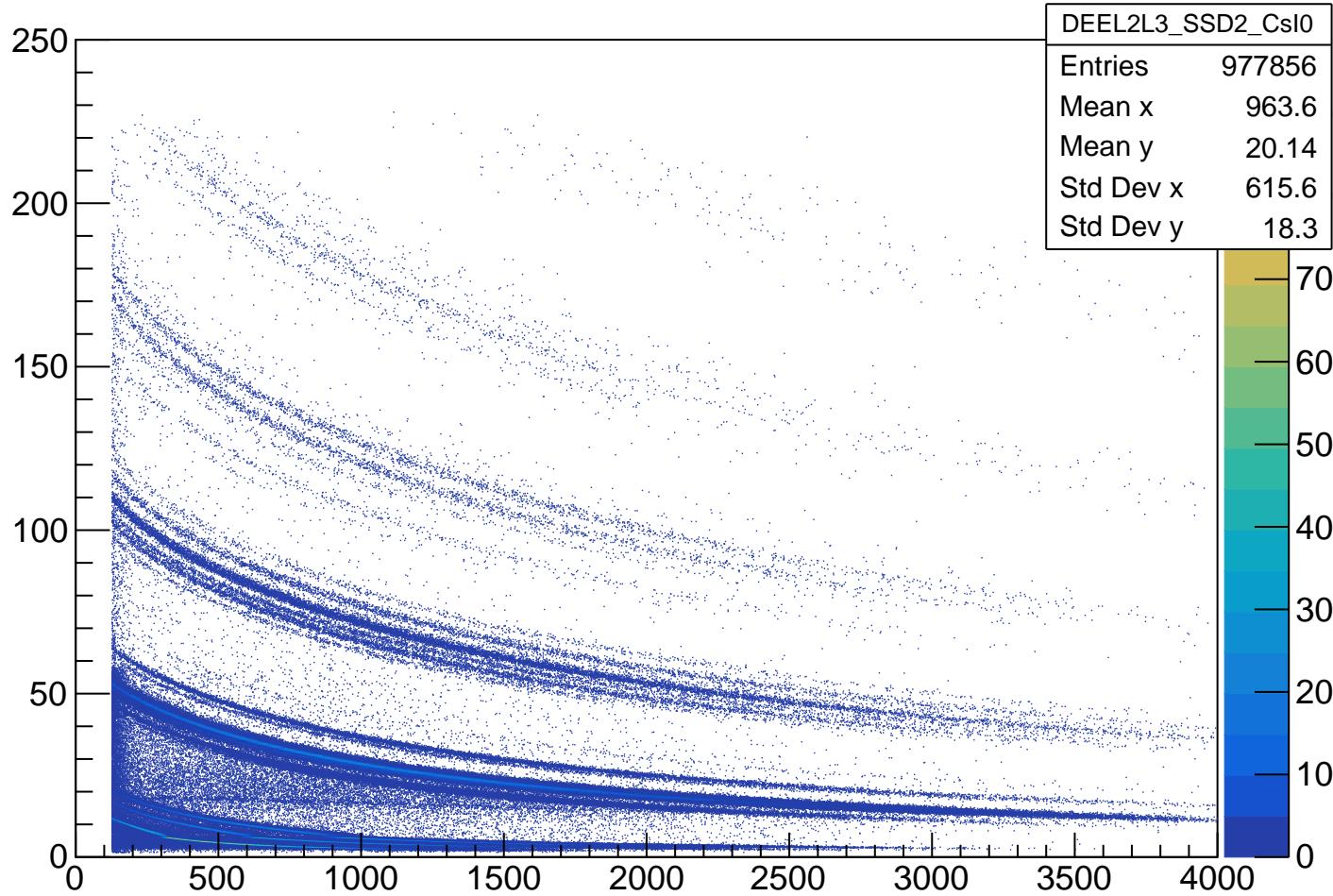
# DEEL2L3\_SSD1\_CsI7



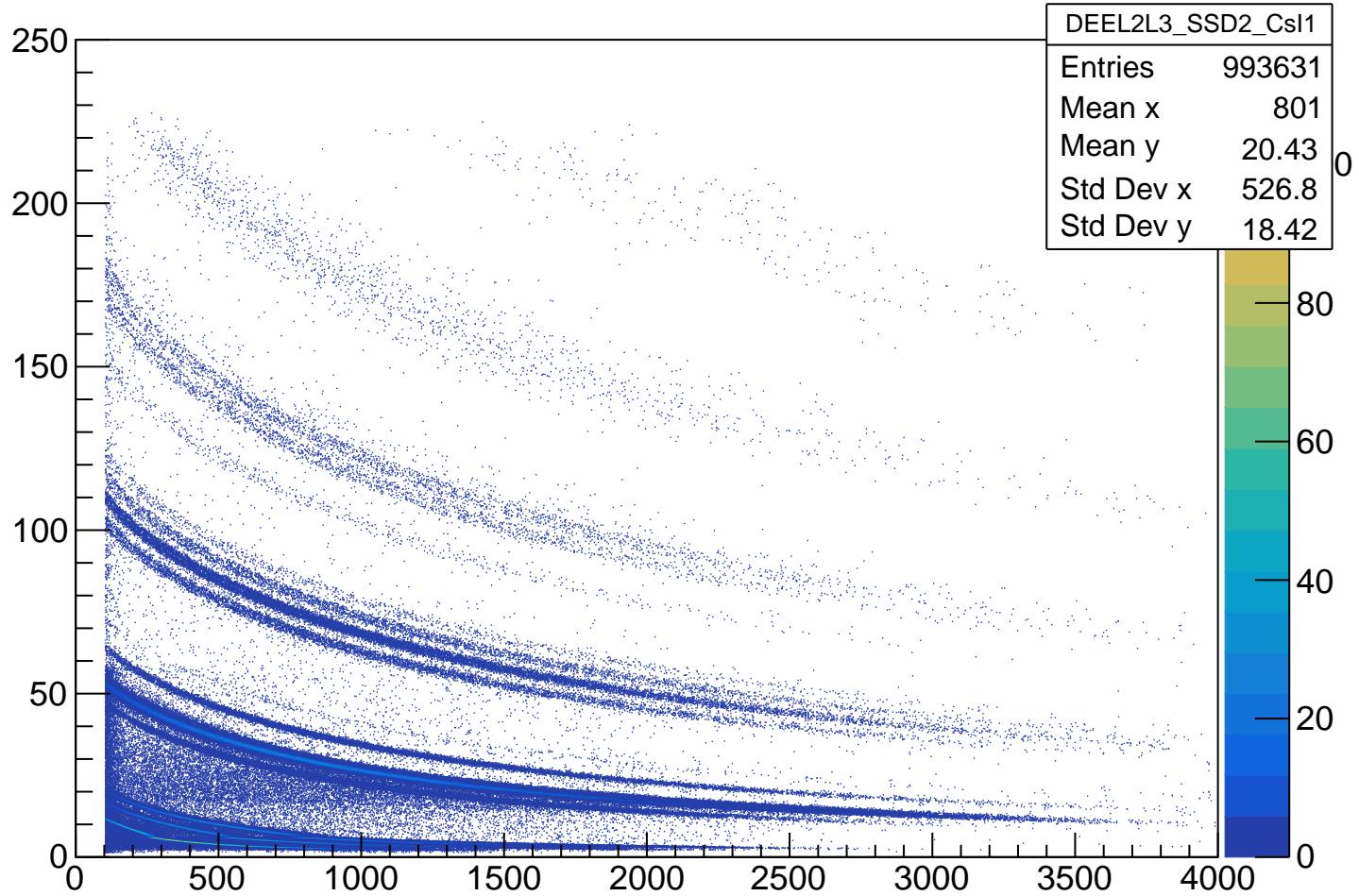
# DEEL2L3\_SSD1\_Csl8



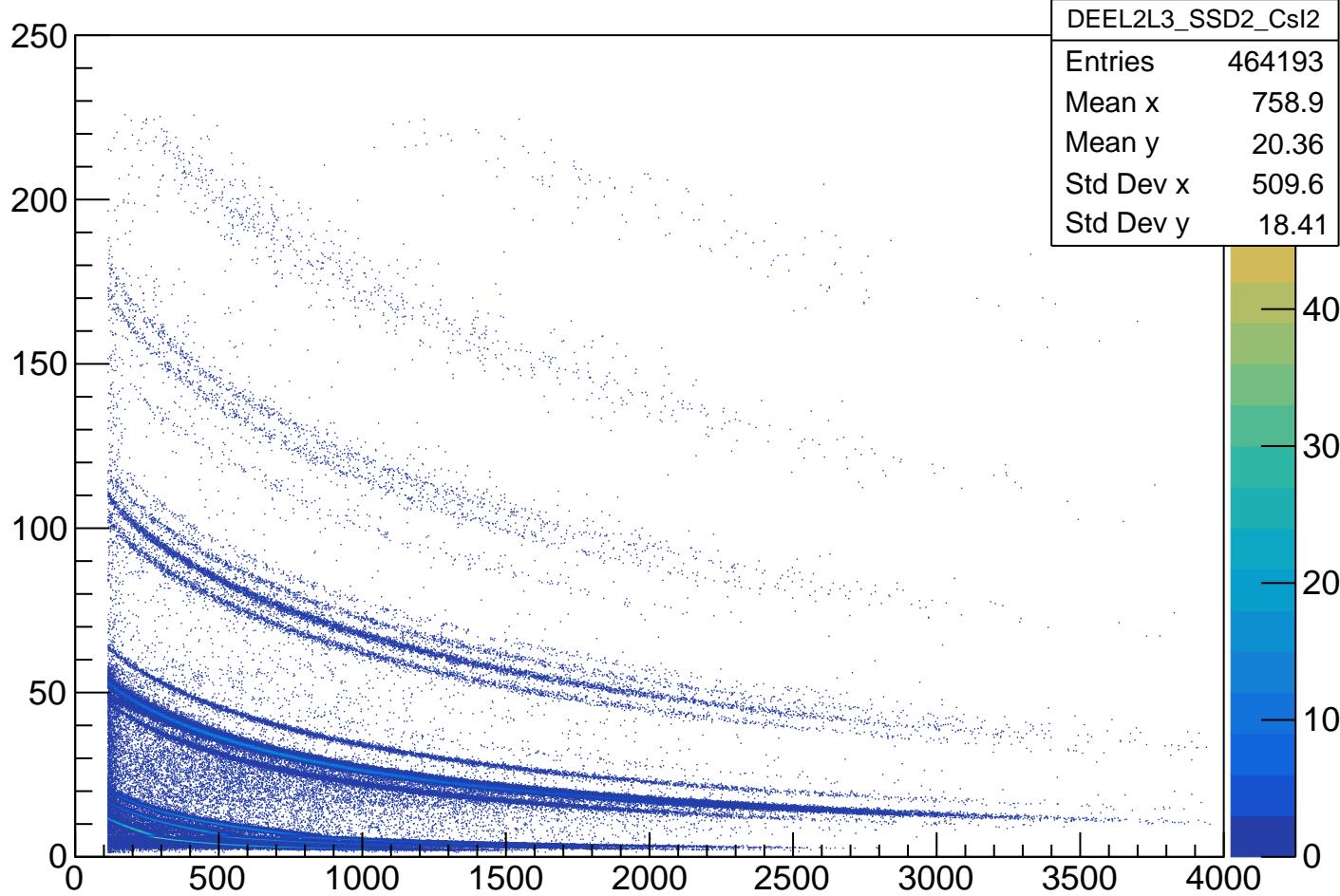
# DEEL2L3\_SSD2\_CsI0



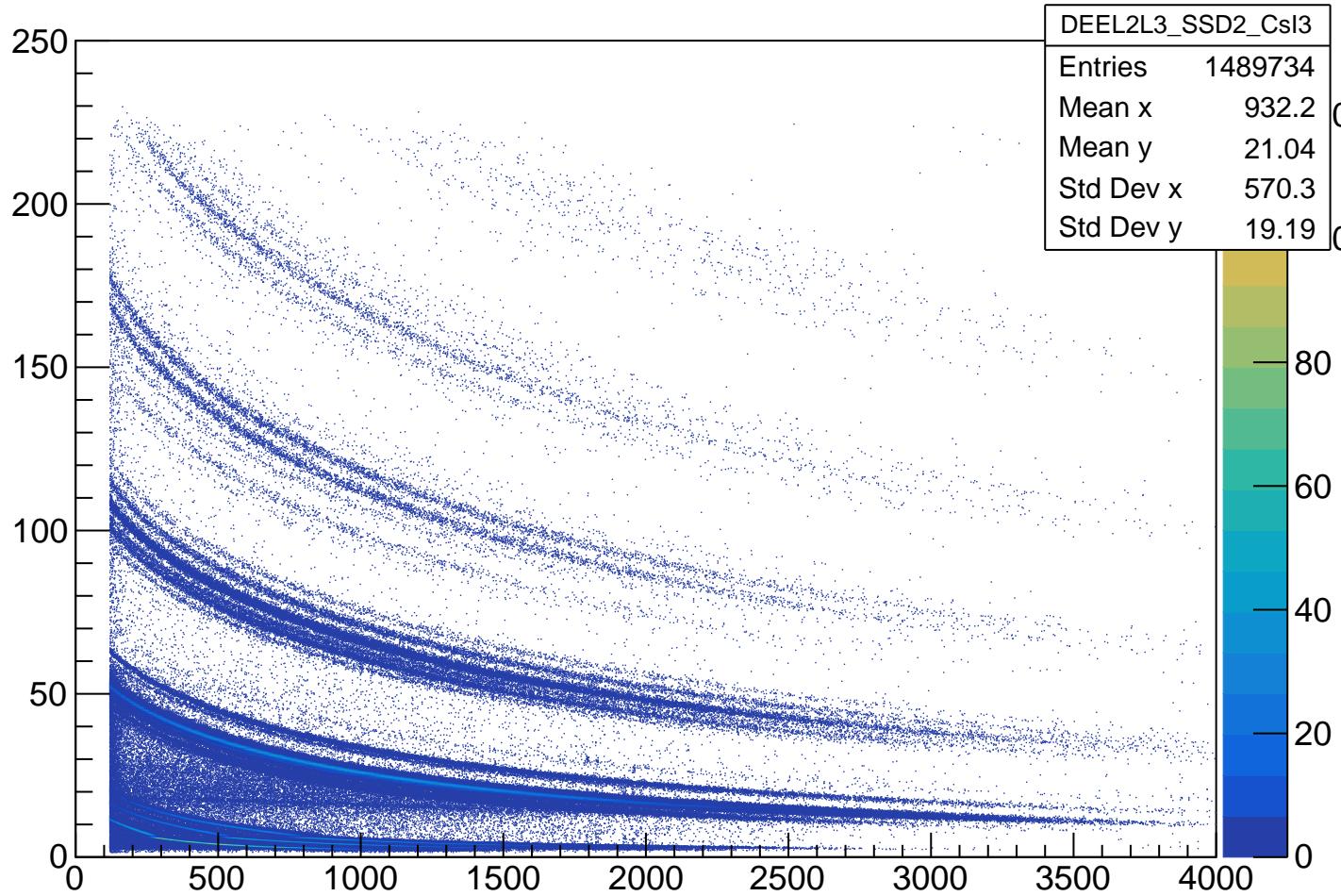
# DEEL2L3\_SSD2\_CsI1



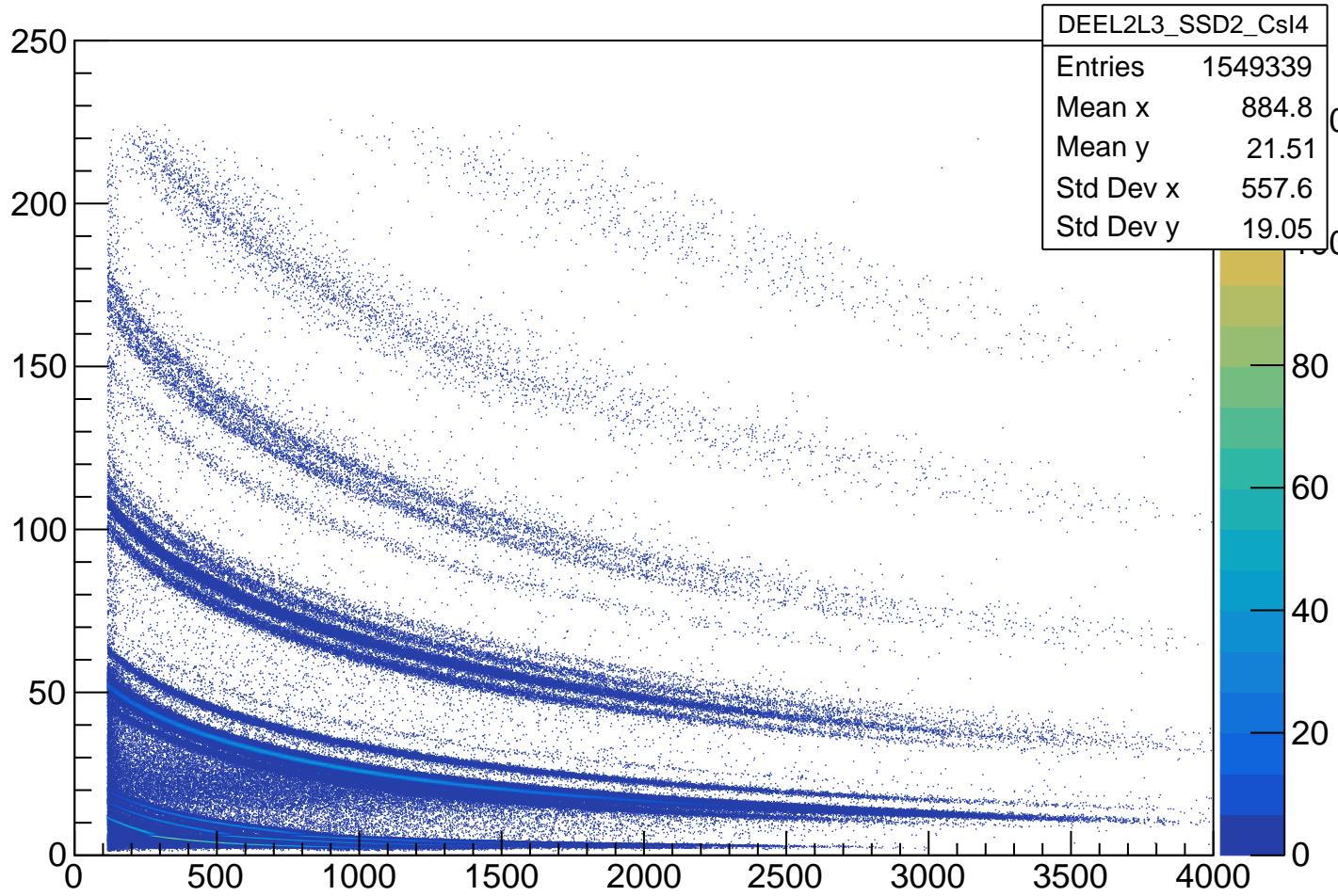
# DEEL2L3\_SSD2\_Csl2



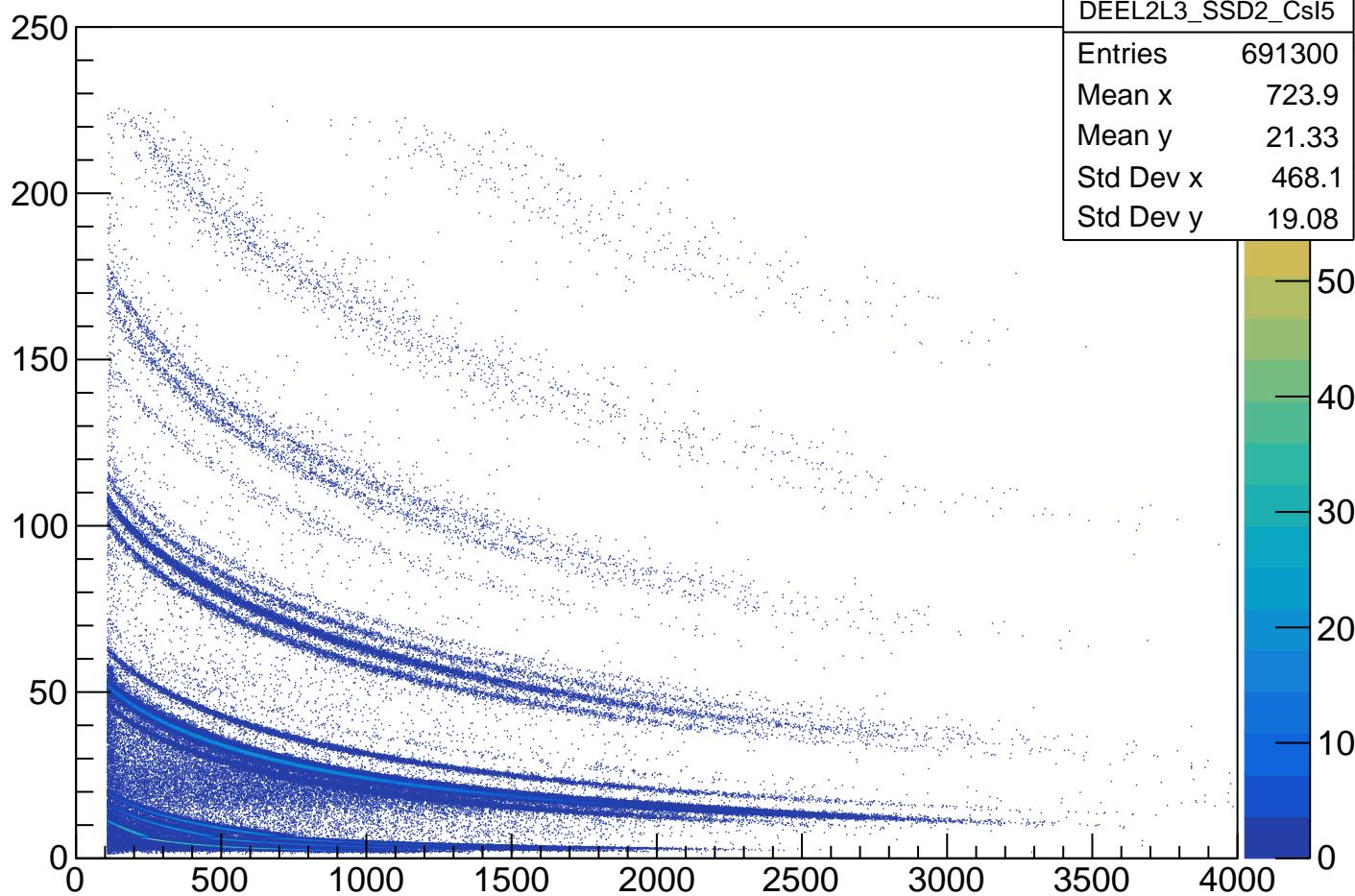
# DEEL2L3\_SSD2\_CsI3



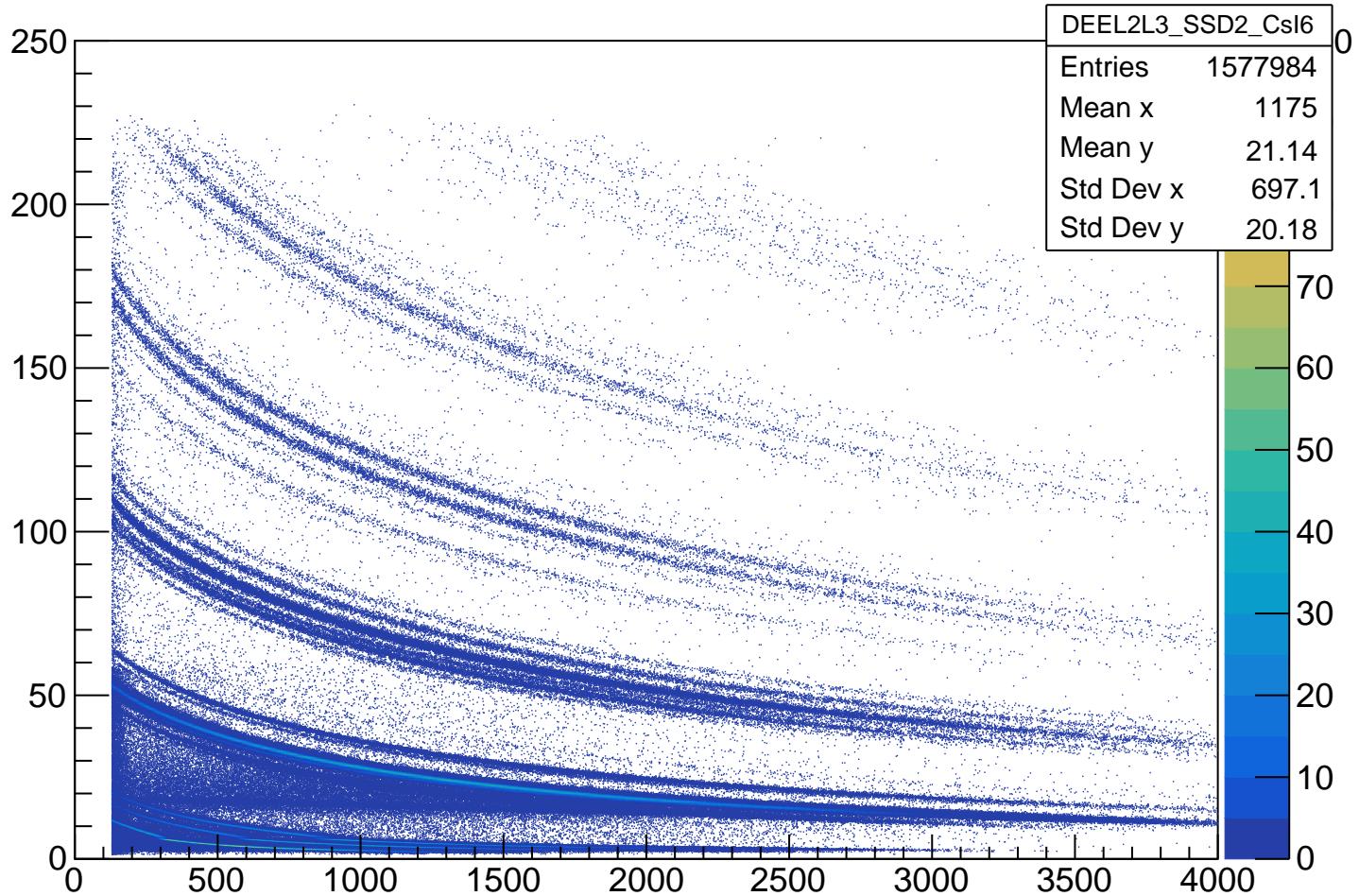
# DEEL2L3\_SSD2\_CsI4



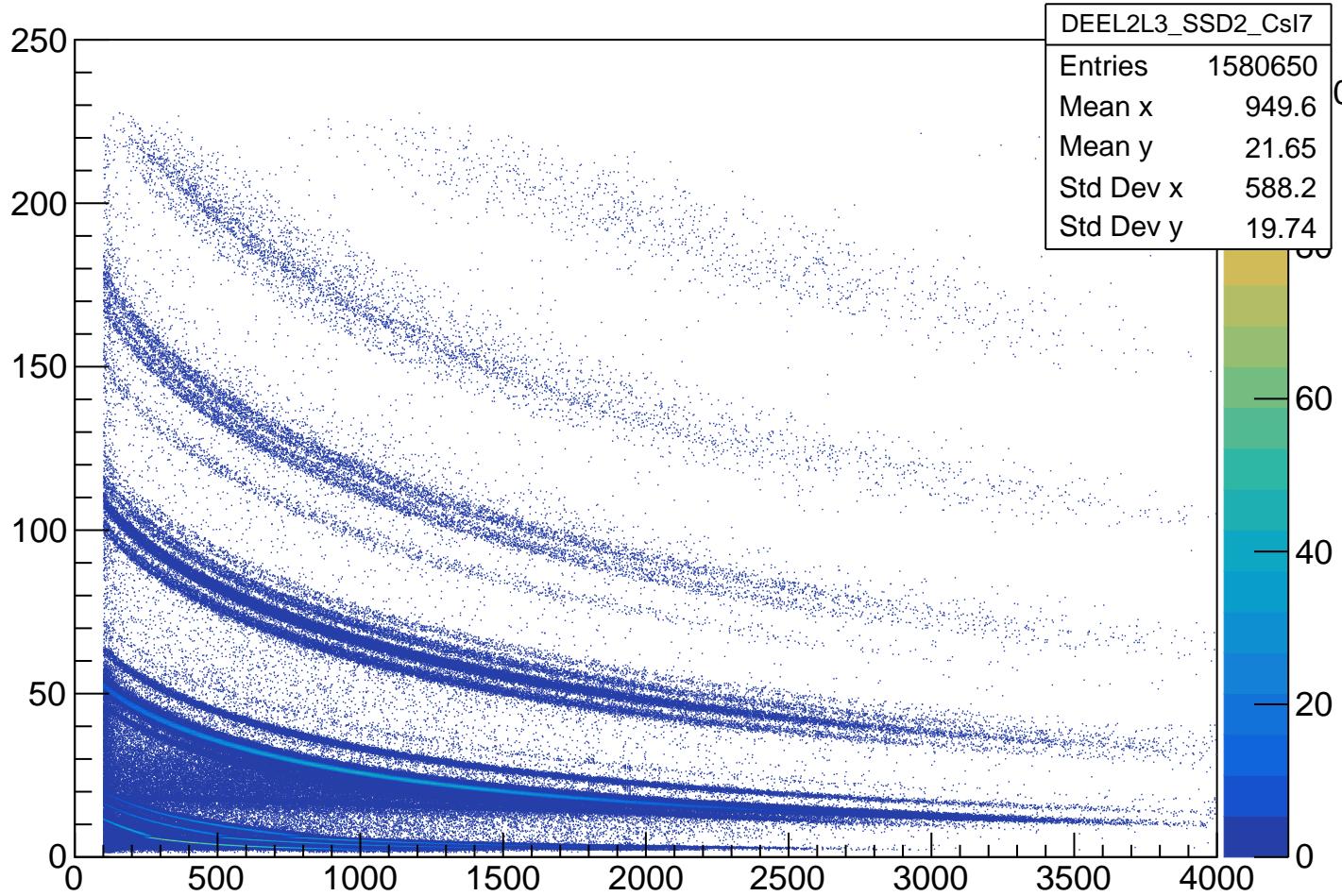
# DEEL2L3\_SSD2\_Csl5



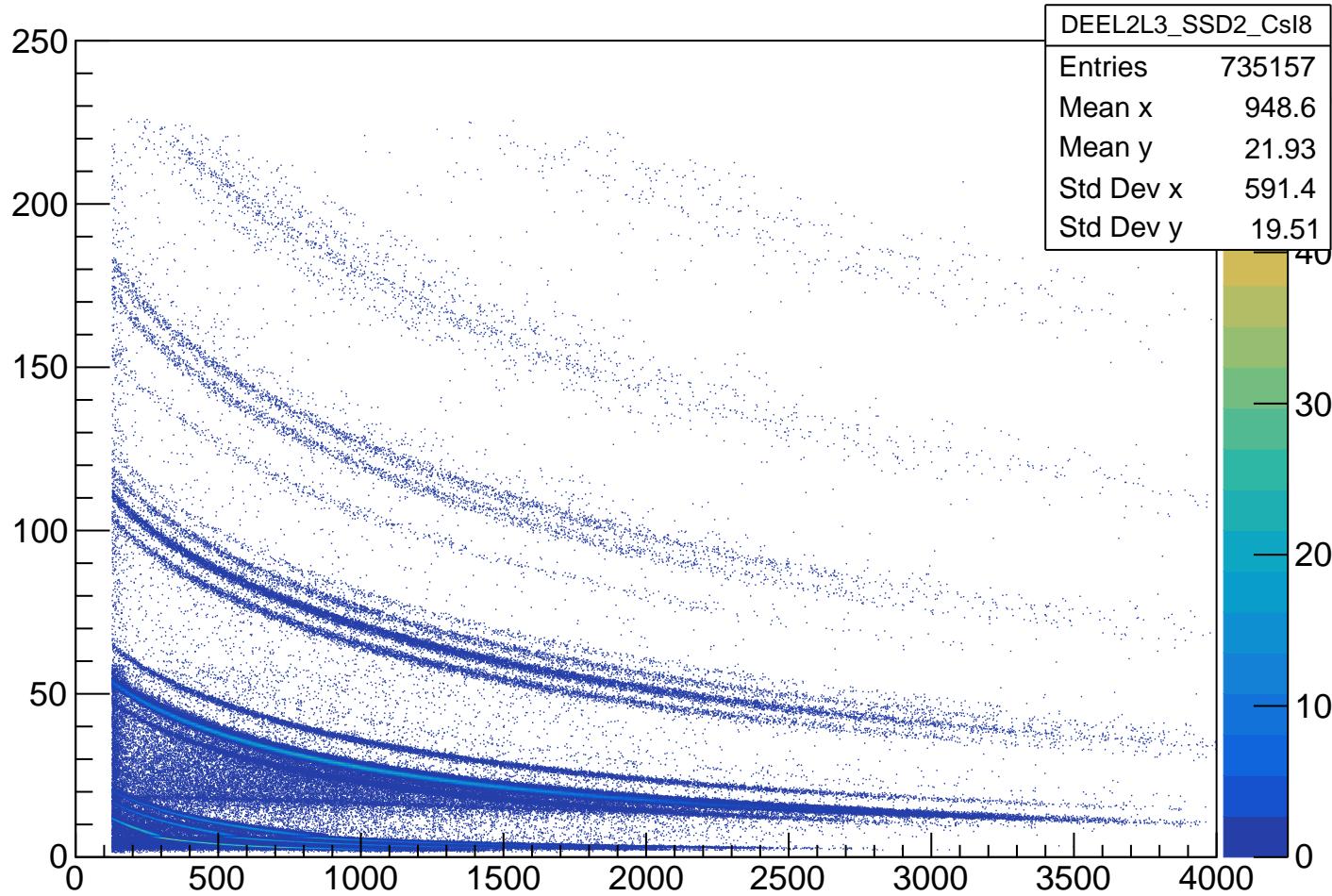
# DEEL2L3\_SSD2\_Csl6



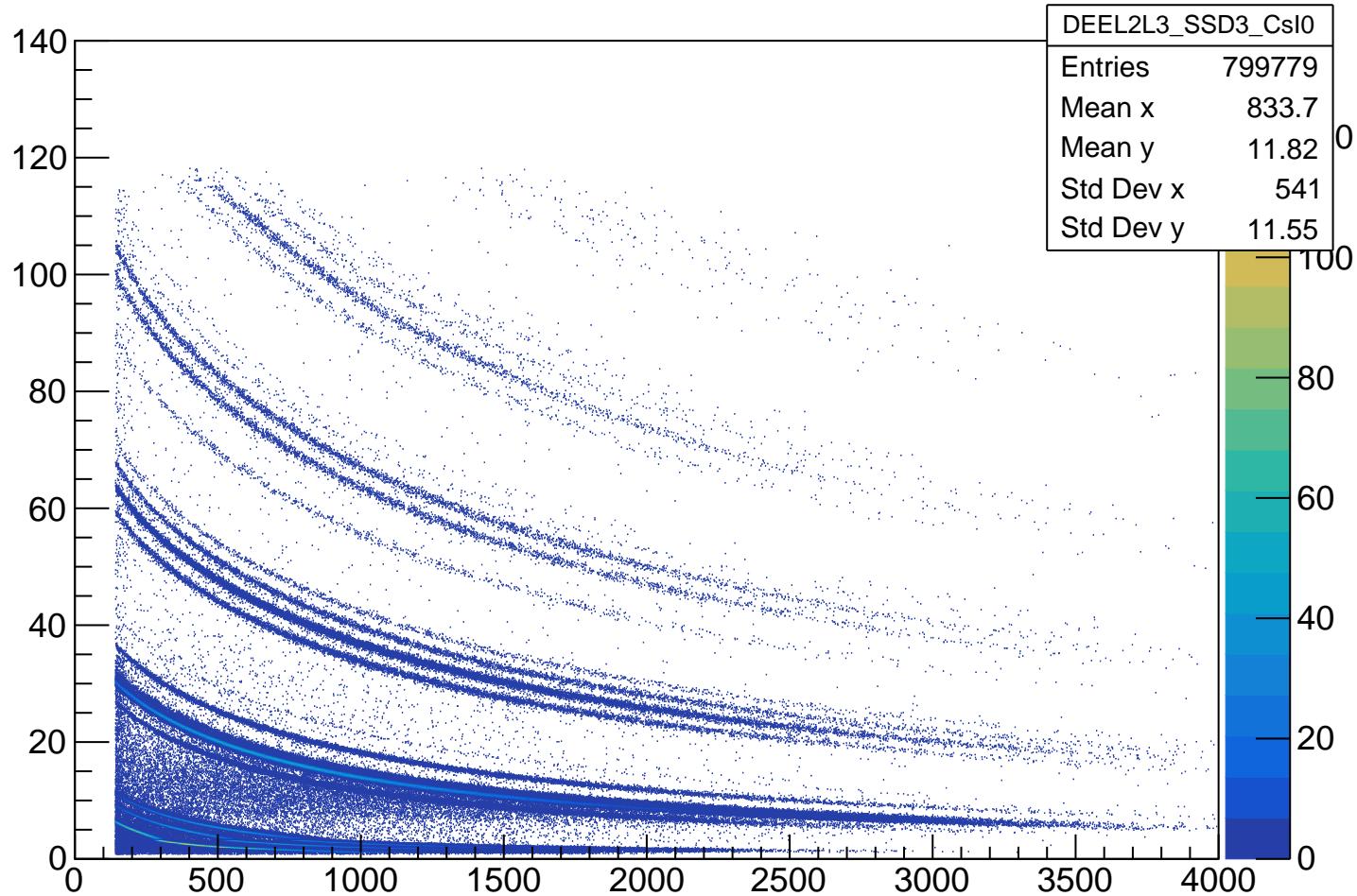
# DEEL2L3\_SSD2\_CsI7



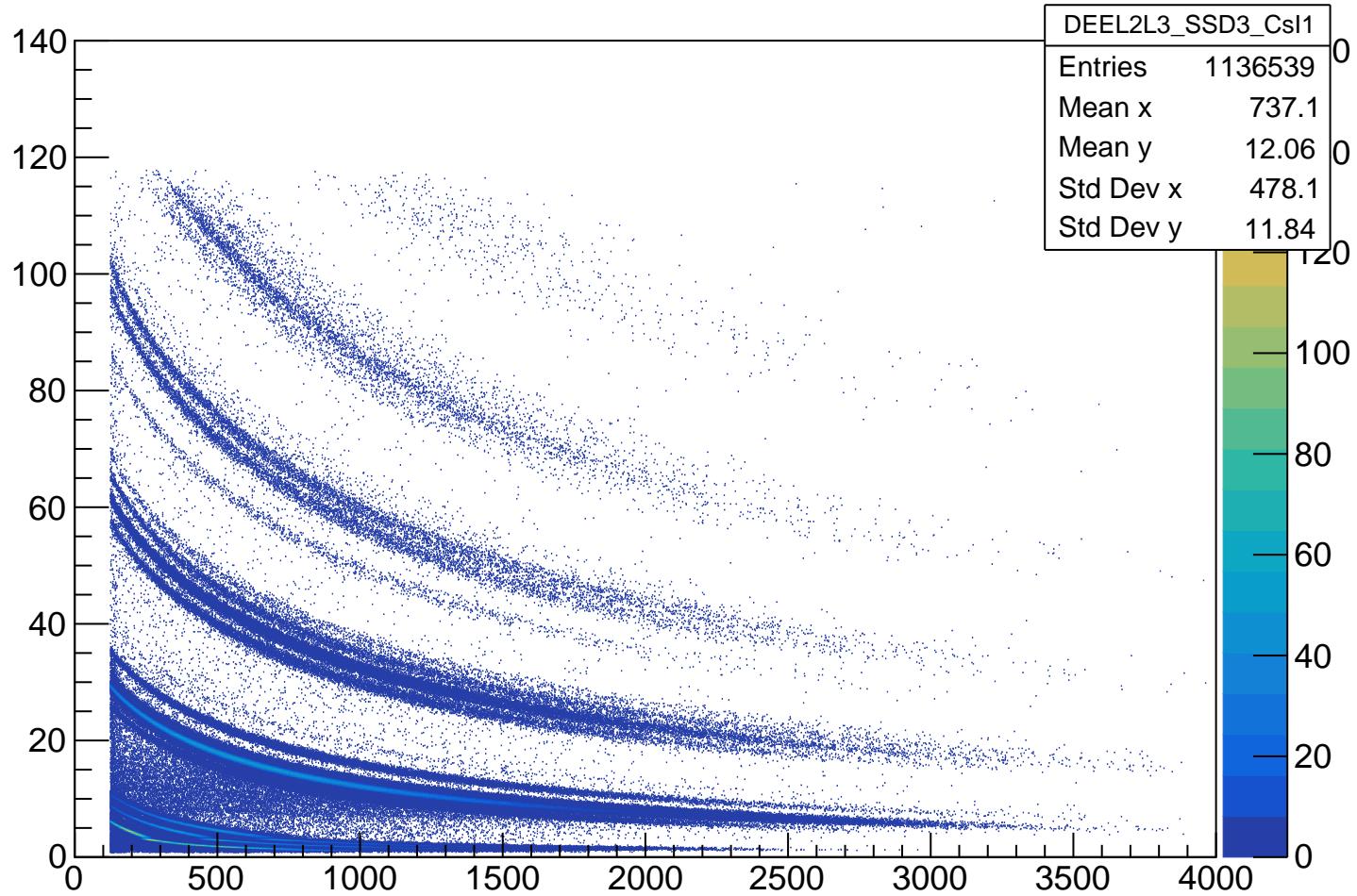
# DEEL2L3\_SSD2\_Csl8



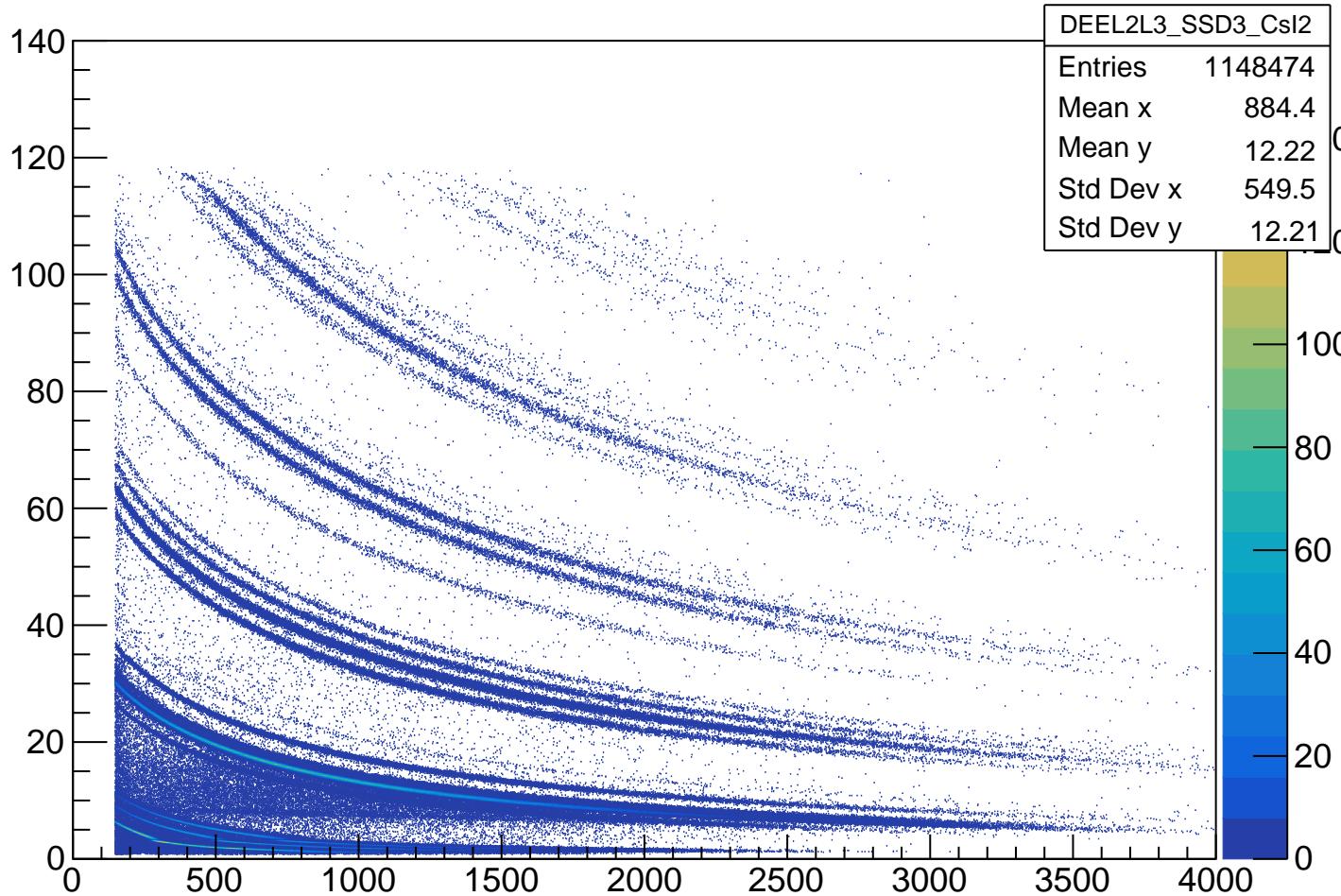
## DEEL2L3\_SSD3\_CsI0



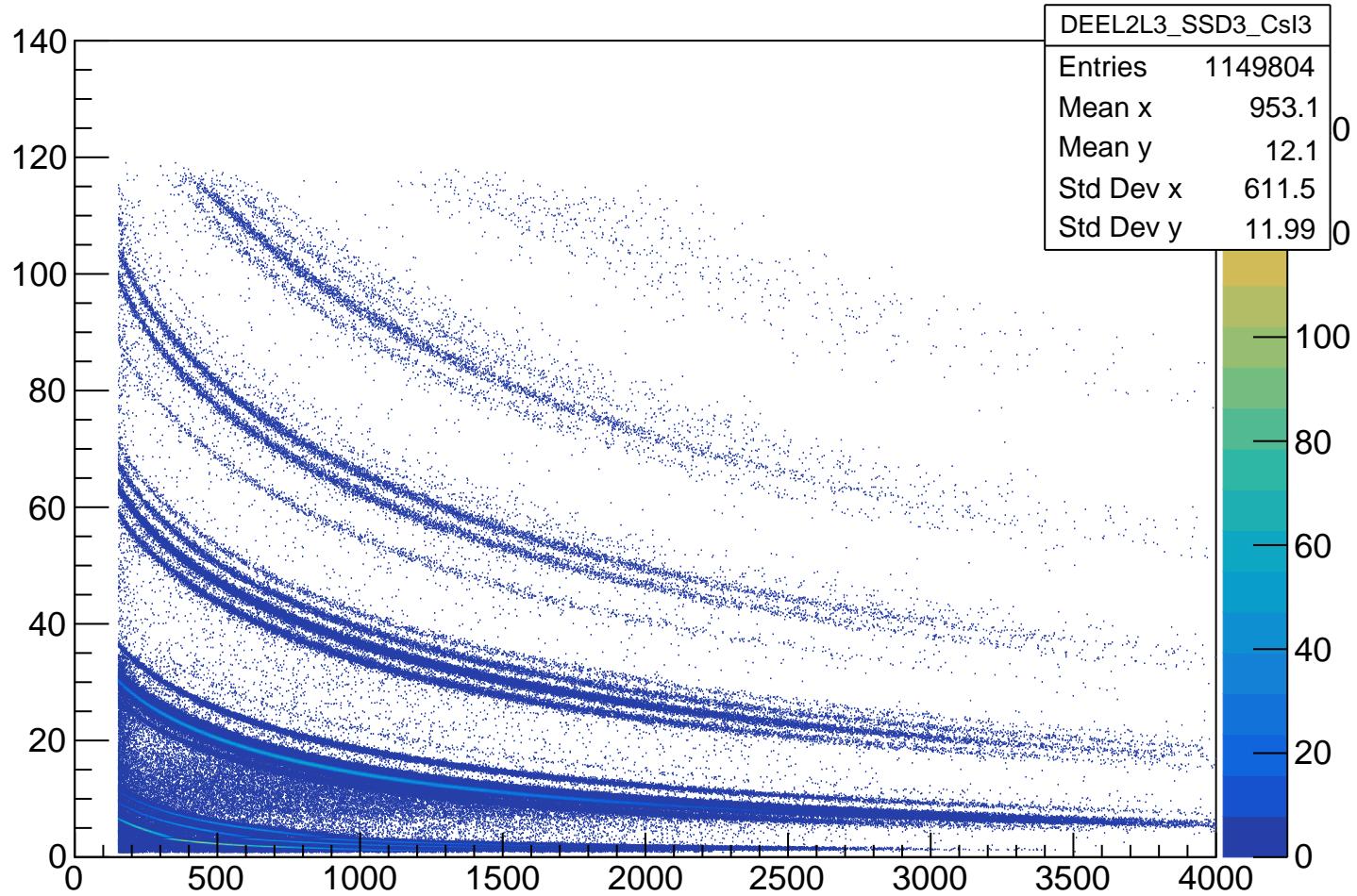
# DEEL2L3\_SSD3\_CsI1



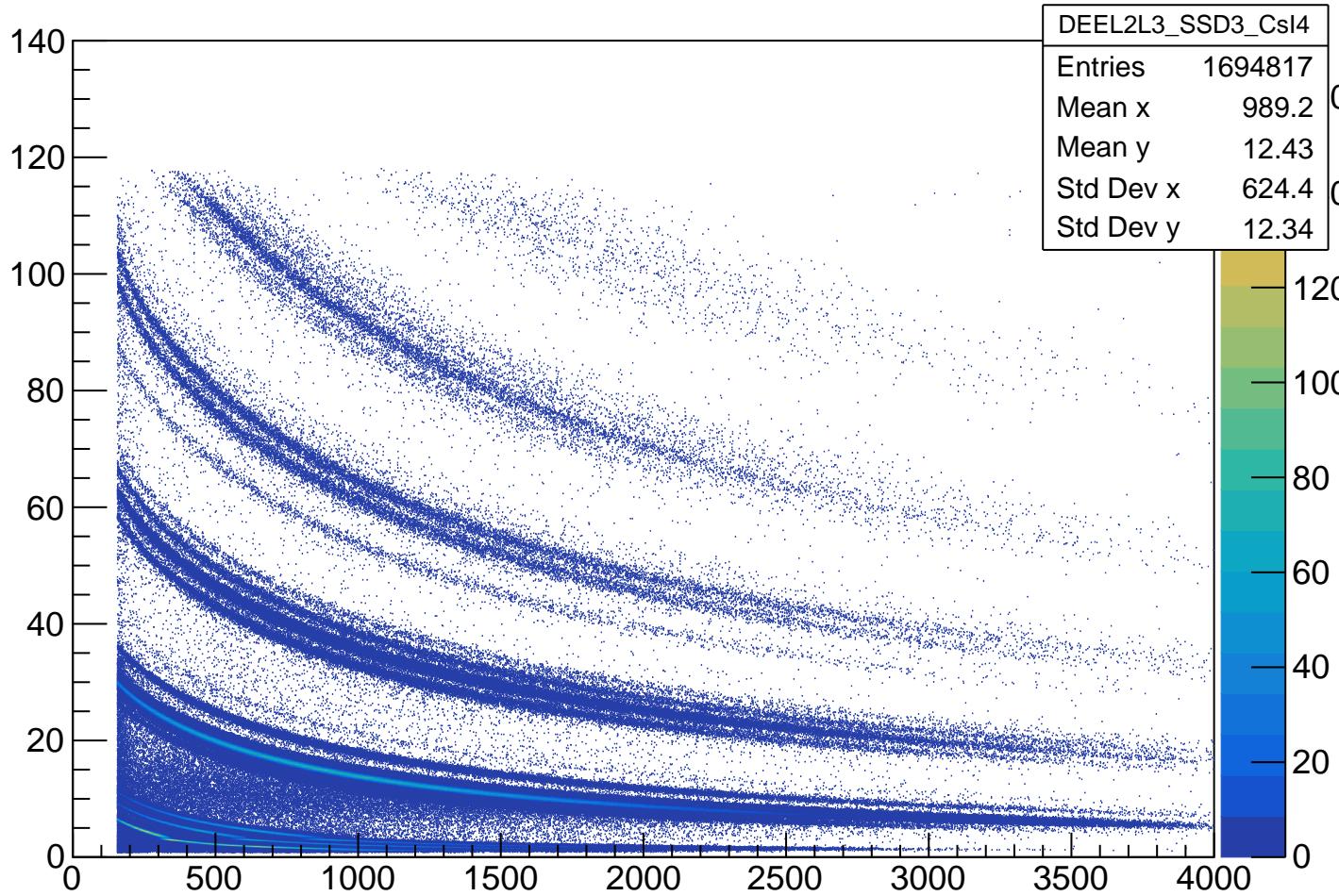
# DEEL2L3\_SSD3\_Csl2



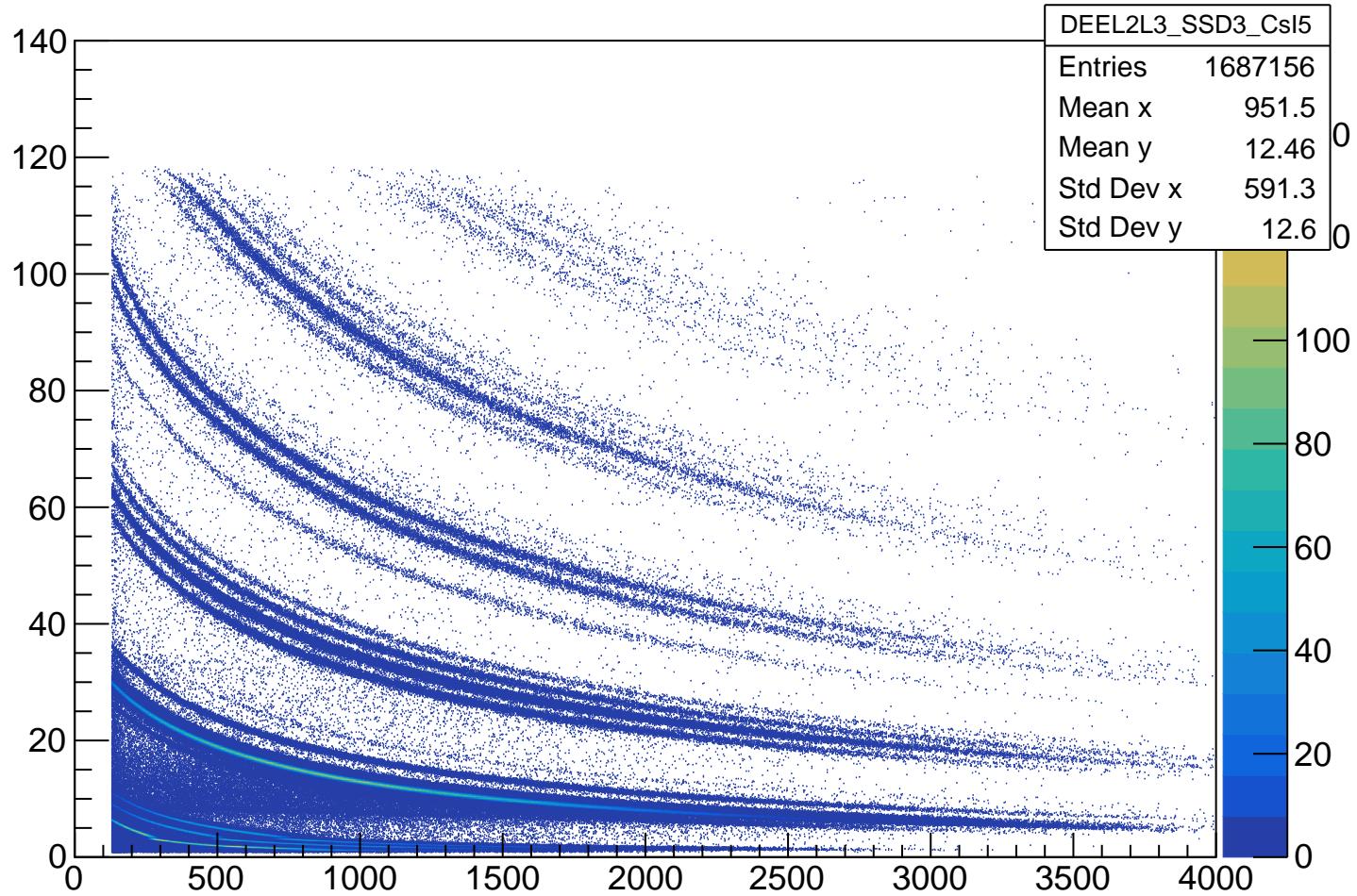
# DEEL2L3\_SSD3\_Csl3



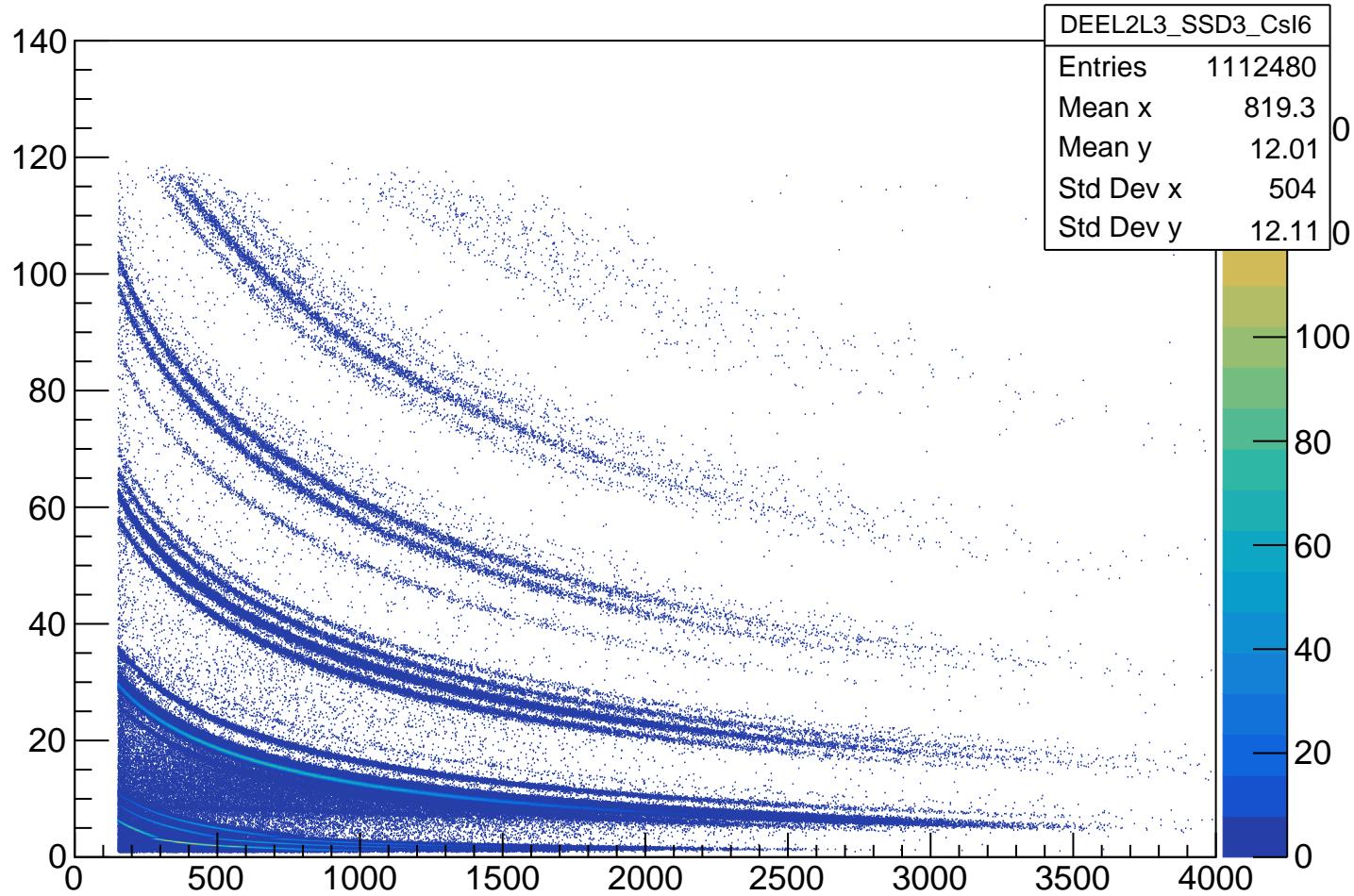
# DEEL2L3\_SSD3\_CsI4



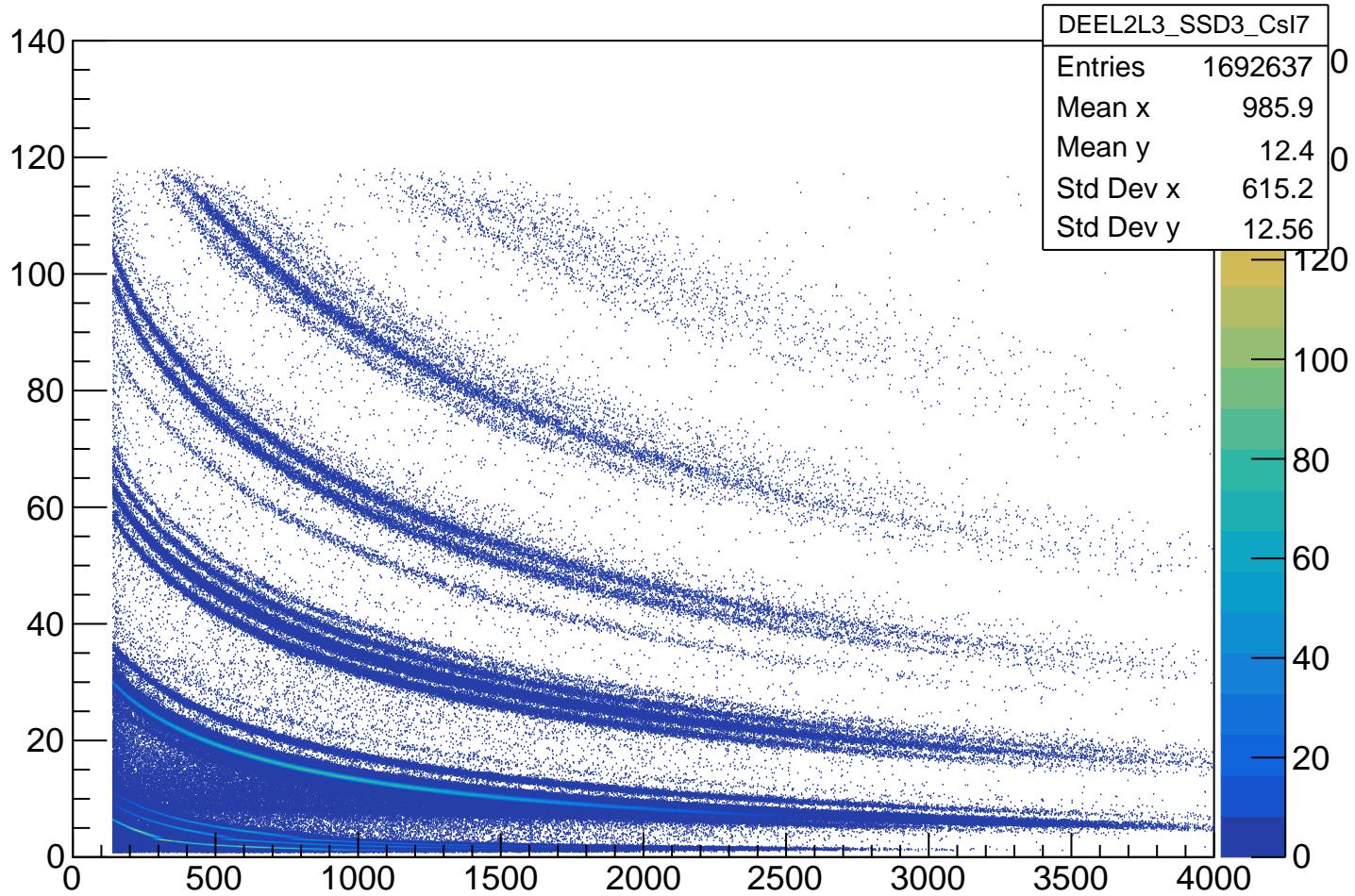
# DEEL2L3\_SSD3\_CsI5



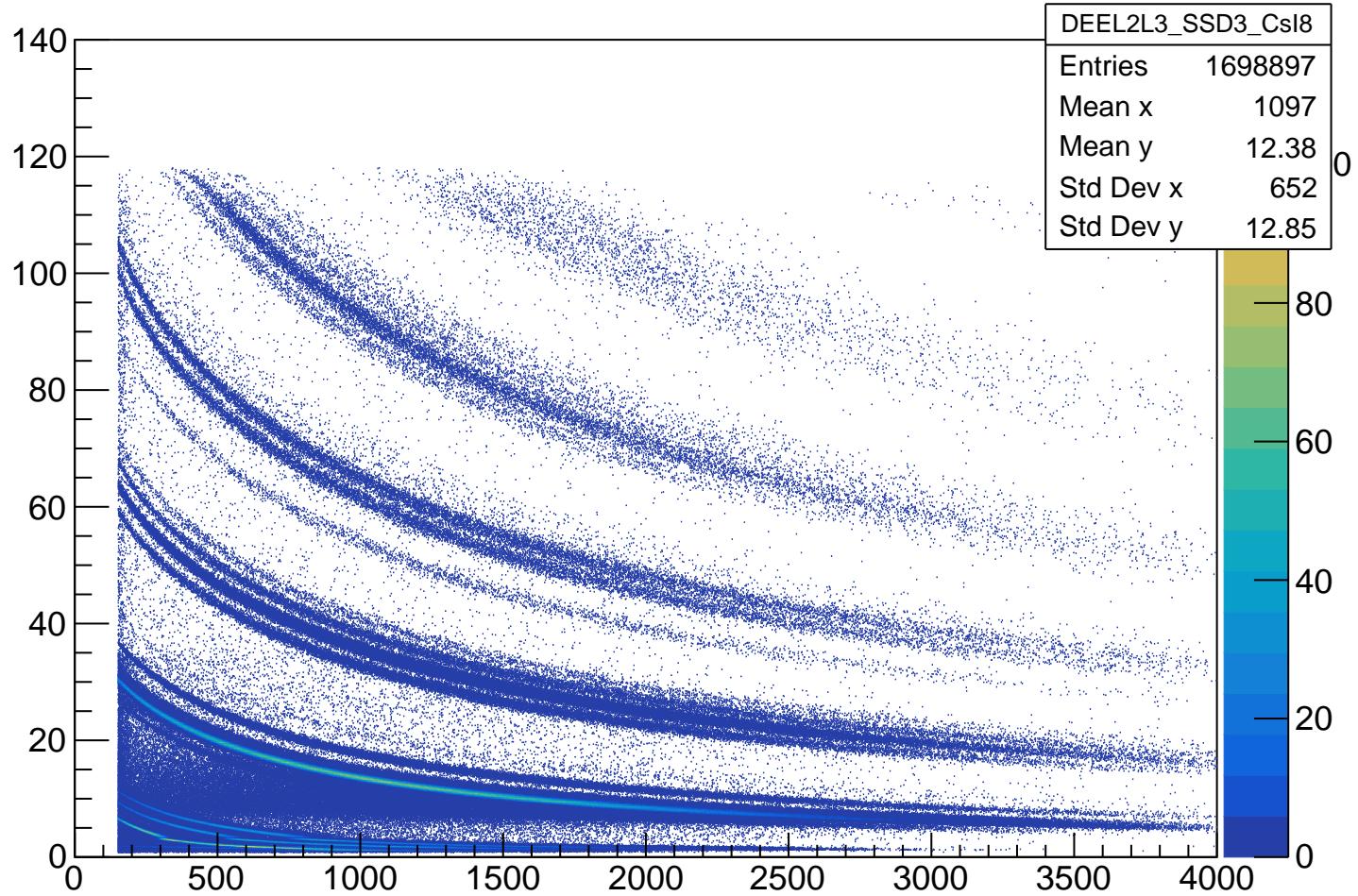
# DEEL2L3\_SSD3\_Csl6



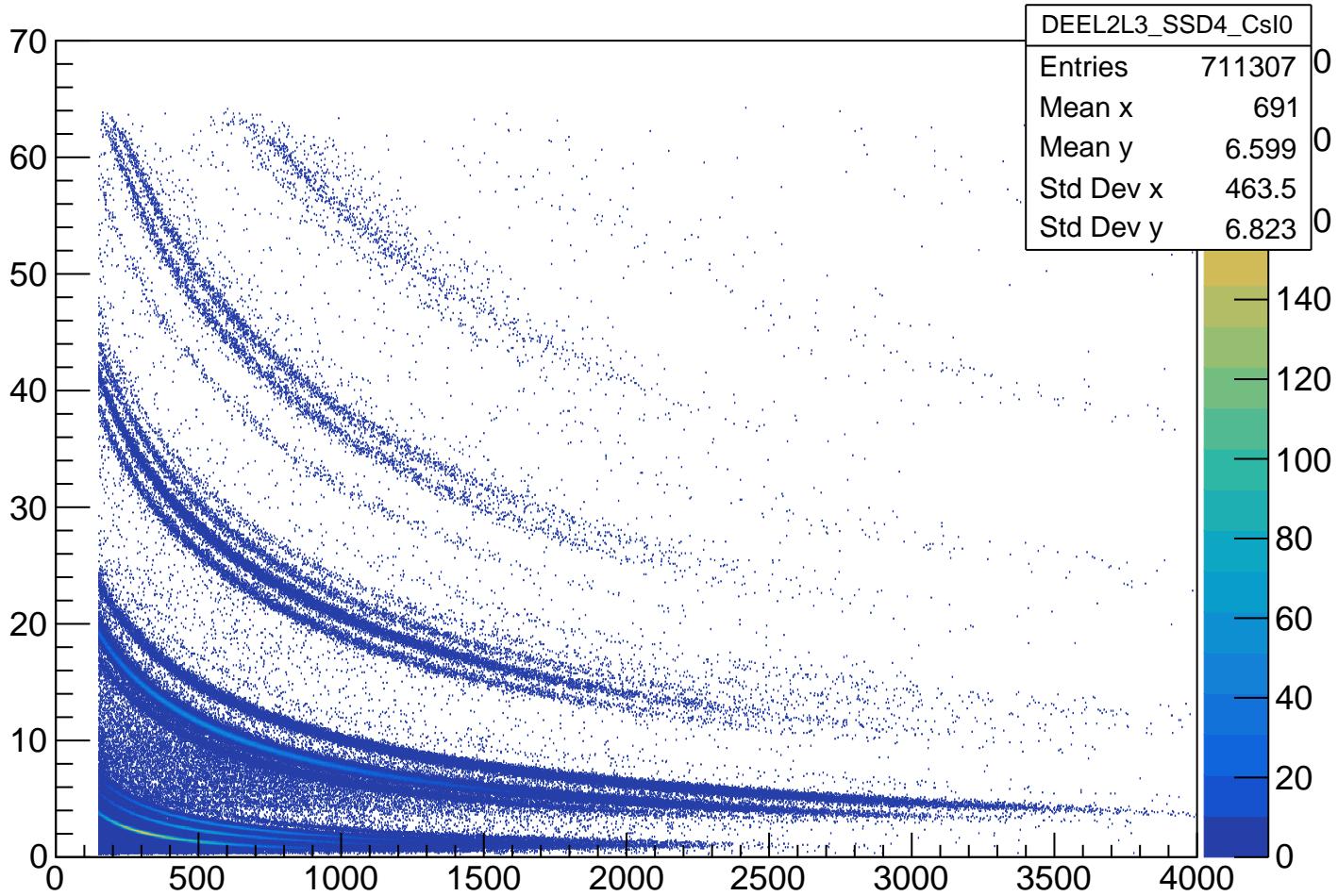
# DEEL2L3\_SSD3\_CsI7



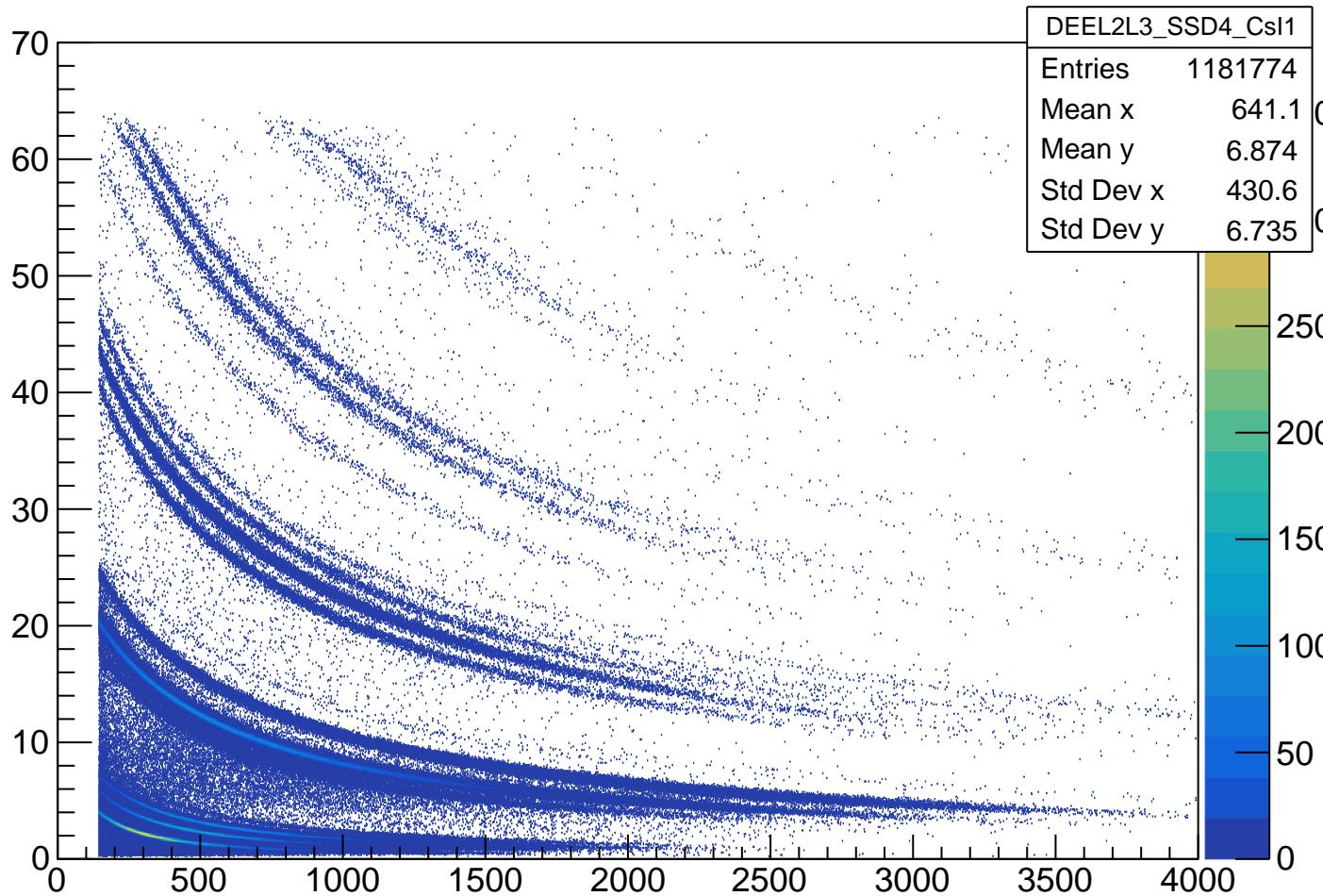
# DEEL2L3\_SSD3\_Csl8



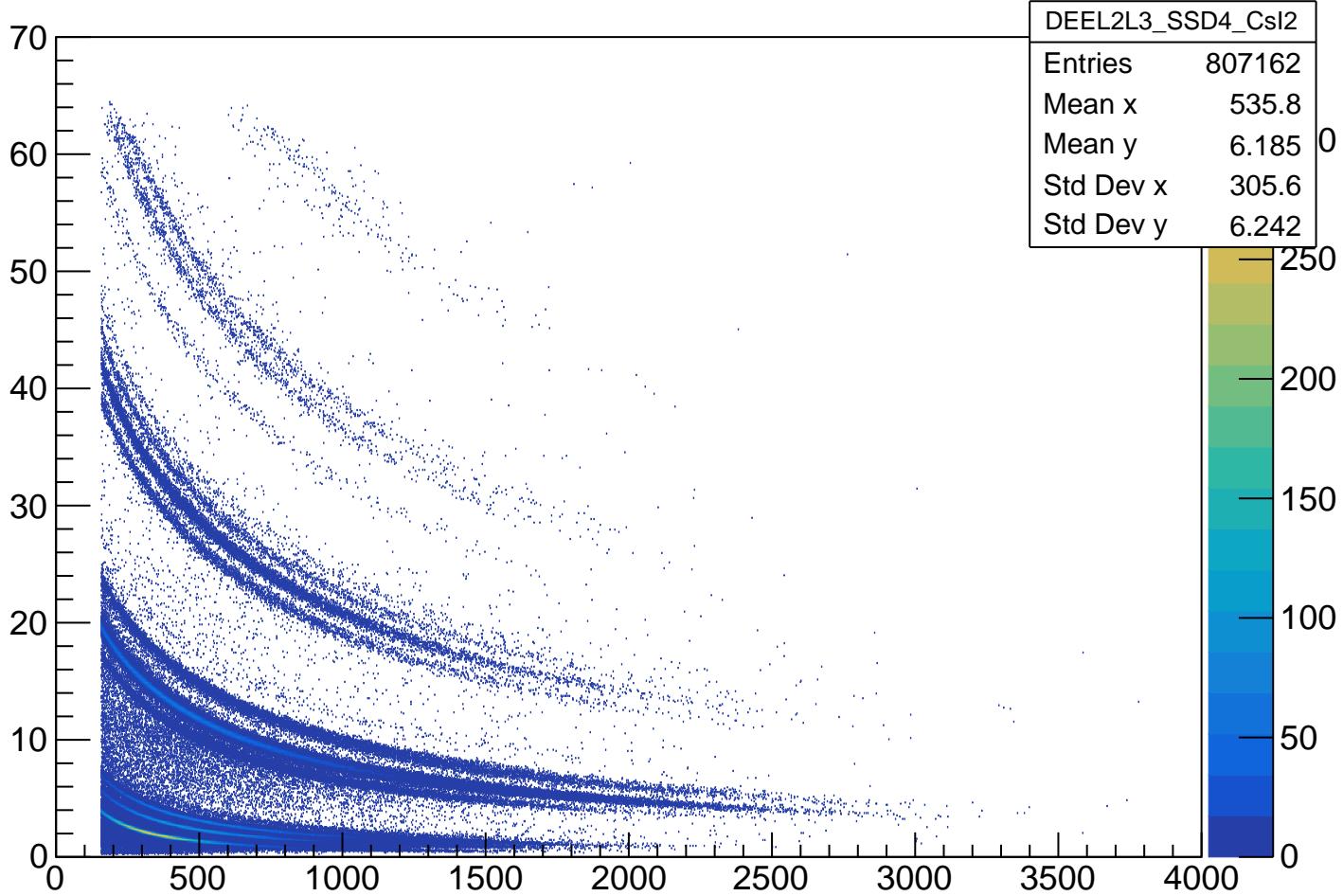
# DEEL2L3\_SSD4\_CsI0



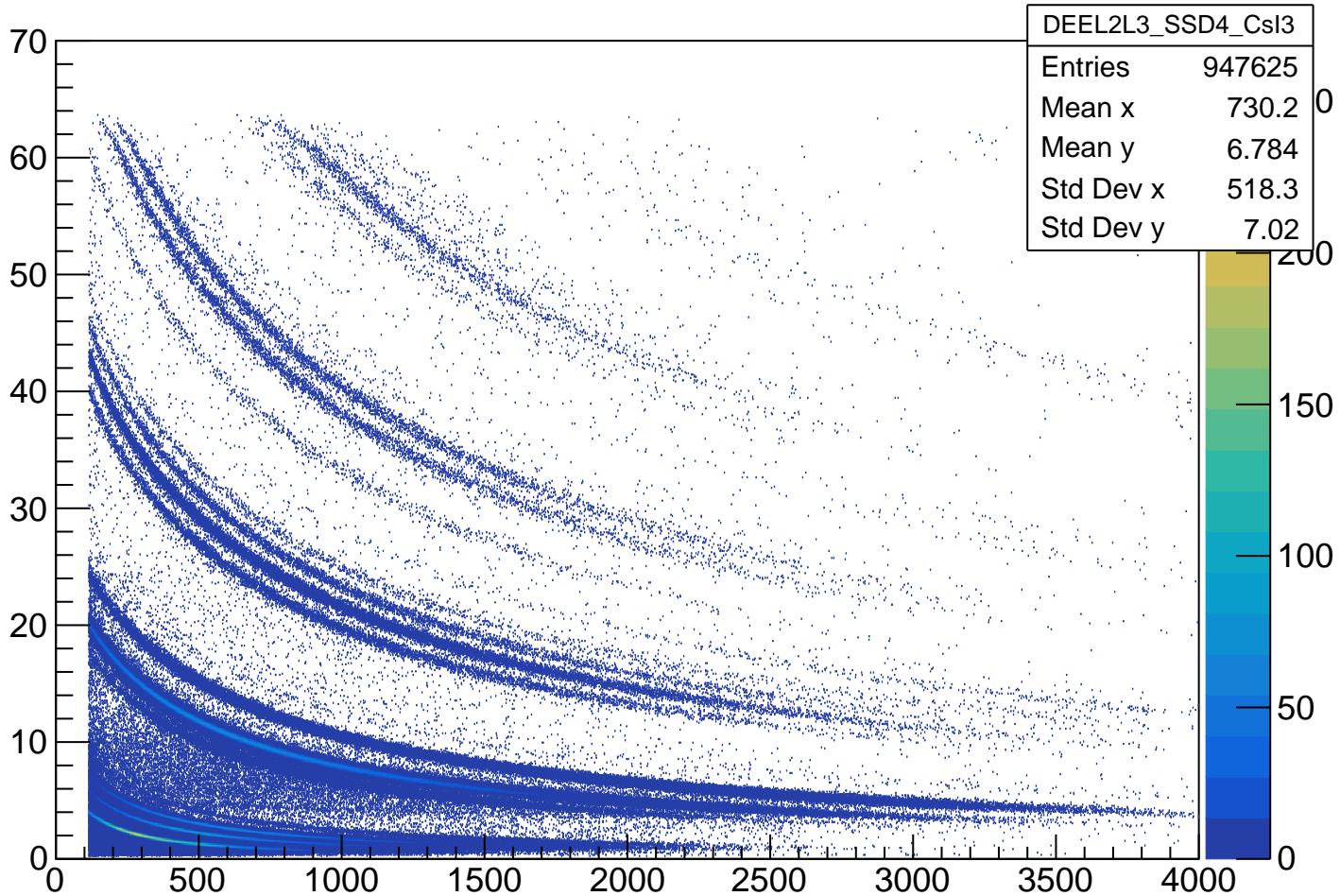
# DEEL2L3\_SSD4\_CsI1



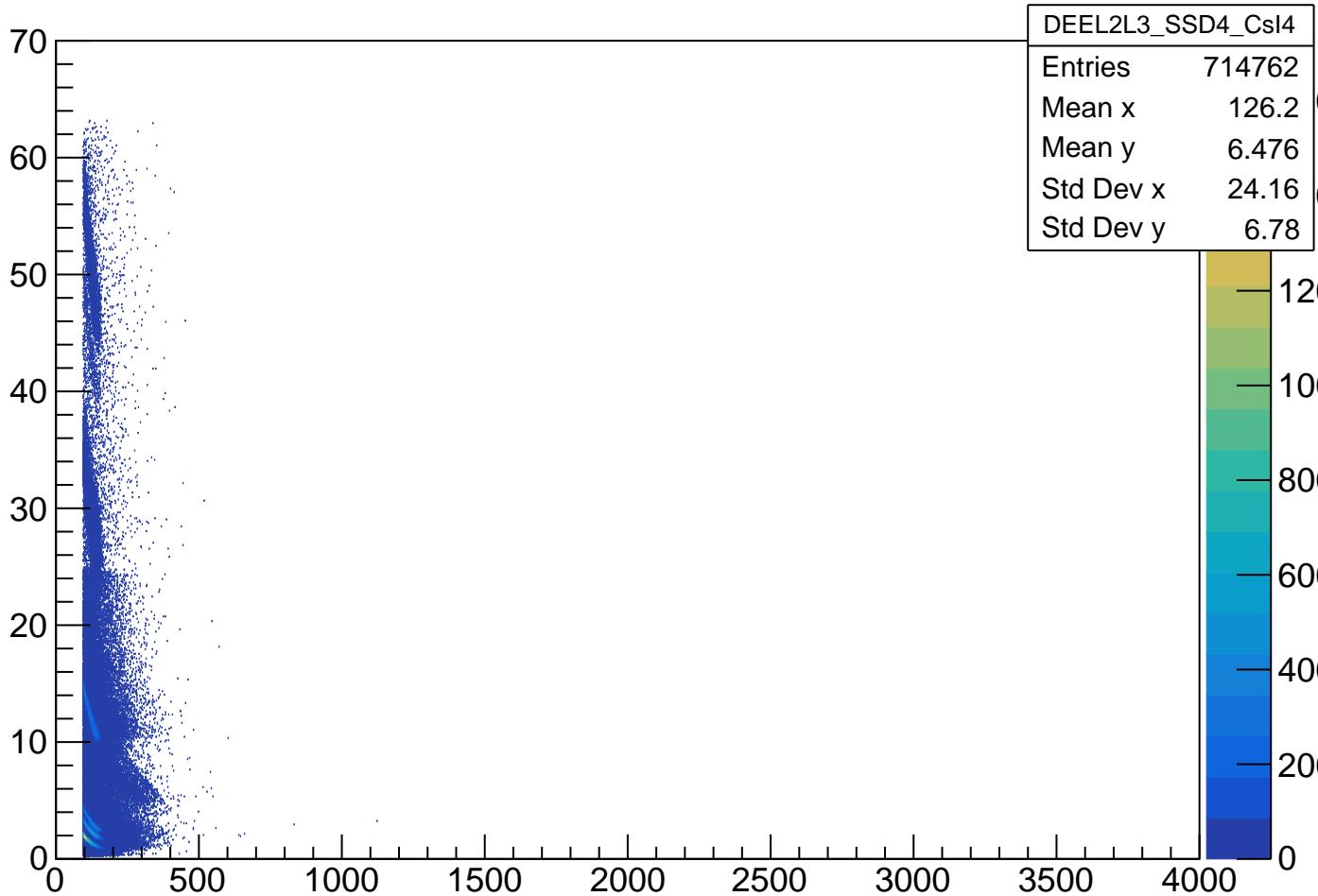
# DEEL2L3\_SSD4\_Csl2



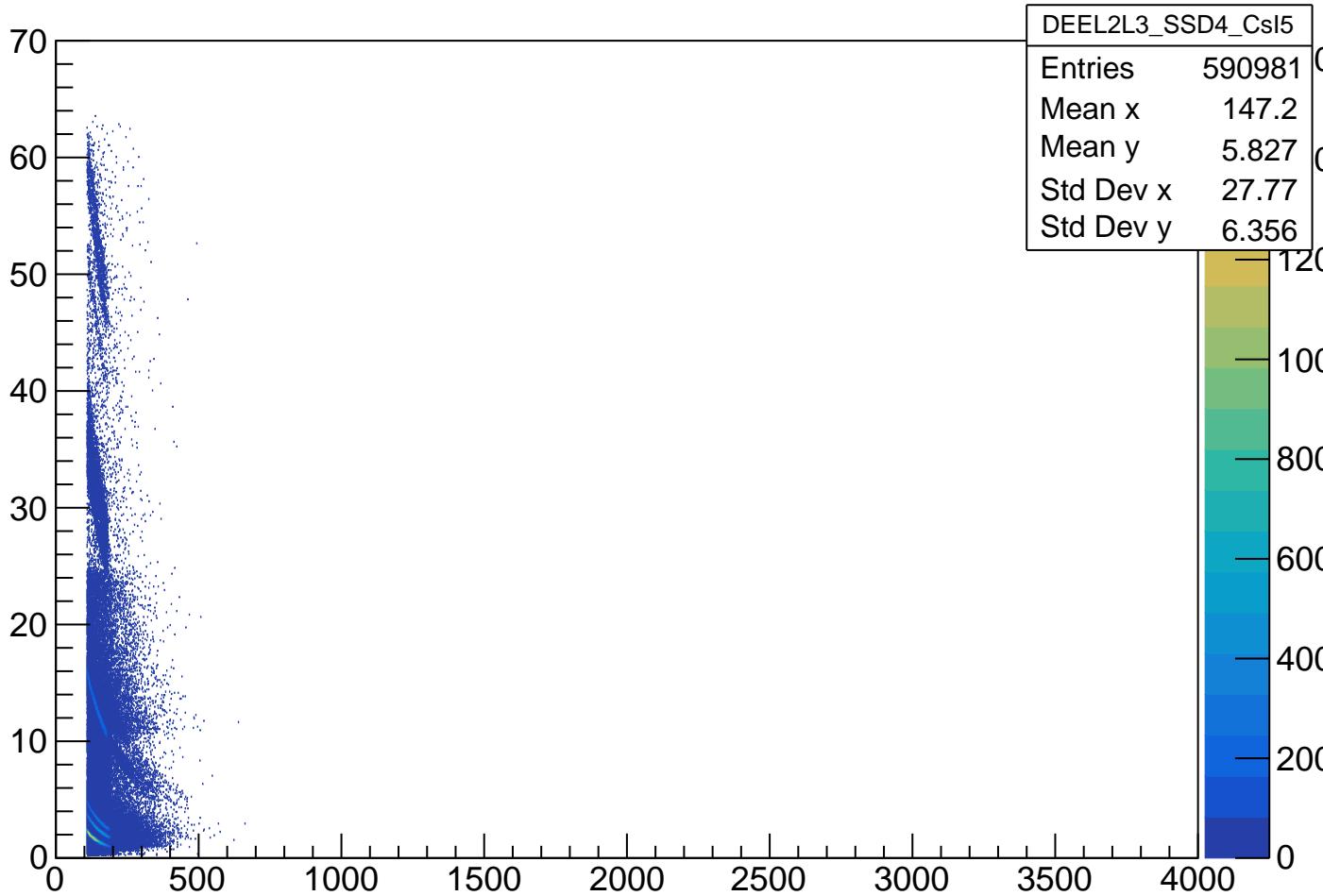
# DEEL2L3\_SSD4\_Csl3



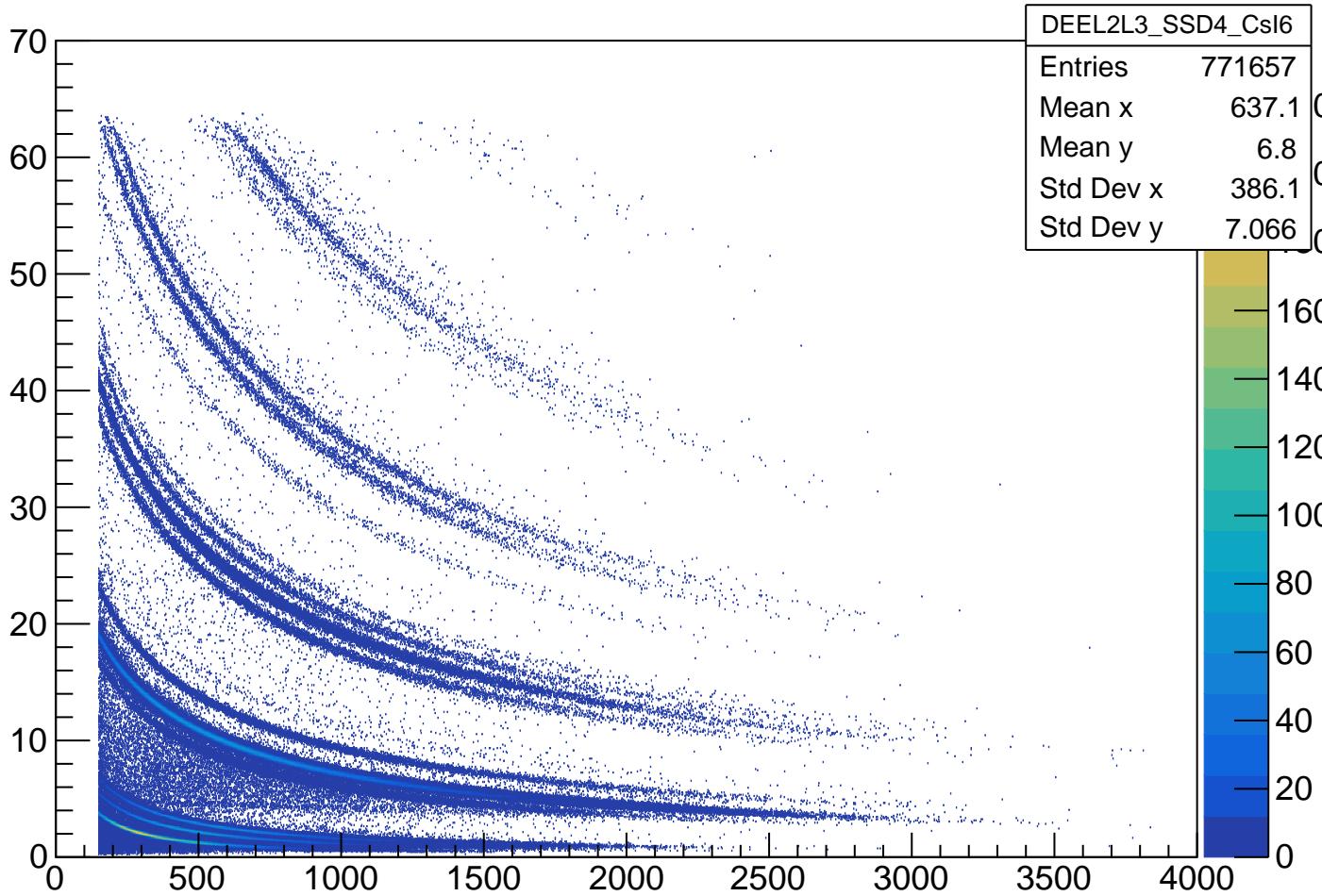
# DEEL2L3\_SSD4\_CsI4



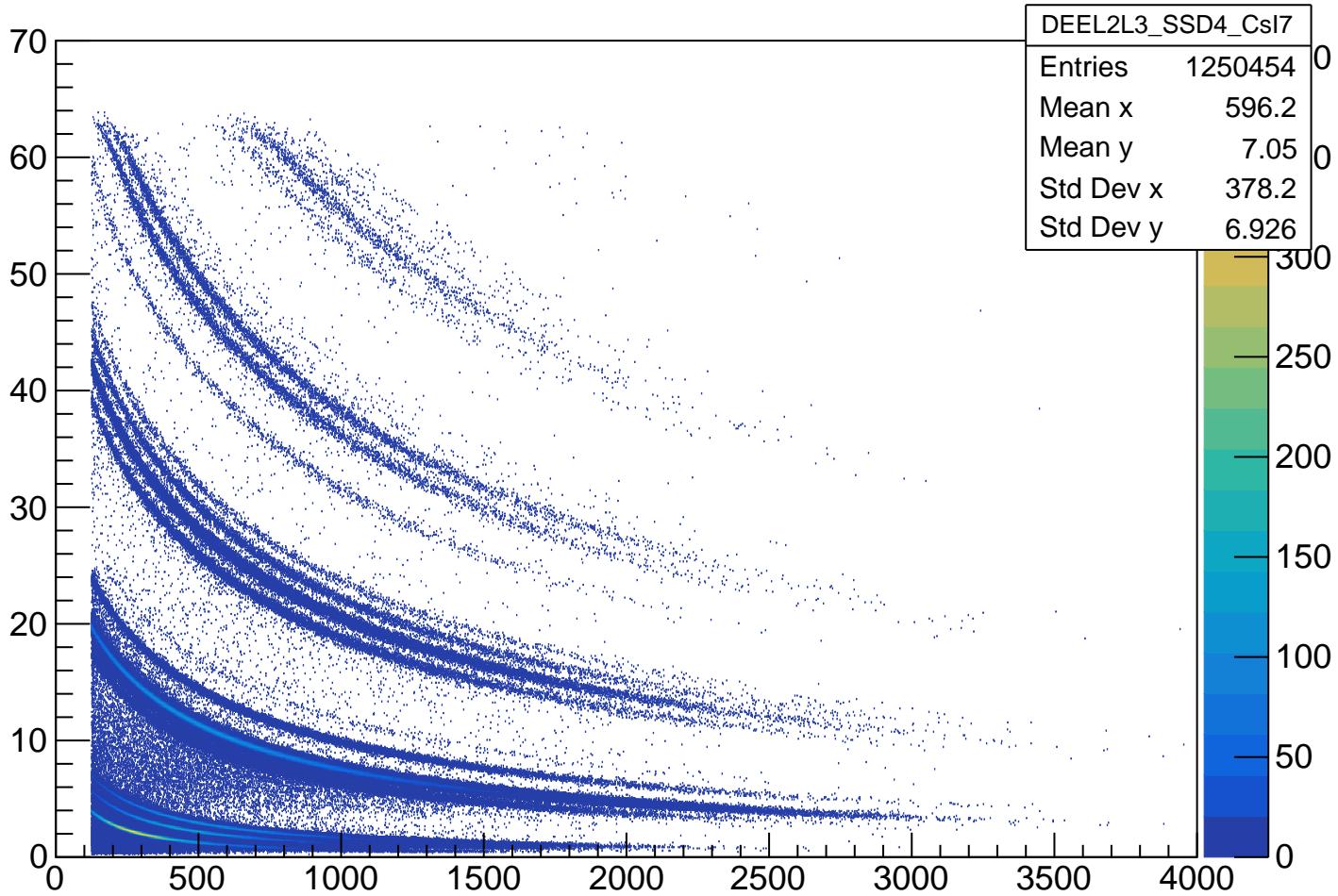
# DEEL2L3\_SSD4\_CsI5



# DEEL2L3\_SSD4\_Csl6



# DEEL2L3\_SSD4\_CsI7



# DEEL2L3\_SSD4\_Csl8

