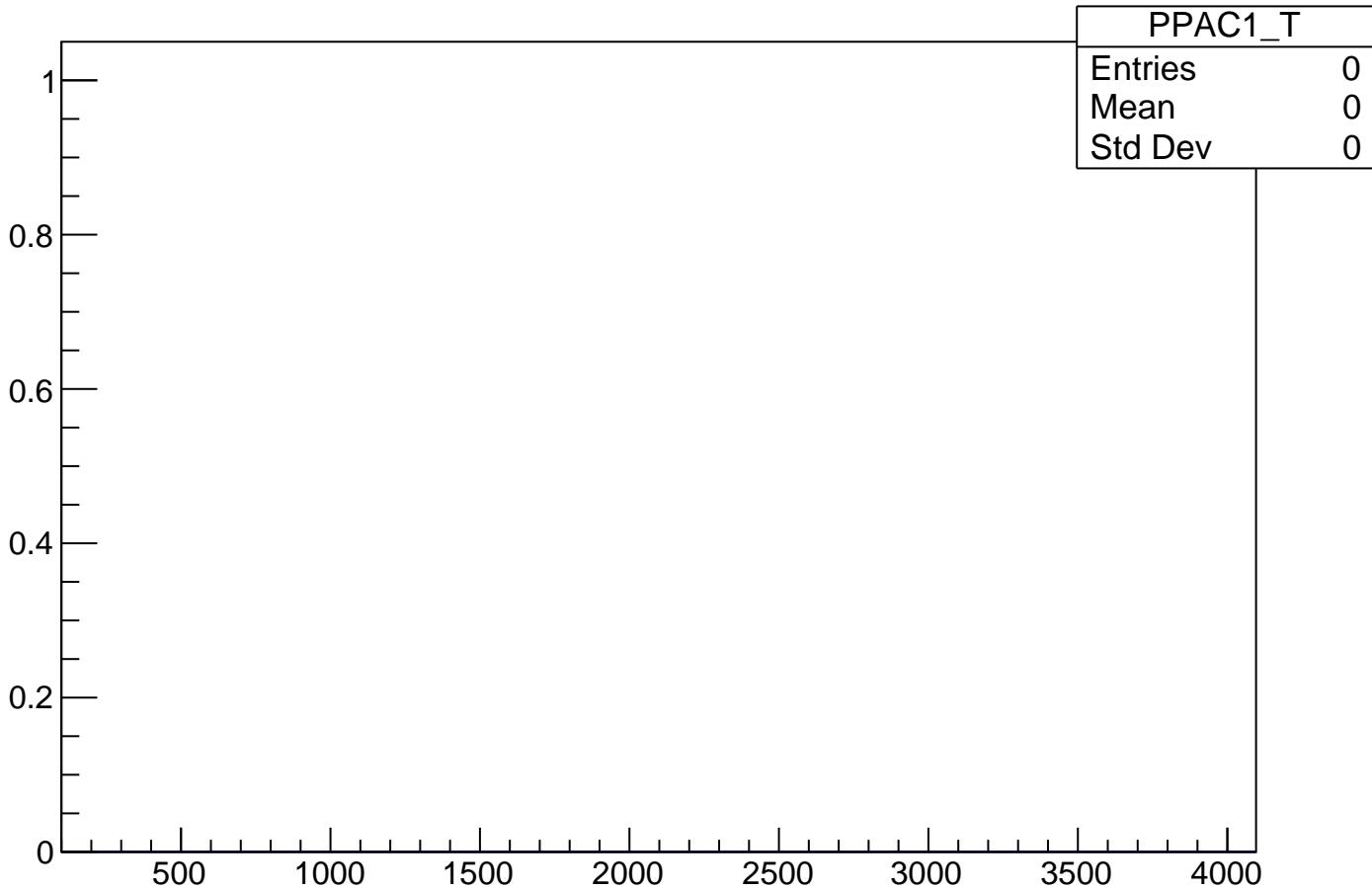
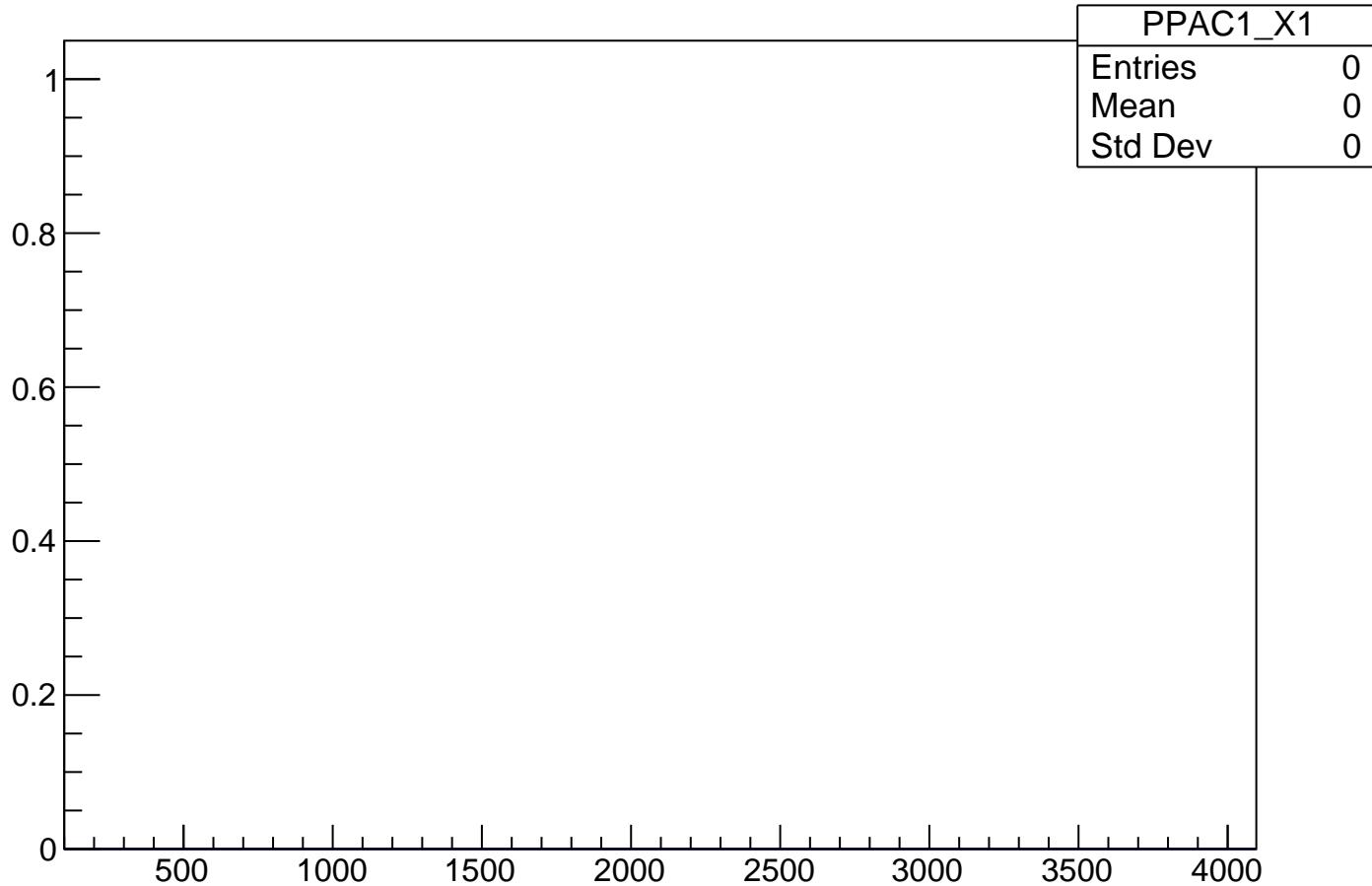


PPAC1_T

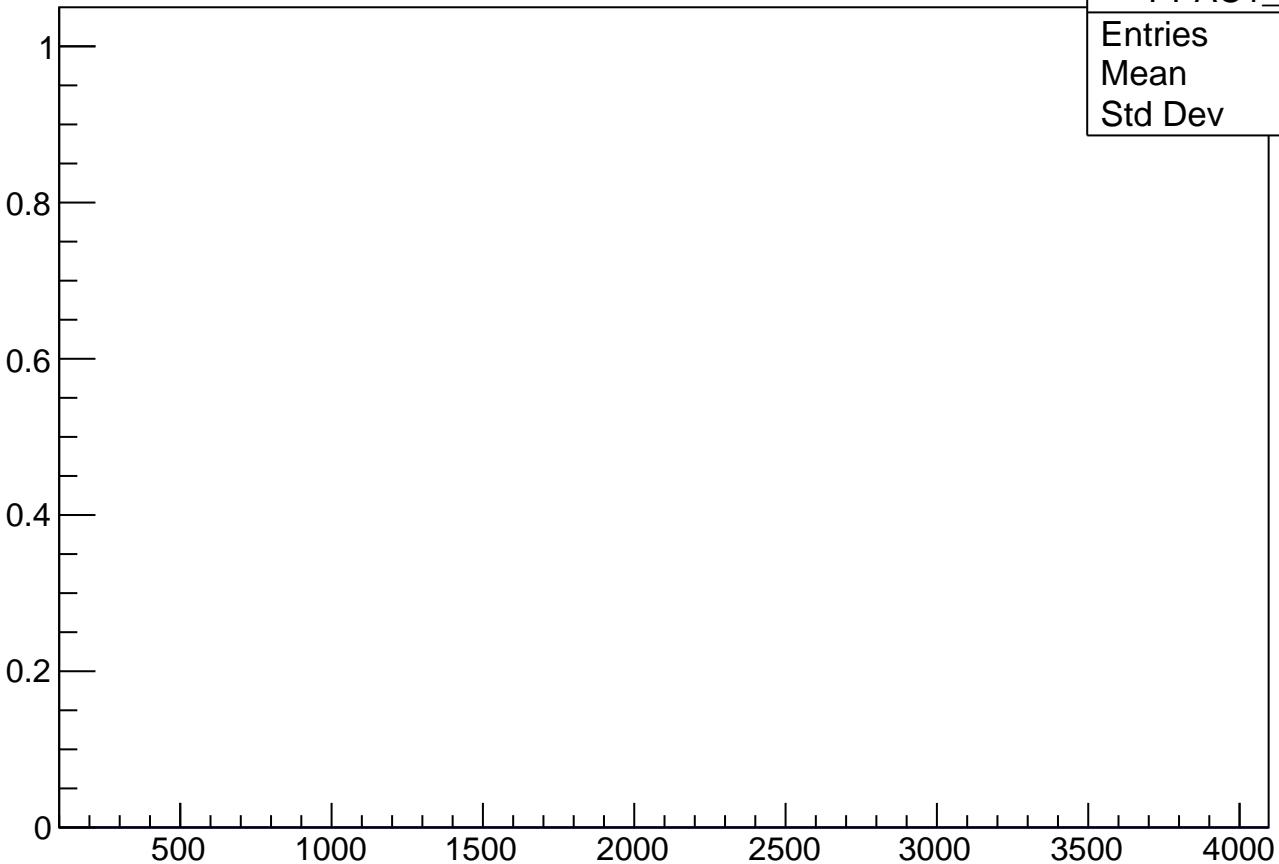


PPAC1_X1

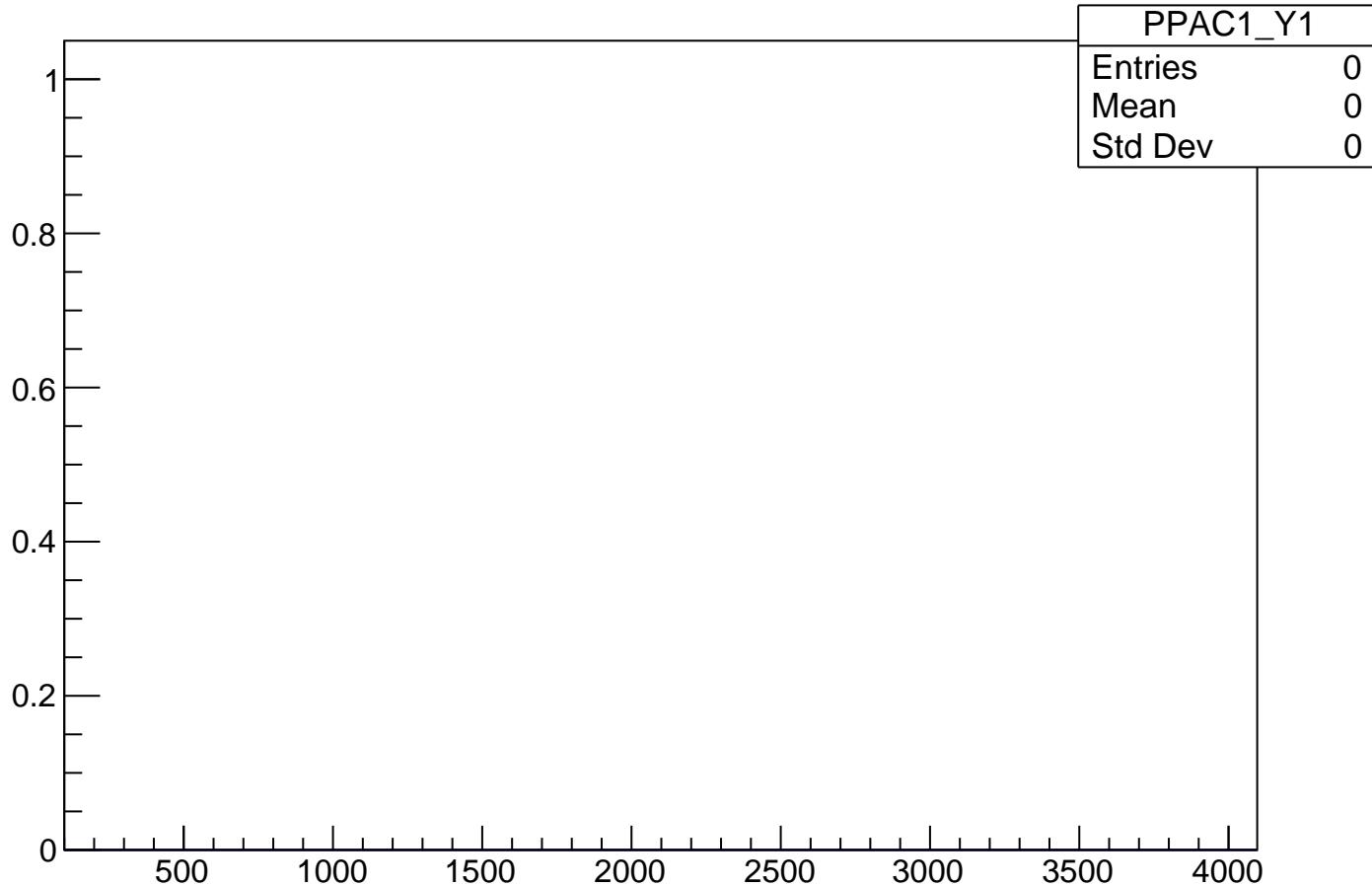


PPAC1_X2

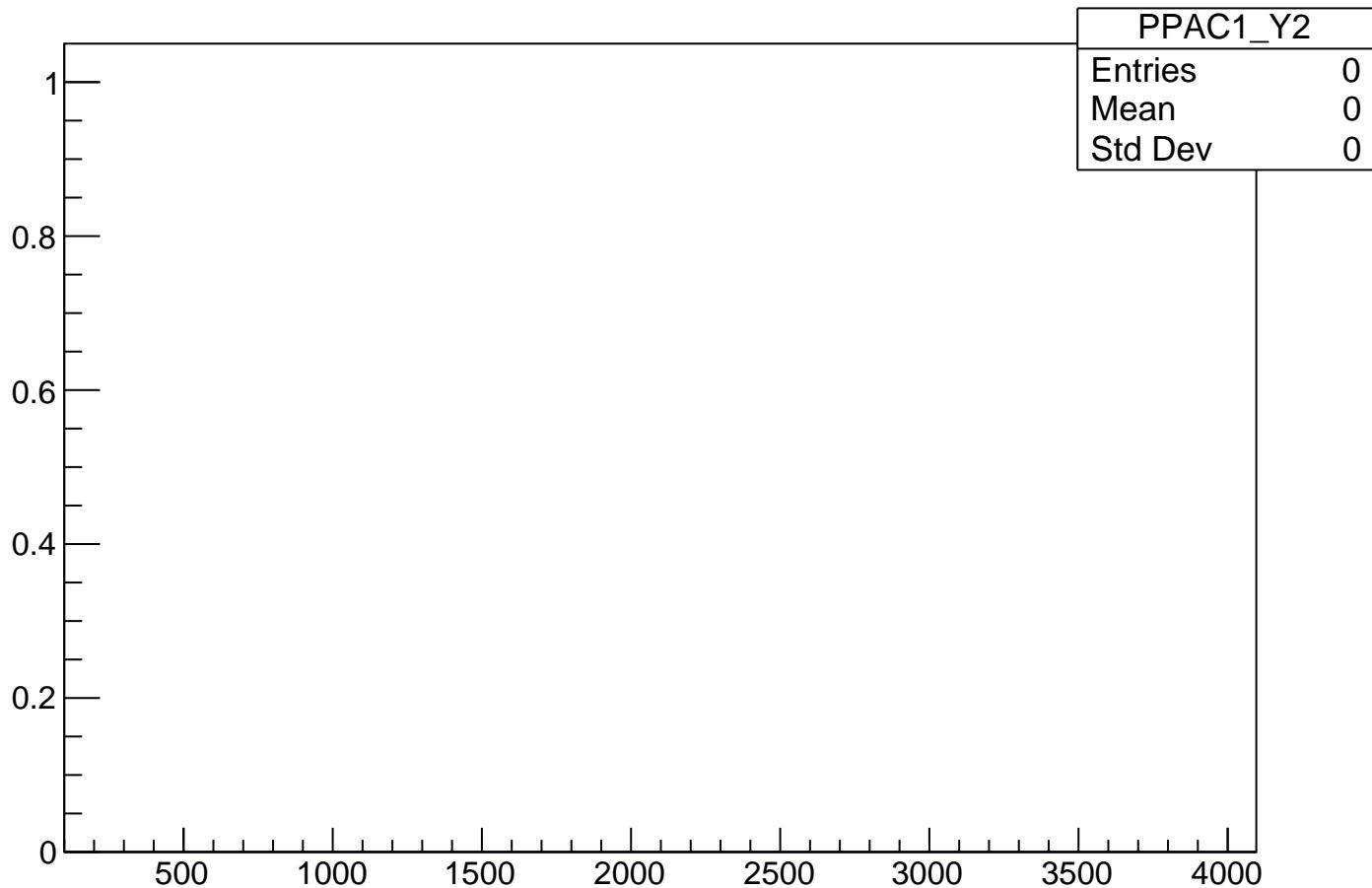
PPAC1_X2	
Entries	0
Mean	0
Std Dev	0



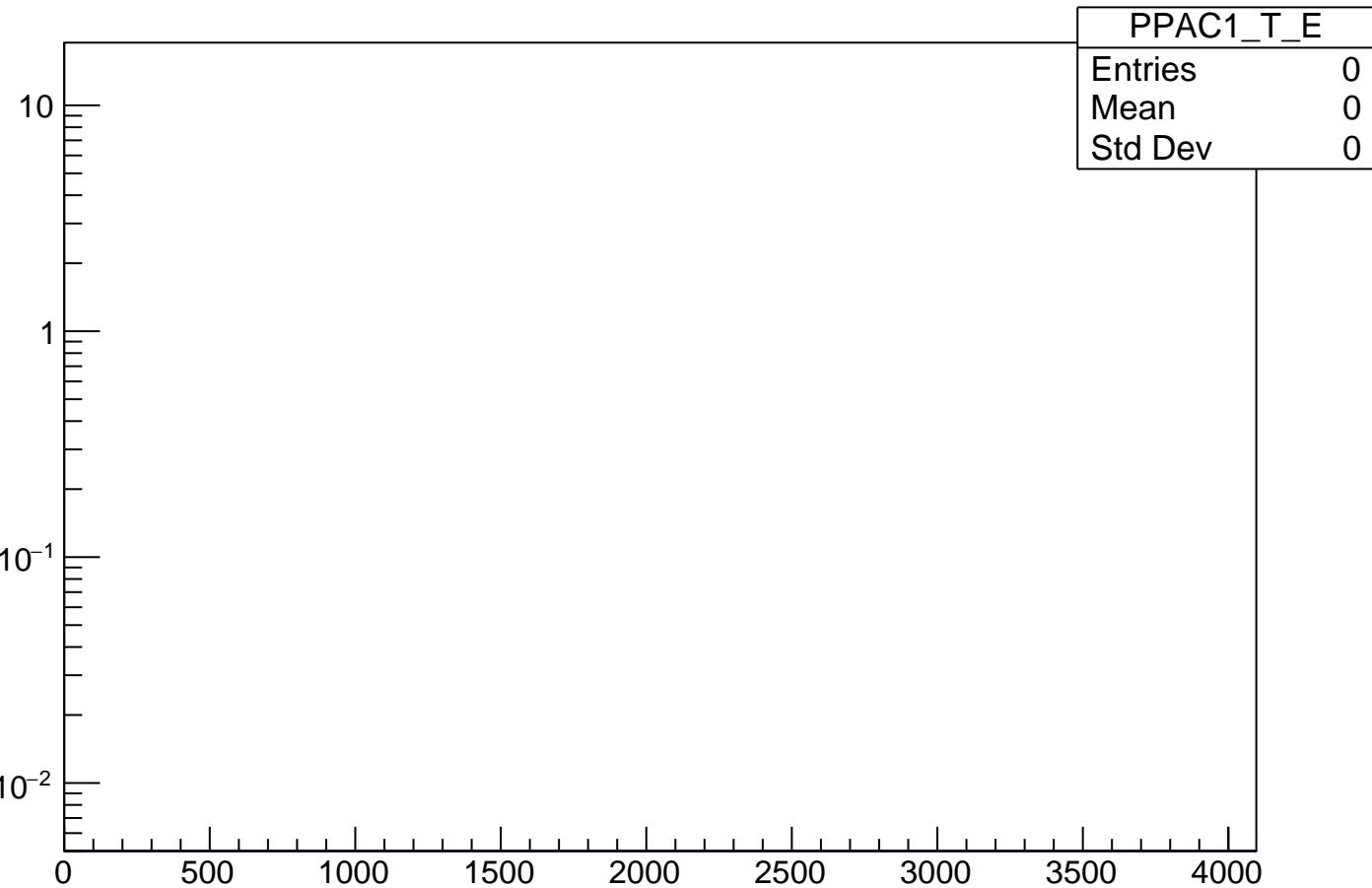
PPAC1_Y1



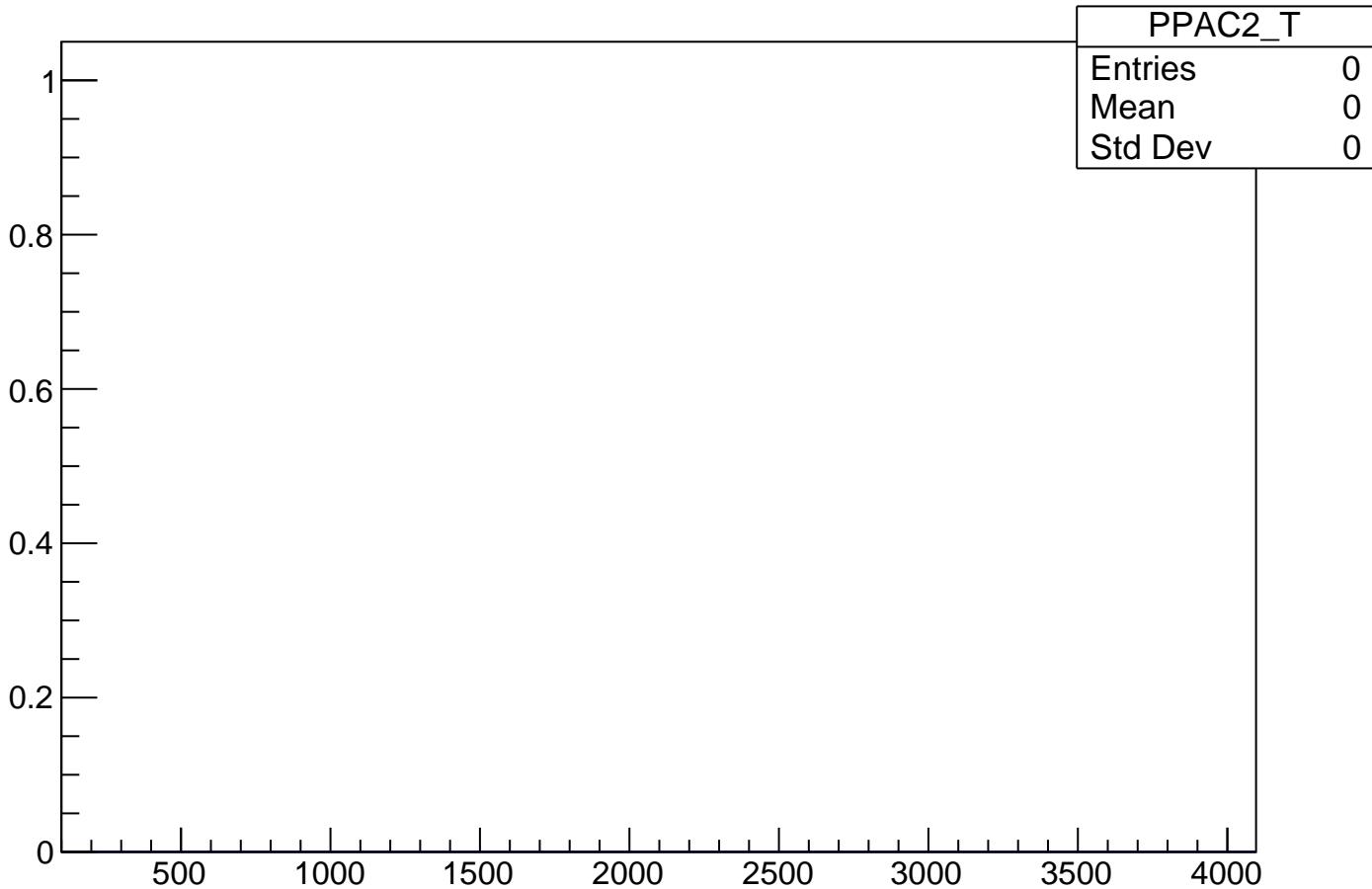
PPAC1_Y2



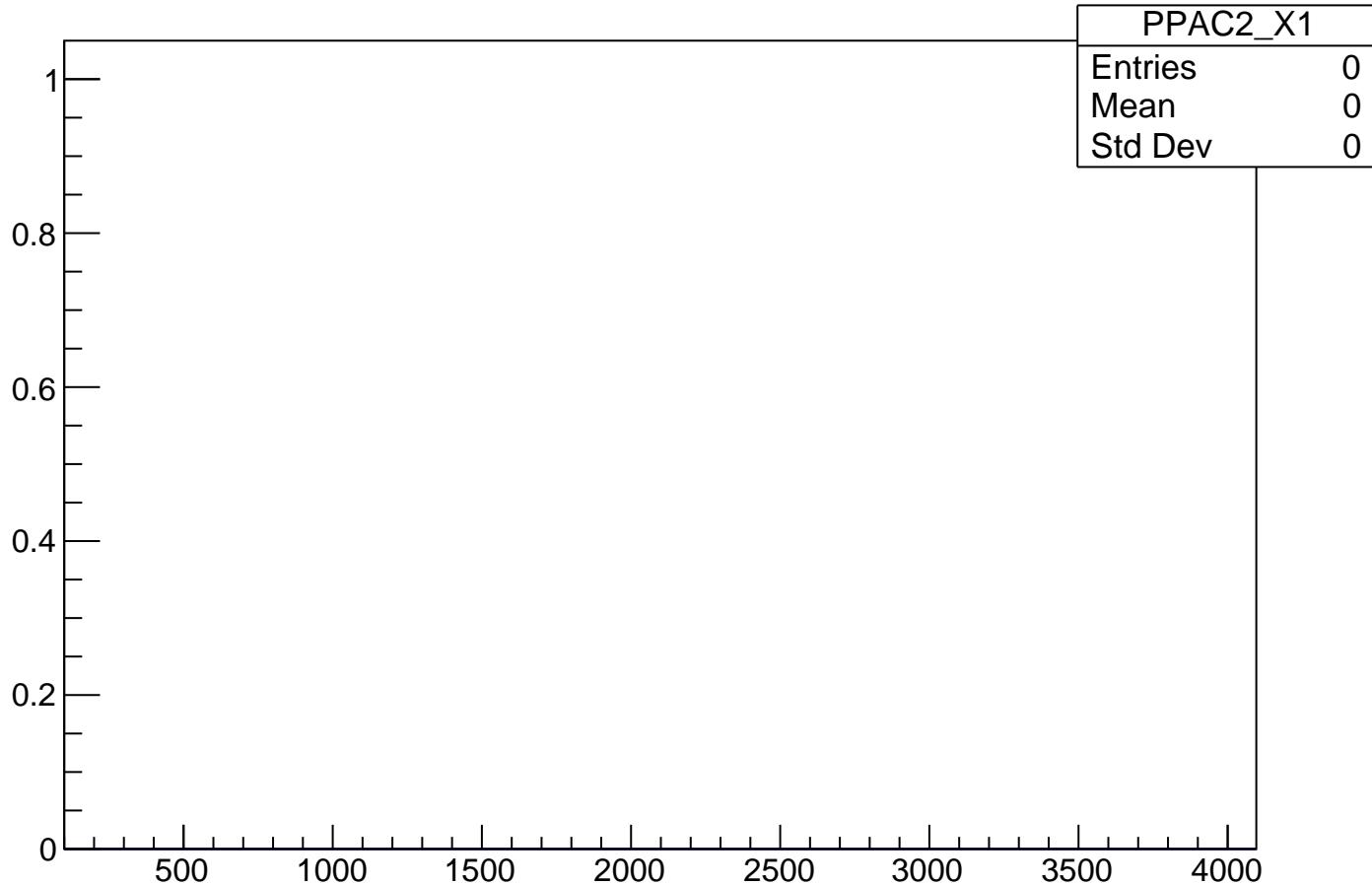
PPAC1_T_E



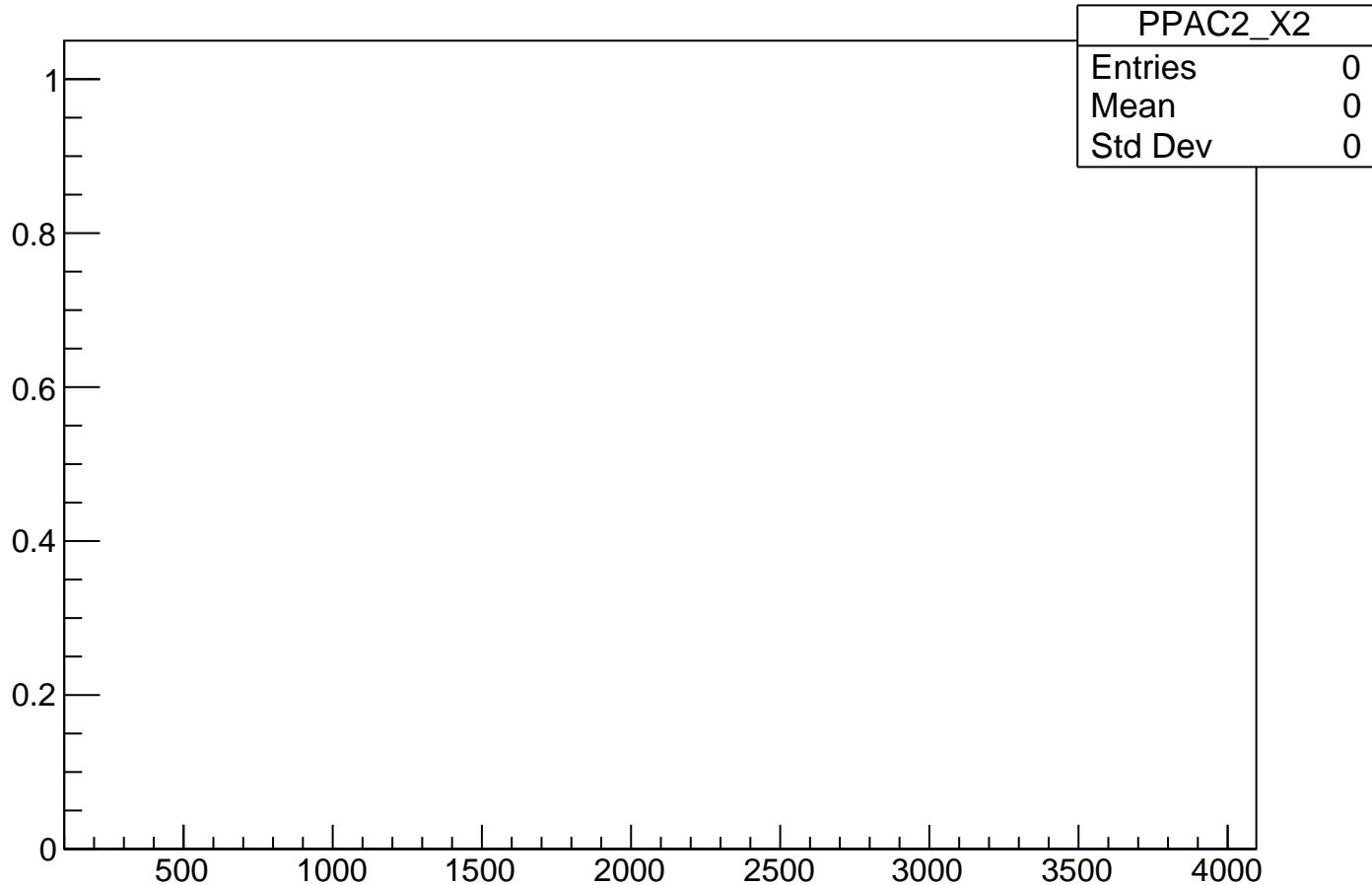
PPAC2_T



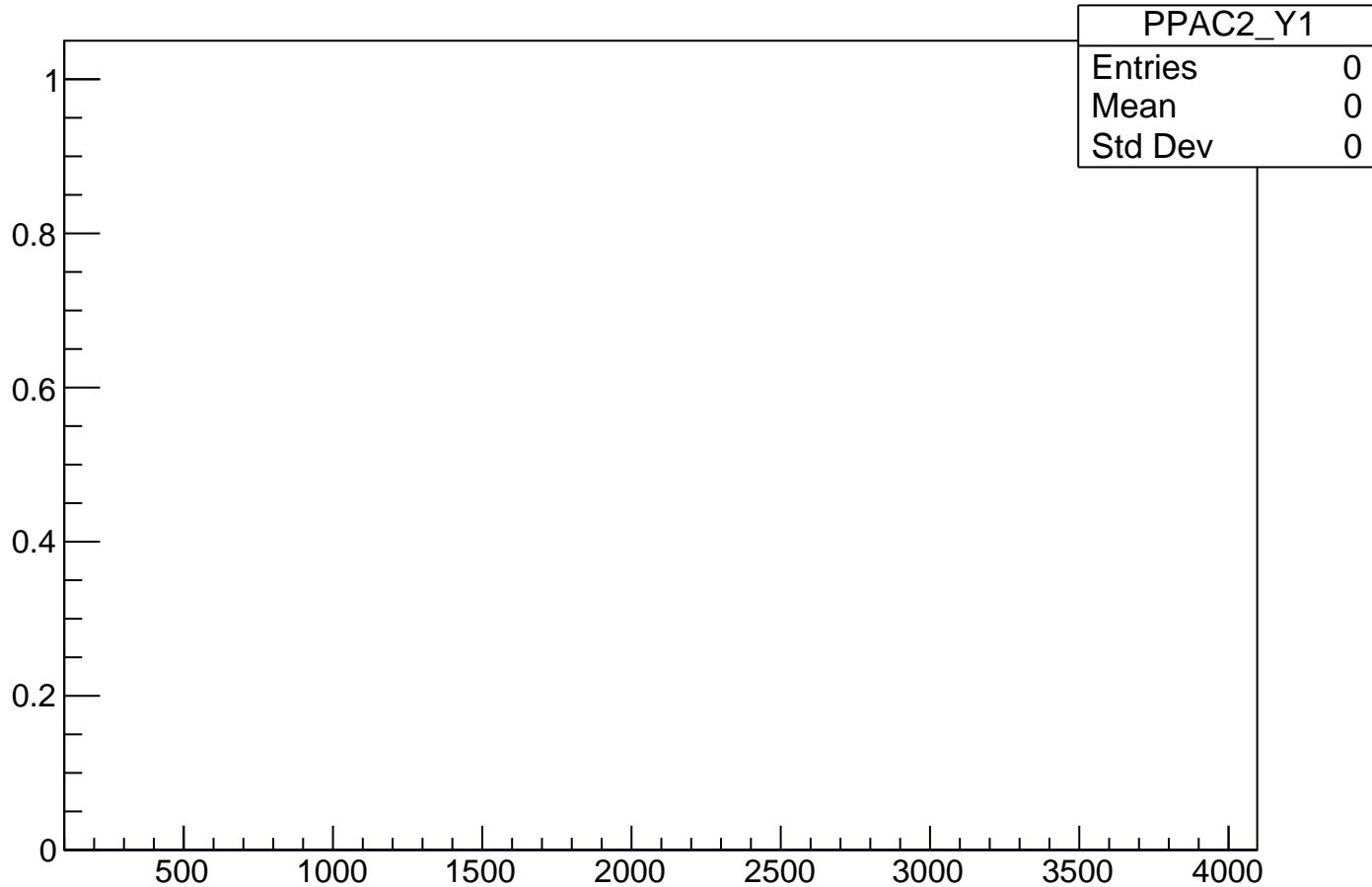
PPAC2_X1



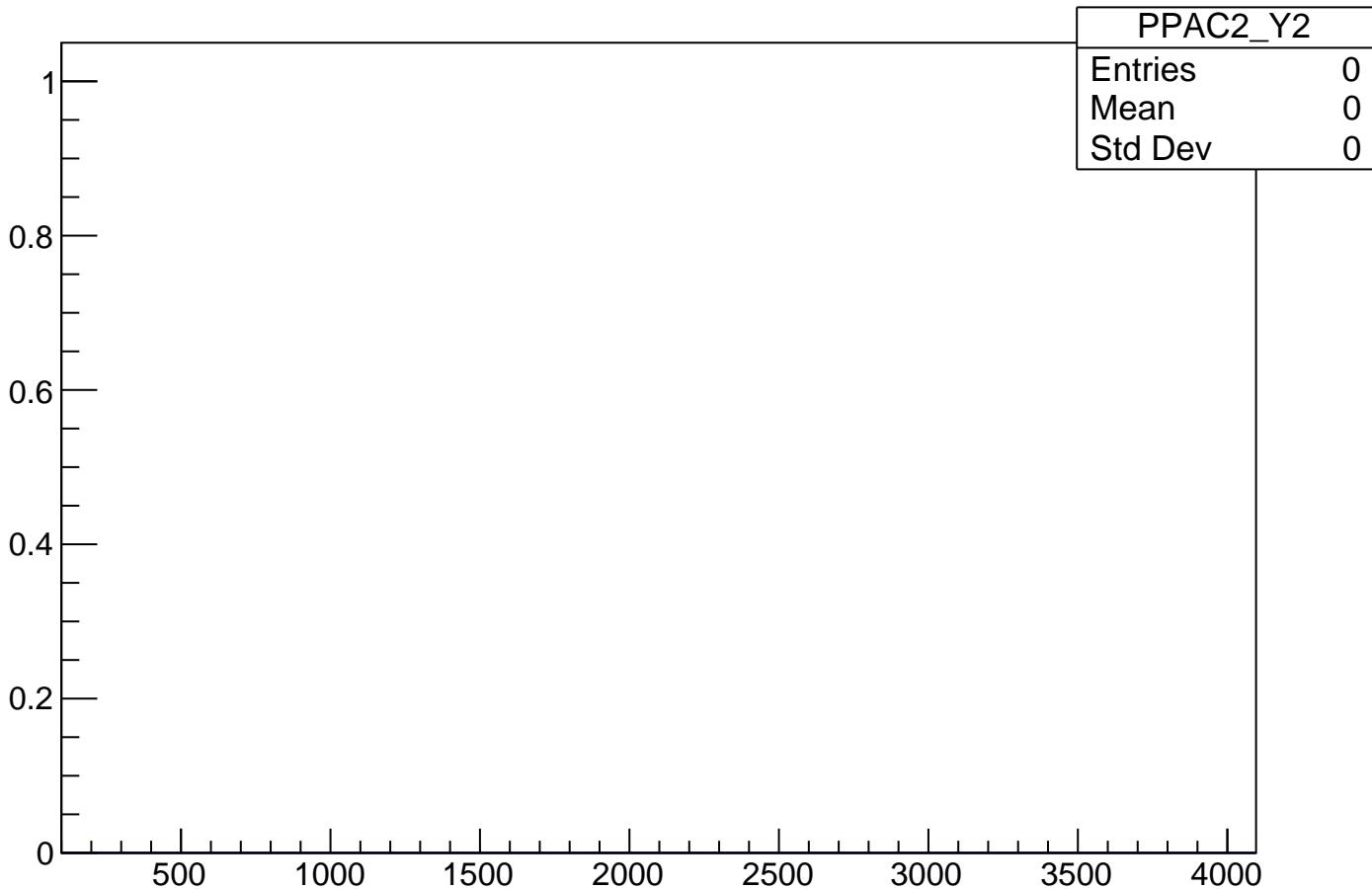
PPAC2_X2



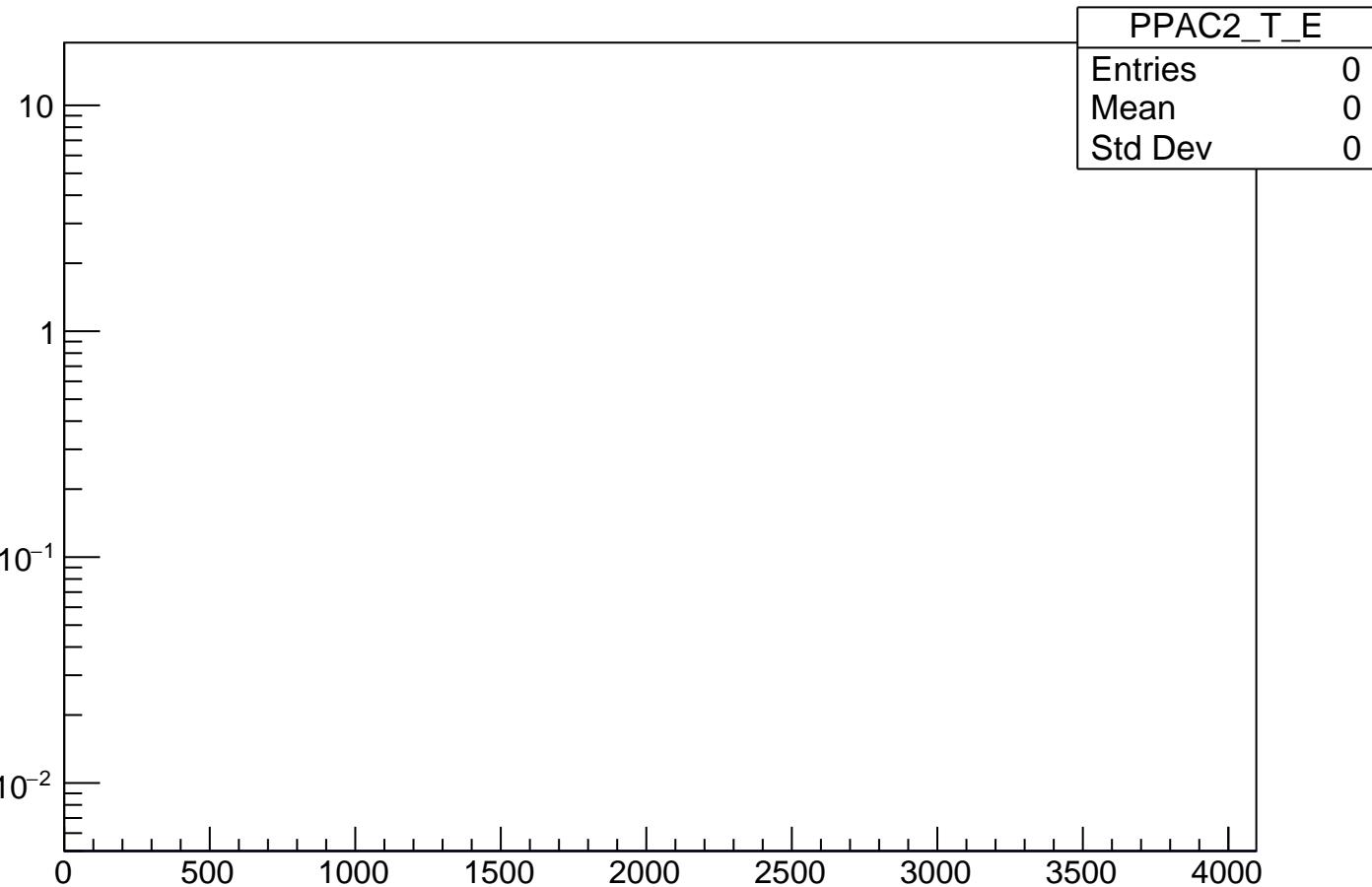
PPAC2_Y1



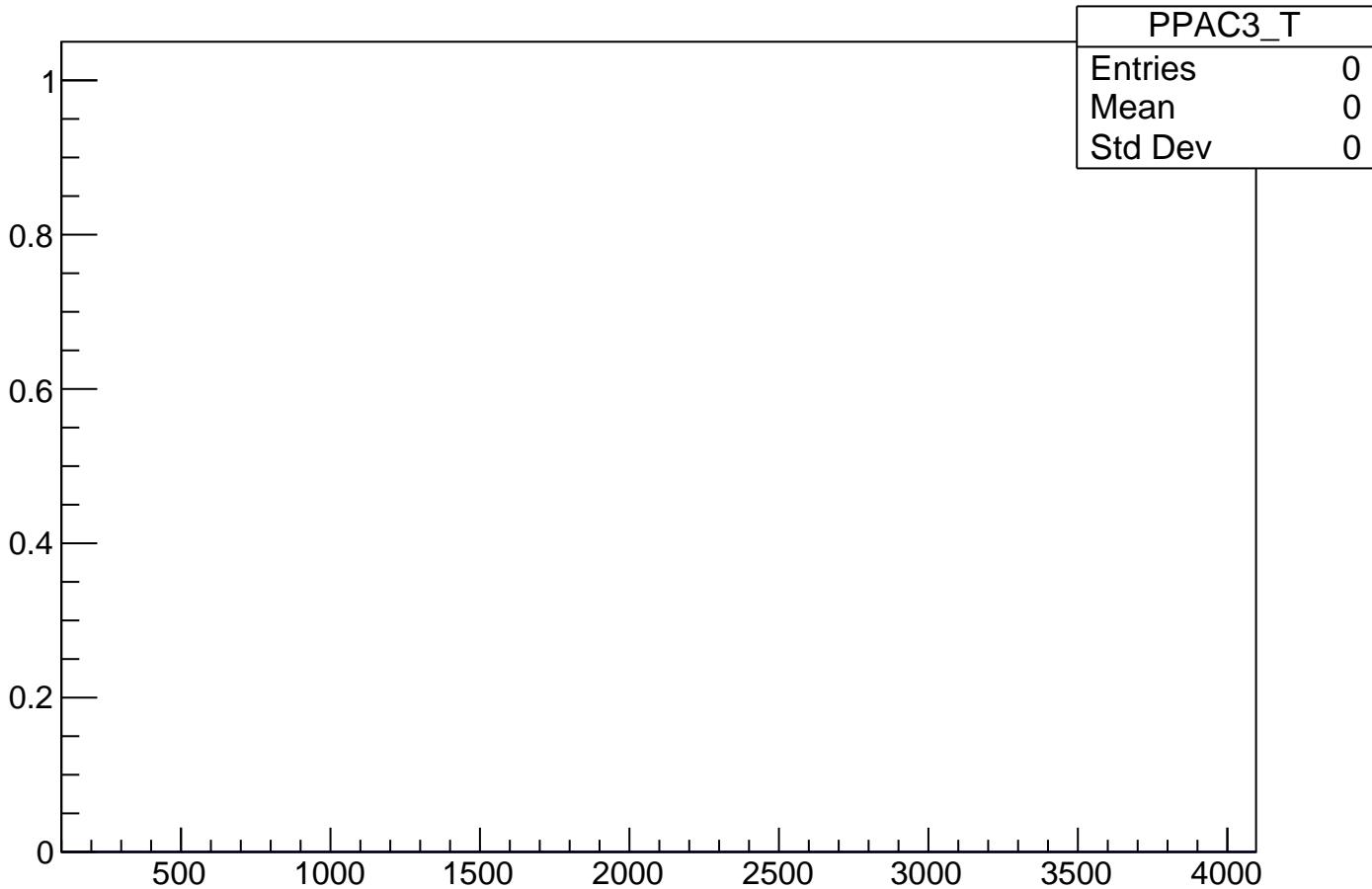
PPAC2_Y2



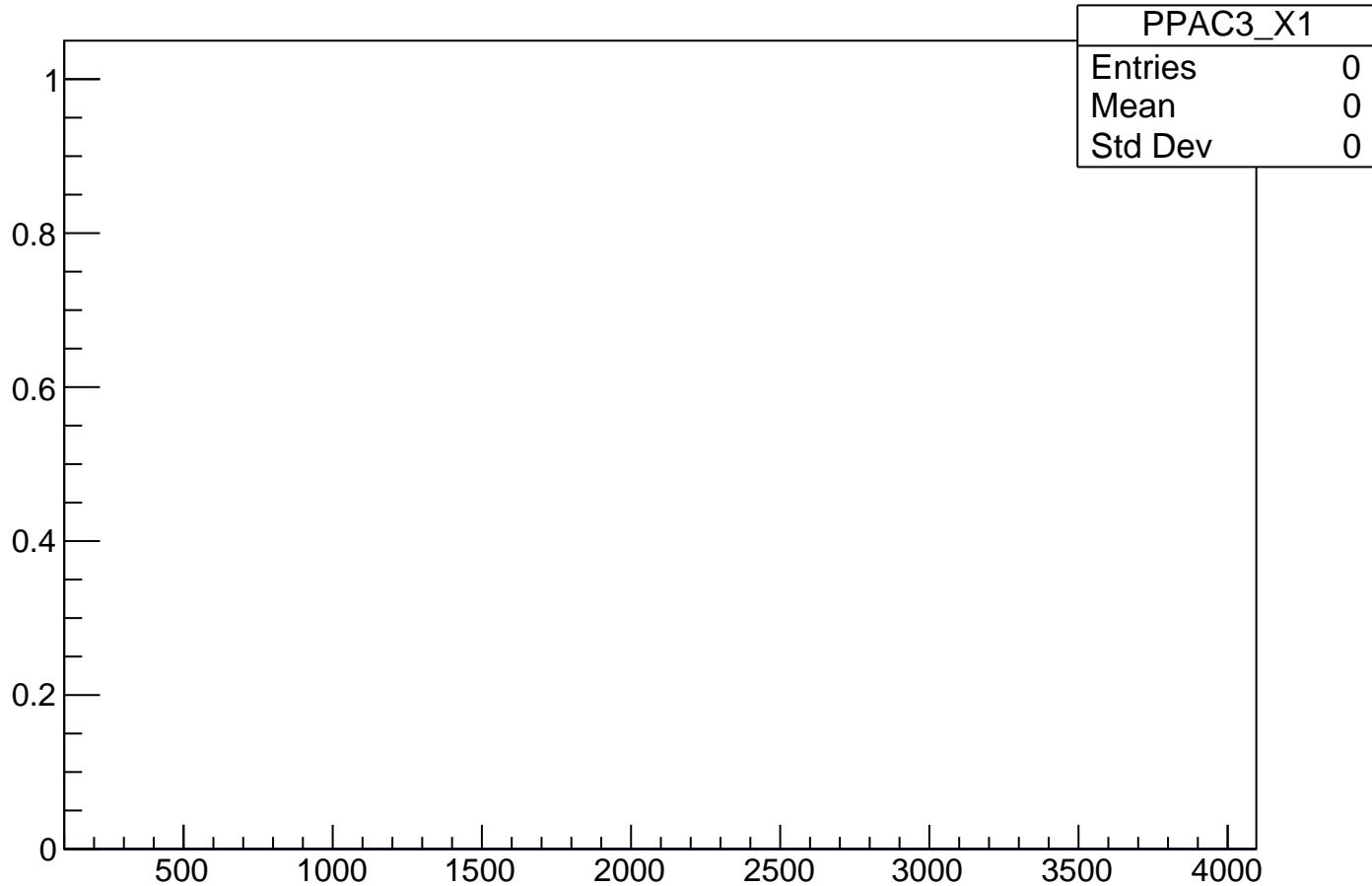
PPAC2_T_E



PPAC3_T

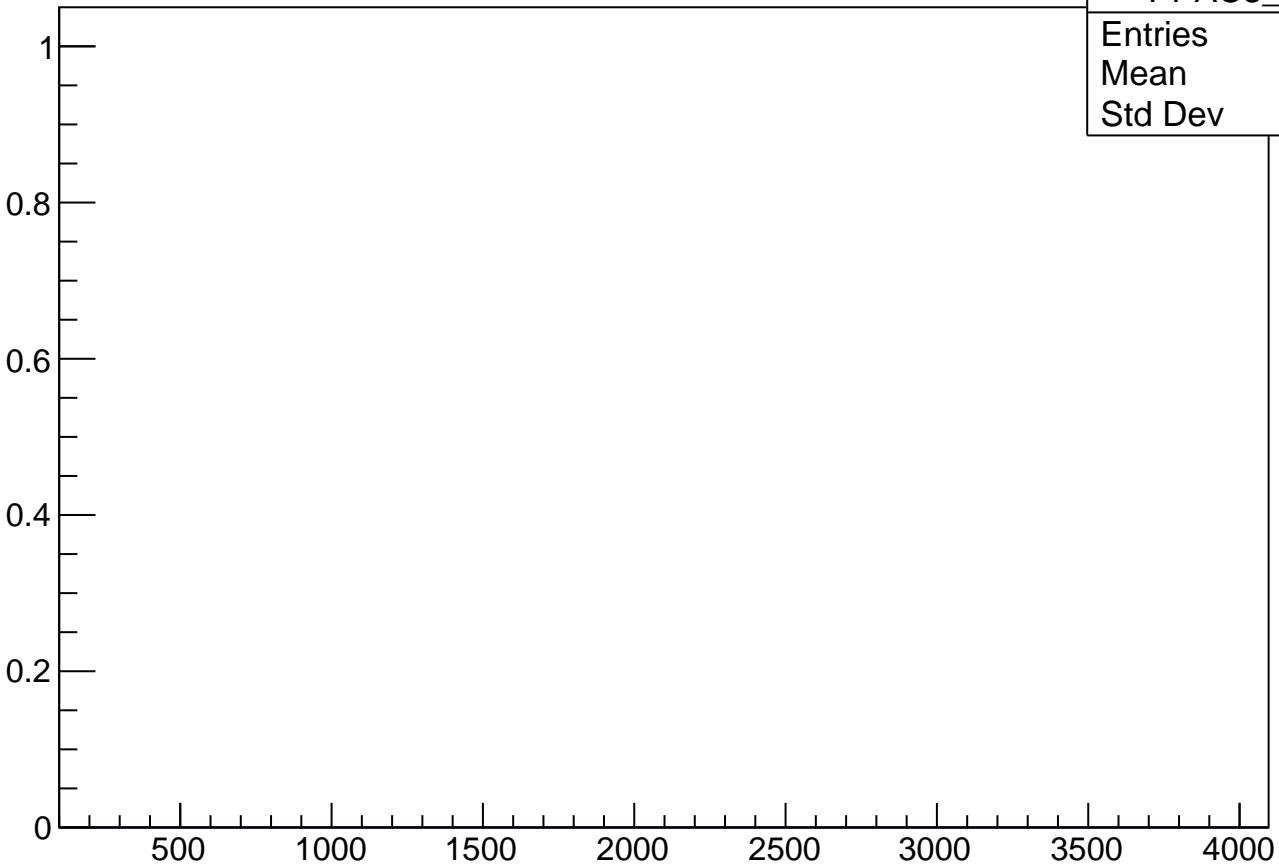


PPAC3_X1

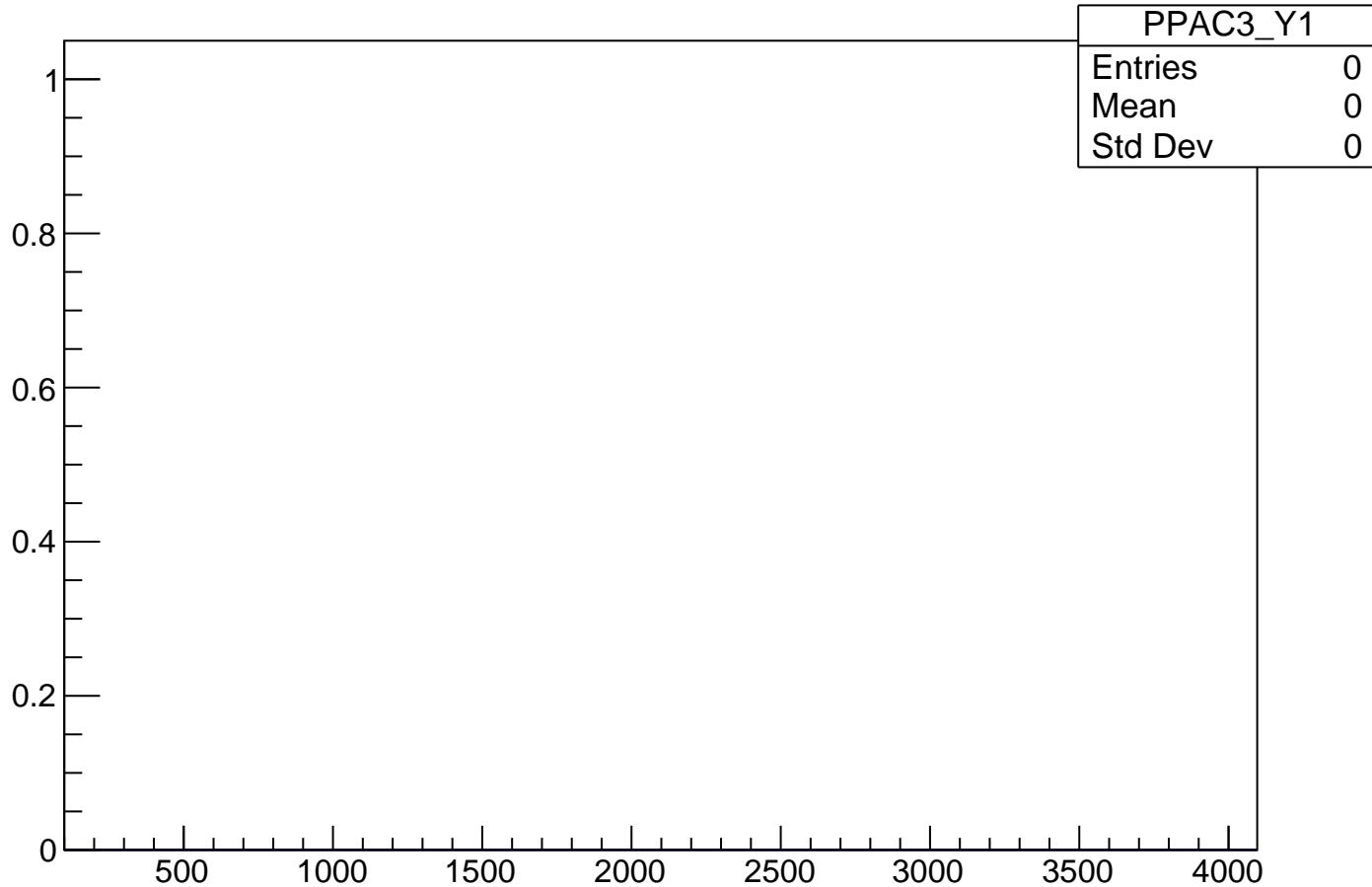


PPAC3_X2

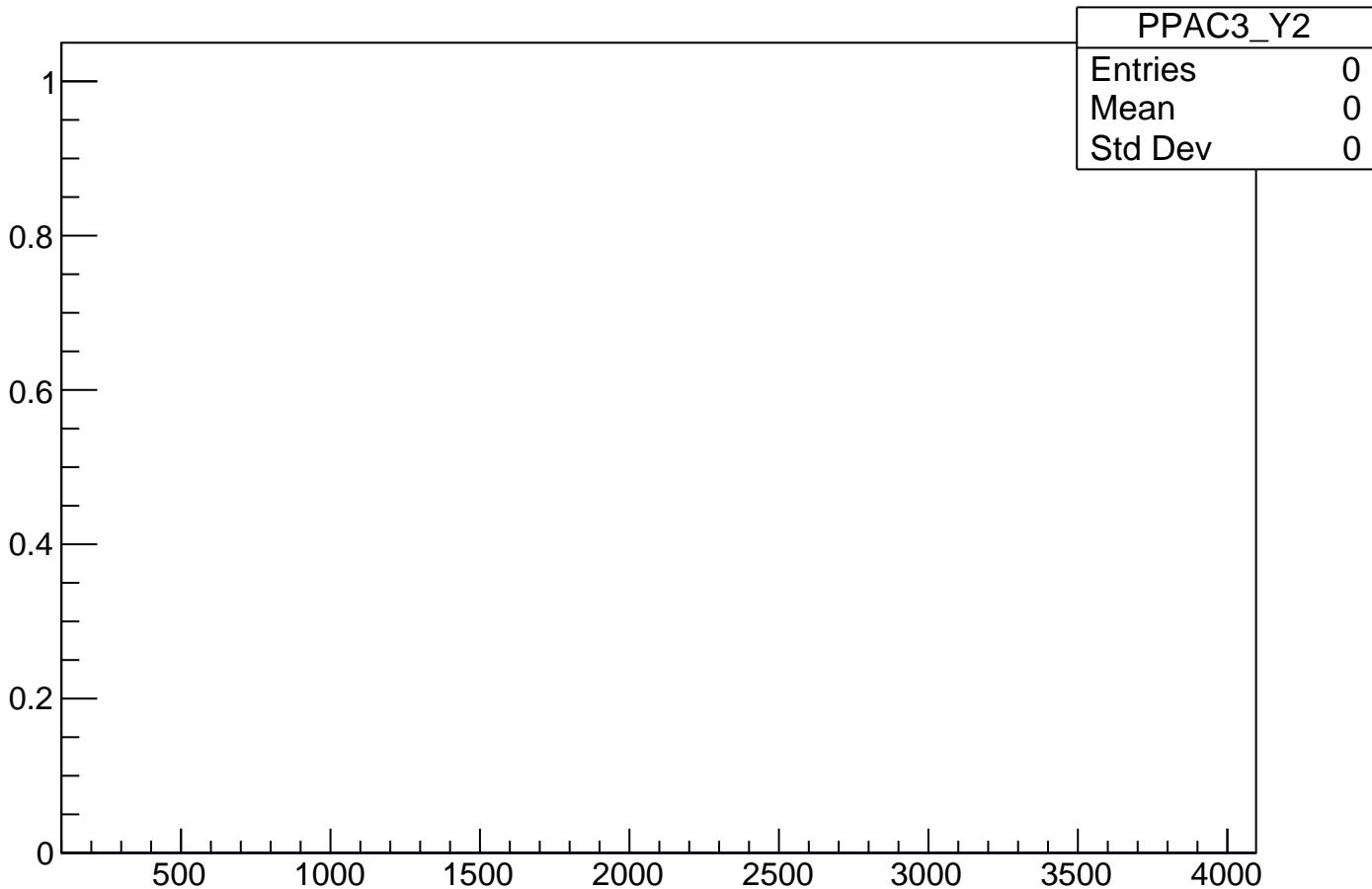
PPAC3_X2	
Entries	0
Mean	0
Std Dev	0



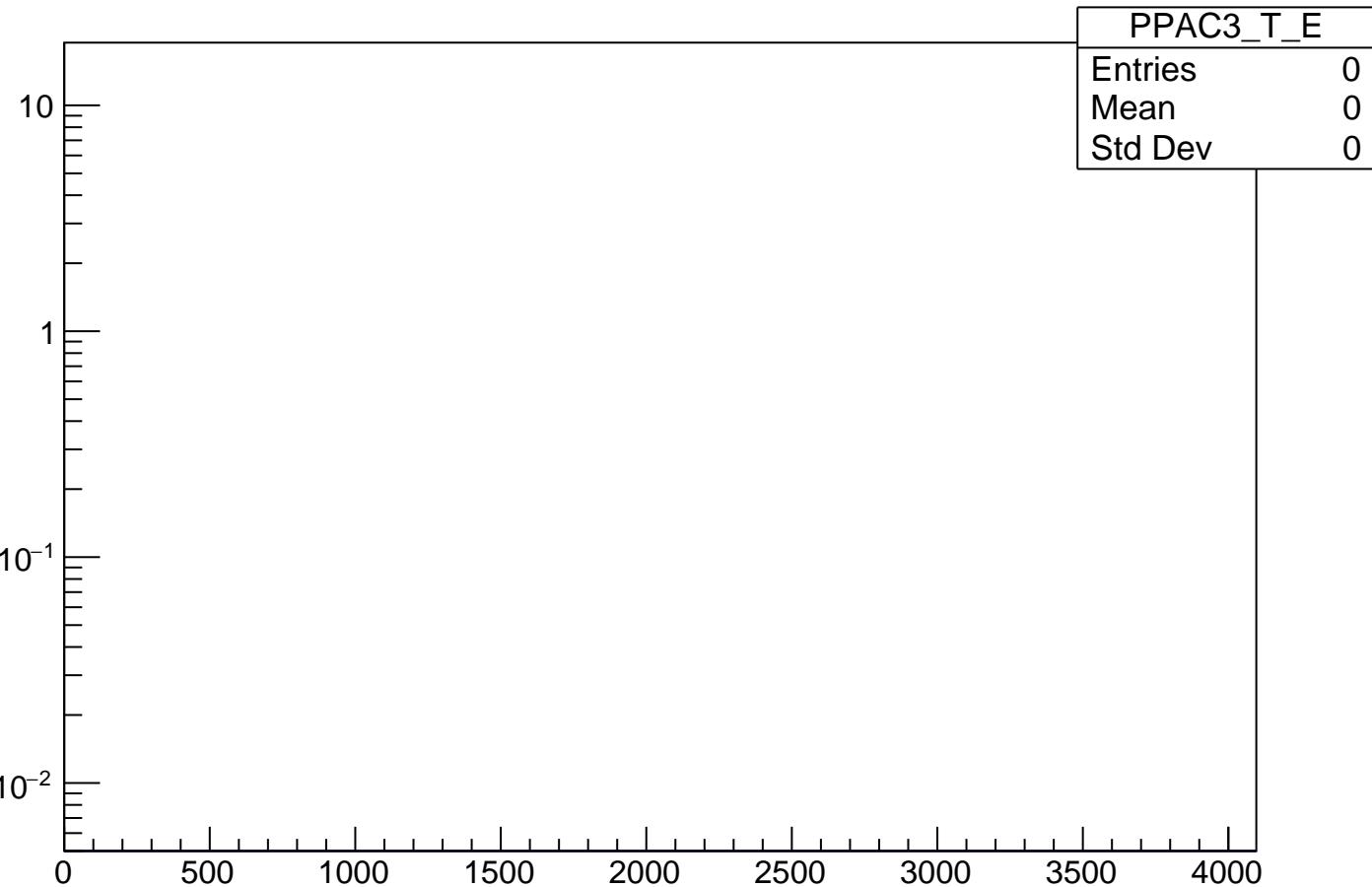
PPAC3_Y1



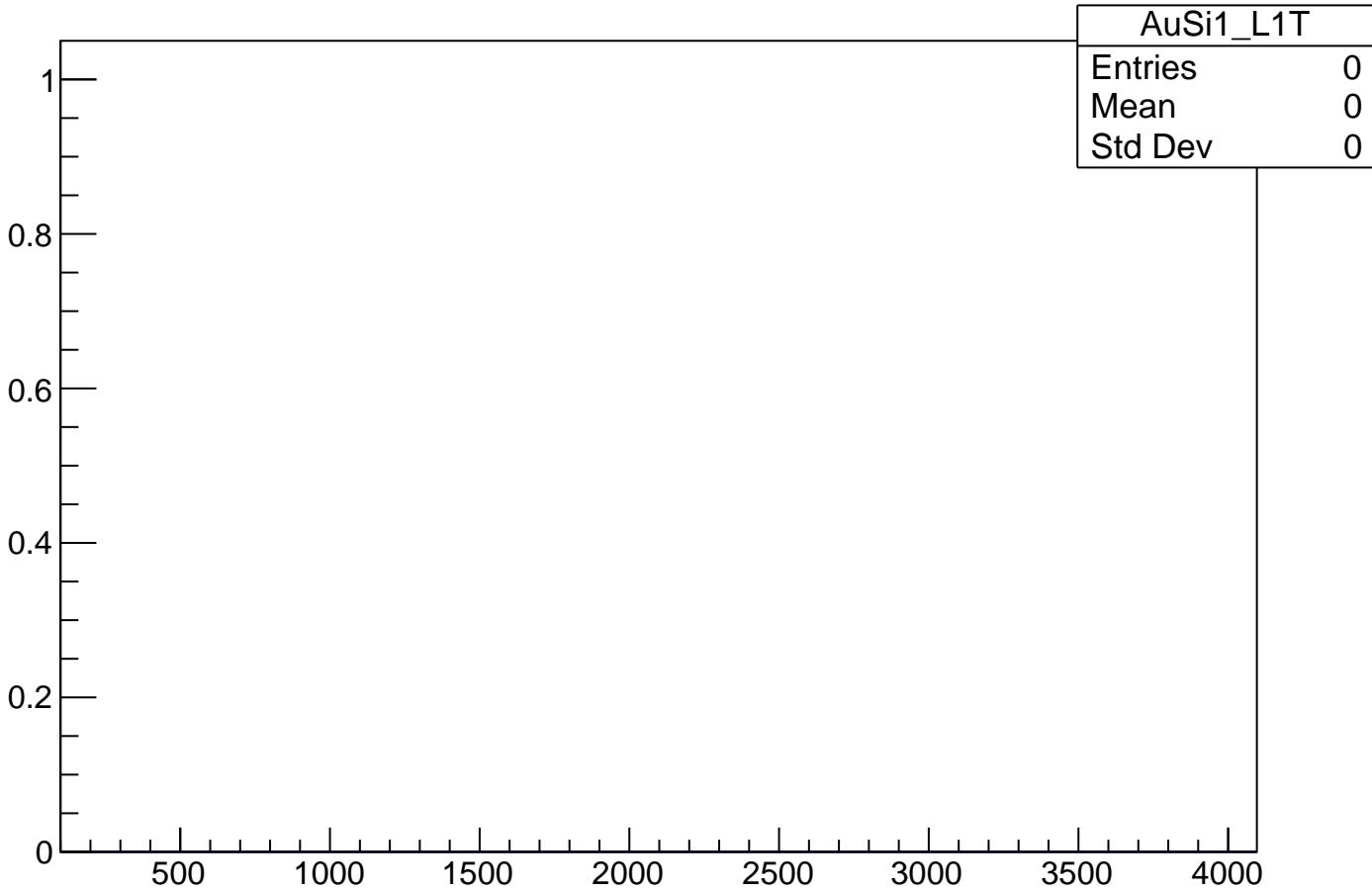
PPAC3_Y2



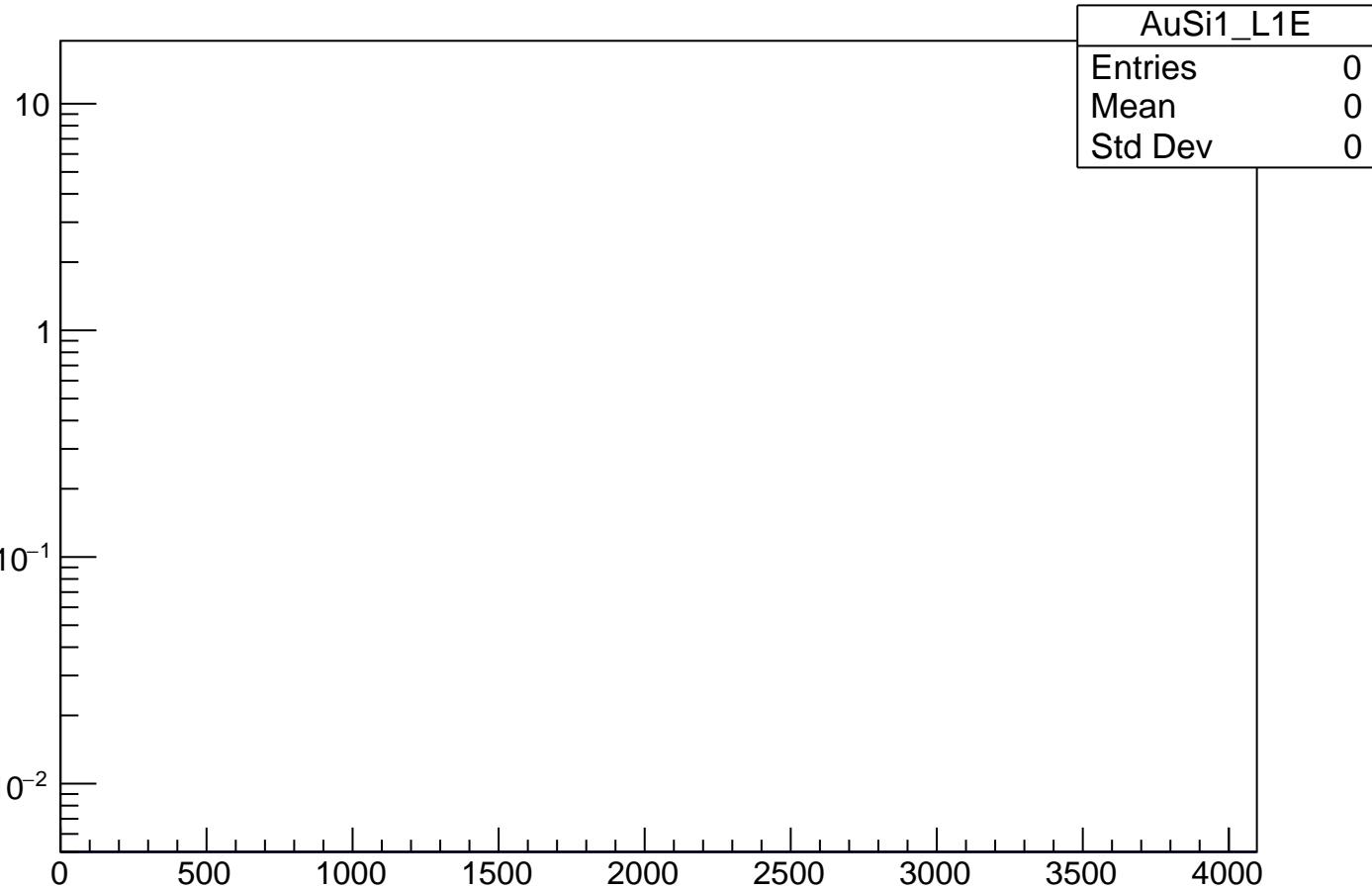
PPAC3_T_E



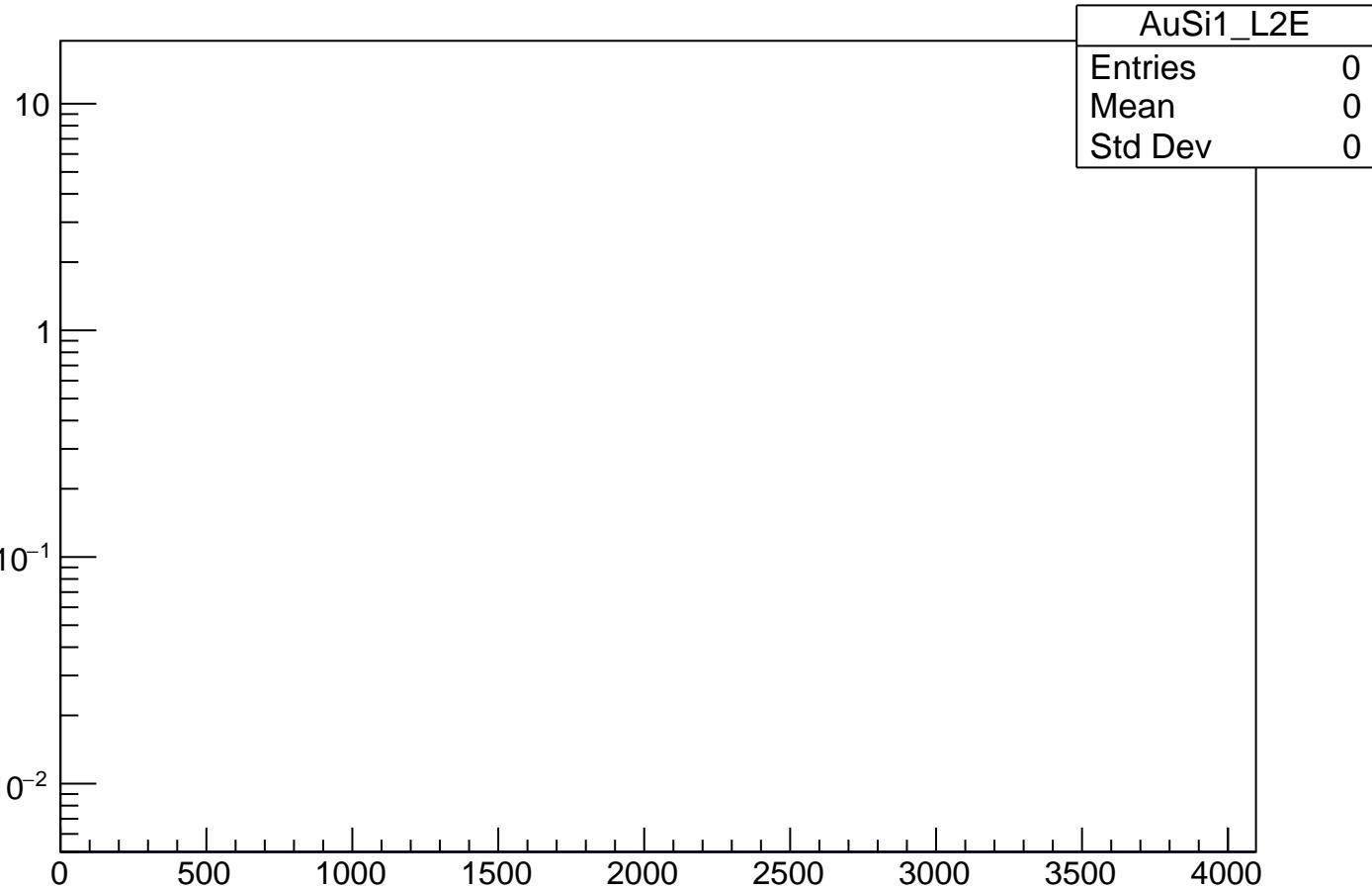
AuSi1_L1T



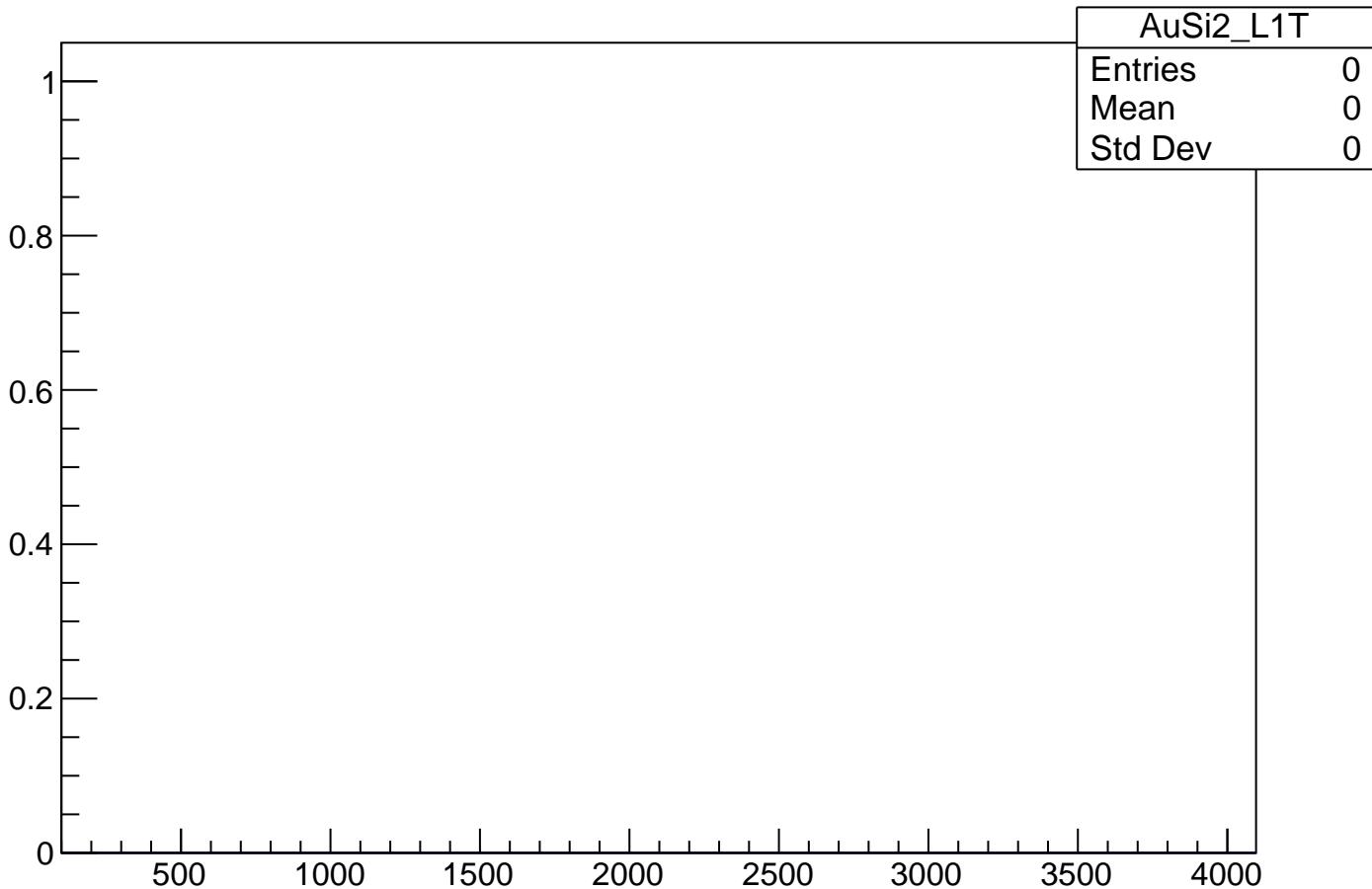
AuSi1_L1E



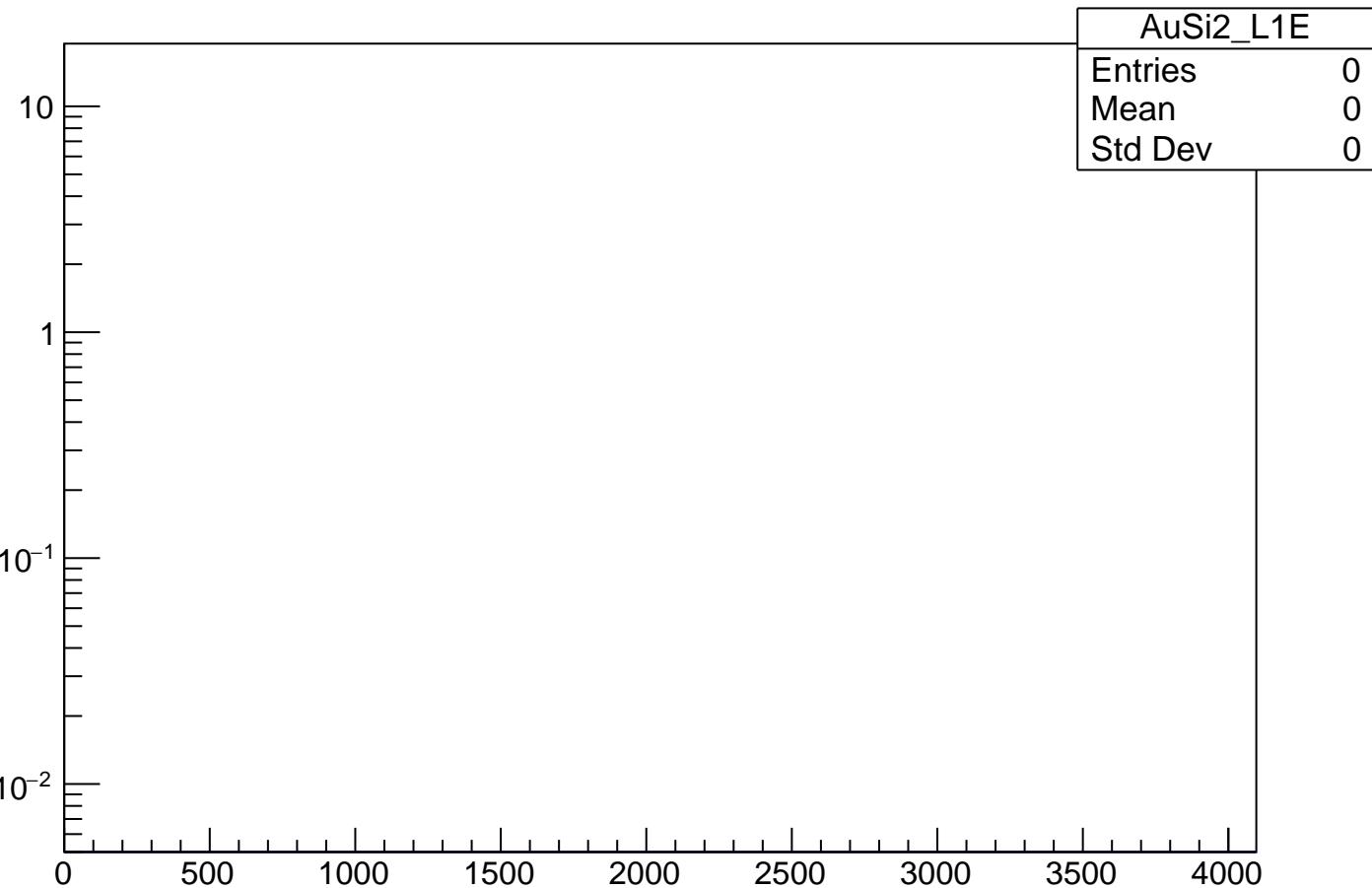
AuSi1_L2E



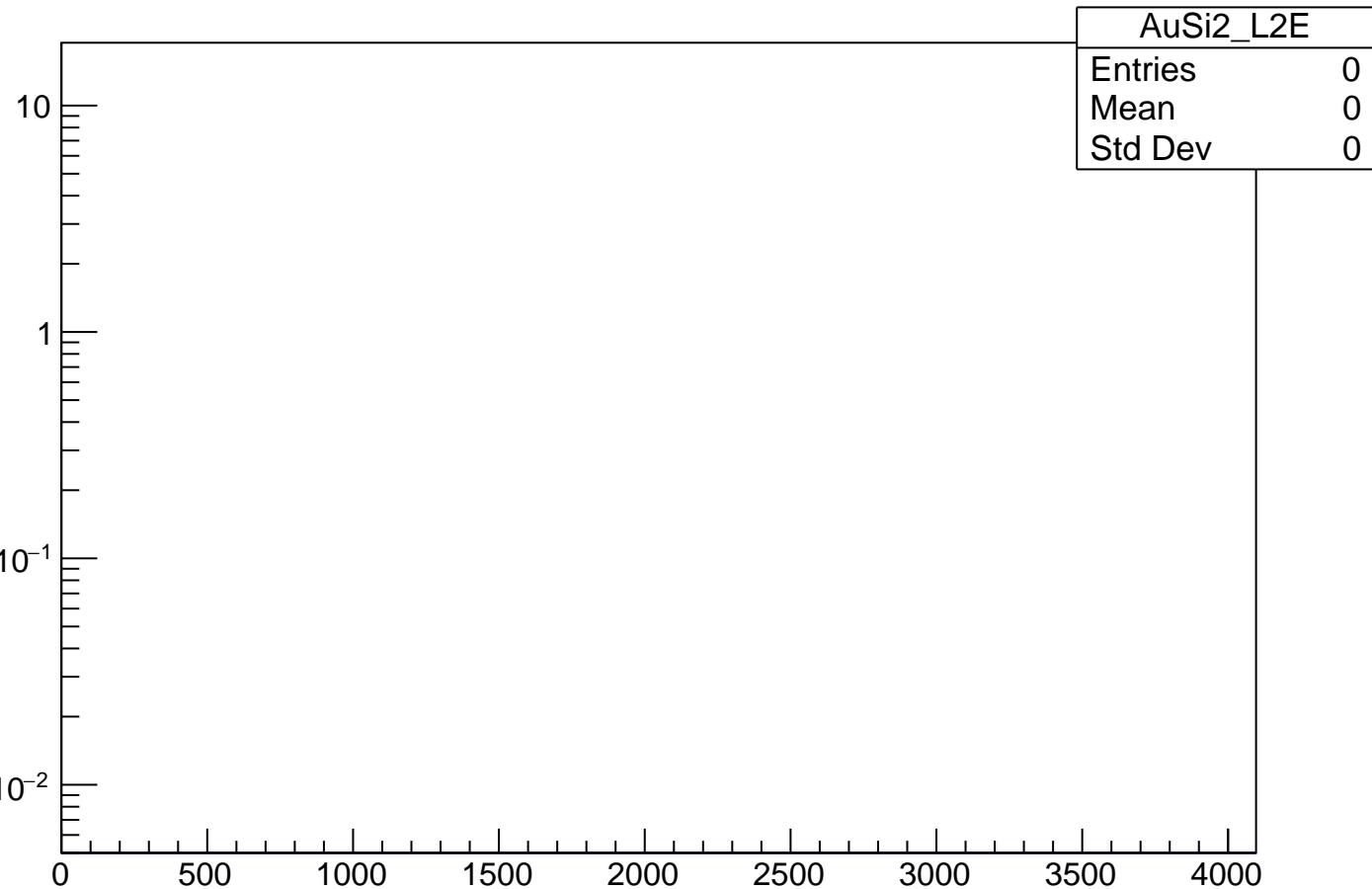
AuSi2_L1T



AuSi2_L1E

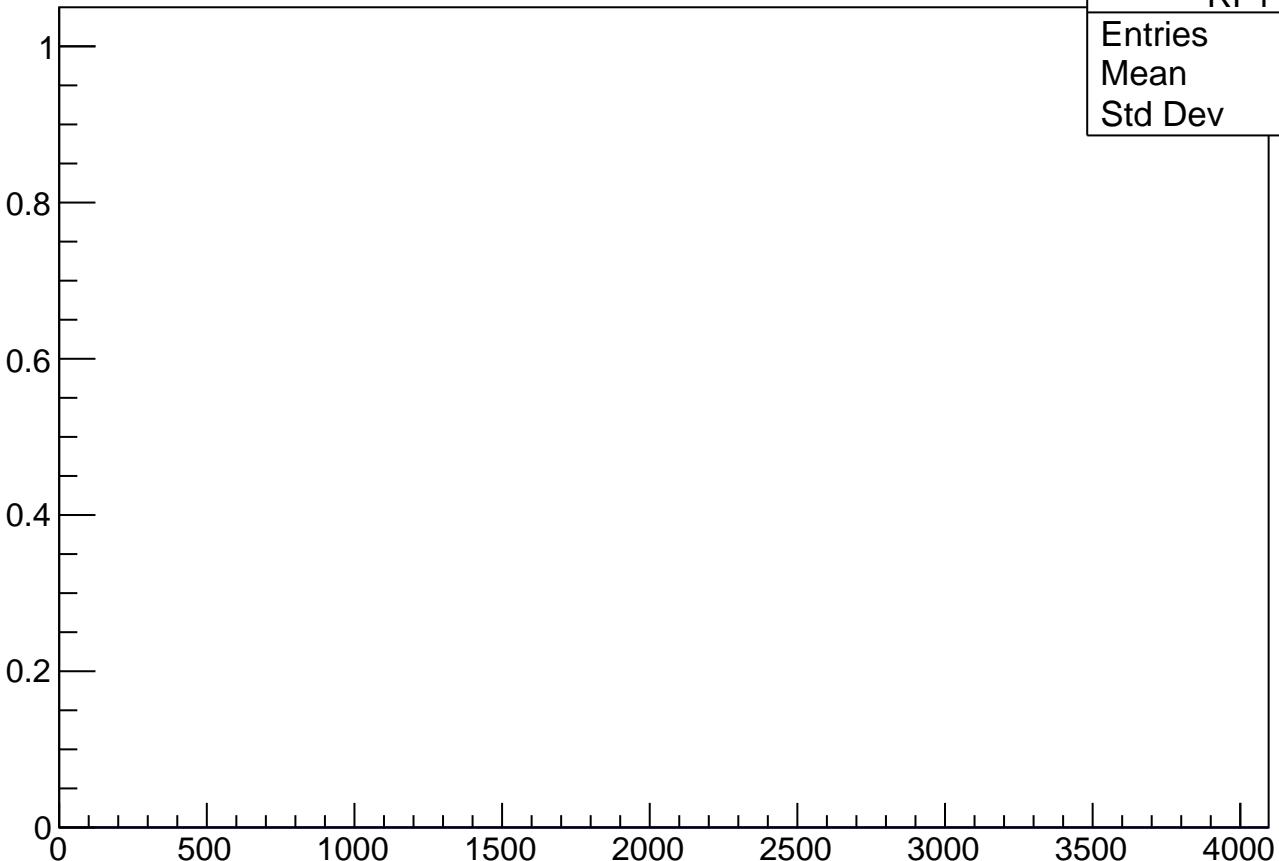


AuSi2_L2E

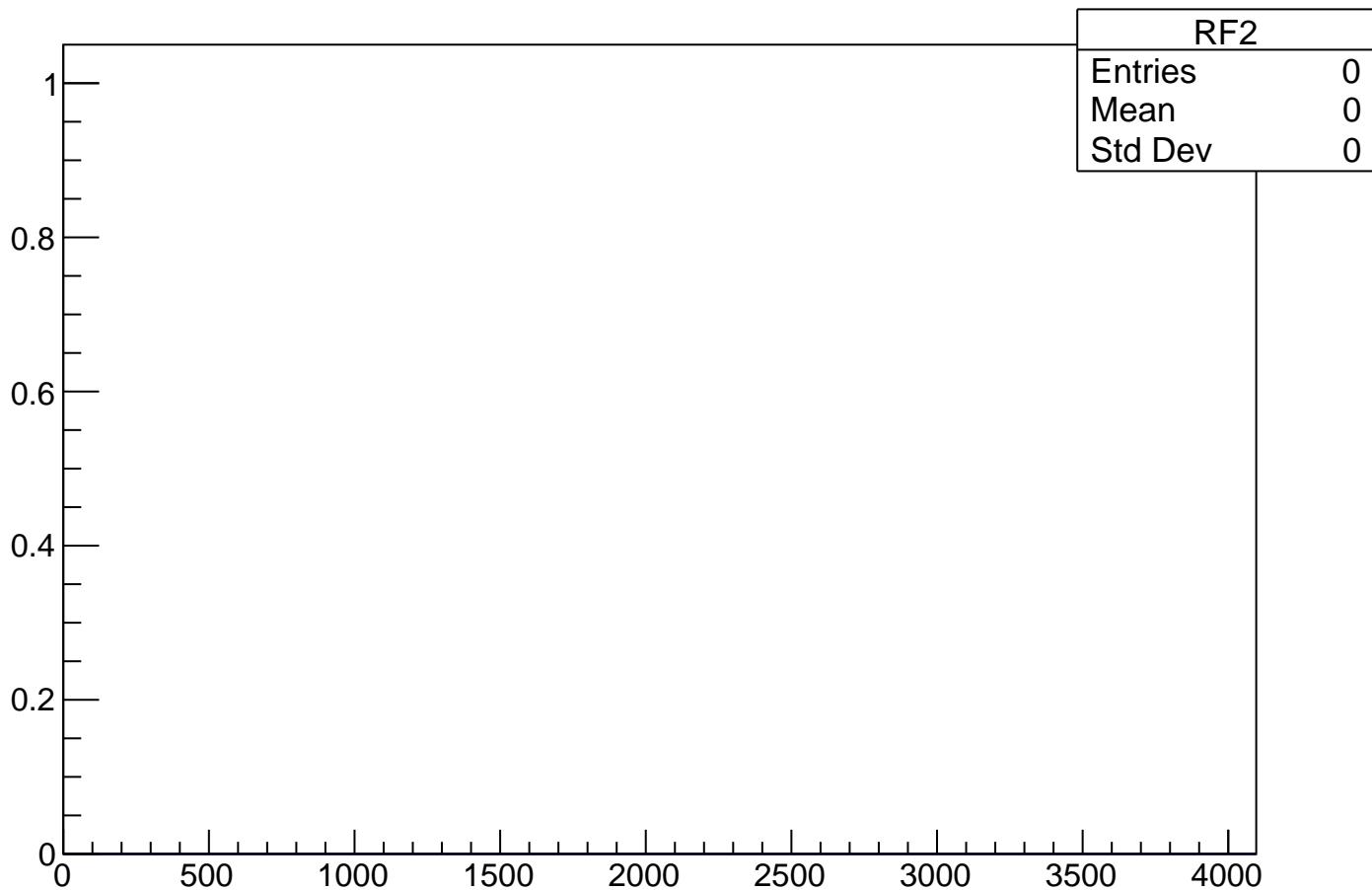


RF1

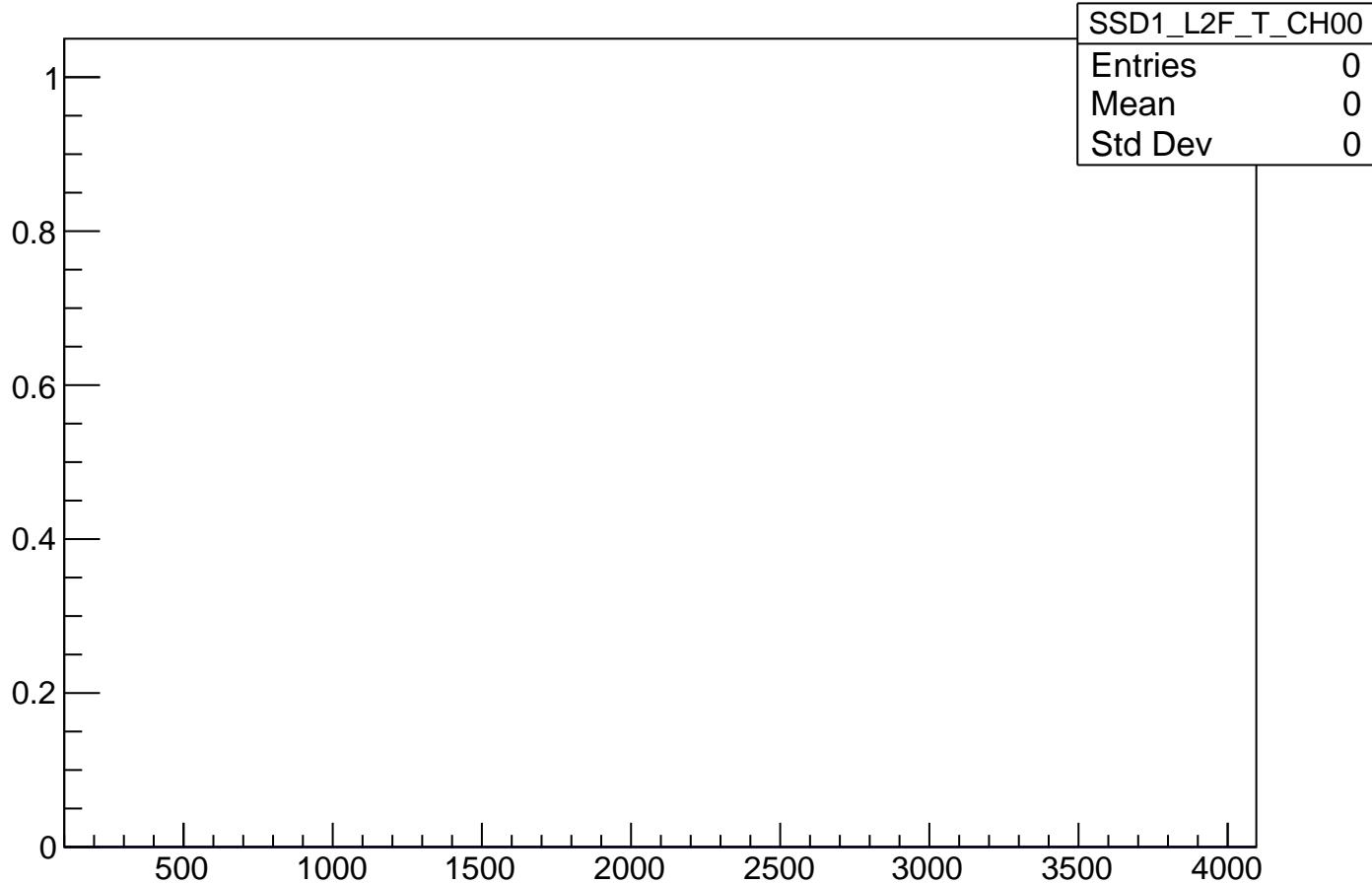
RF1	
Entries	0
Mean	0
Std Dev	0



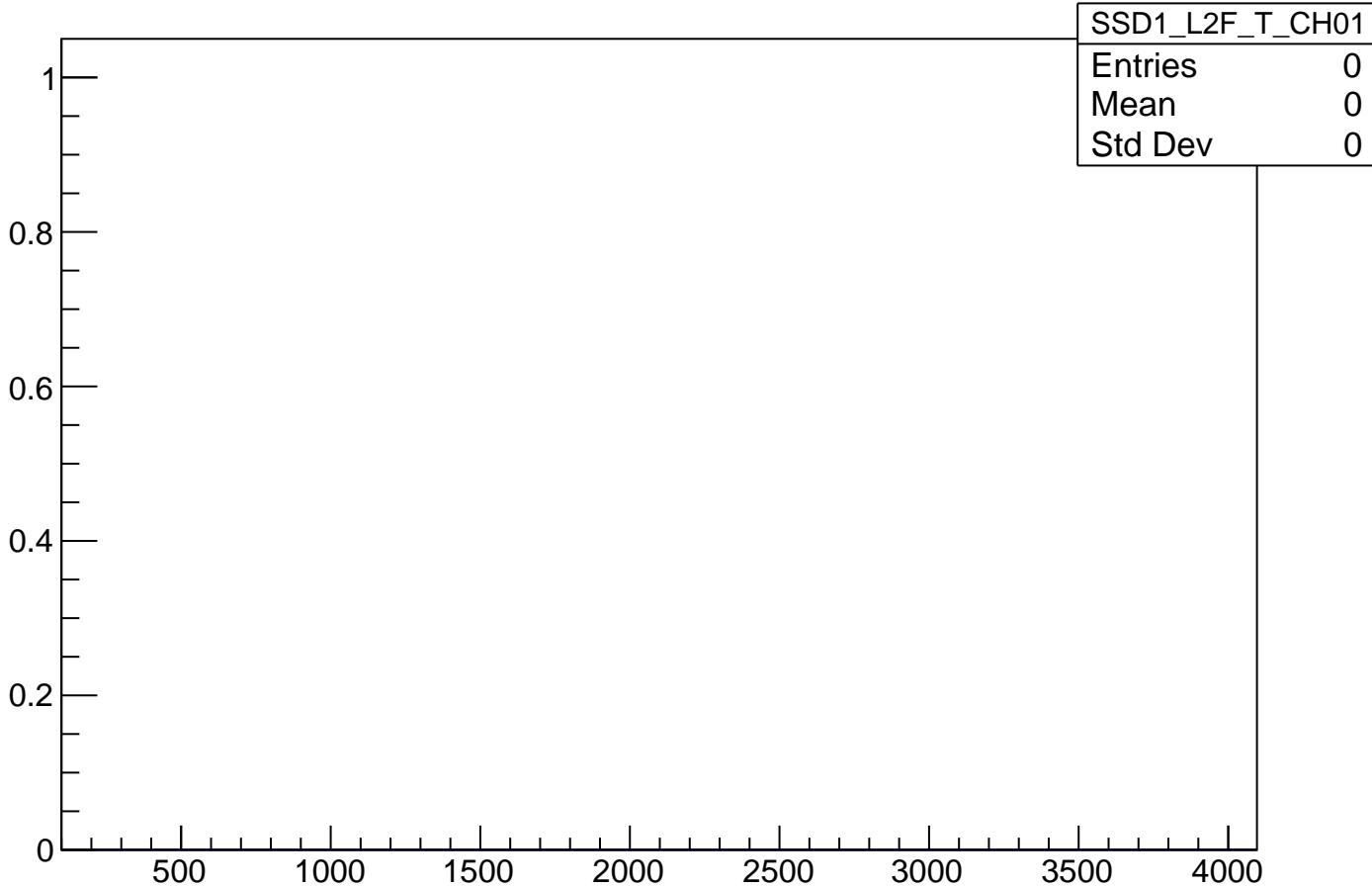
RF2



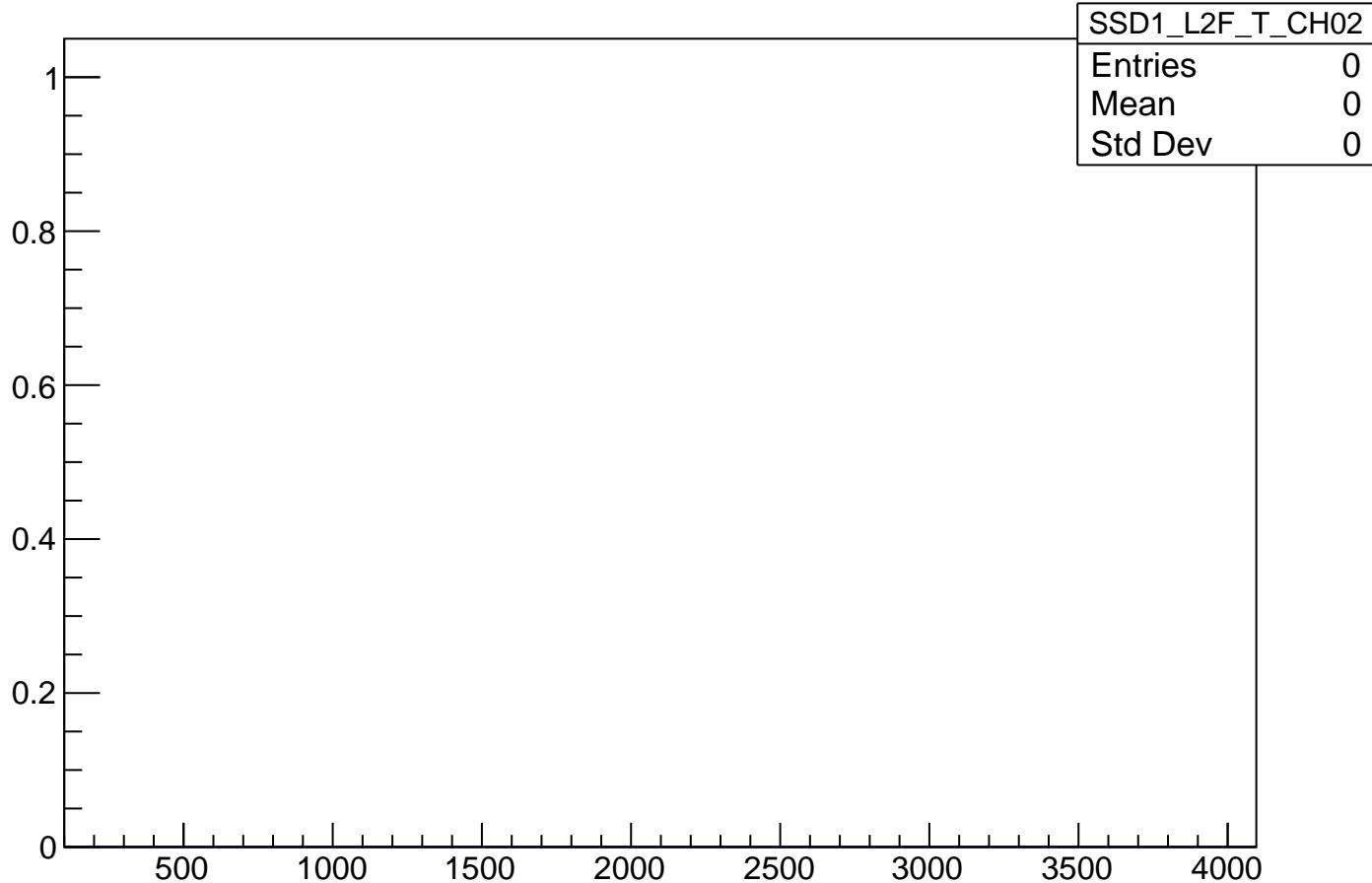
SSD1_L2F_T_CH00



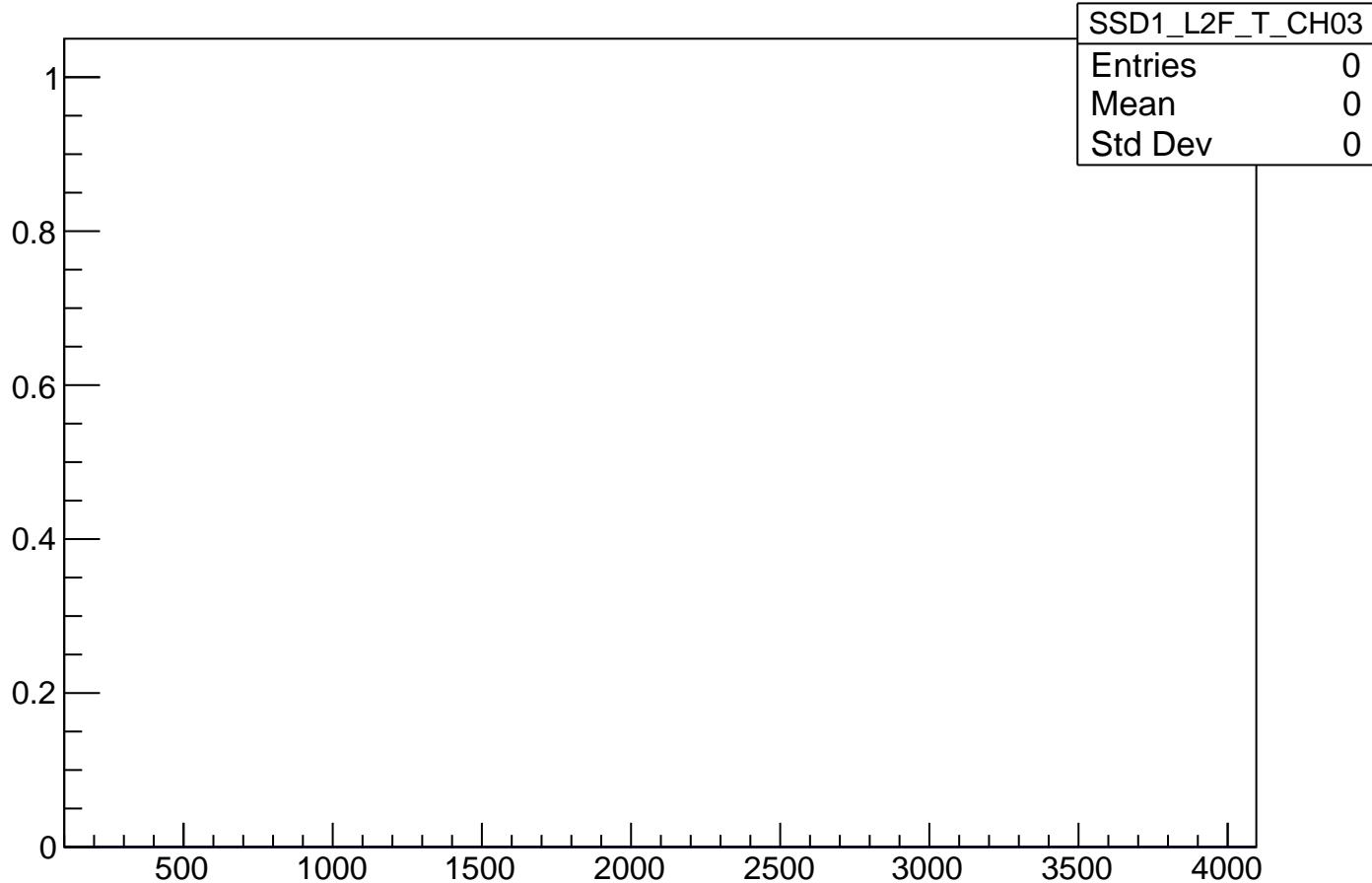
SSD1_L2F_T_CH01



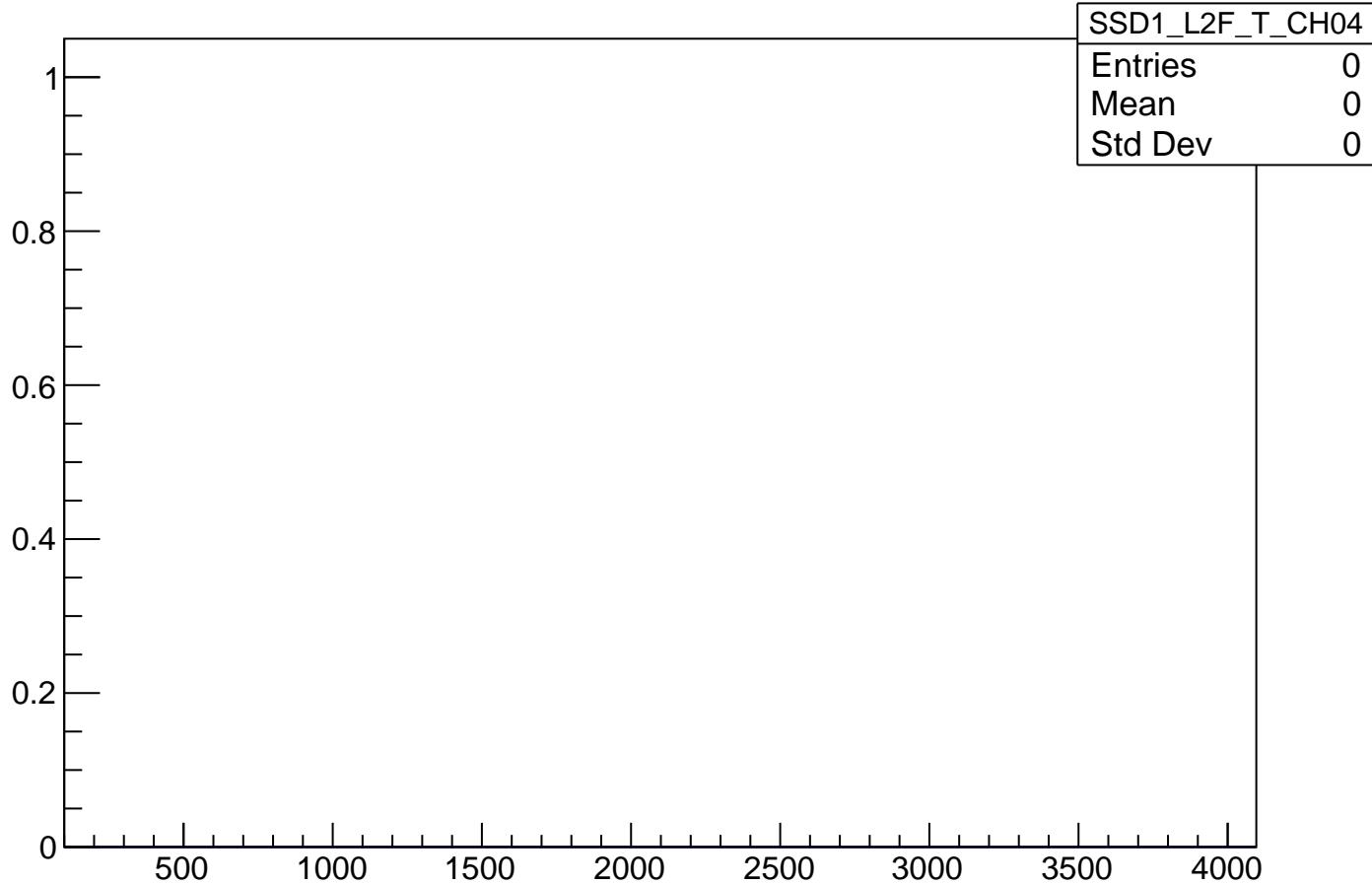
SSD1_L2F_T_CH02



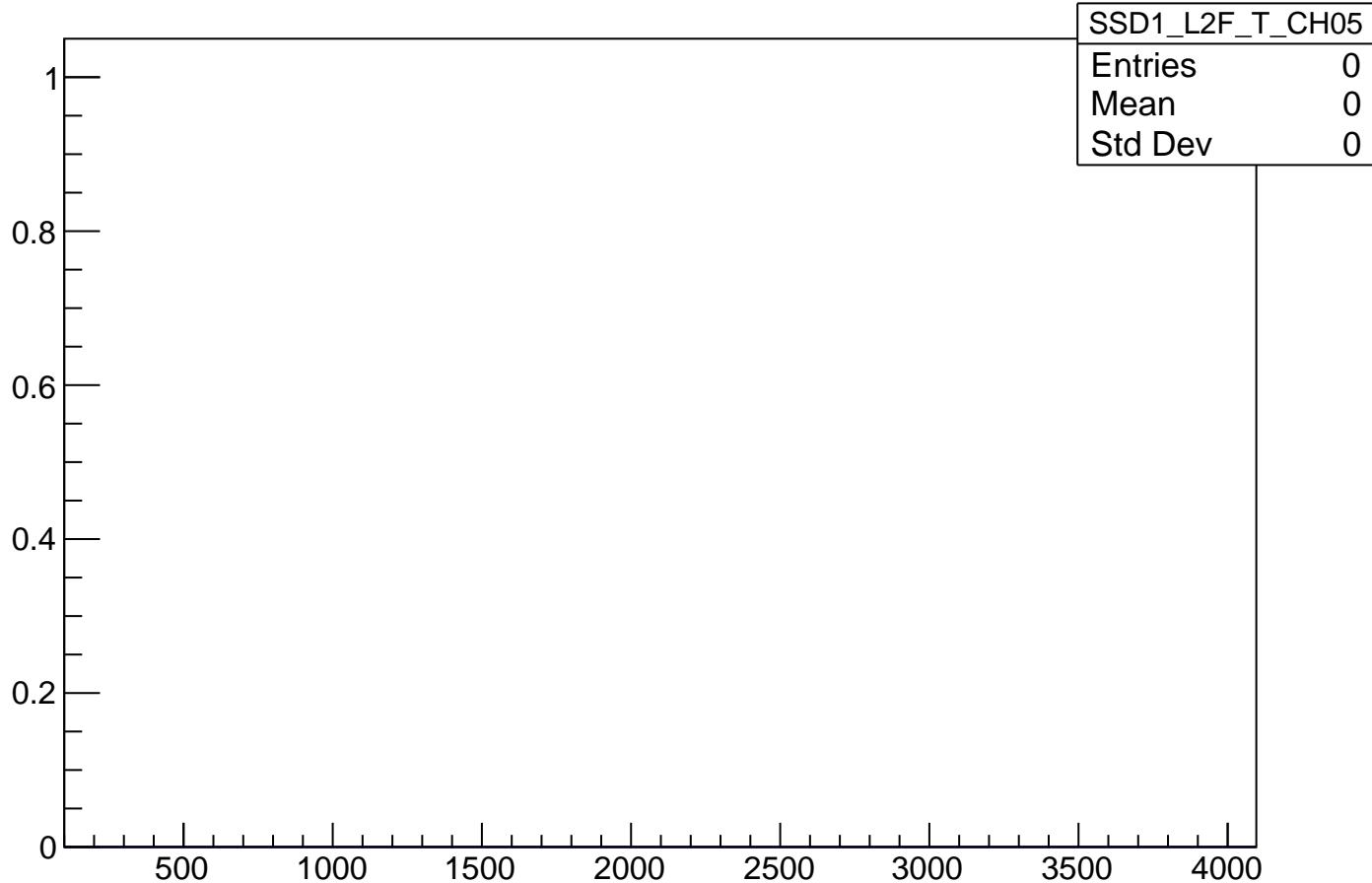
SSD1_L2F_T_CH03



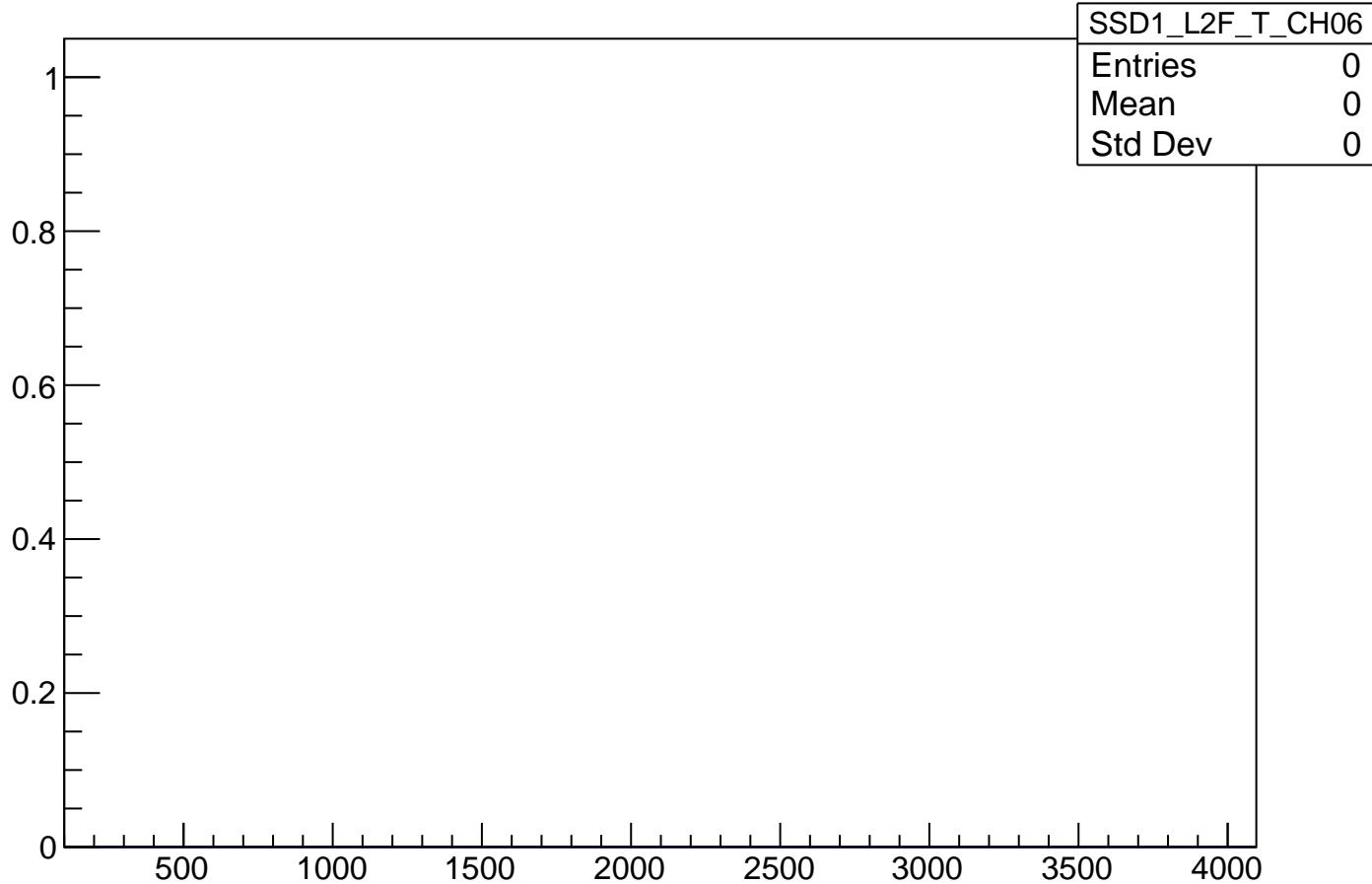
SSD1_L2F_T_CH04



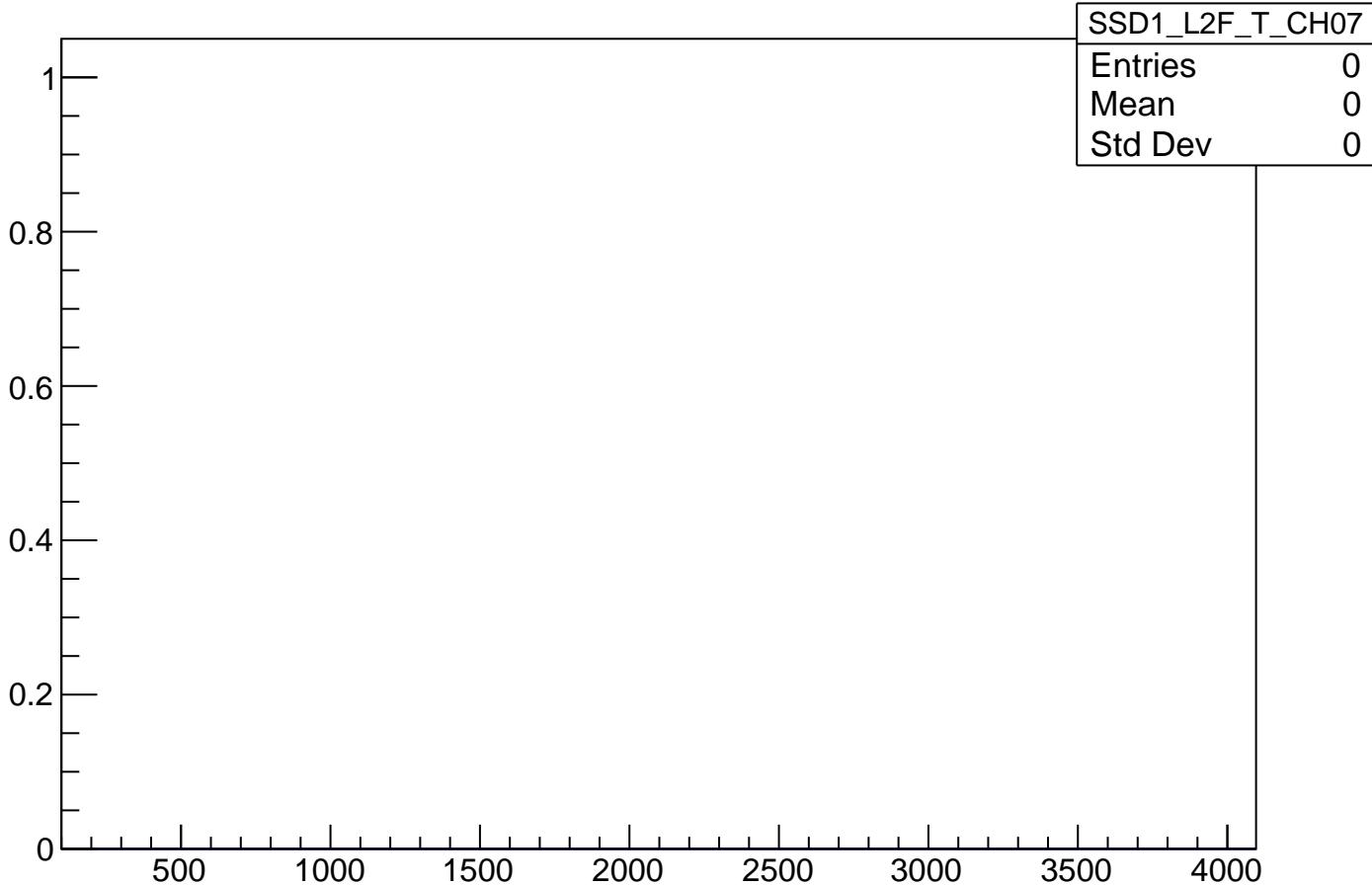
SSD1_L2F_T_CH05



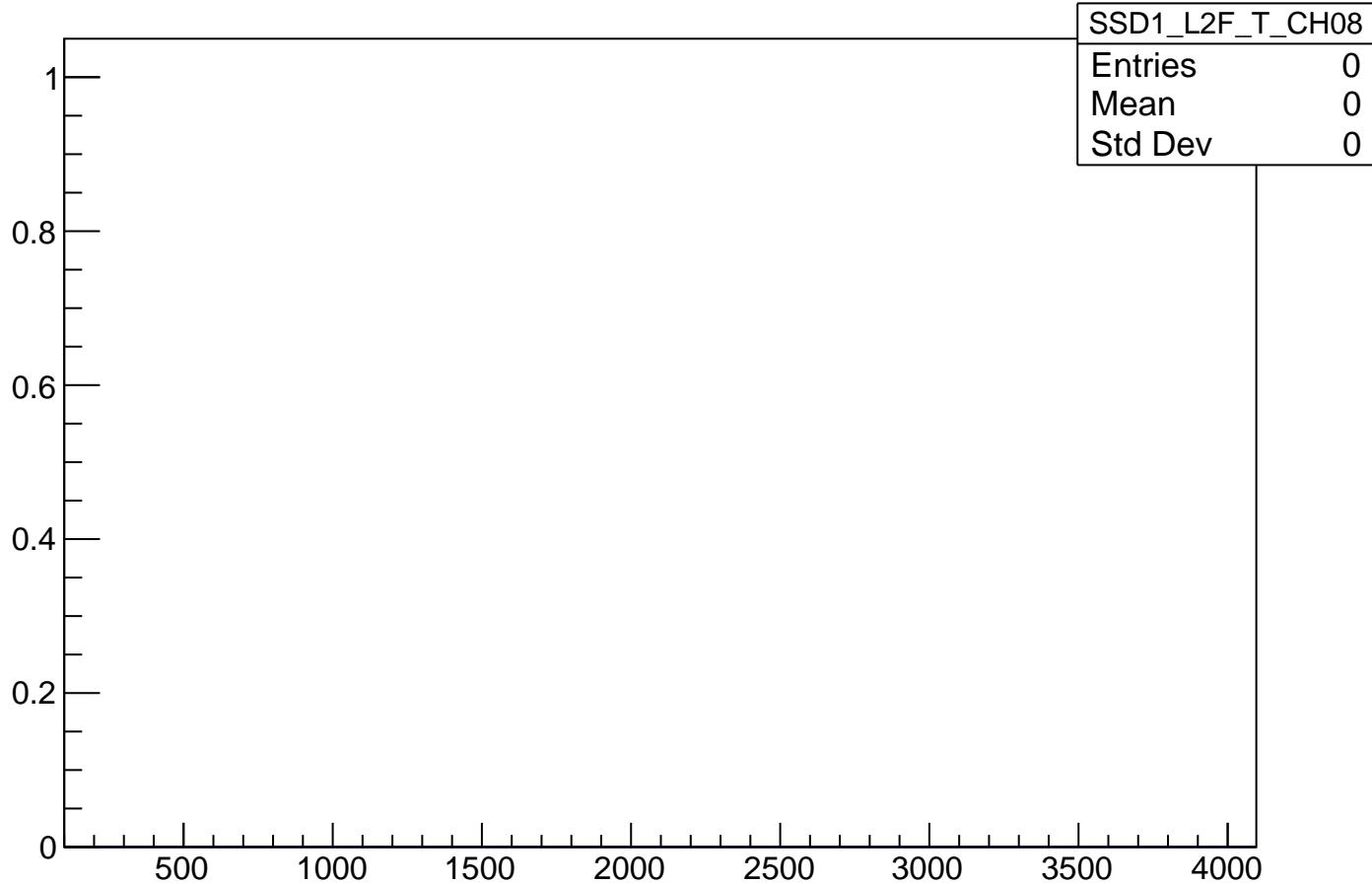
SSD1_L2F_T_CH06



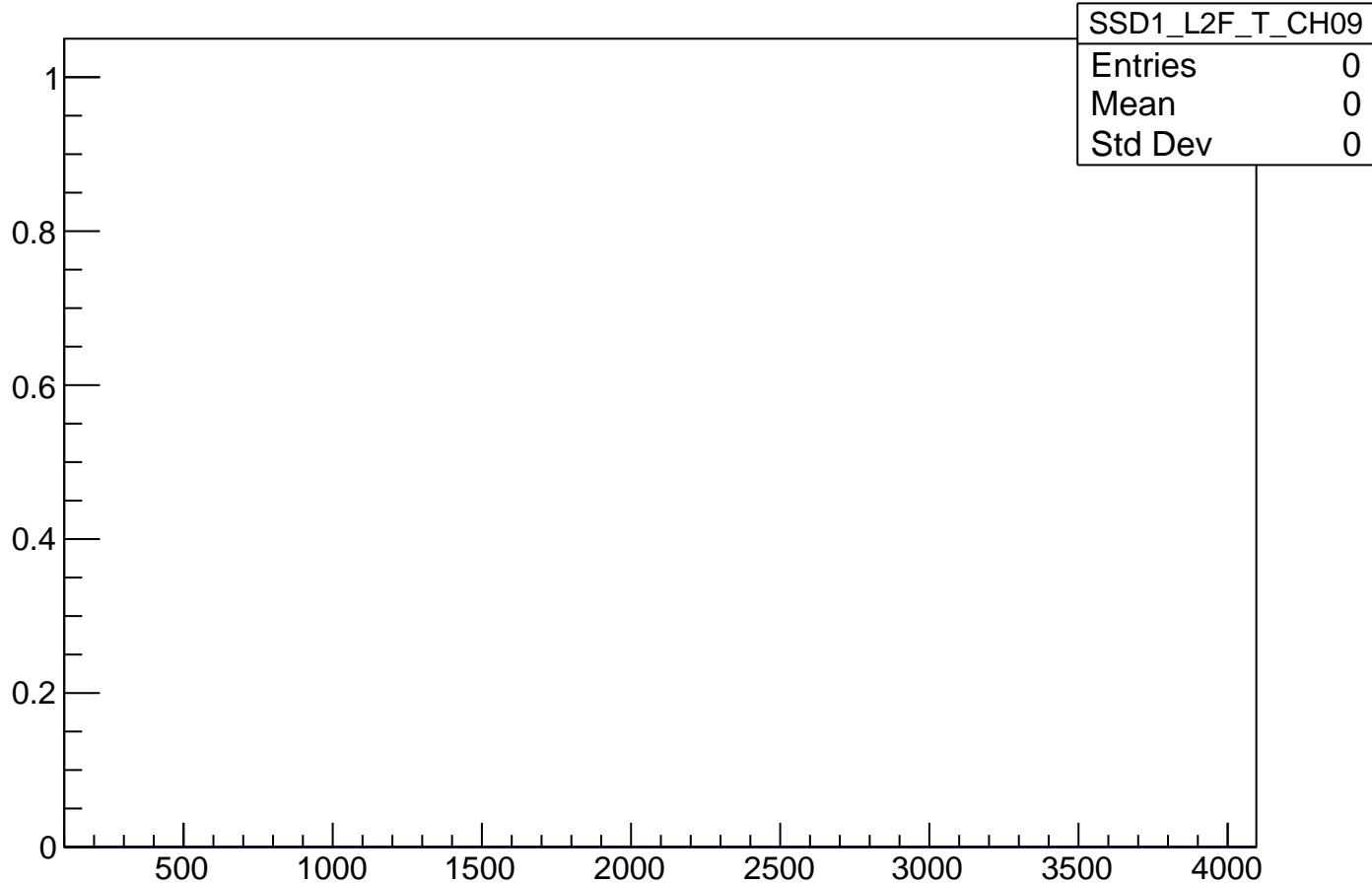
SSD1_L2F_T_CH07



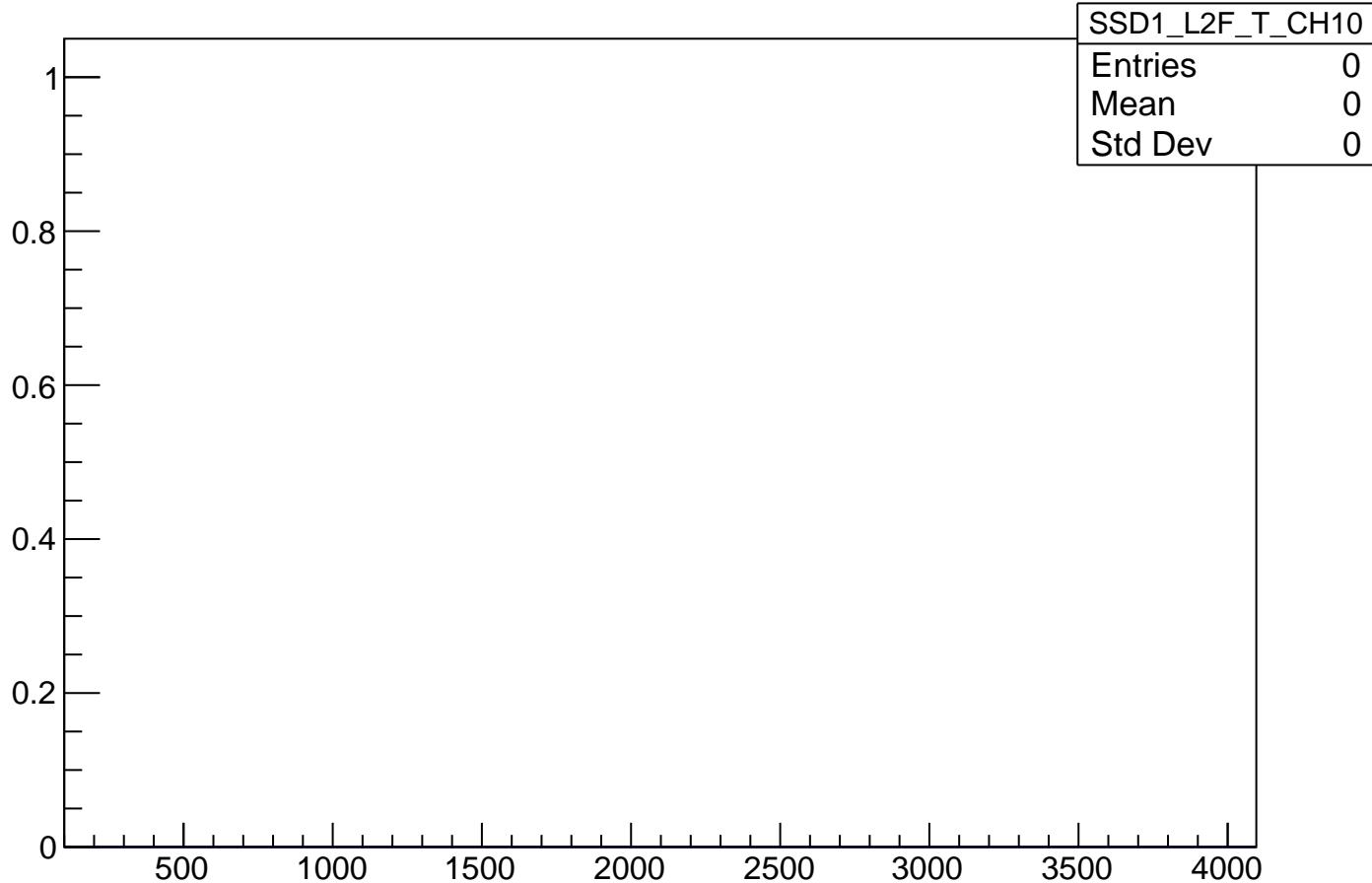
SSD1_L2F_T_CH08



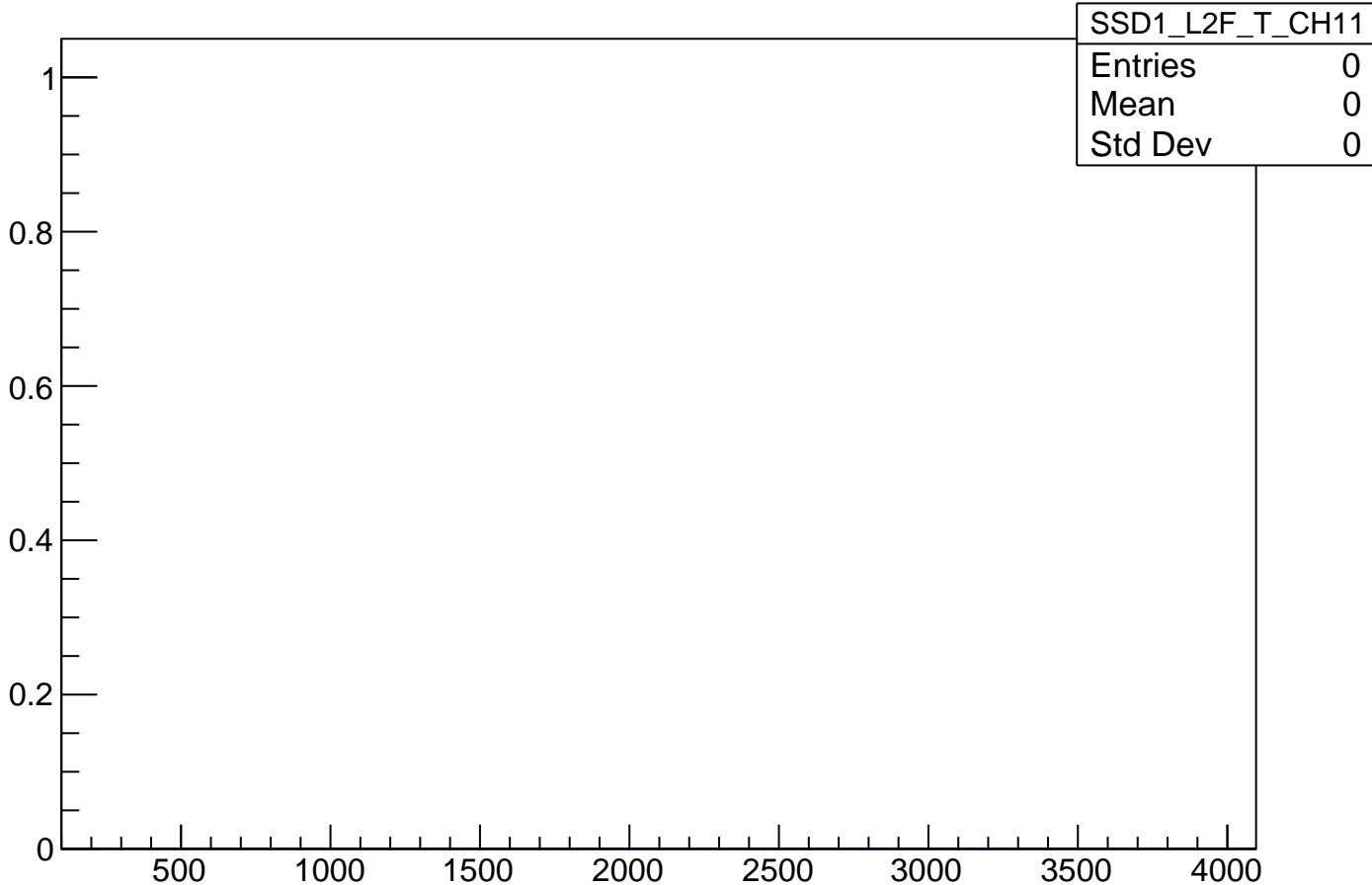
SSD1_L2F_T_CH09



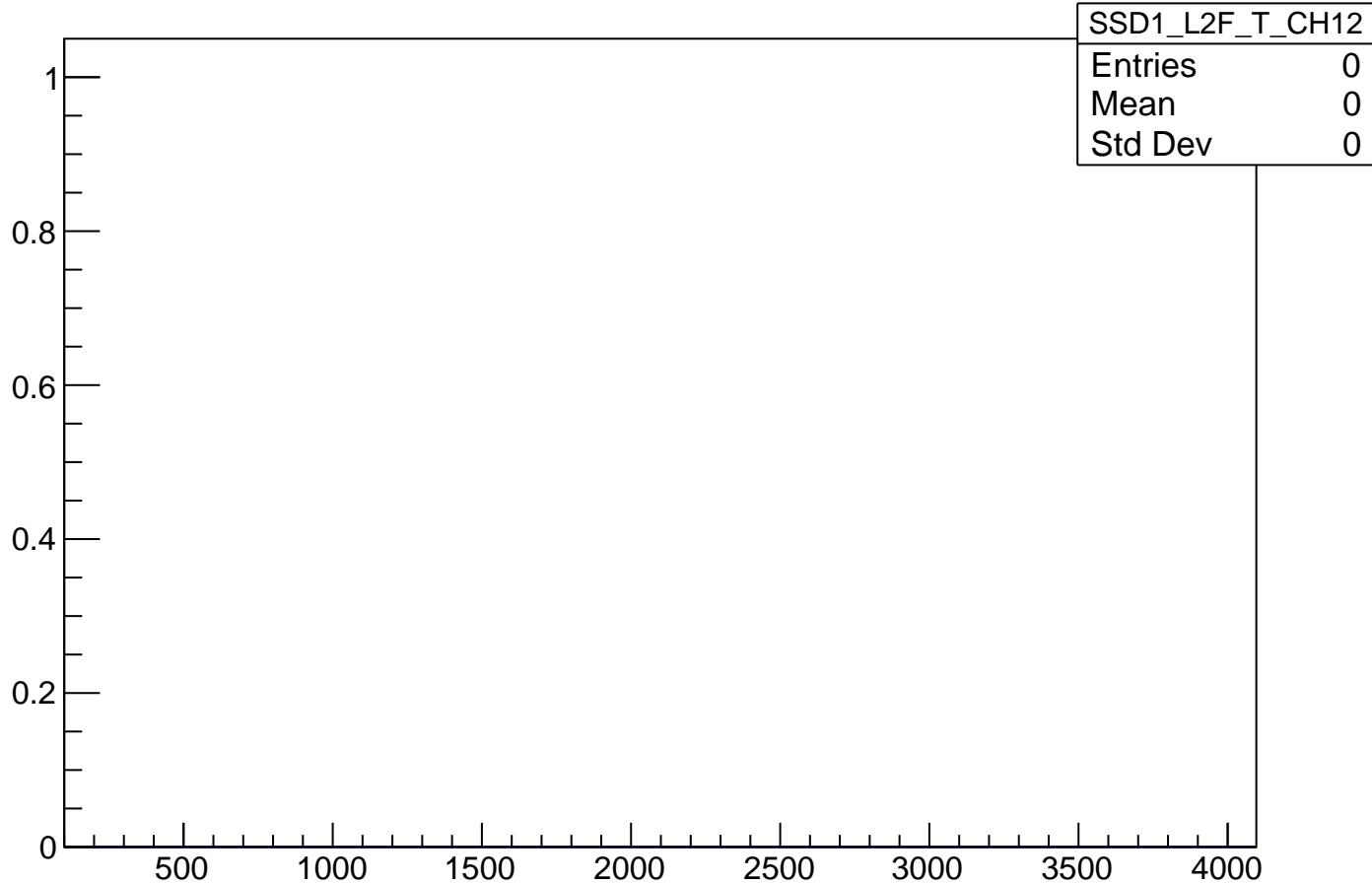
SSD1_L2F_T_CH10



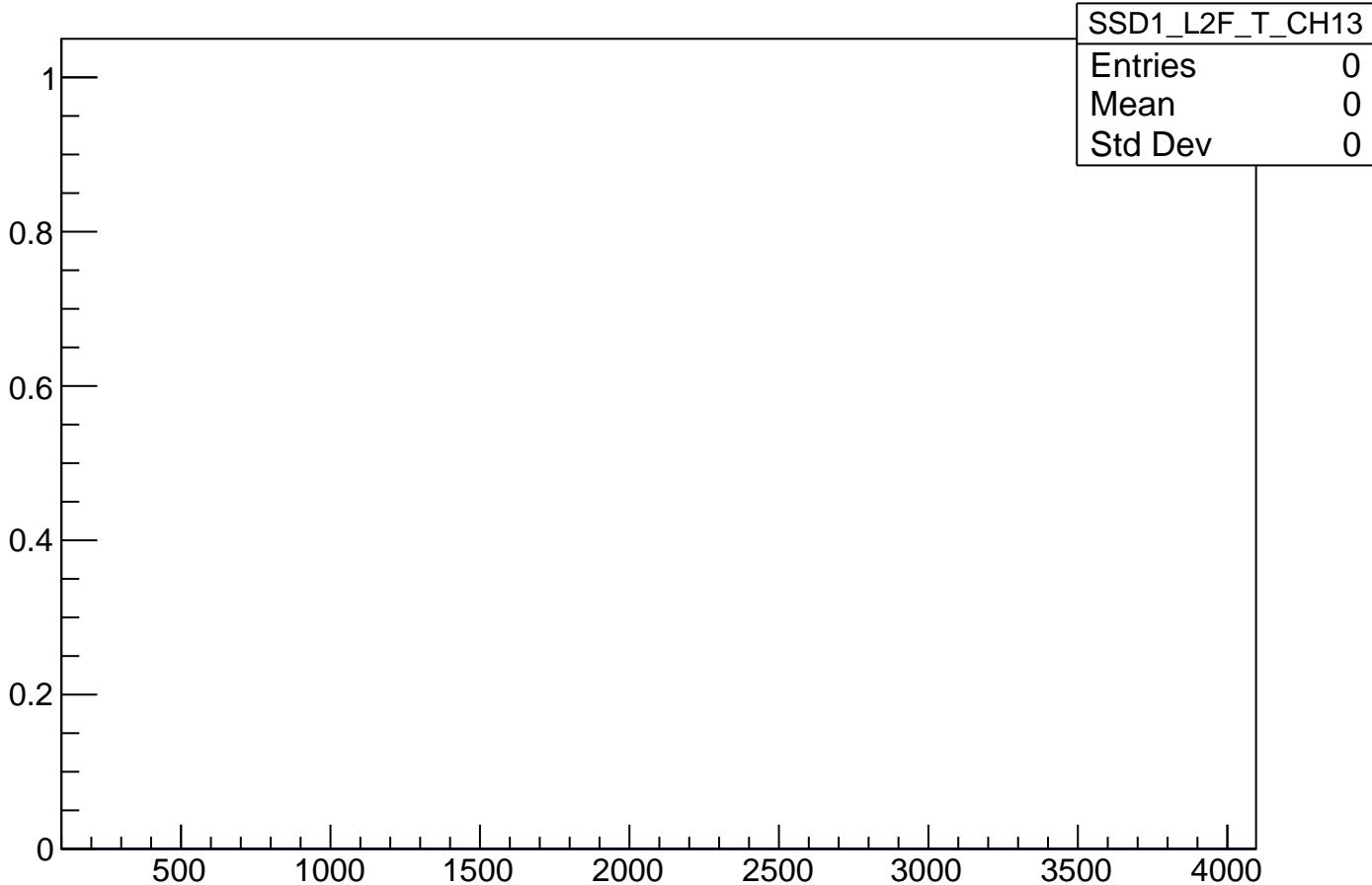
SSD1_L2F_T_CH11



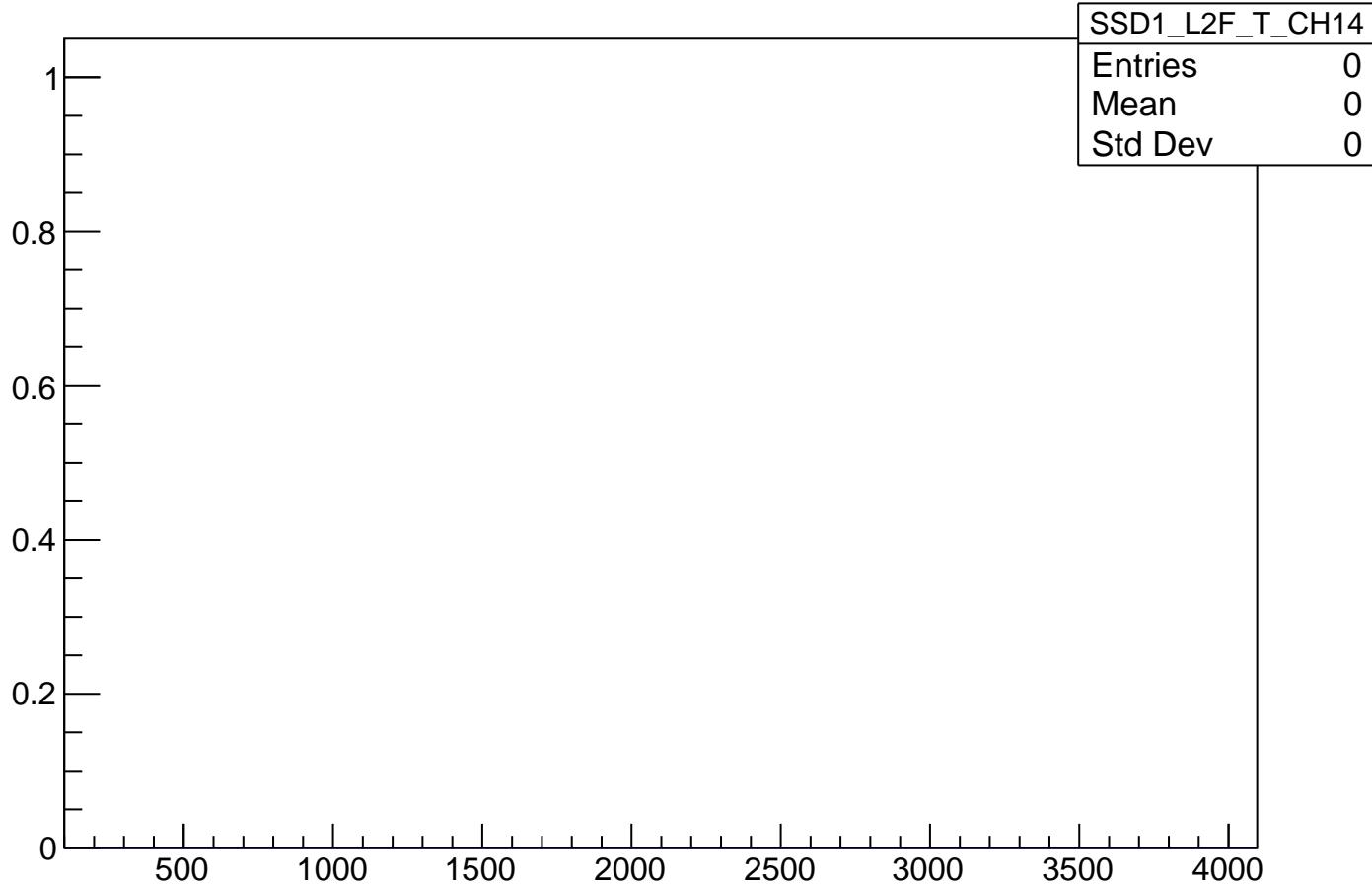
SSD1_L2F_T_CH12



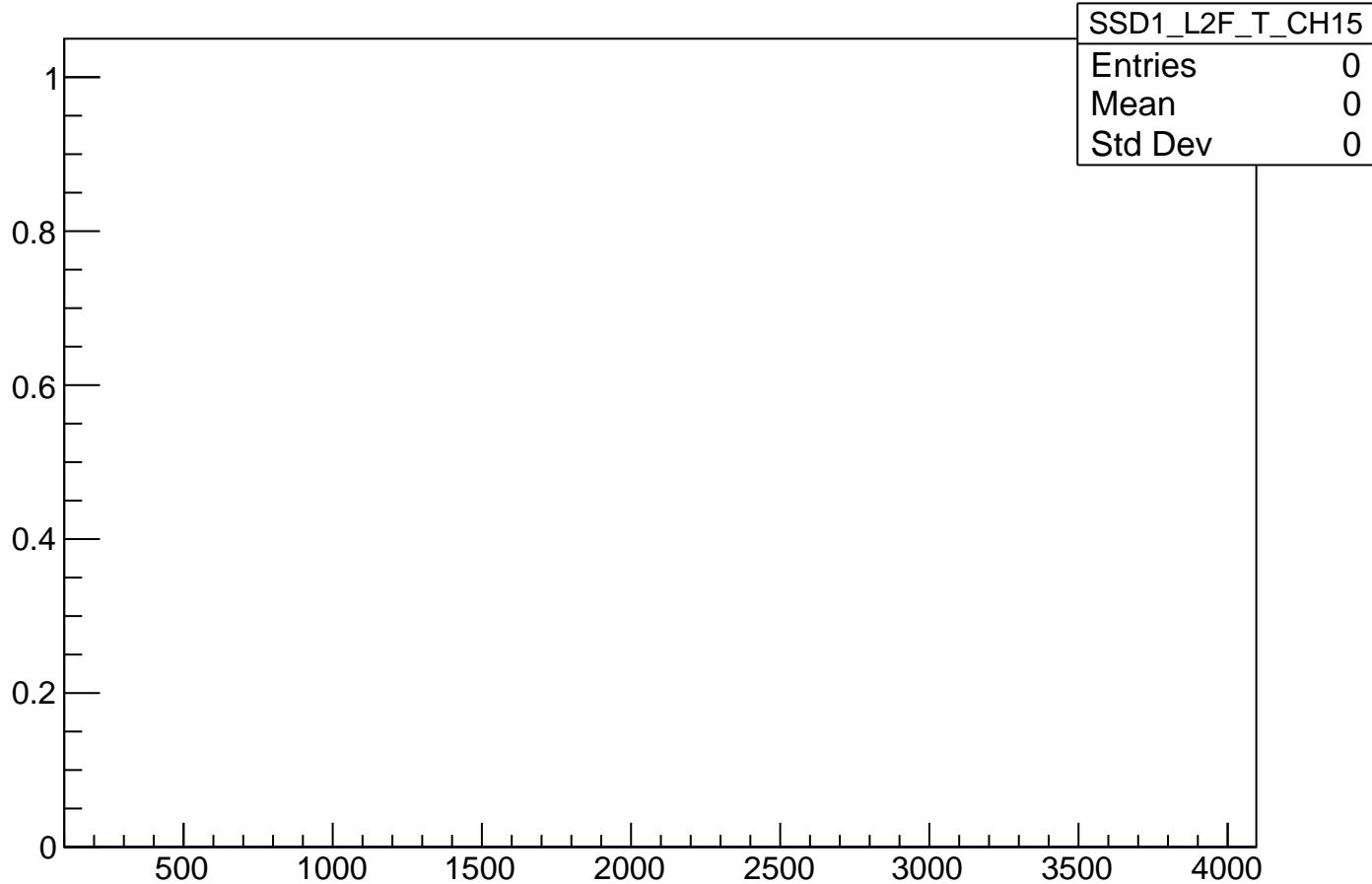
SSD1_L2F_T_CH13



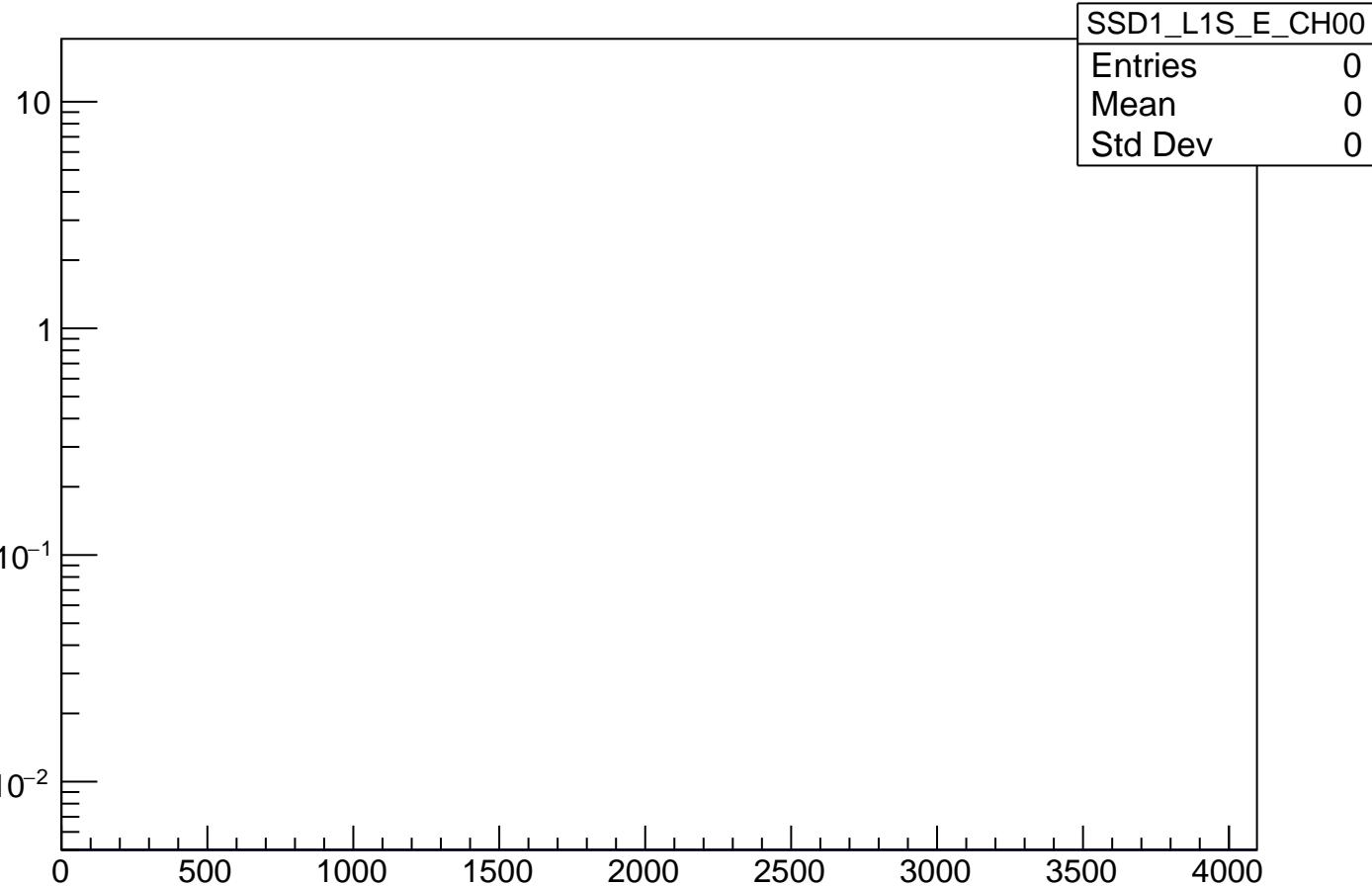
SSD1_L2F_T_CH14



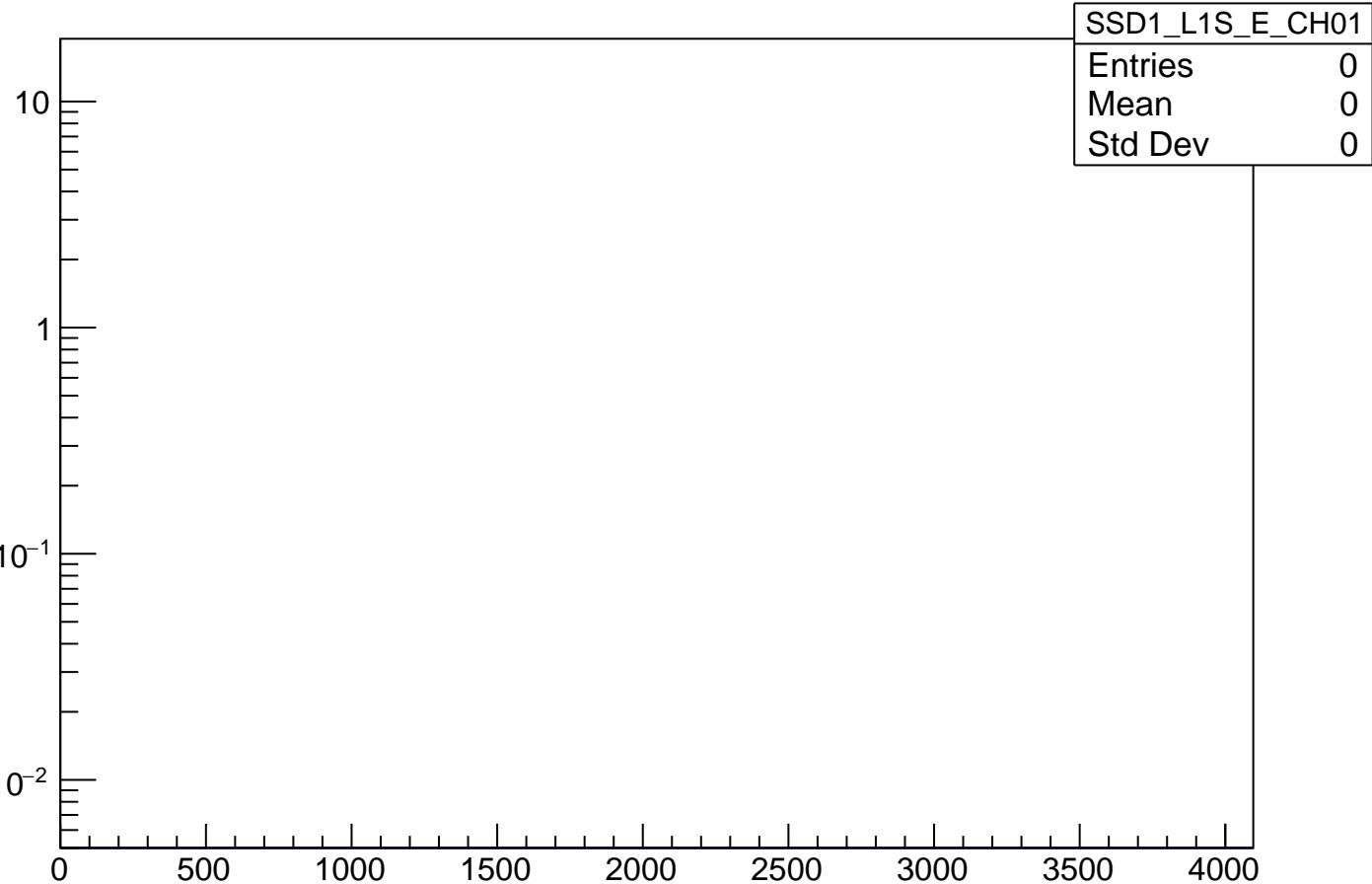
SSD1_L2F_T_CH15



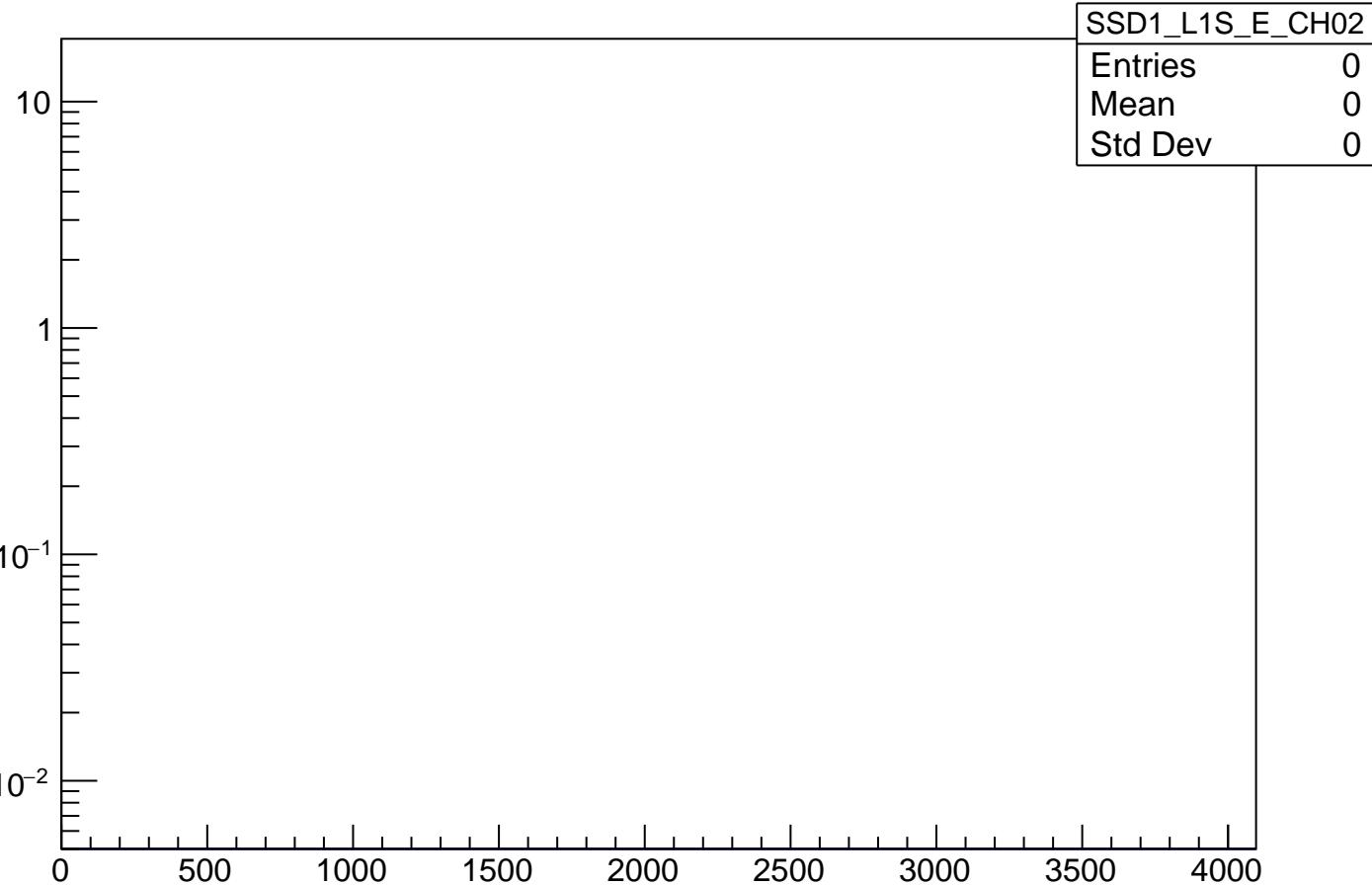
SSD1_L1S_E_CH00



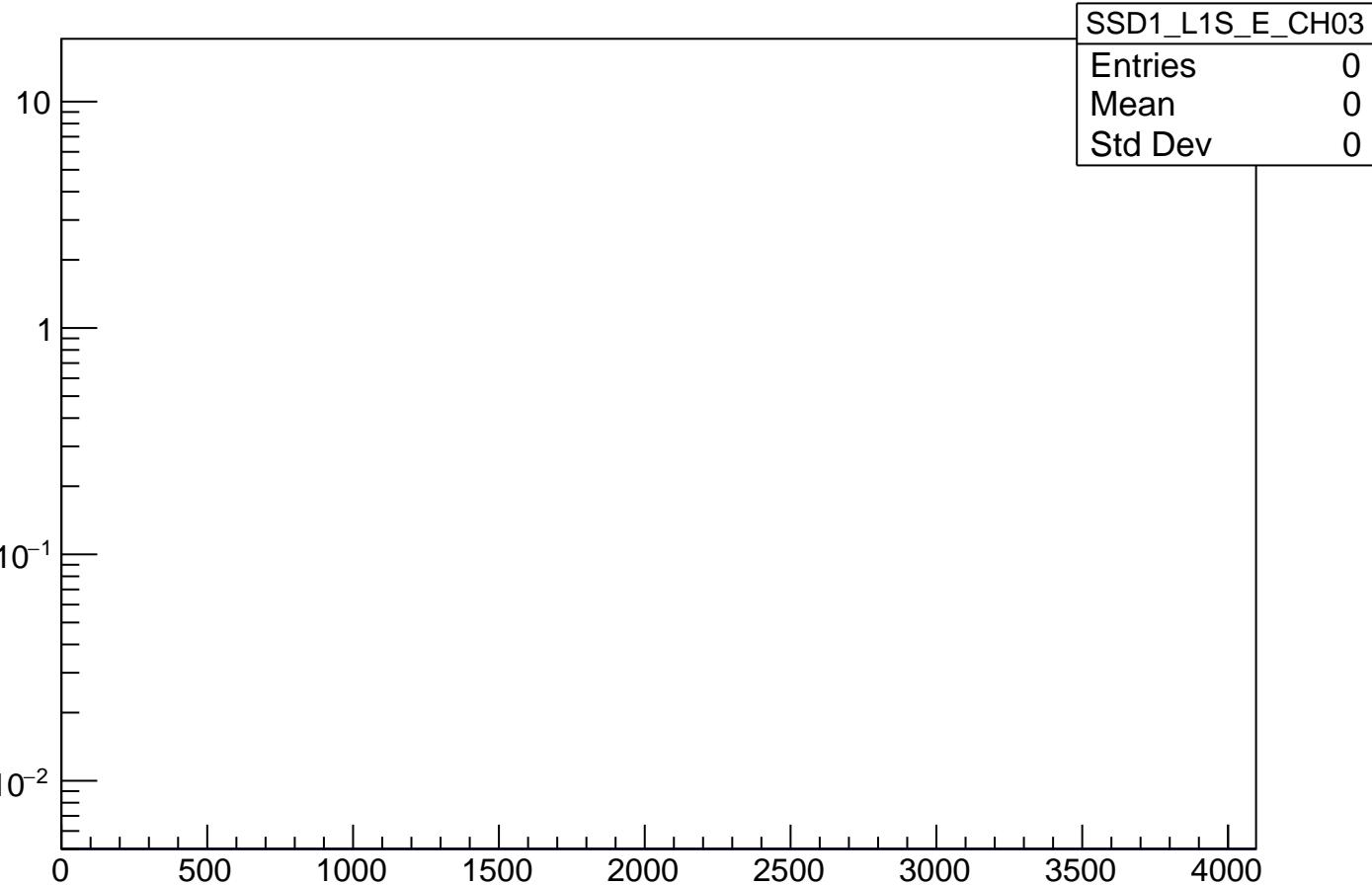
SSD1_L1S_E_CH01



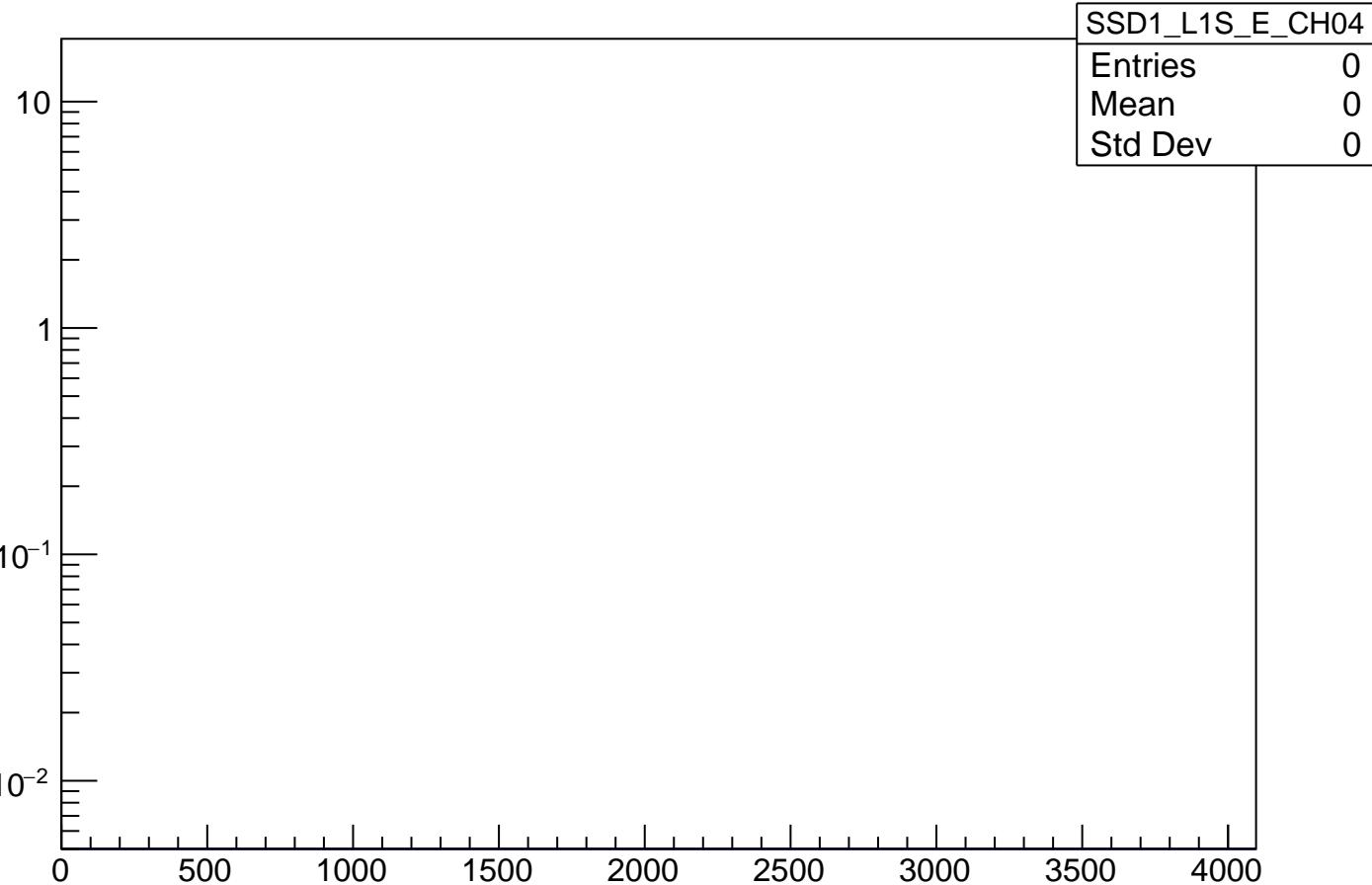
SSD1_L1S_E_CH02



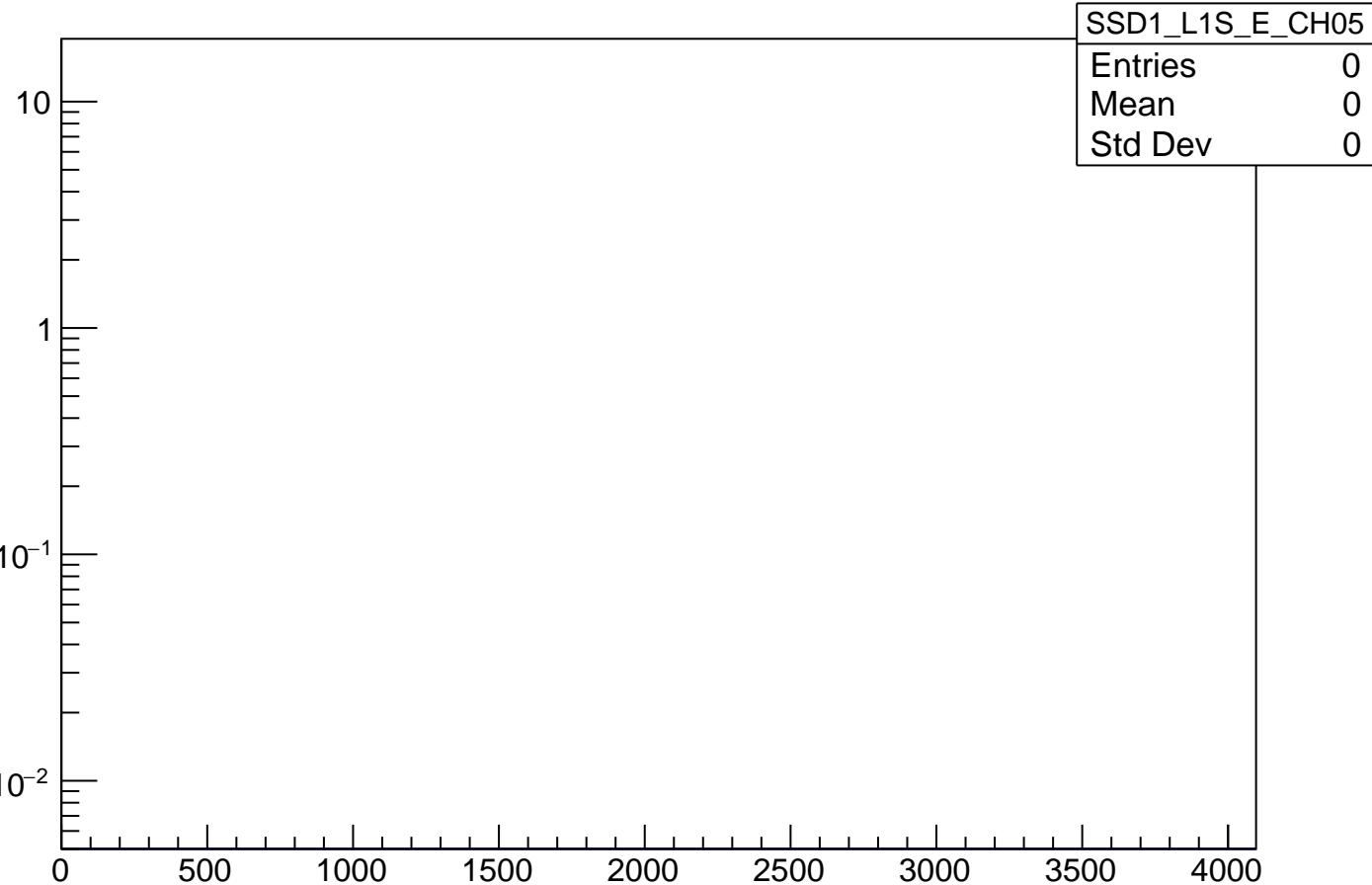
SSD1_L1S_E_CH03



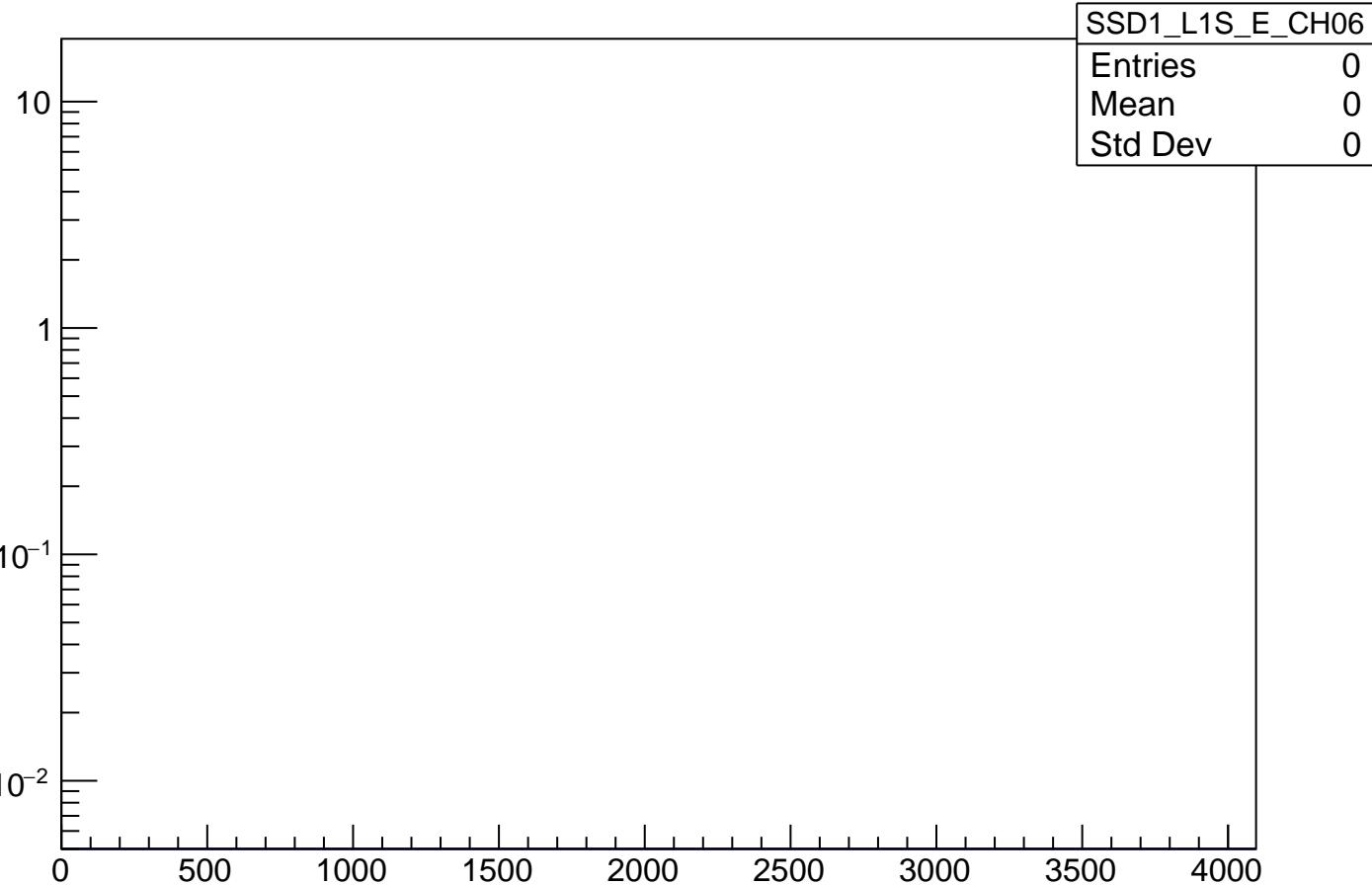
SSD1_L1S_E_CH04



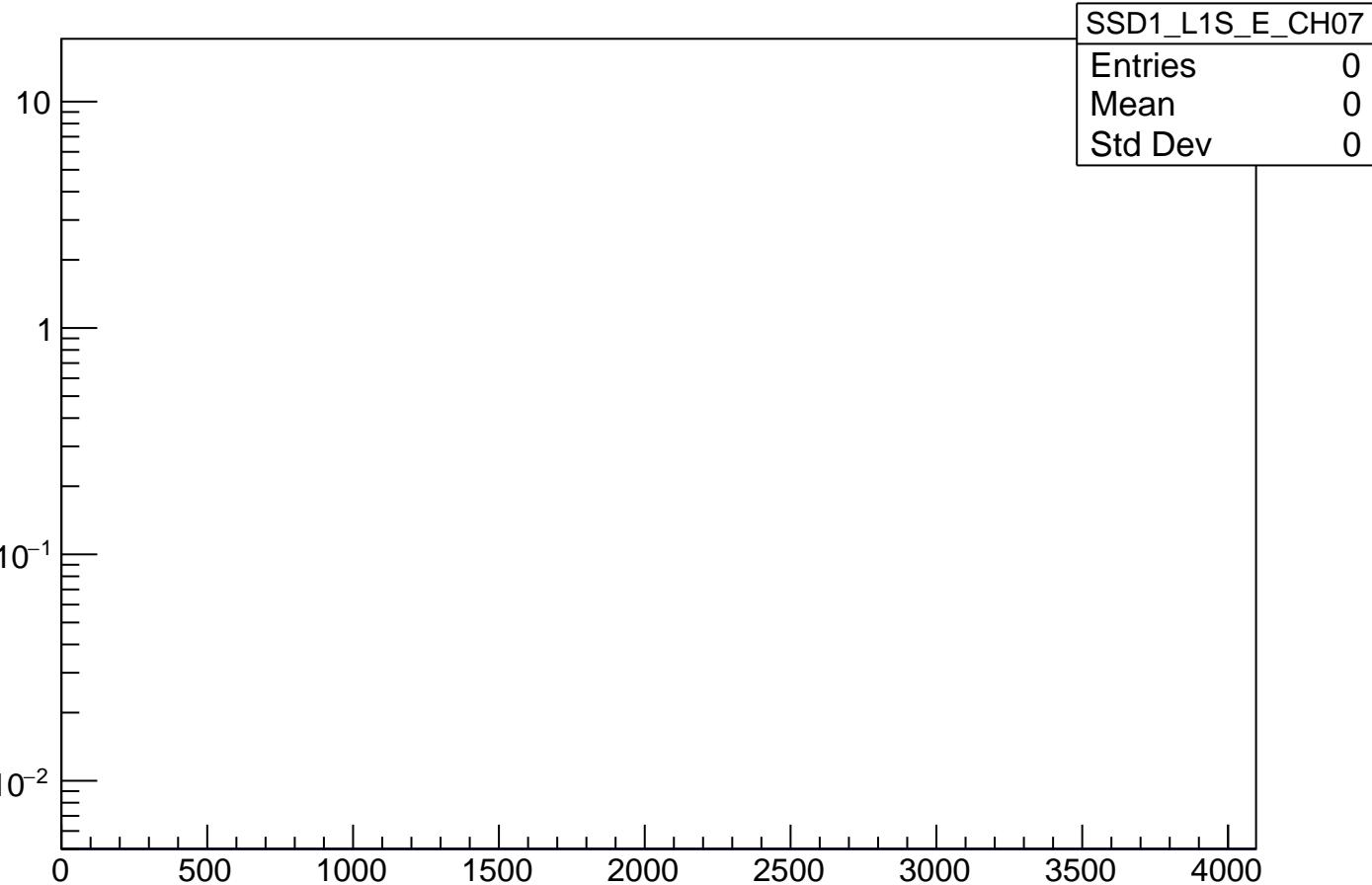
SSD1_L1S_E_CH05



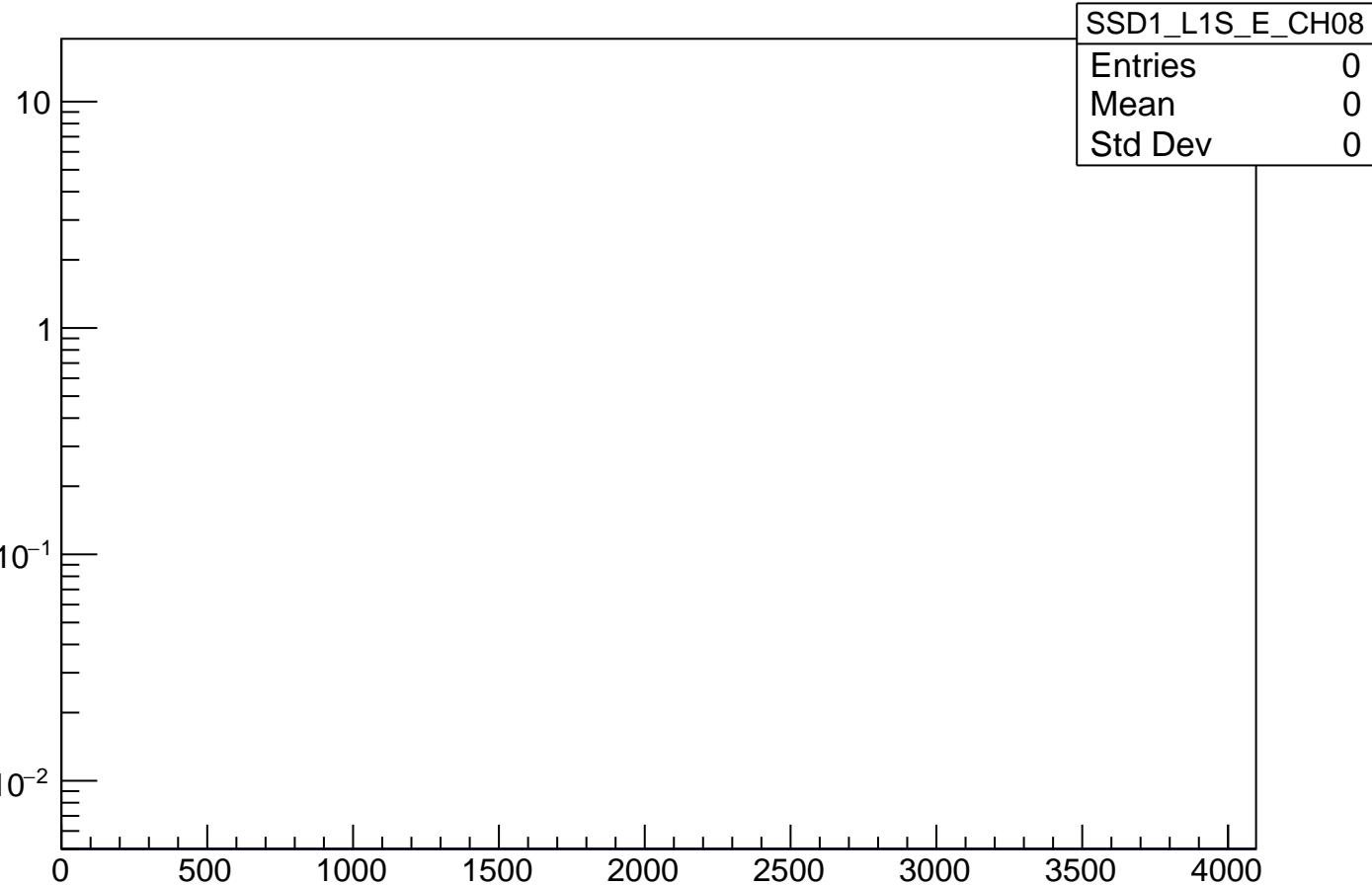
SSD1_L1S_E_CH06



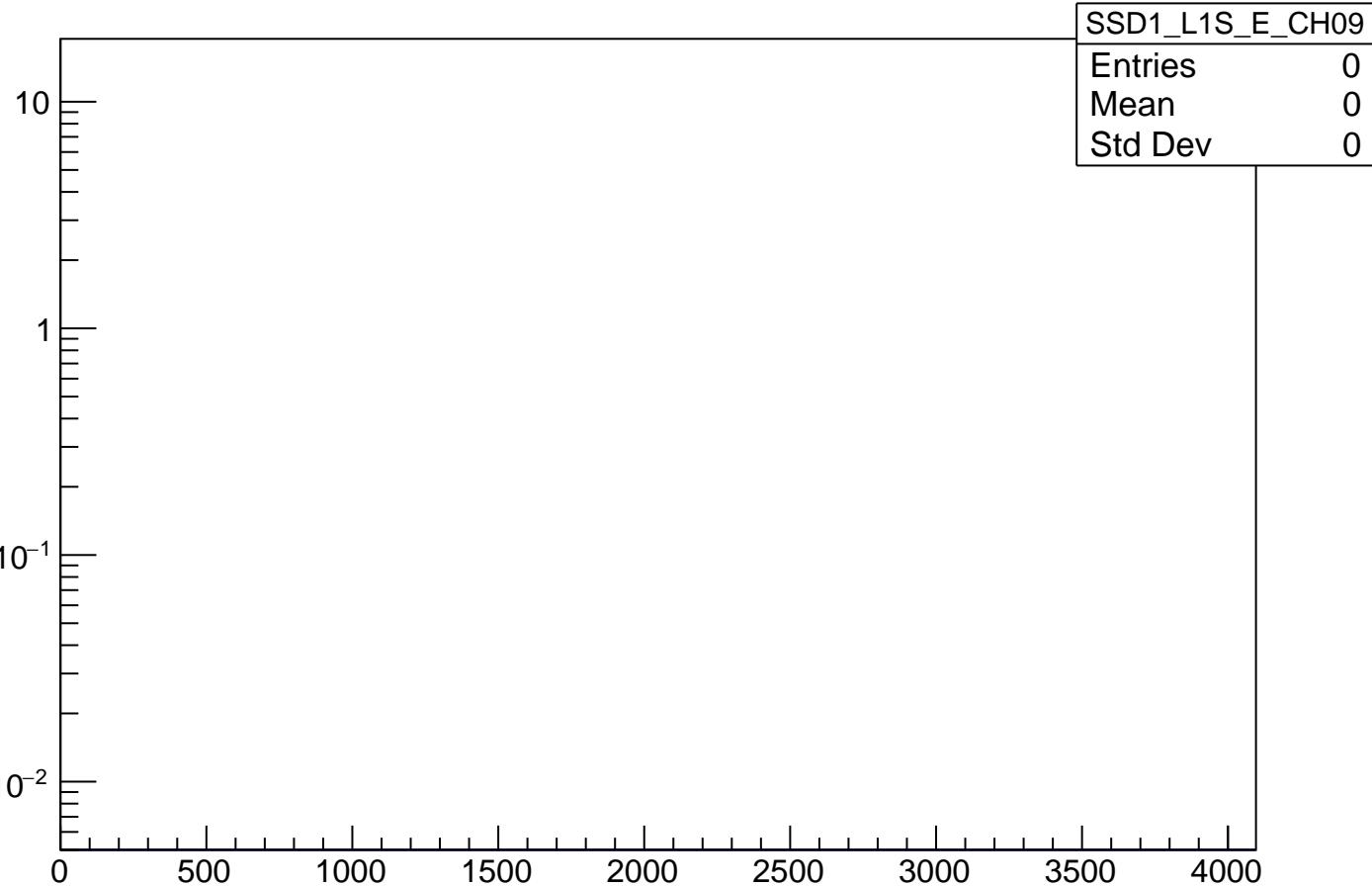
SSD1_L1S_E_CH07



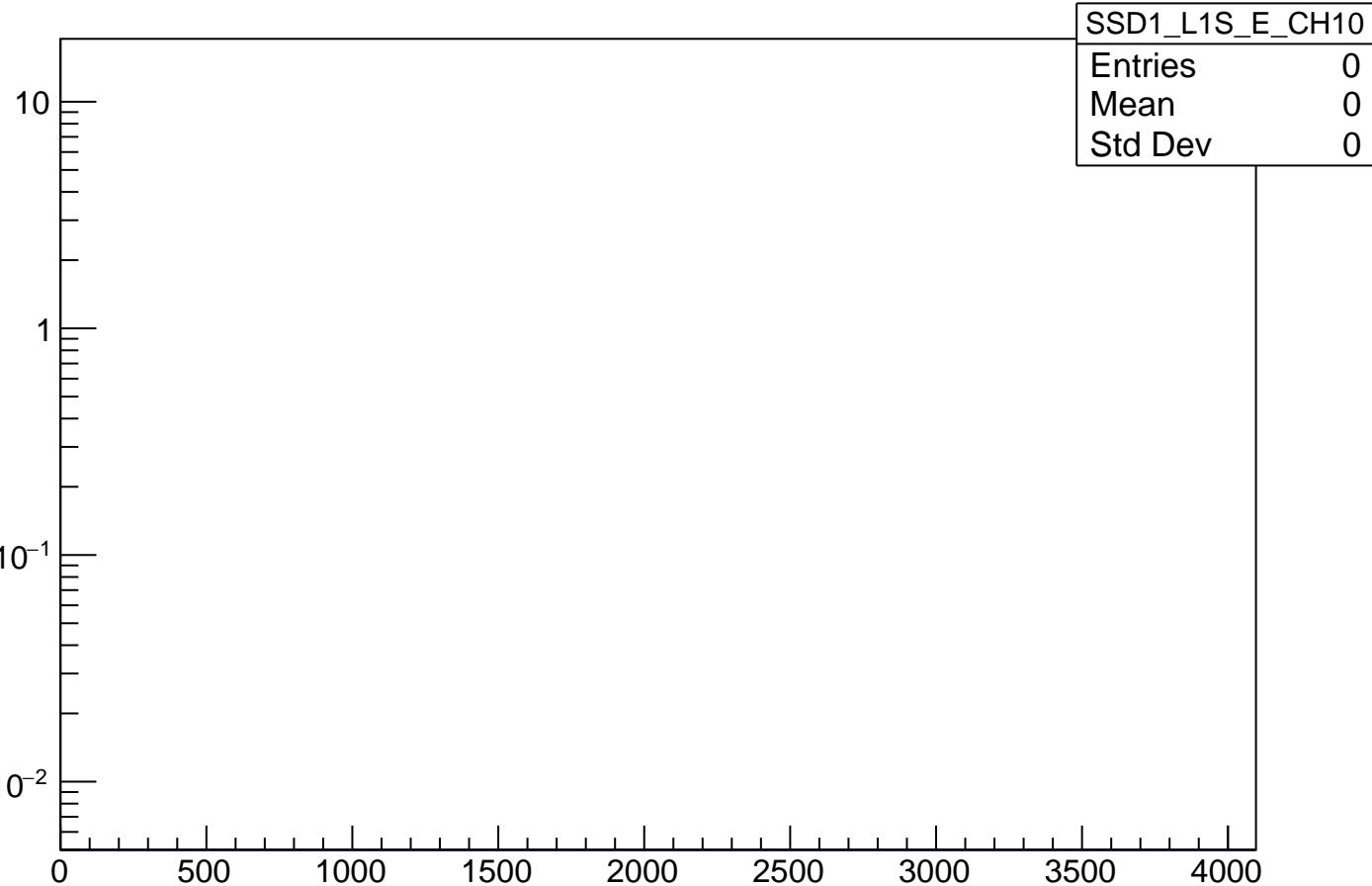
SSD1_L1S_E_CH08



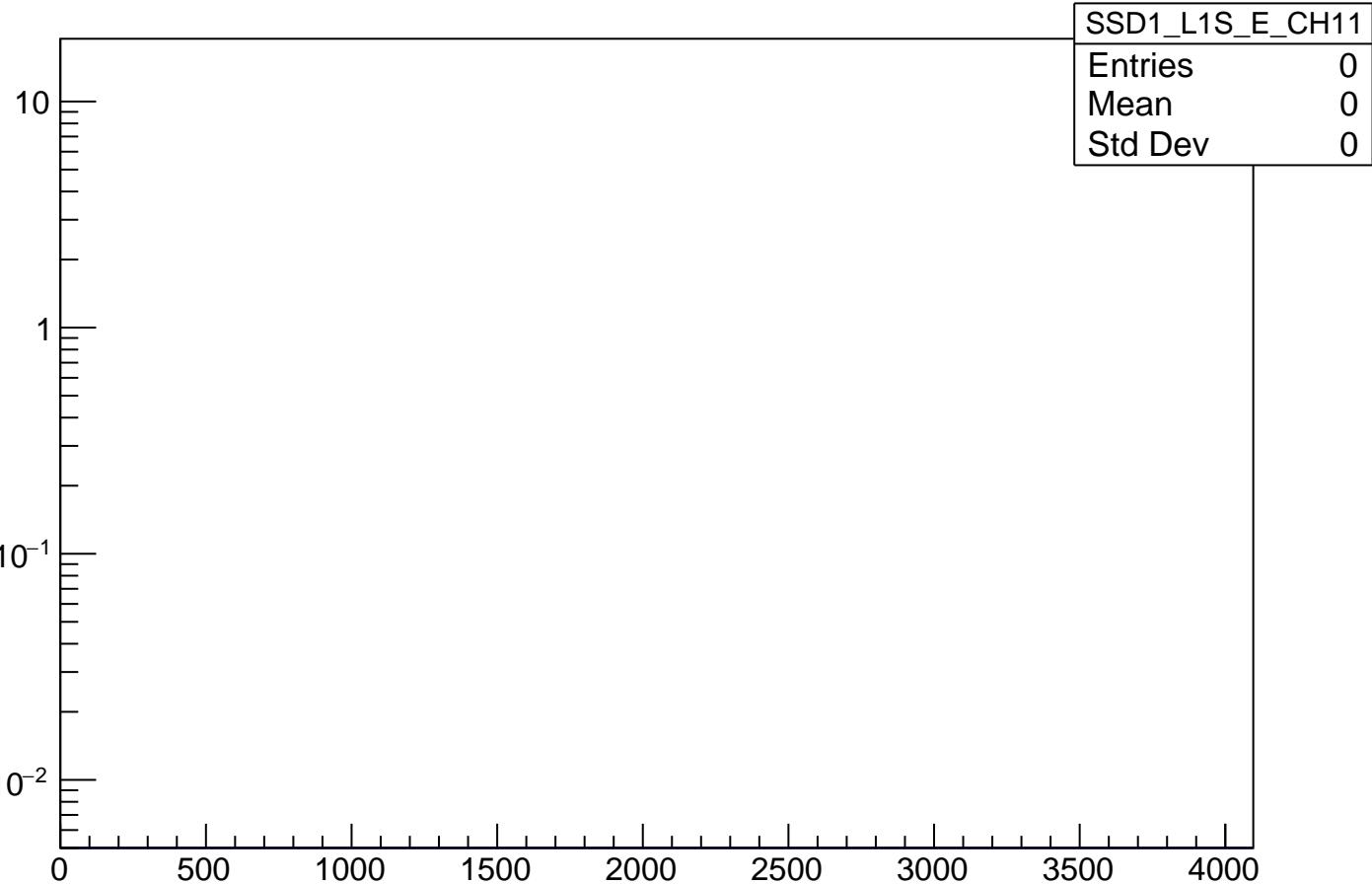
SSD1_L1S_E_CH09



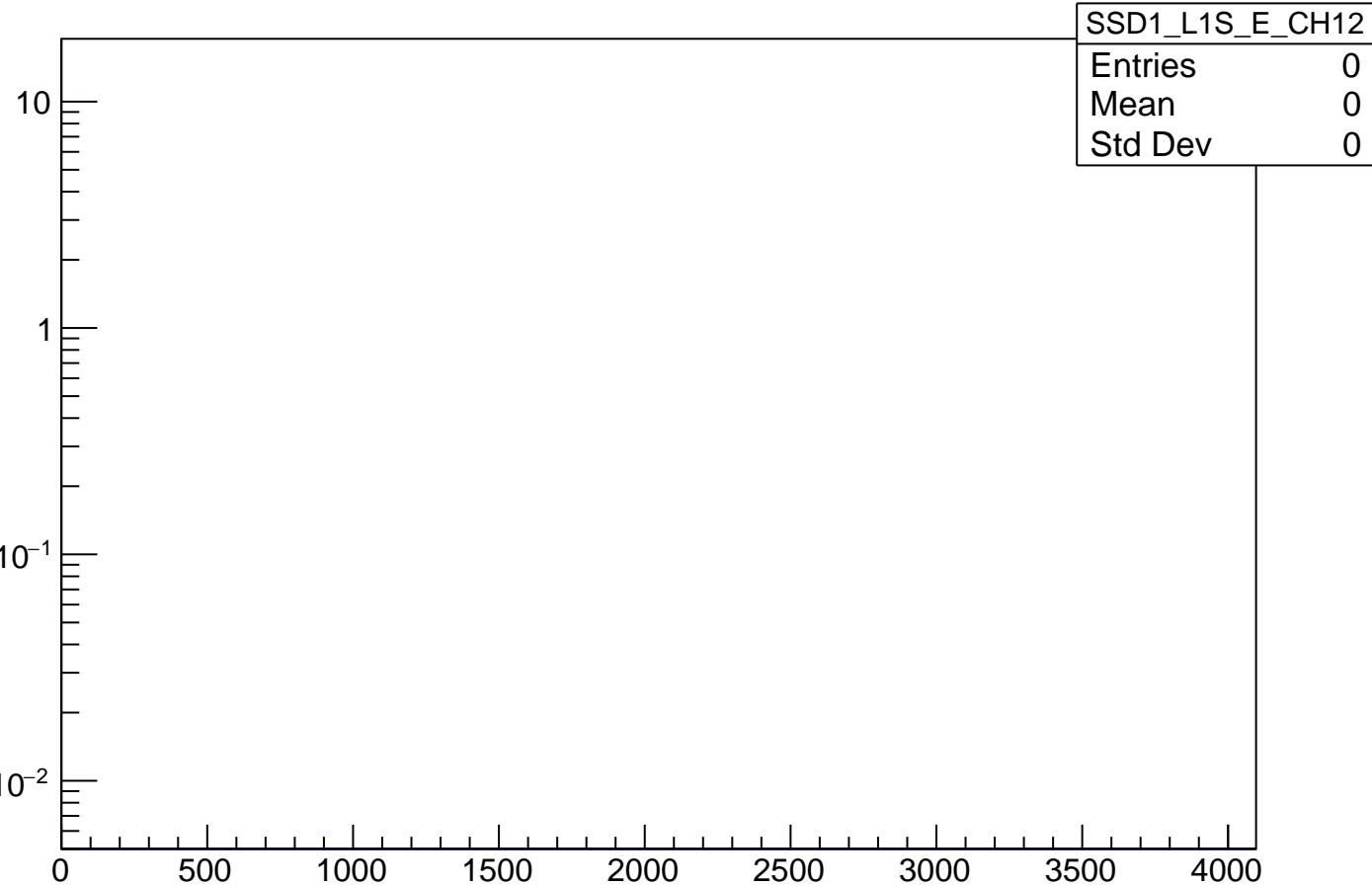
SSD1_L1S_E_CH10



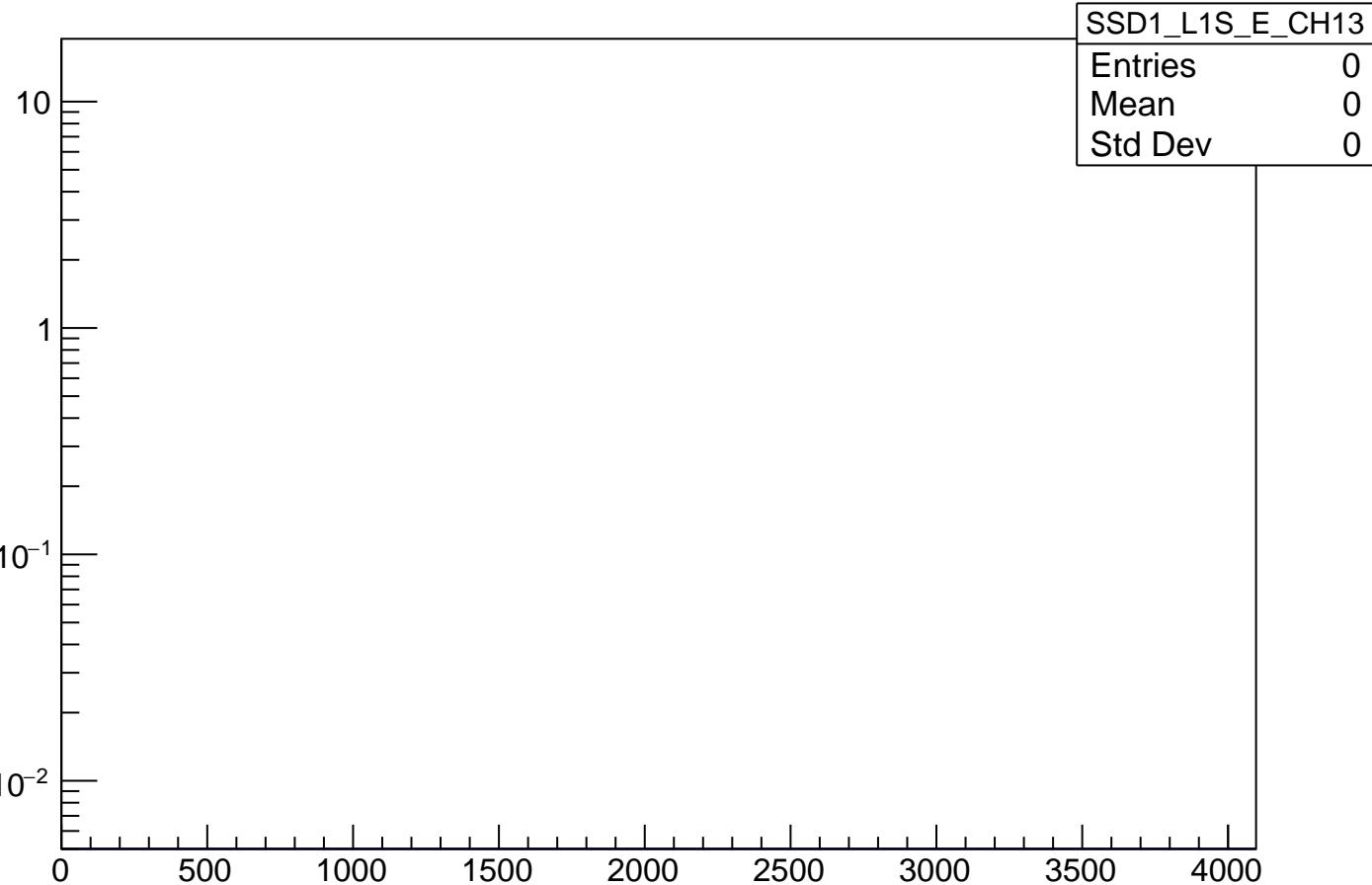
SSD1_L1S_E_CH11



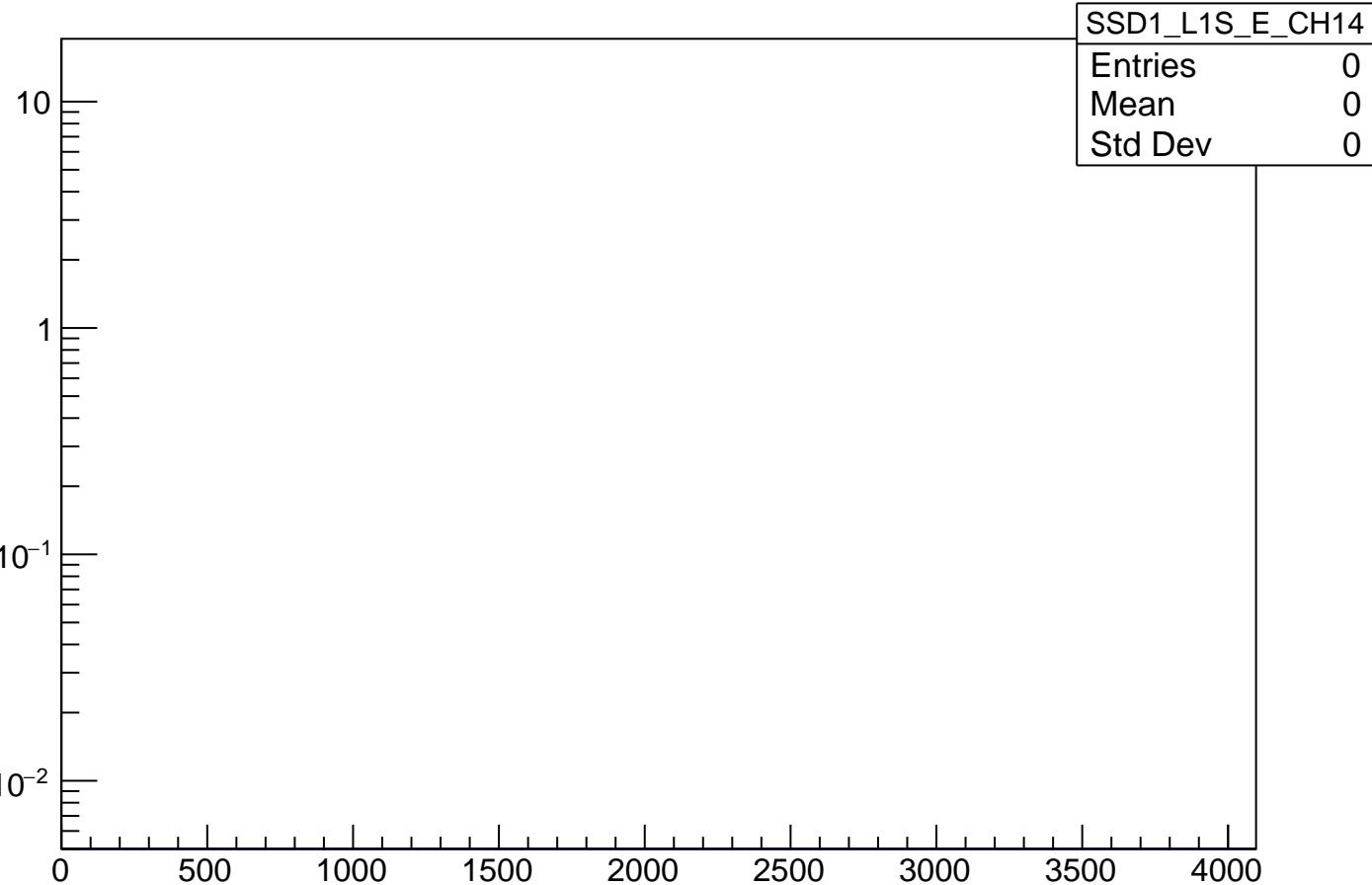
SSD1_L1S_E_CH12



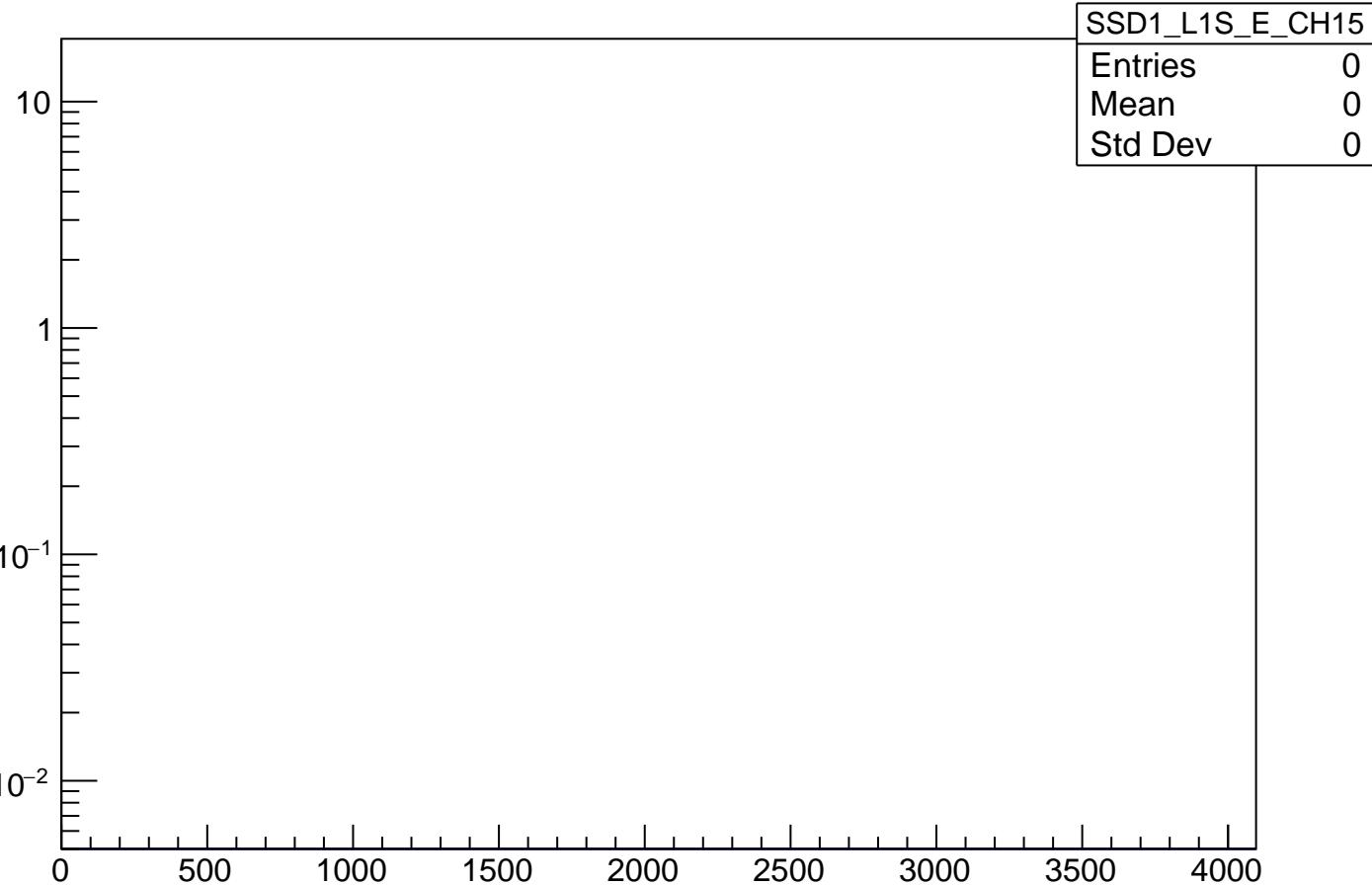
SSD1_L1S_E_CH13



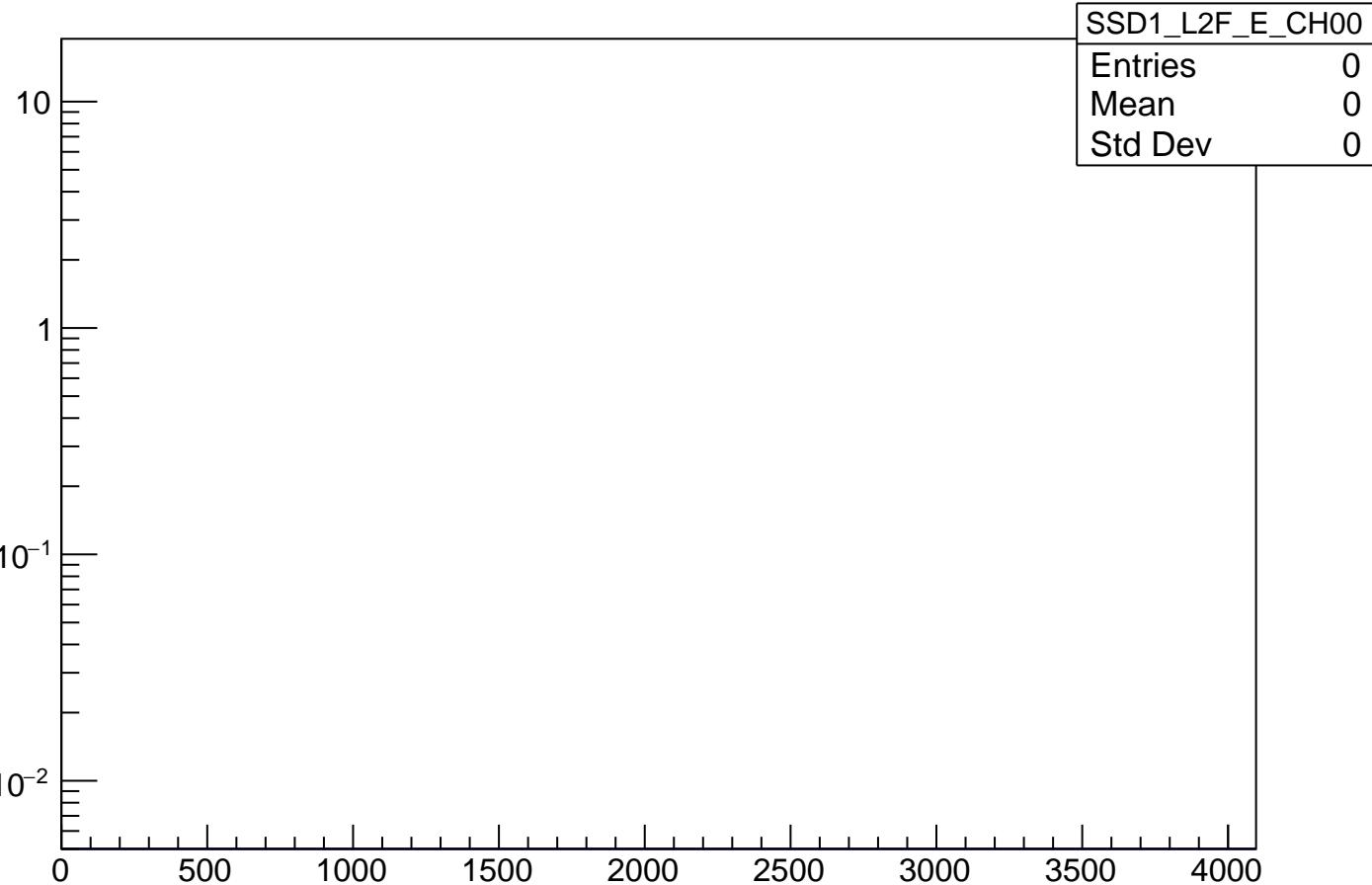
SSD1_L1S_E_CH14



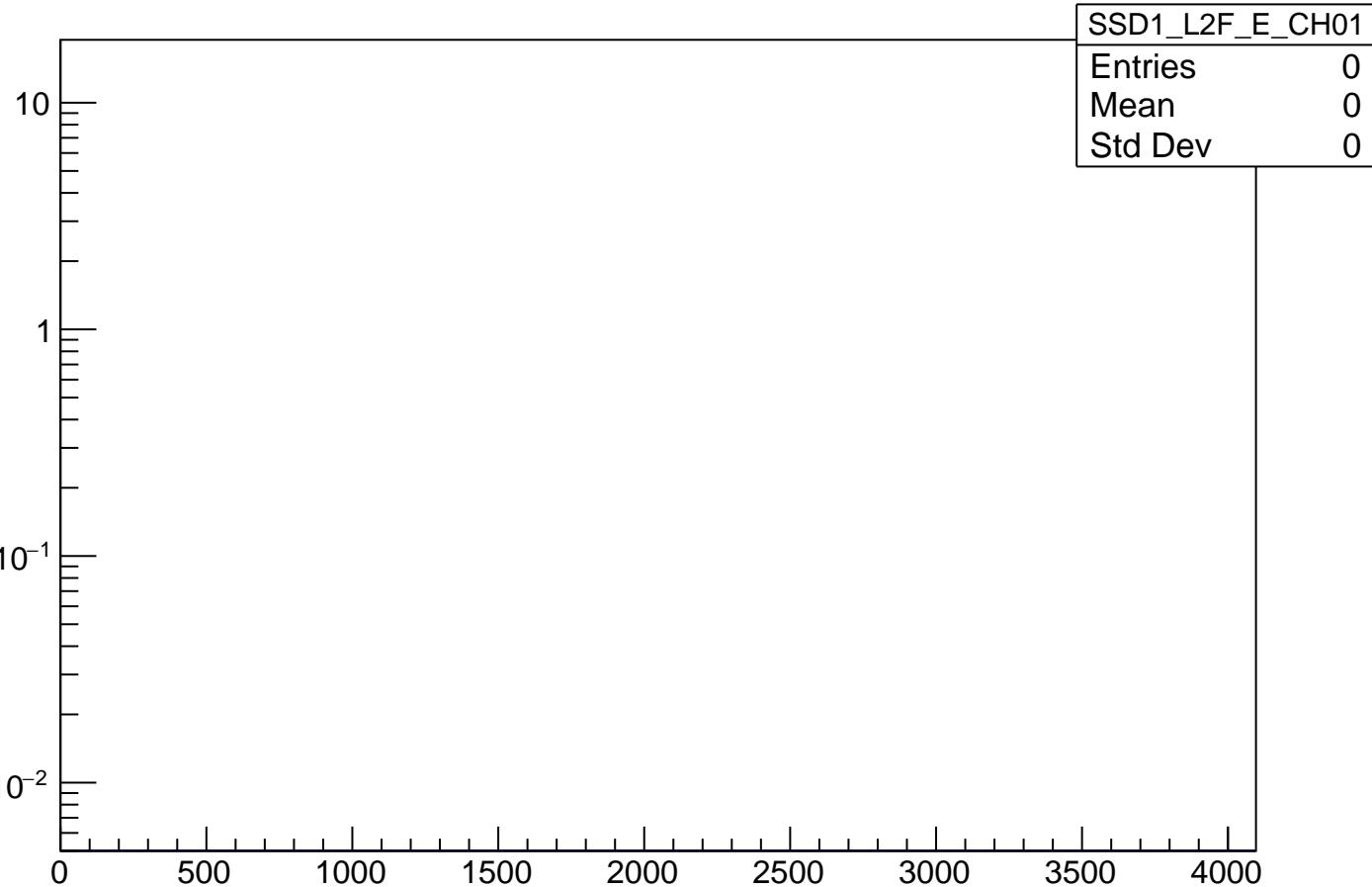
SSD1_L1S_E_CH15



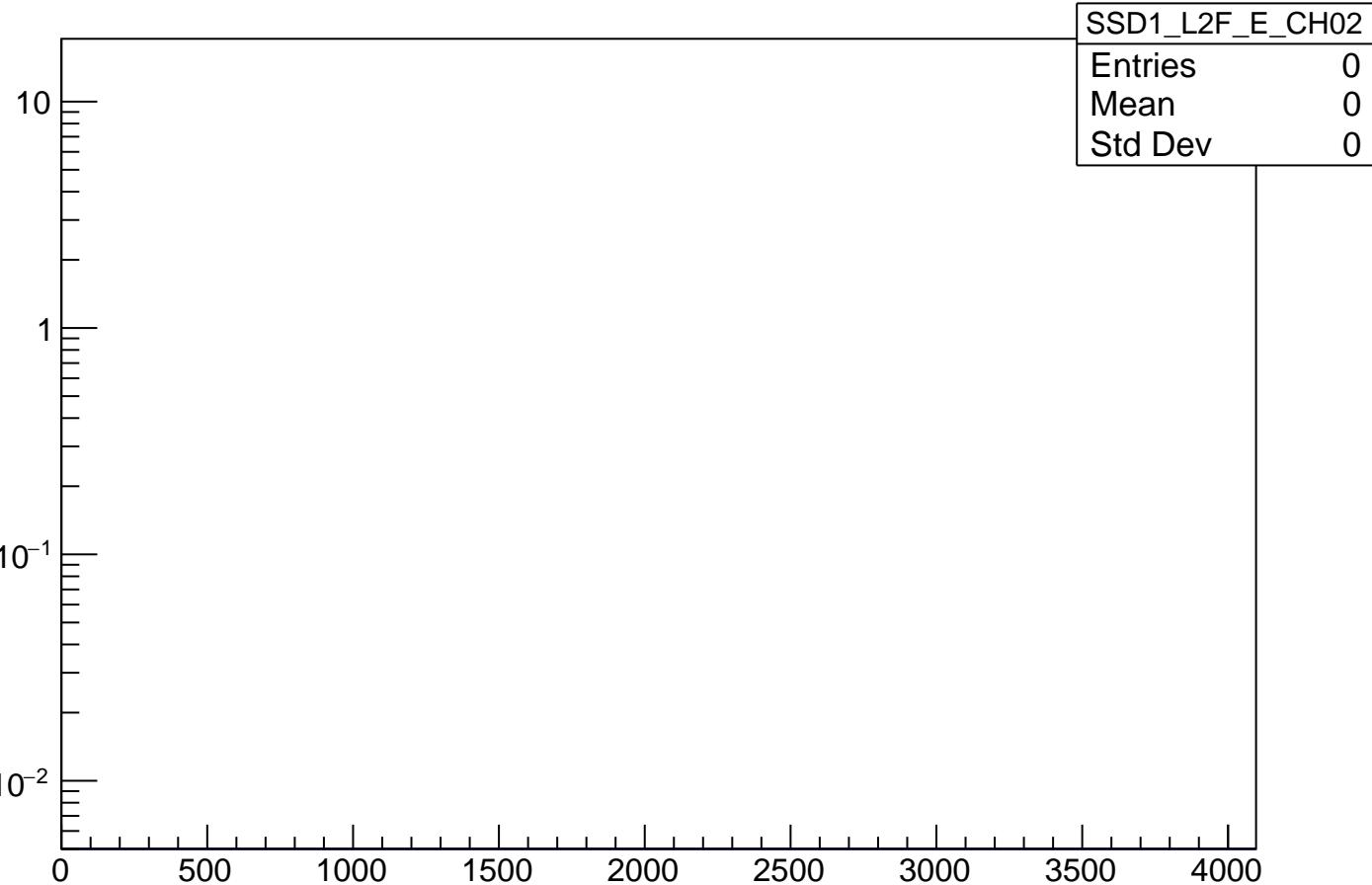
SSD1_L2F_E_CH00



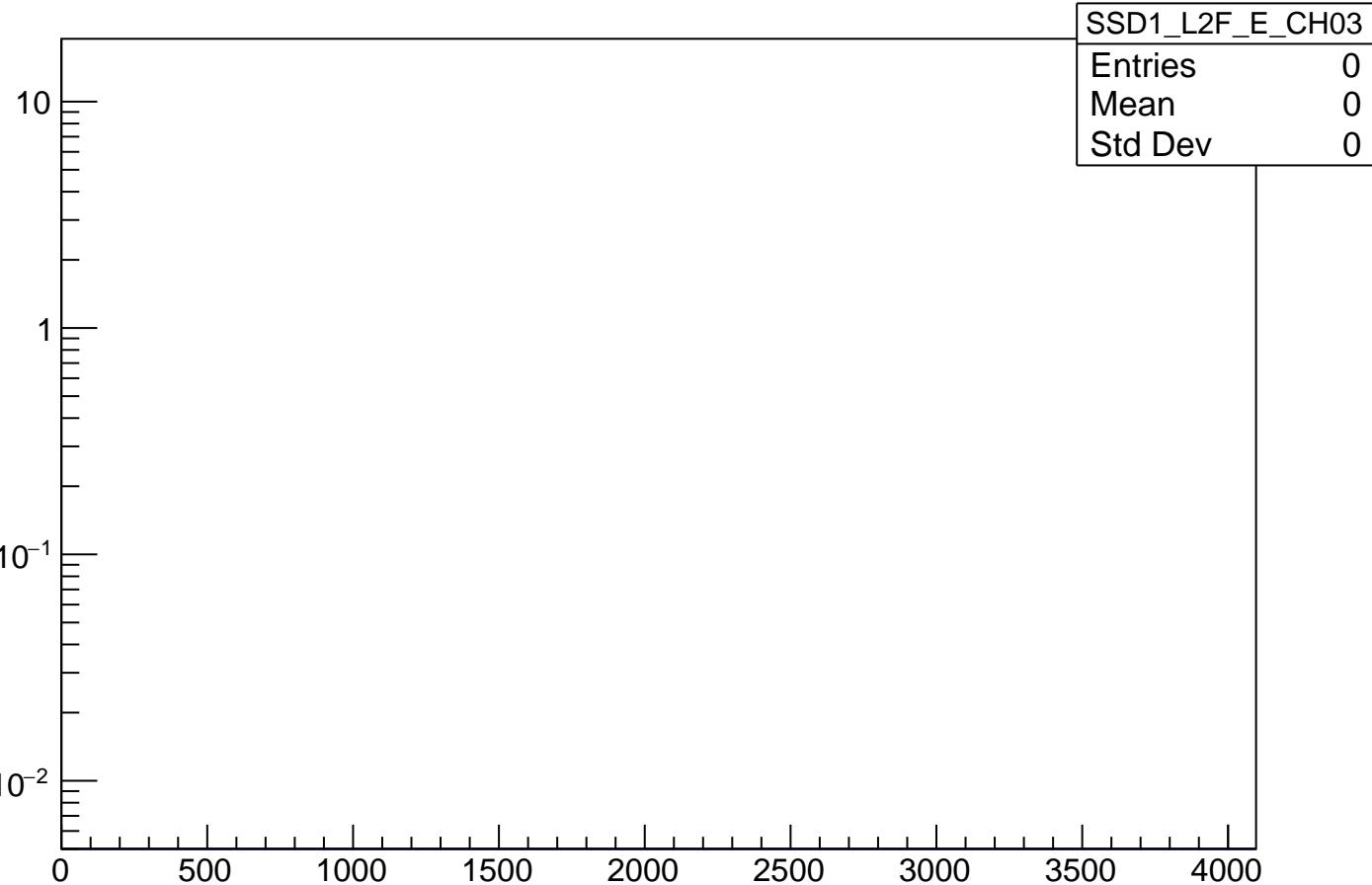
SSD1_L2F_E_CH01



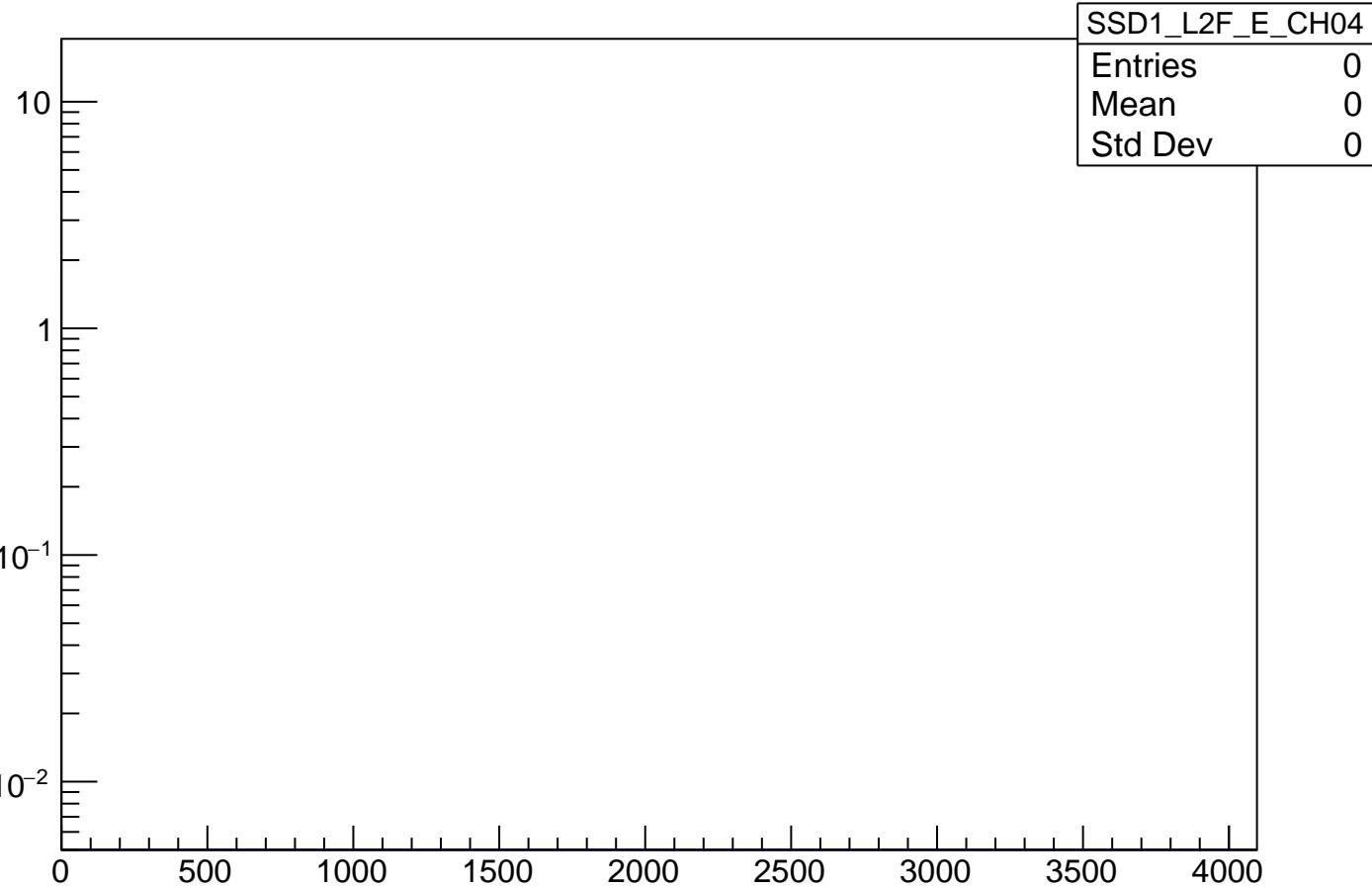
SSD1_L2F_E_CH02



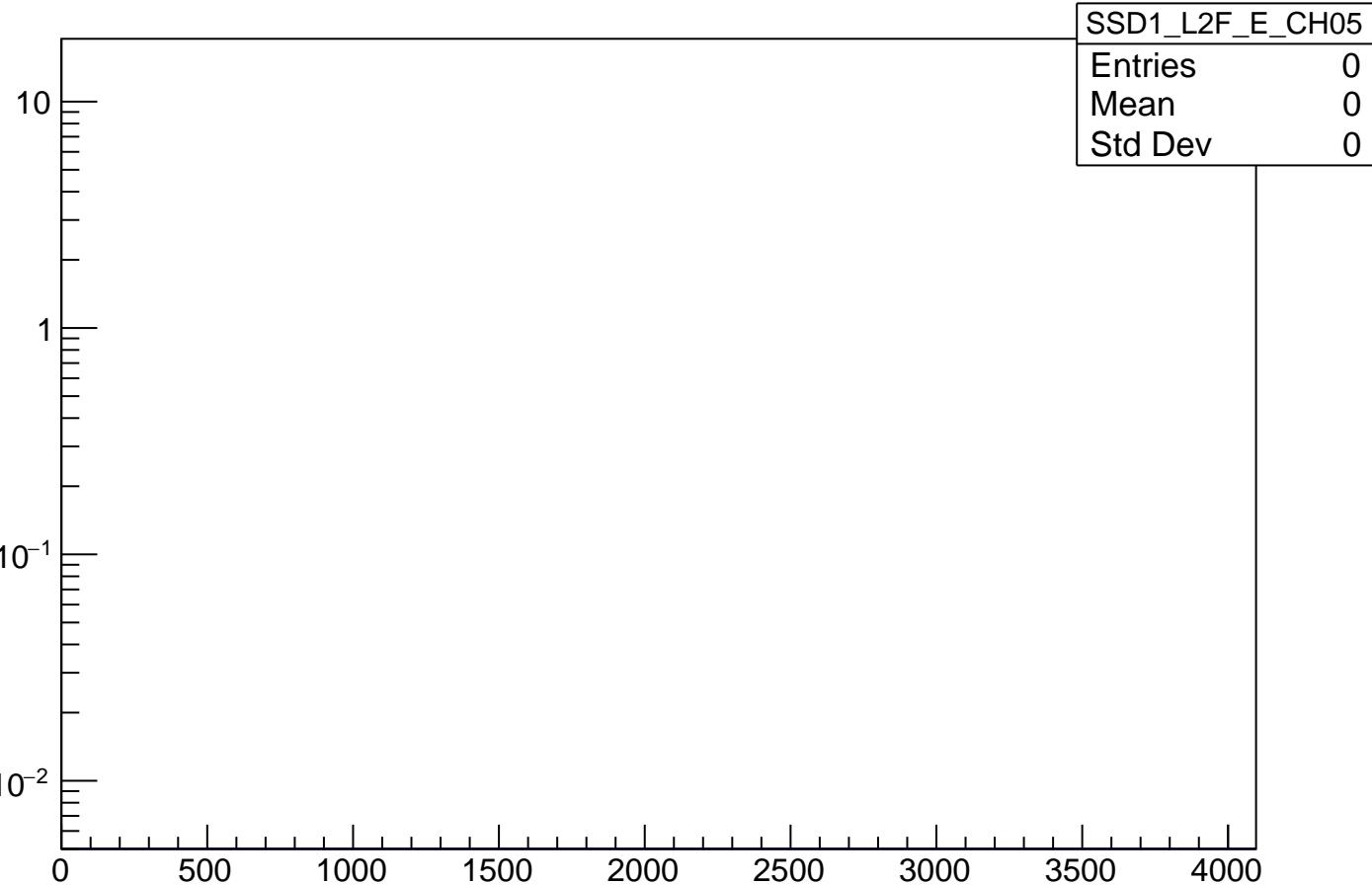
SSD1_L2F_E_CH03



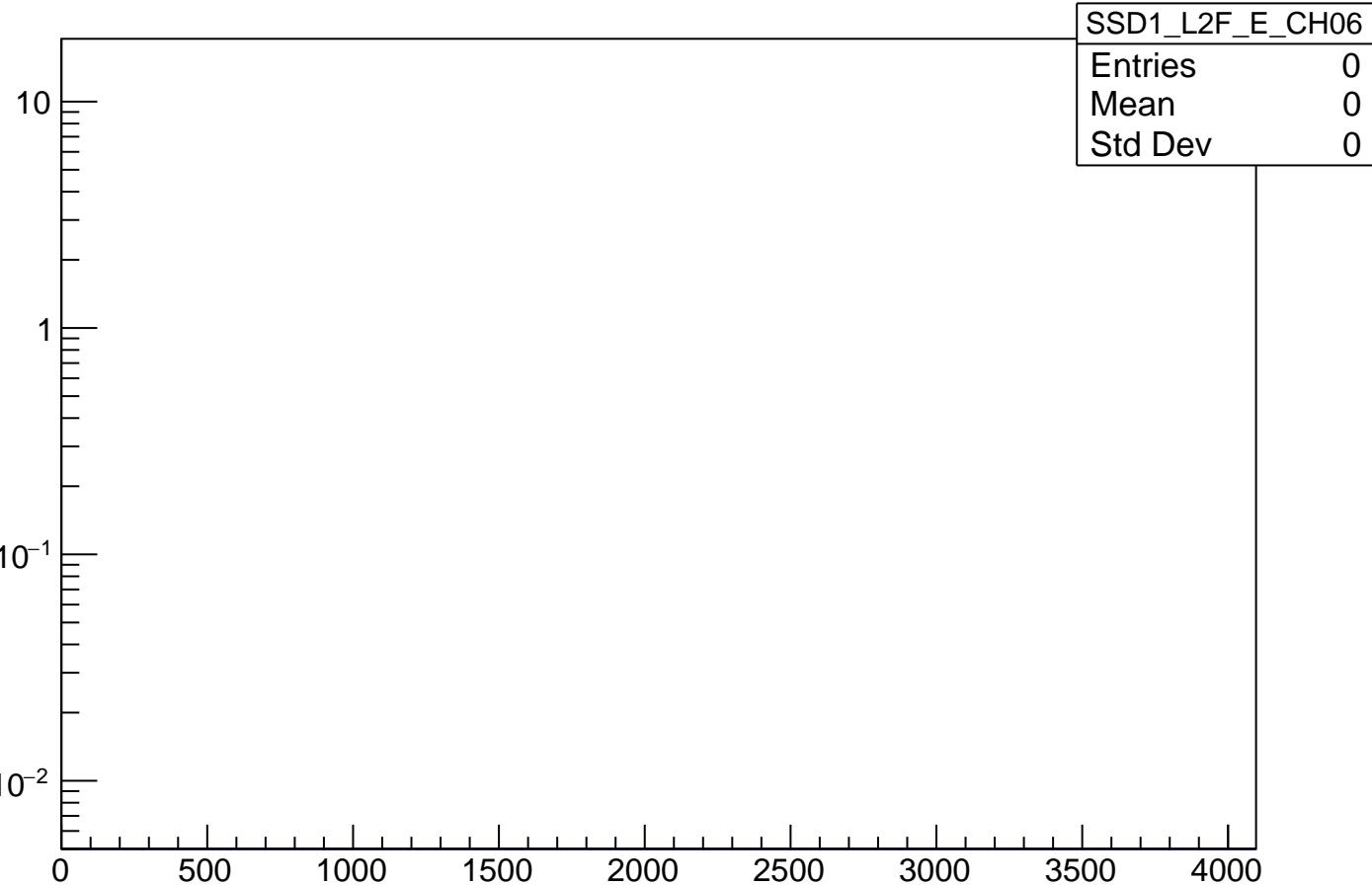
SSD1_L2F_E_CH04



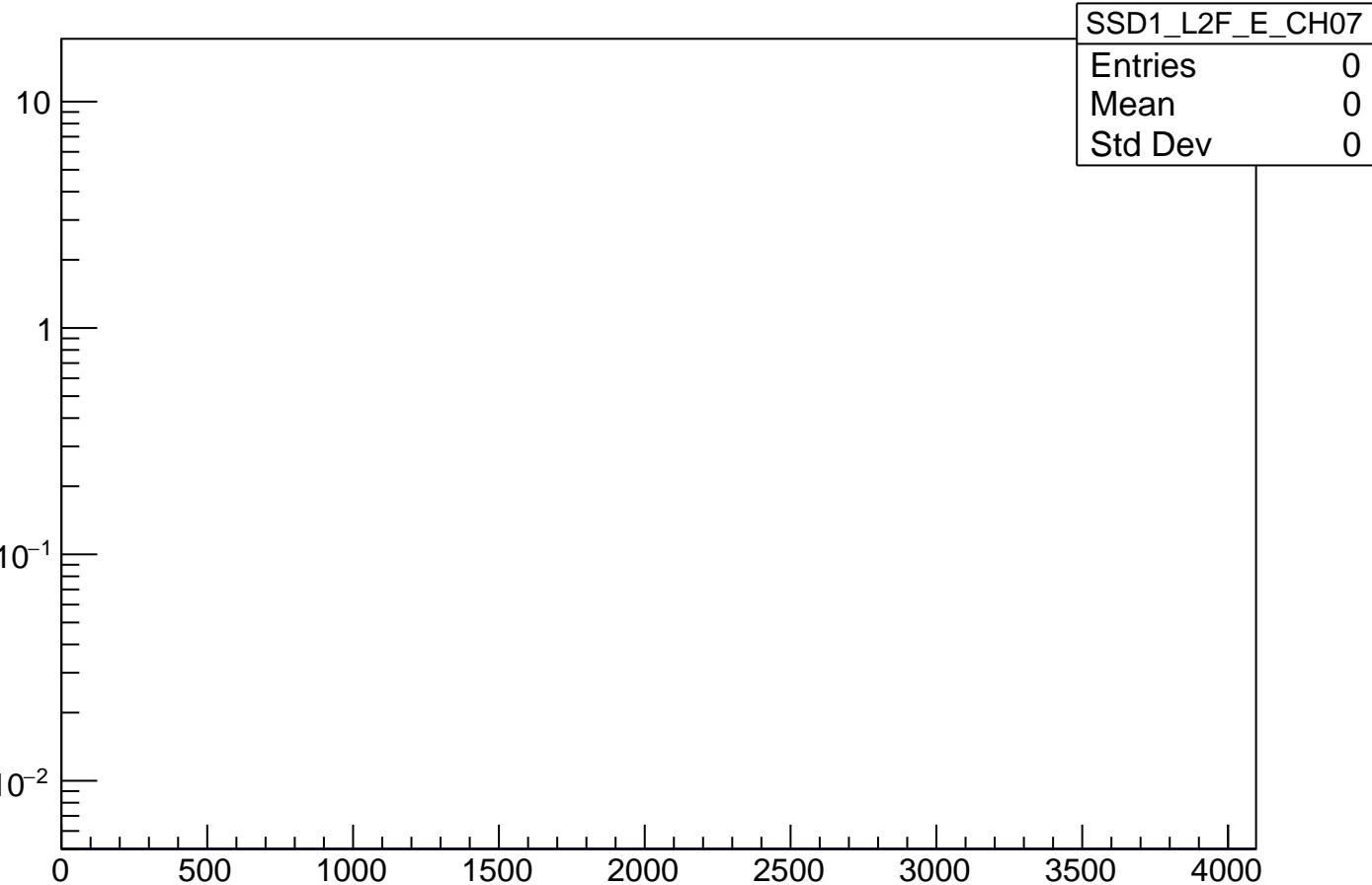
SSD1_L2F_E_CH05



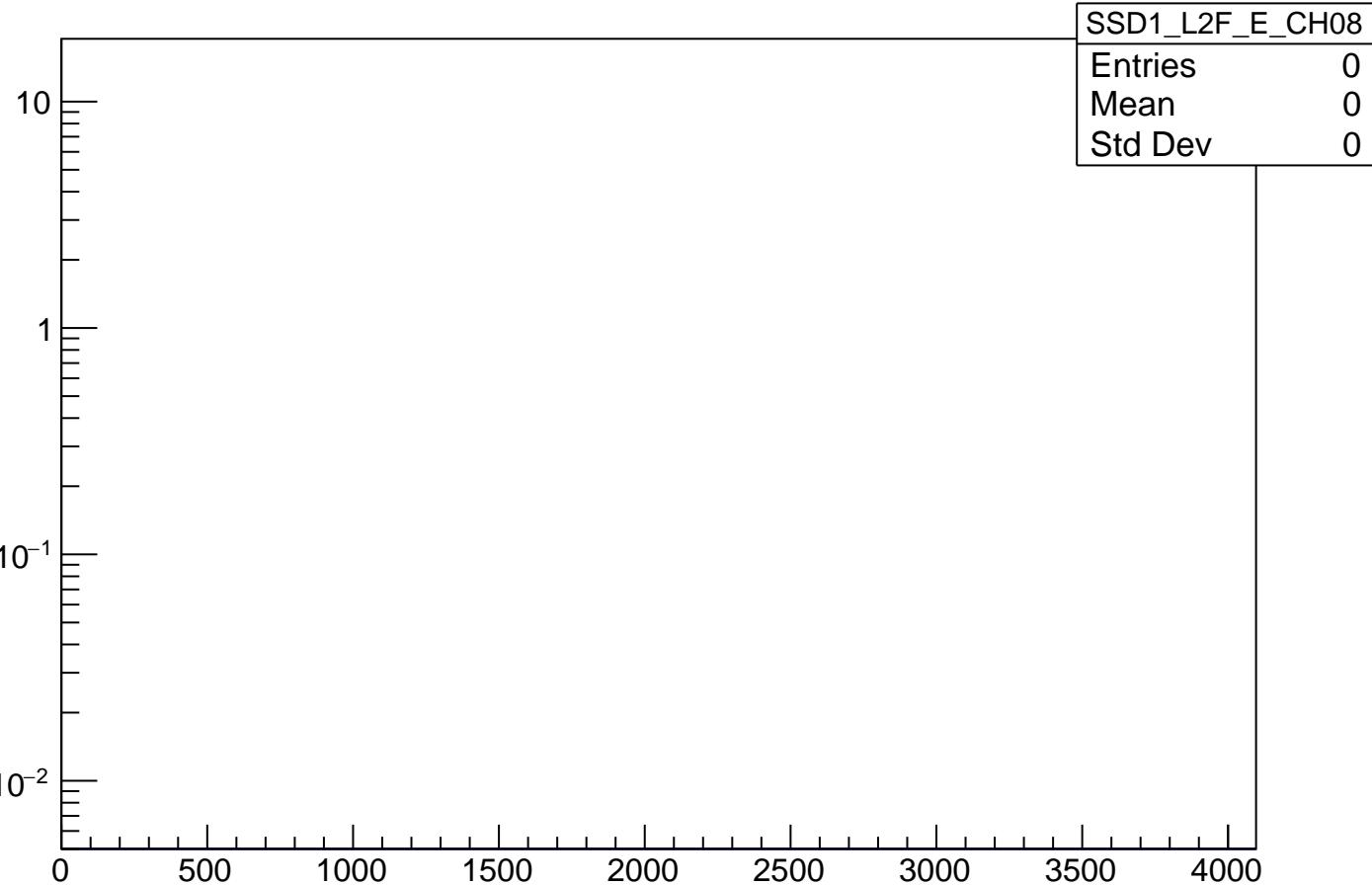
SSD1_L2F_E_CH06



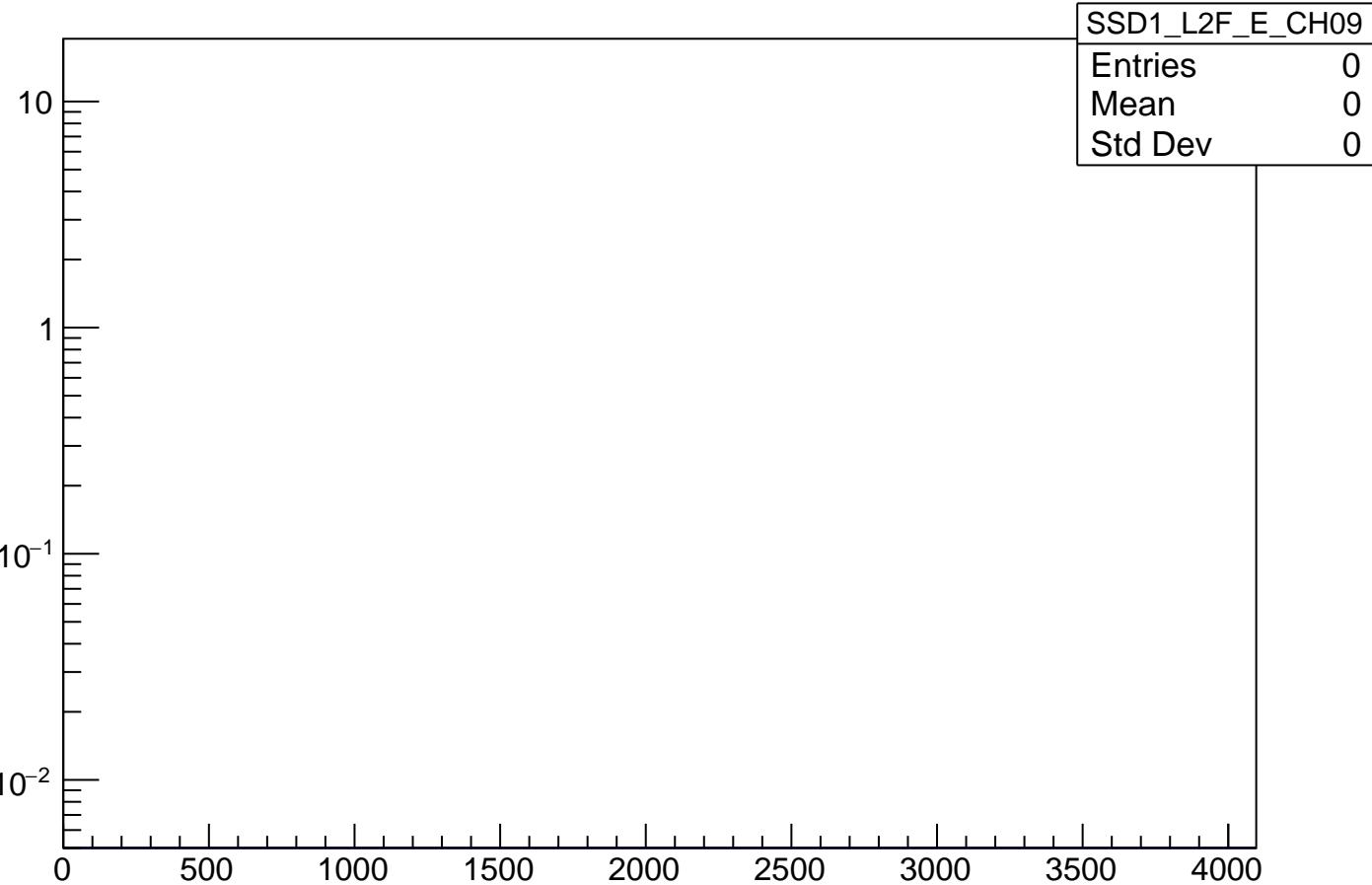
SSD1_L2F_E_CH07



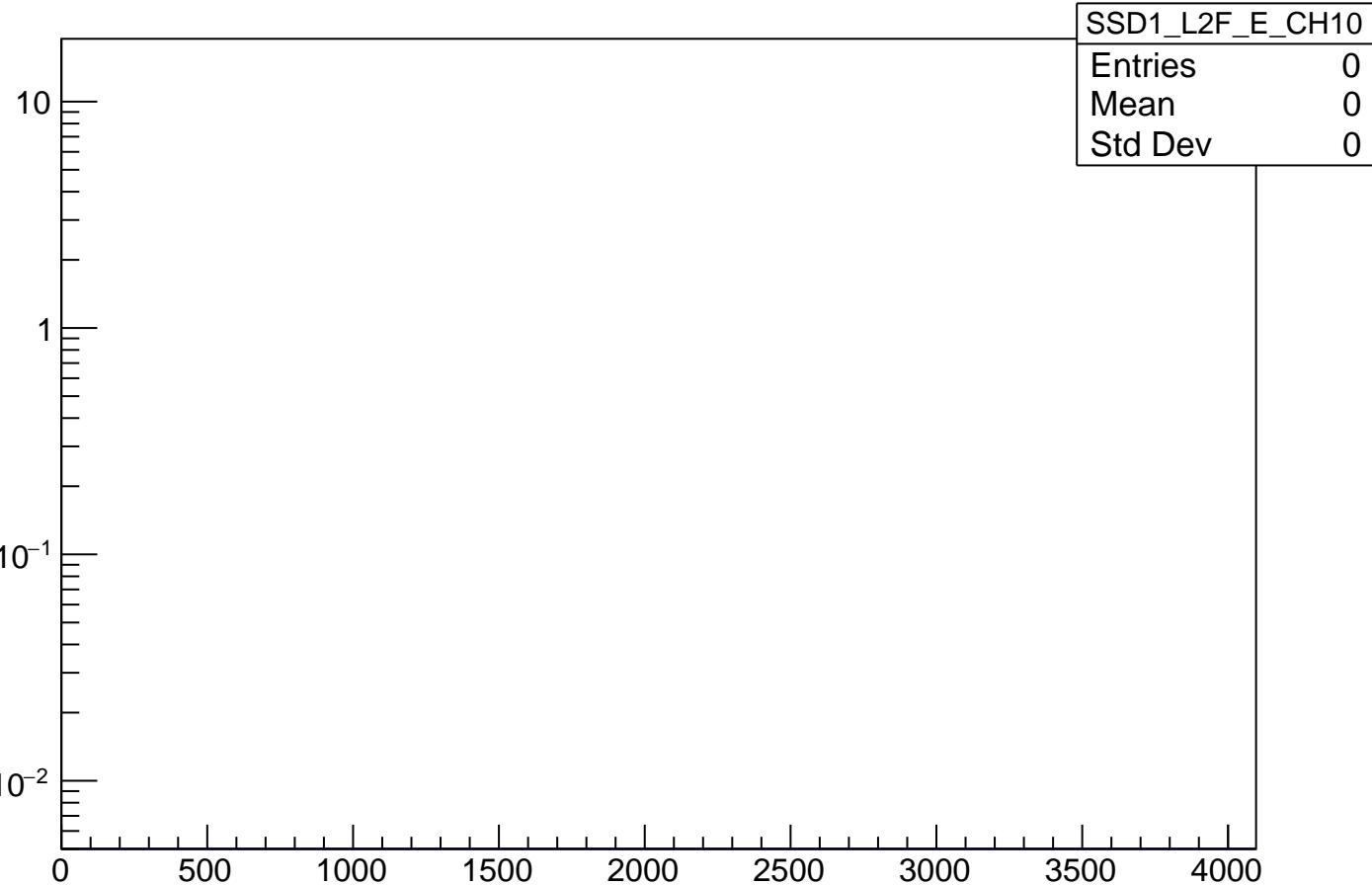
SSD1_L2F_E_CH08



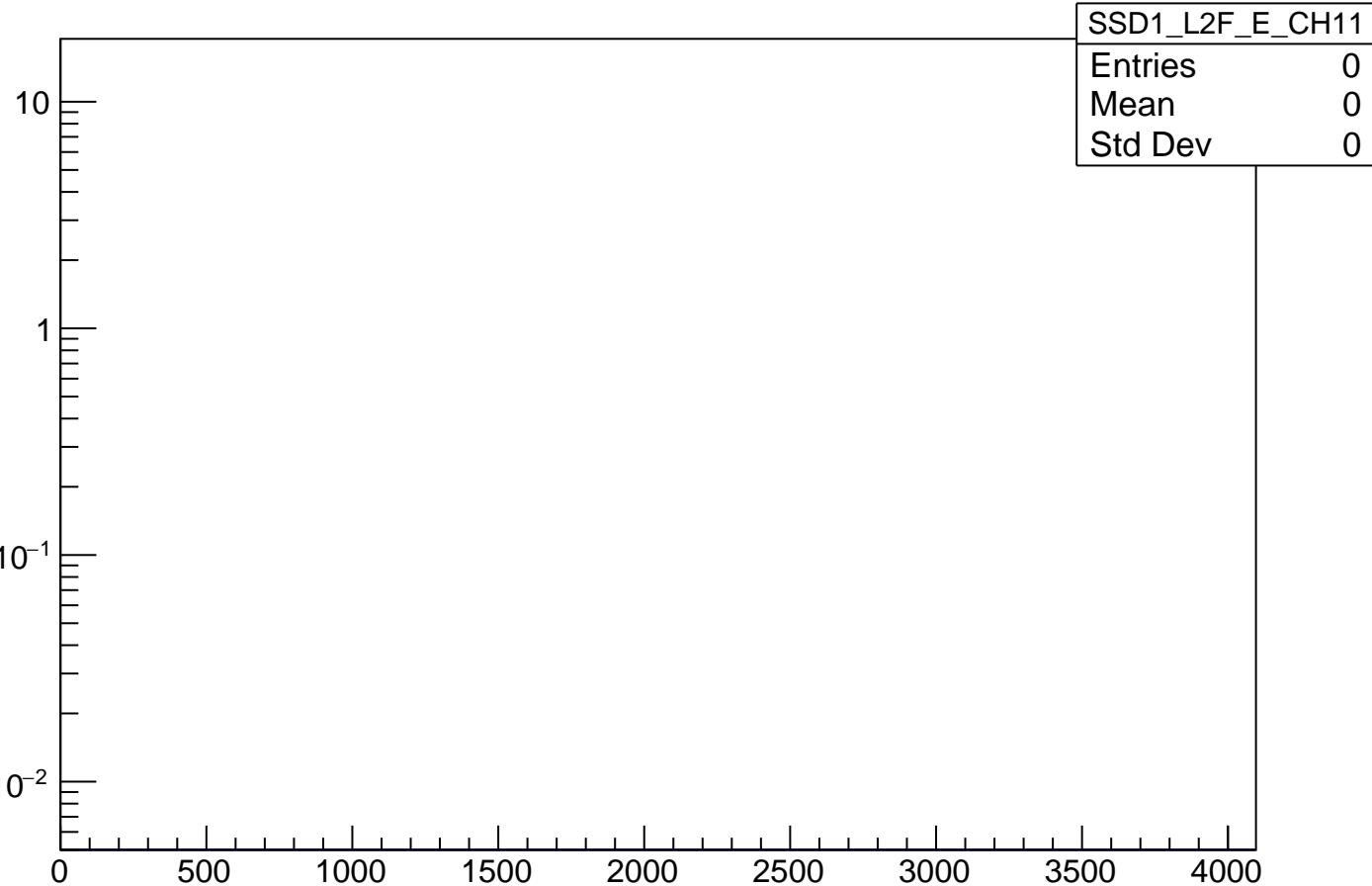
SSD1_L2F_E_CH09



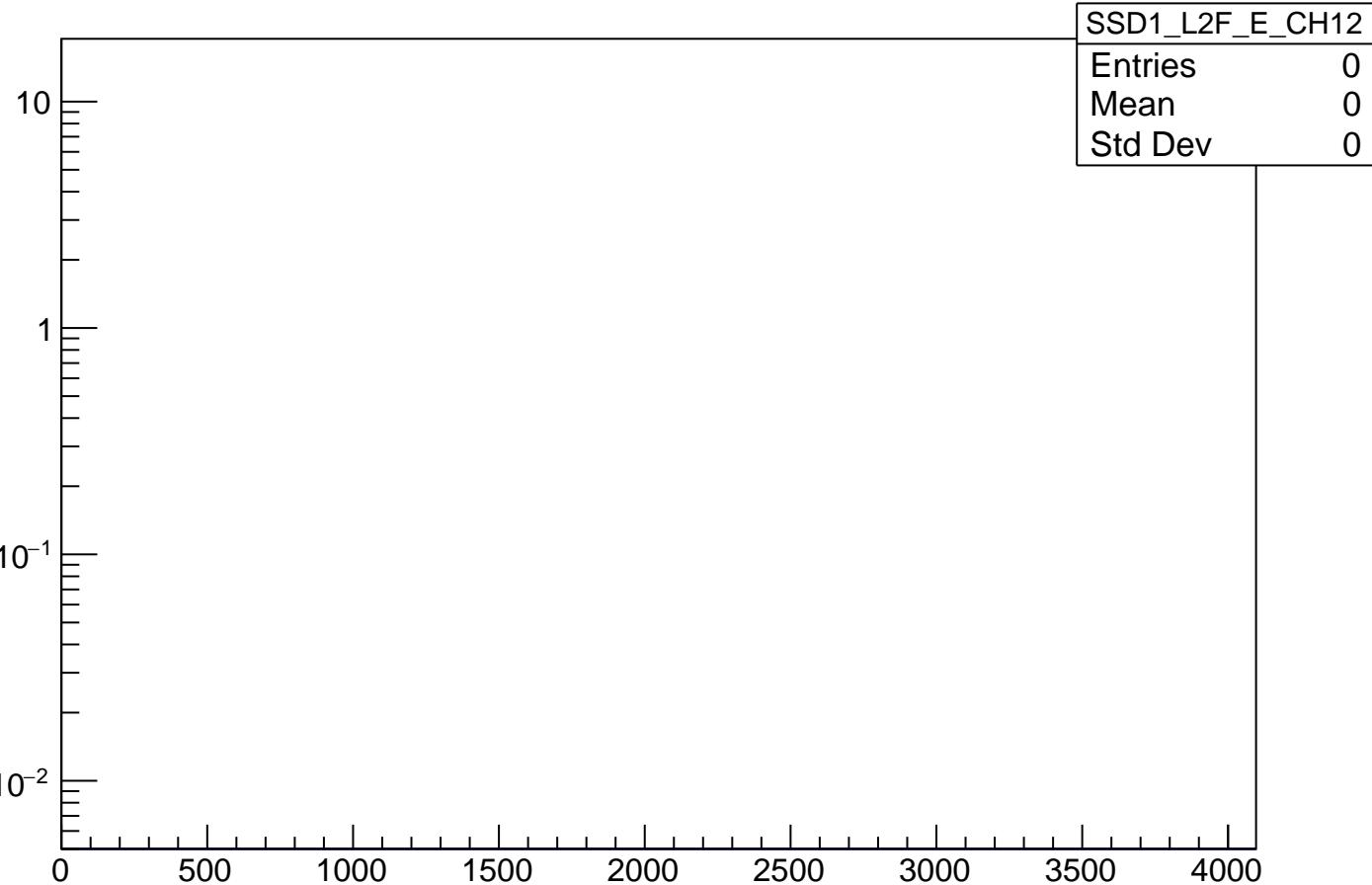
SSD1_L2F_E_CH10



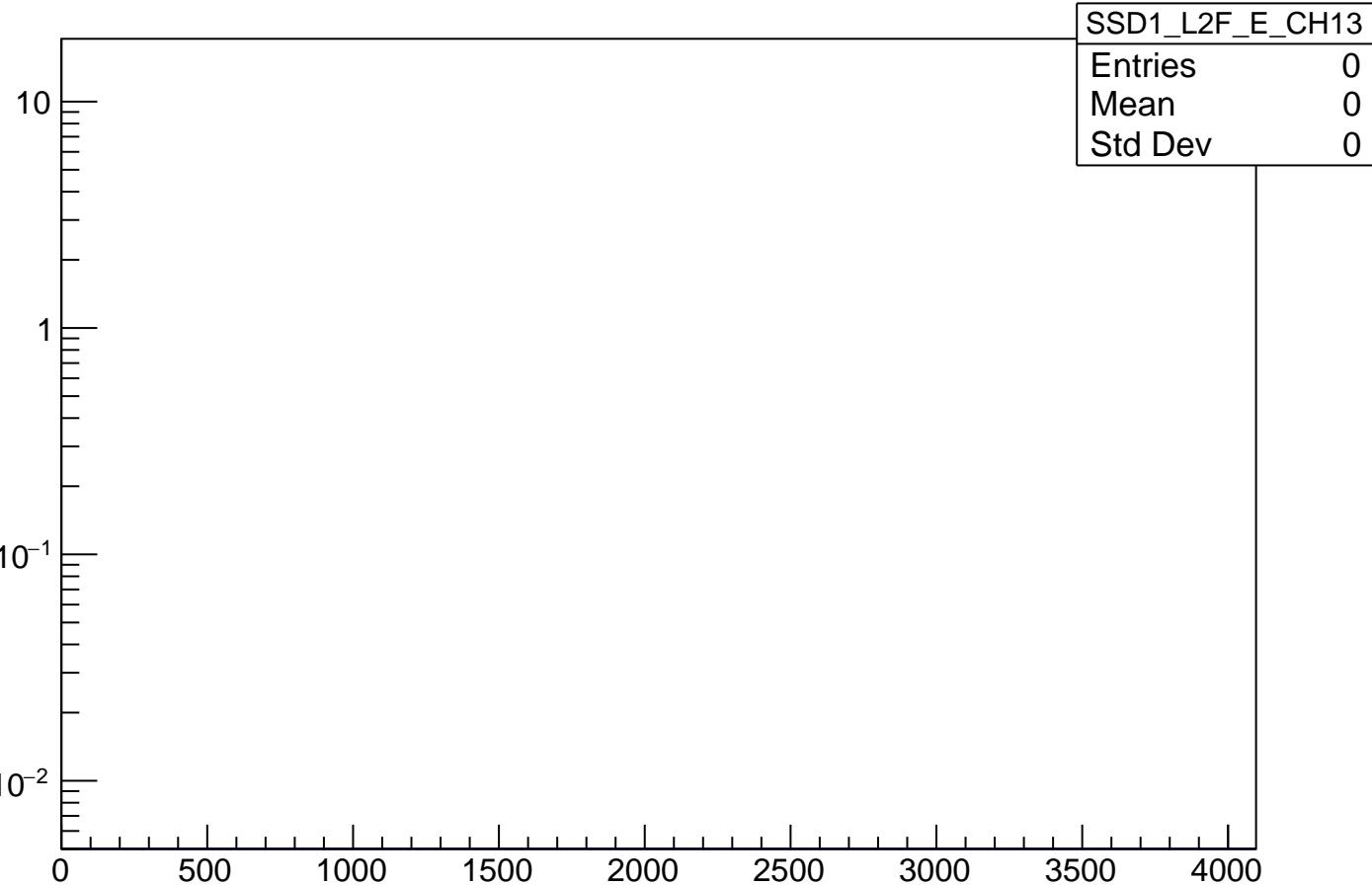
SSD1_L2F_E_CH11



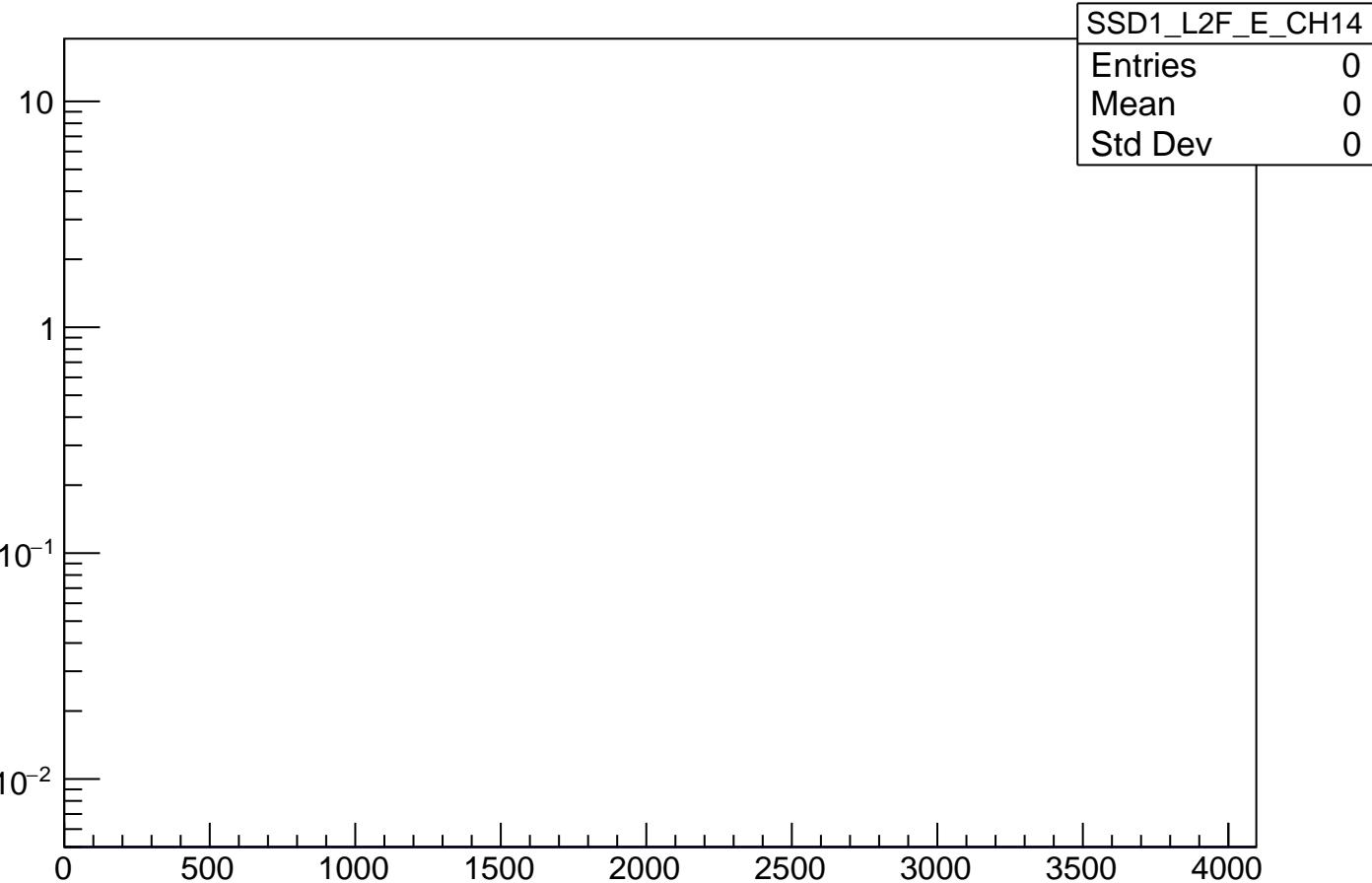
SSD1_L2F_E_CH12



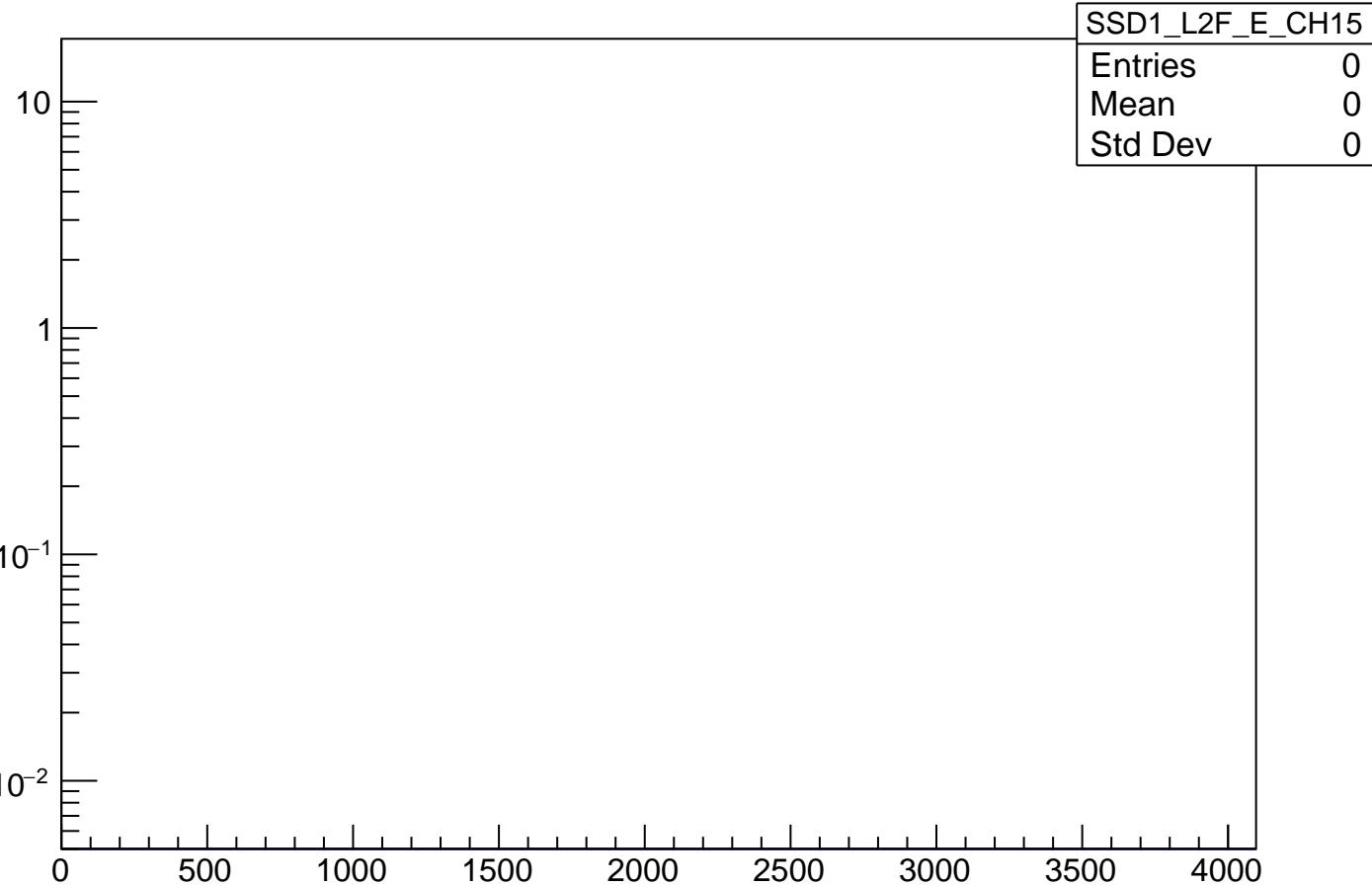
SSD1_L2F_E_CH13



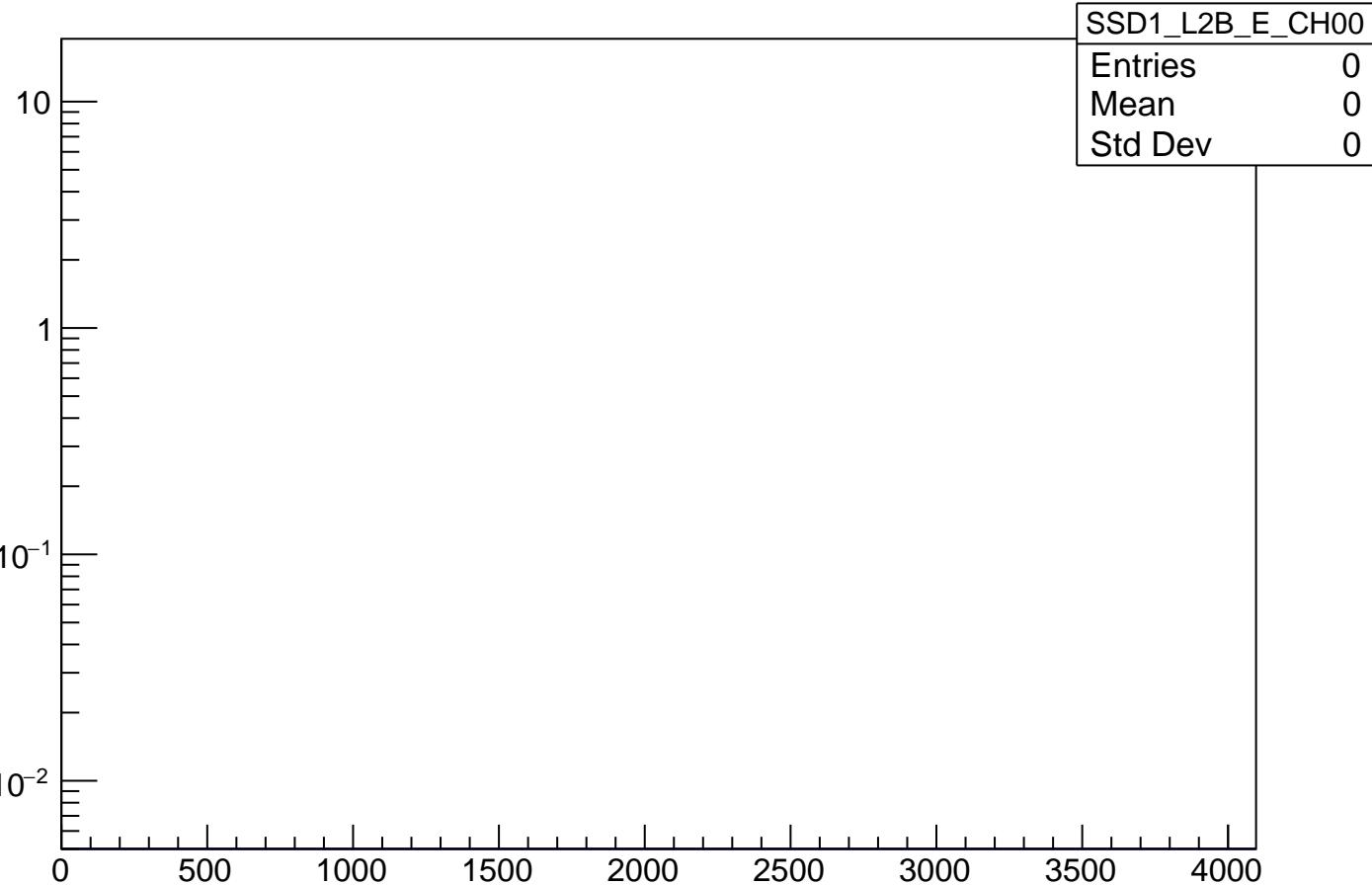
SSD1_L2F_E_CH14



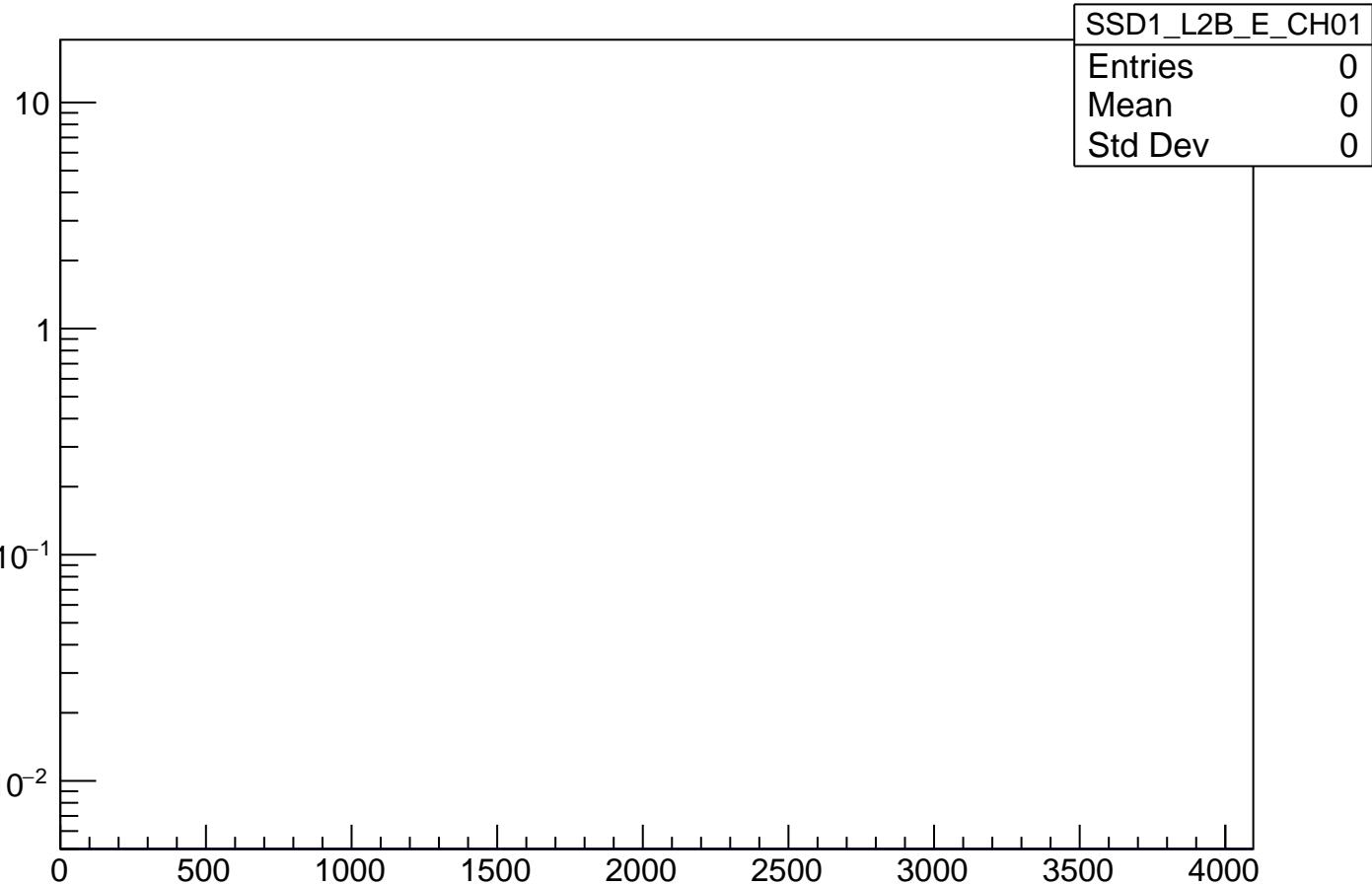
SSD1_L2F_E_CH15



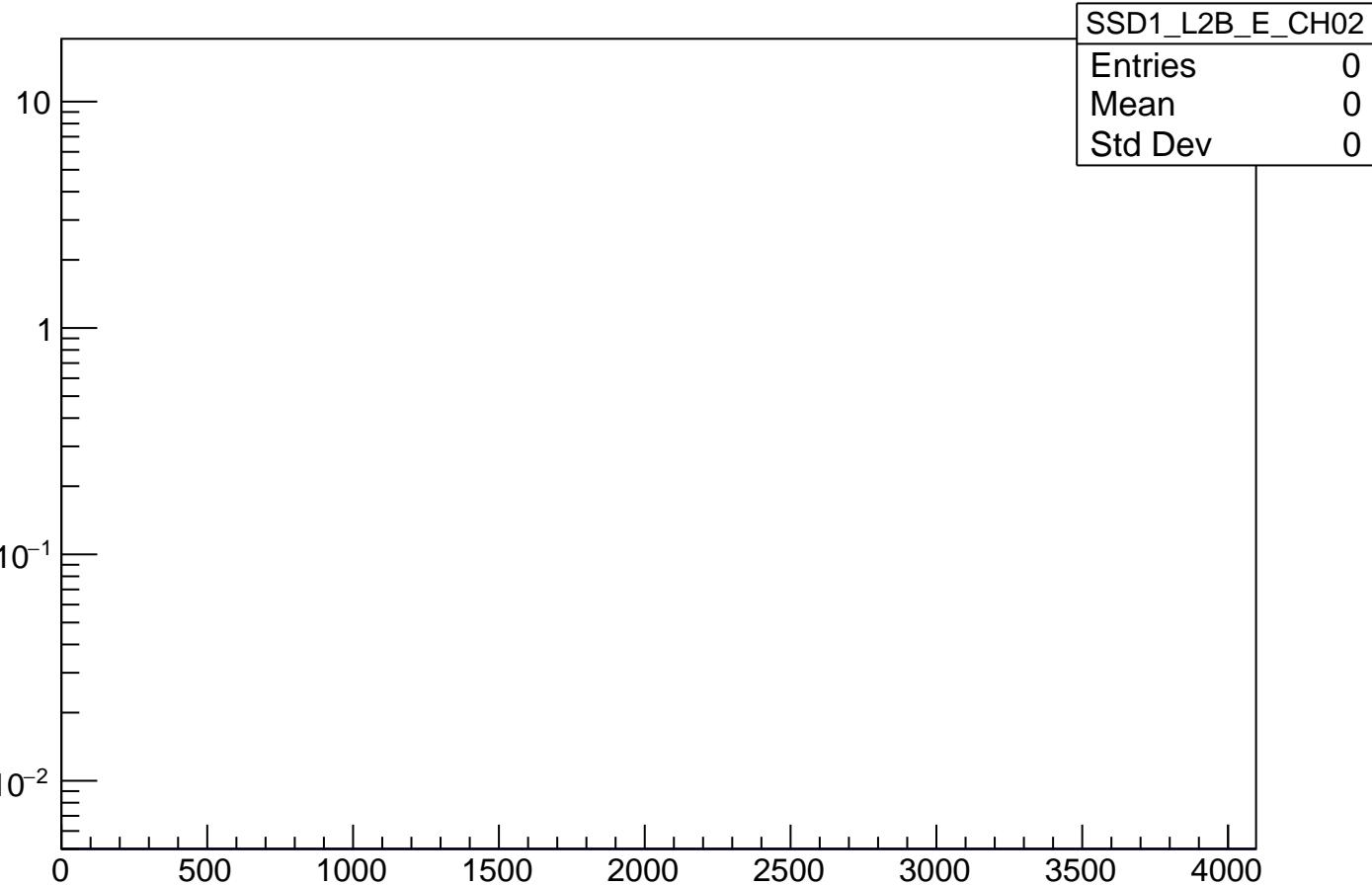
SSD1_L2B_E_CH00



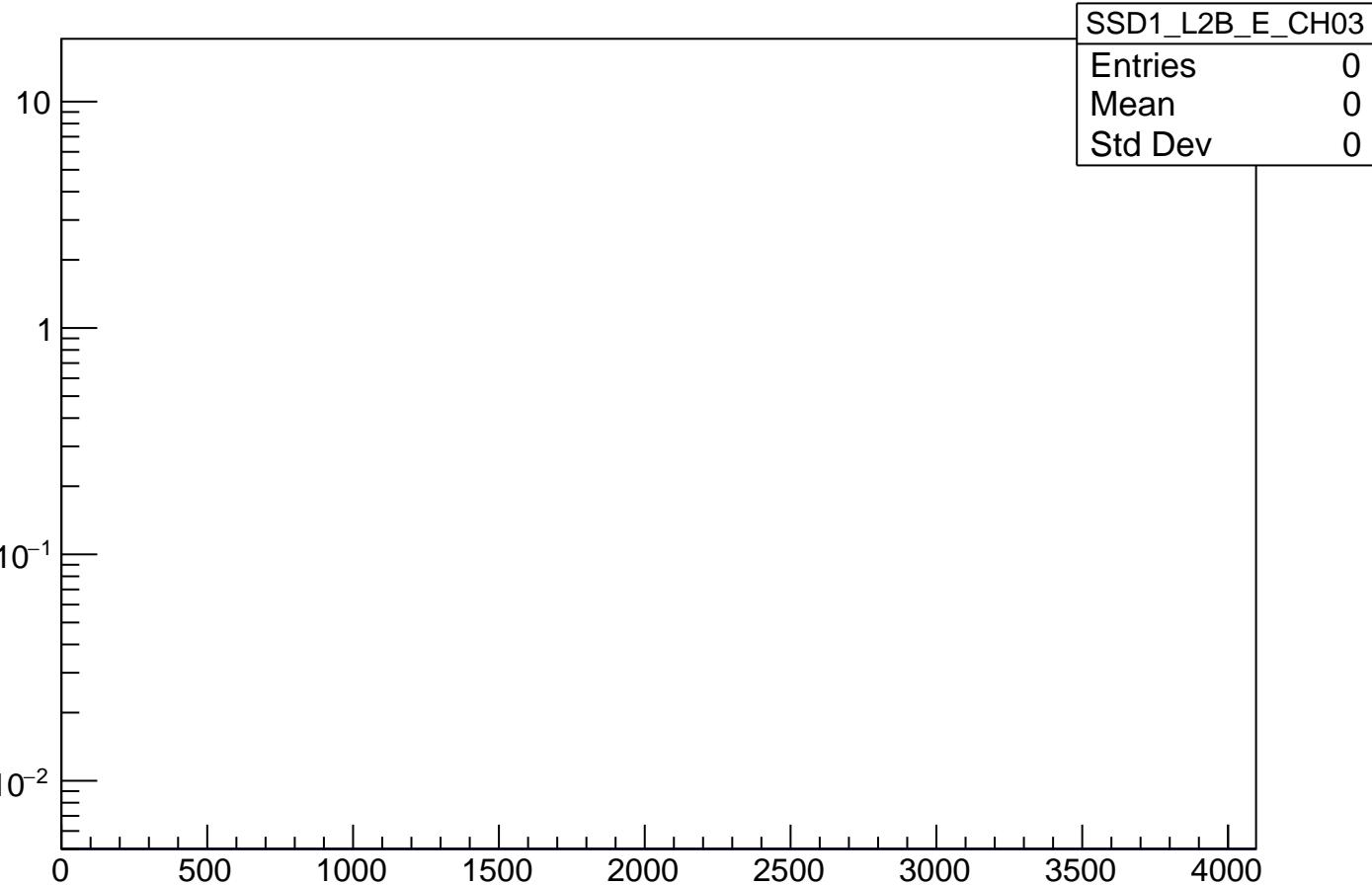
SSD1_L2B_E_CH01



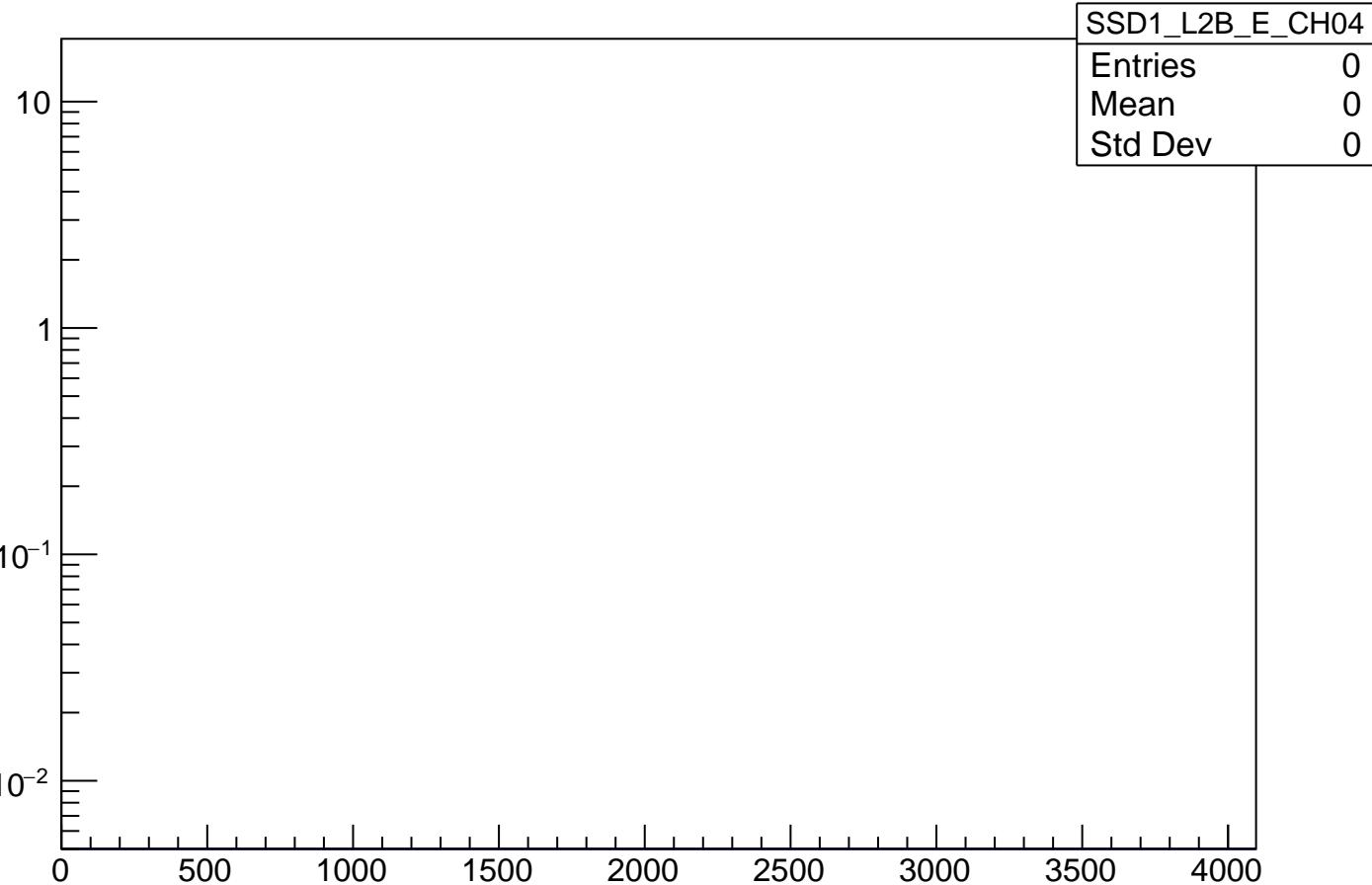
SSD1_L2B_E_CH02



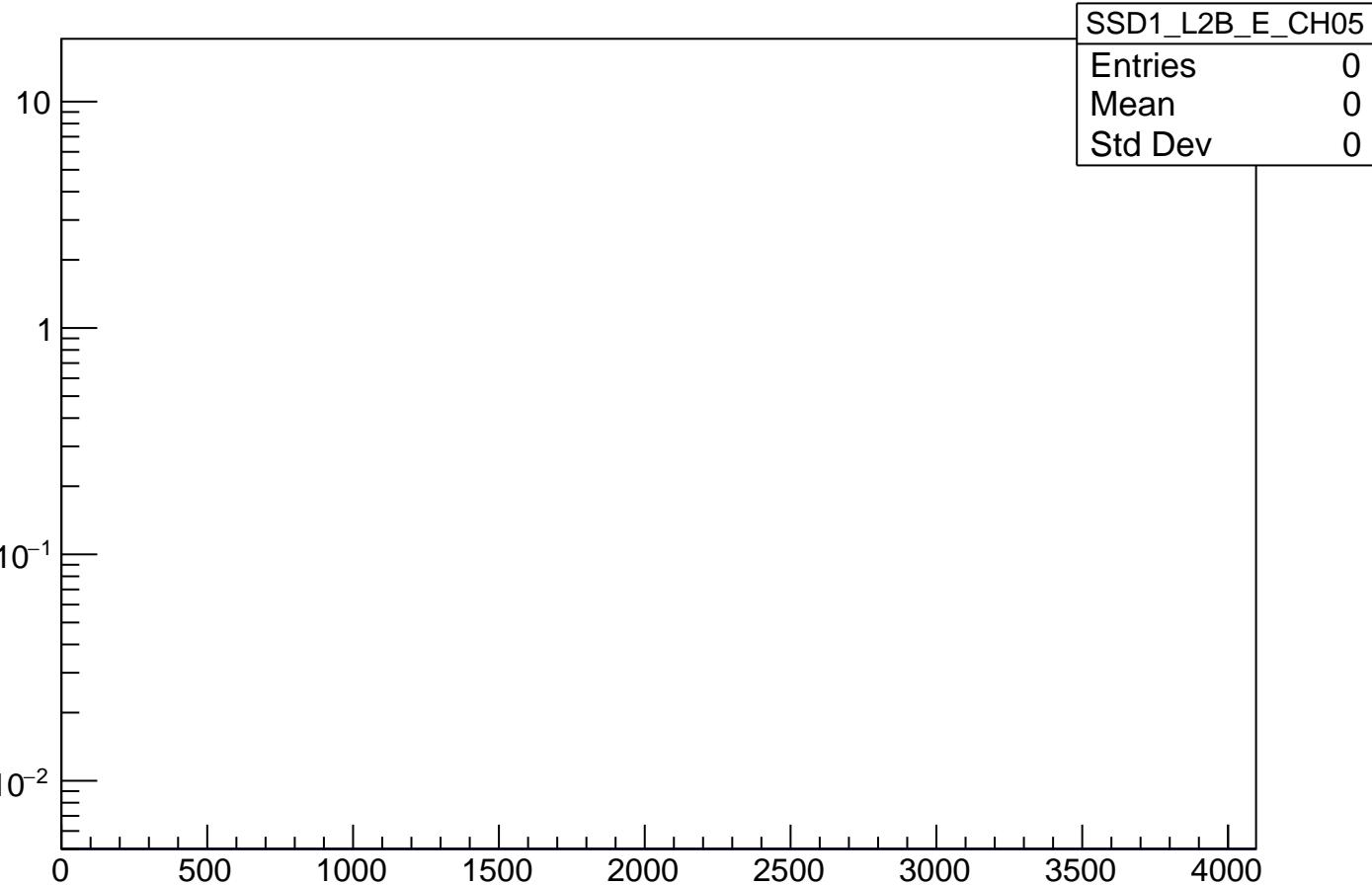
SSD1_L2B_E_CH03



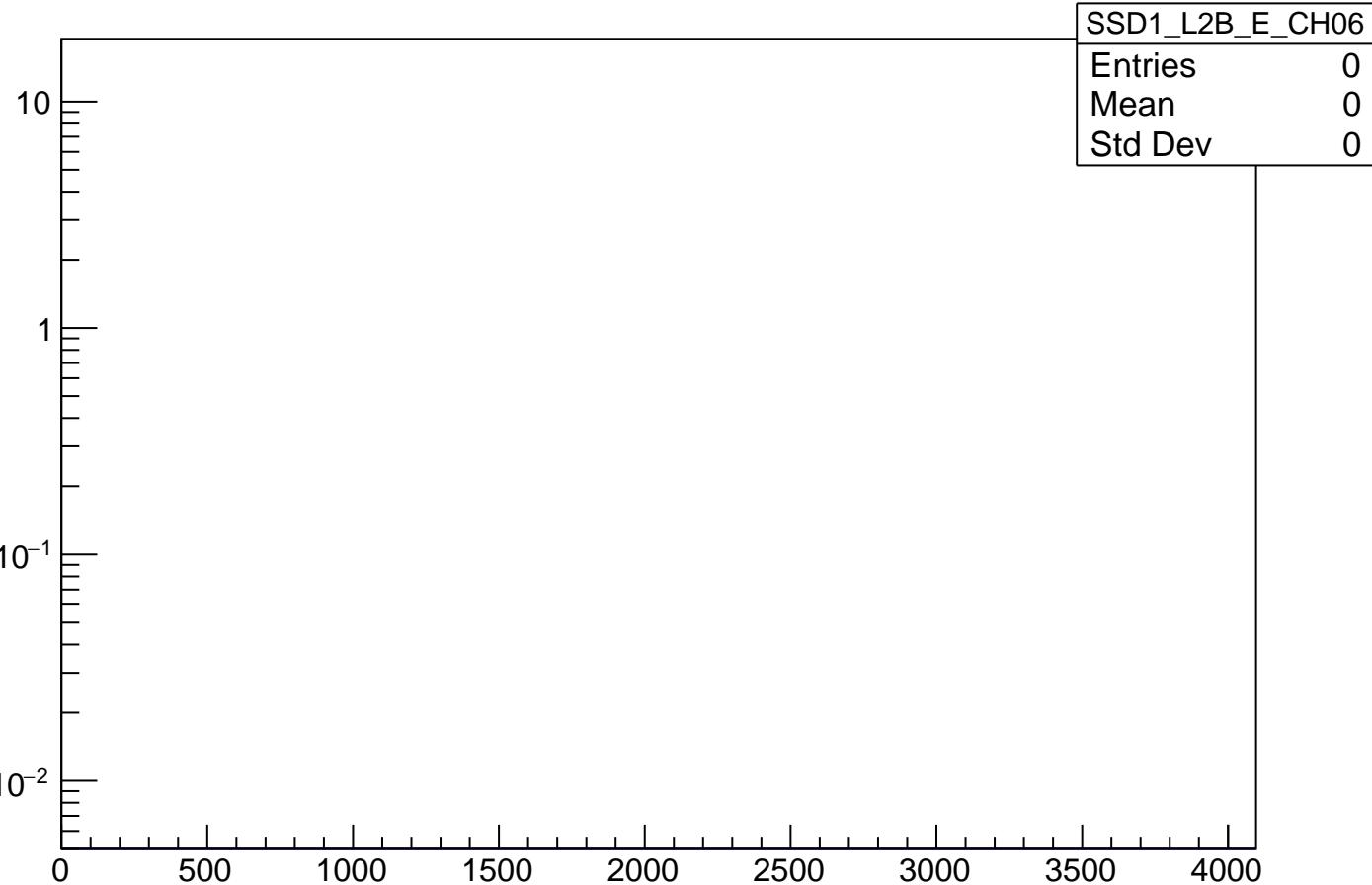
SSD1_L2B_E_CH04



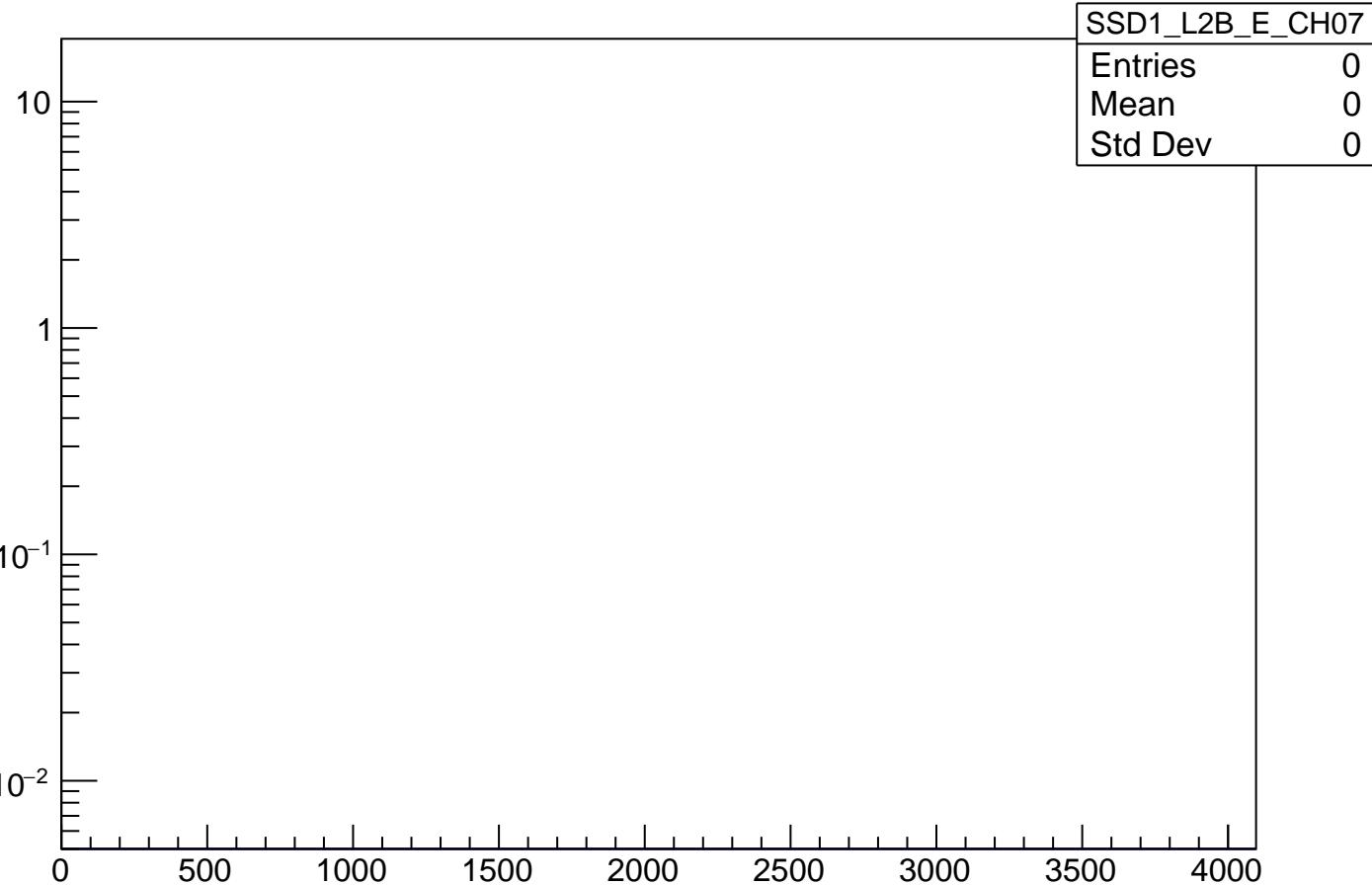
SSD1_L2B_E_CH05



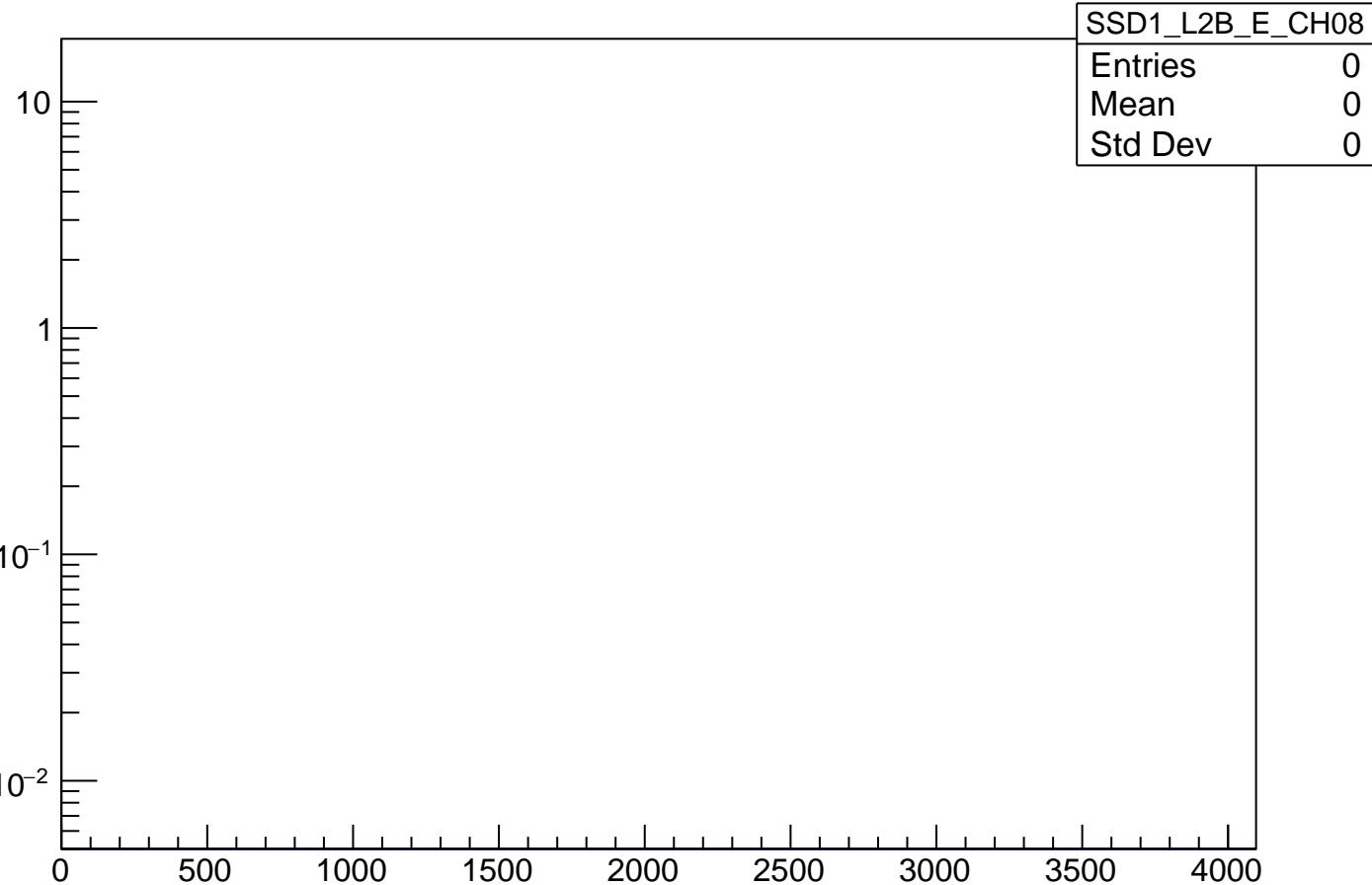
SSD1_L2B_E_CH06



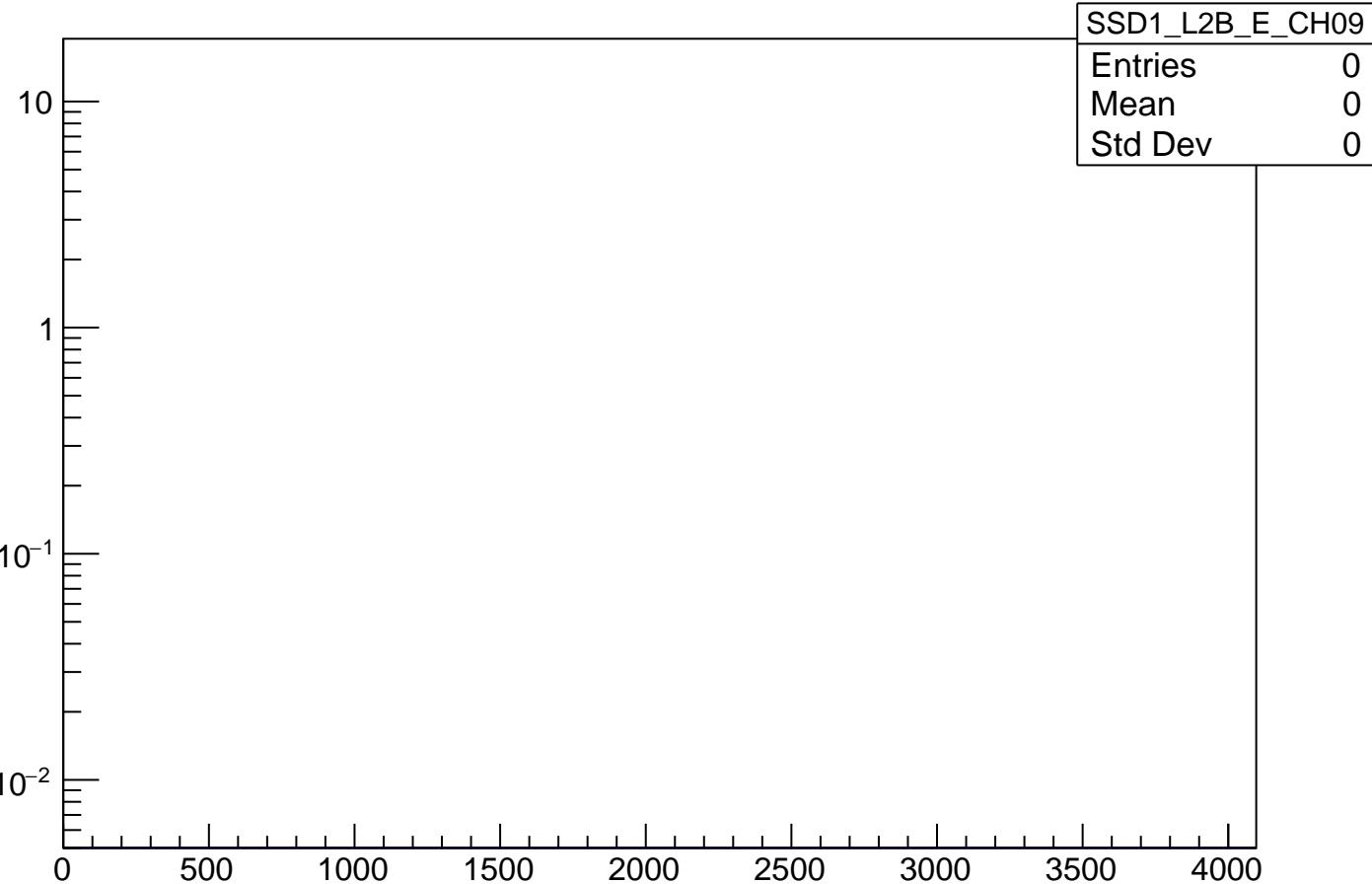
SSD1_L2B_E_CH07



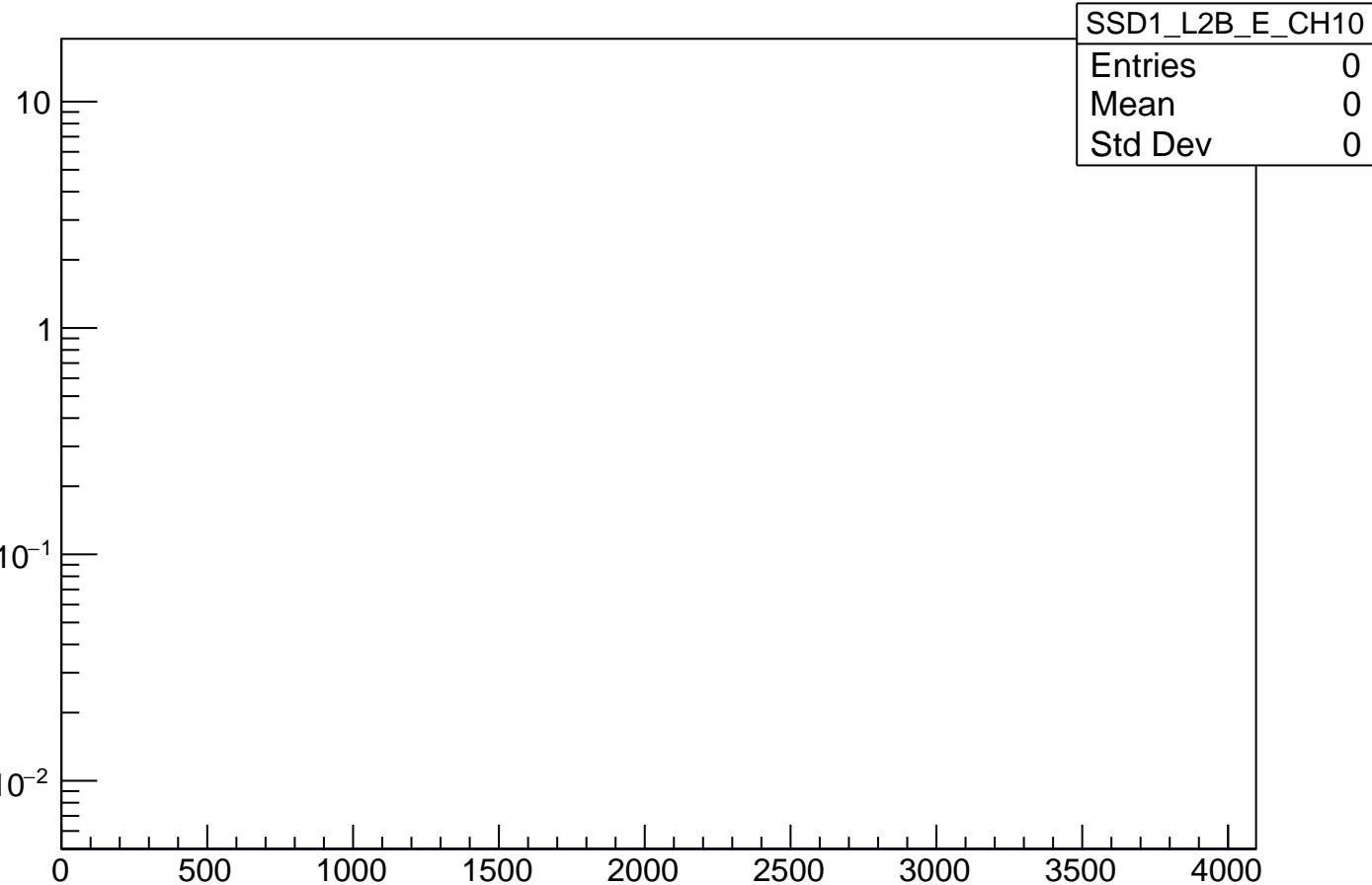
SSD1_L2B_E_CH08



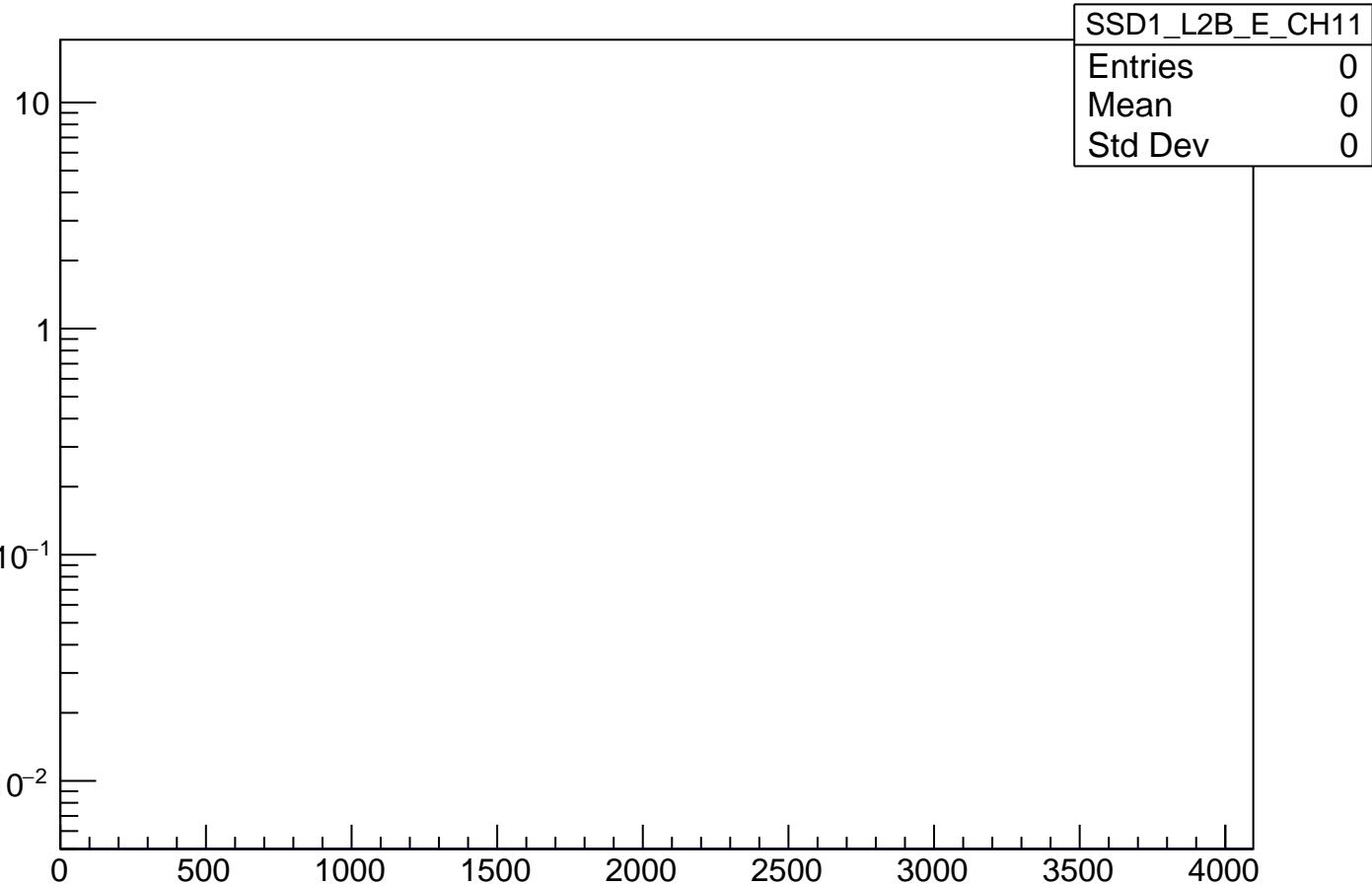
SSD1_L2B_E_CH09



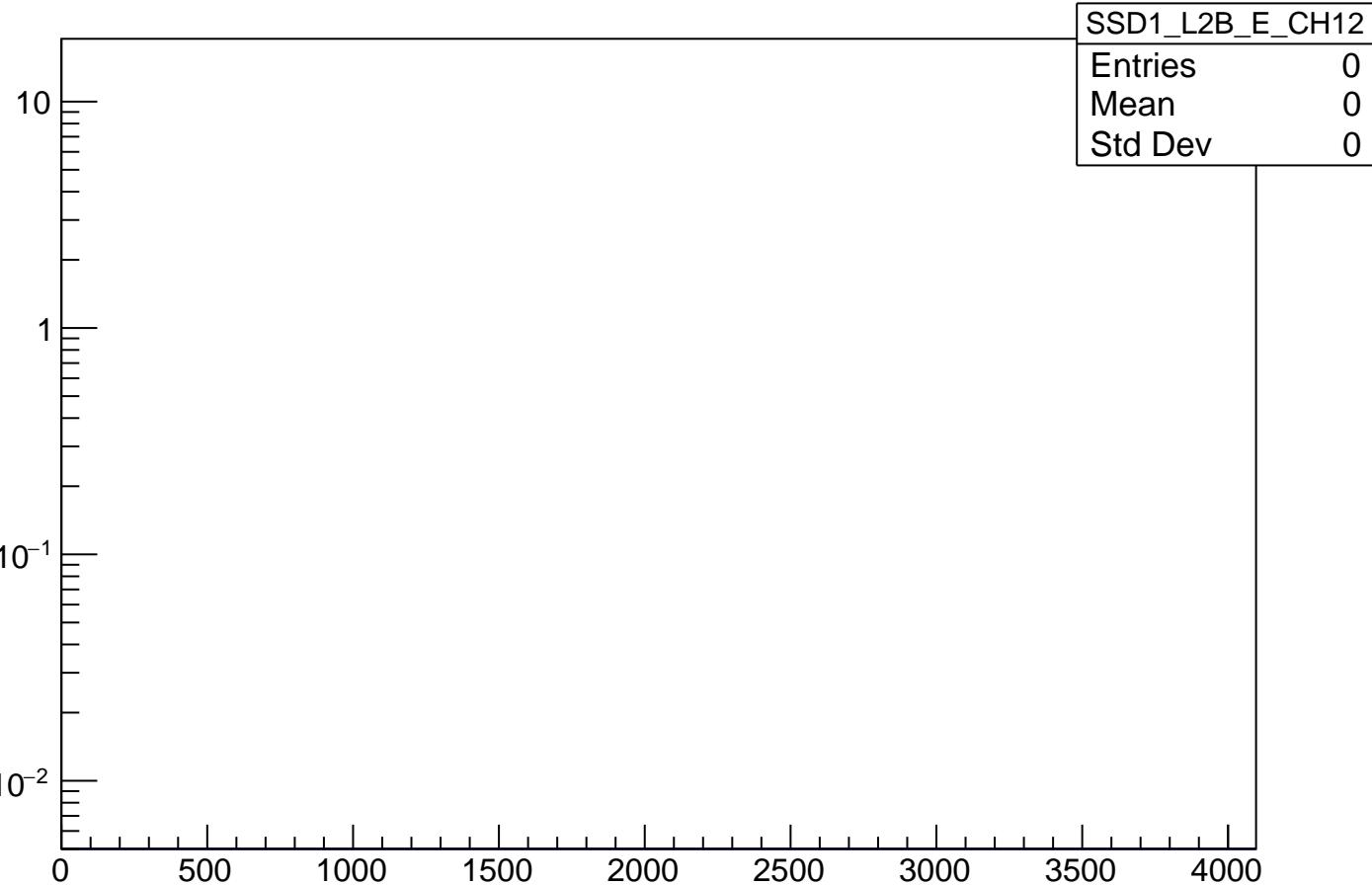
SSD1_L2B_E_CH10



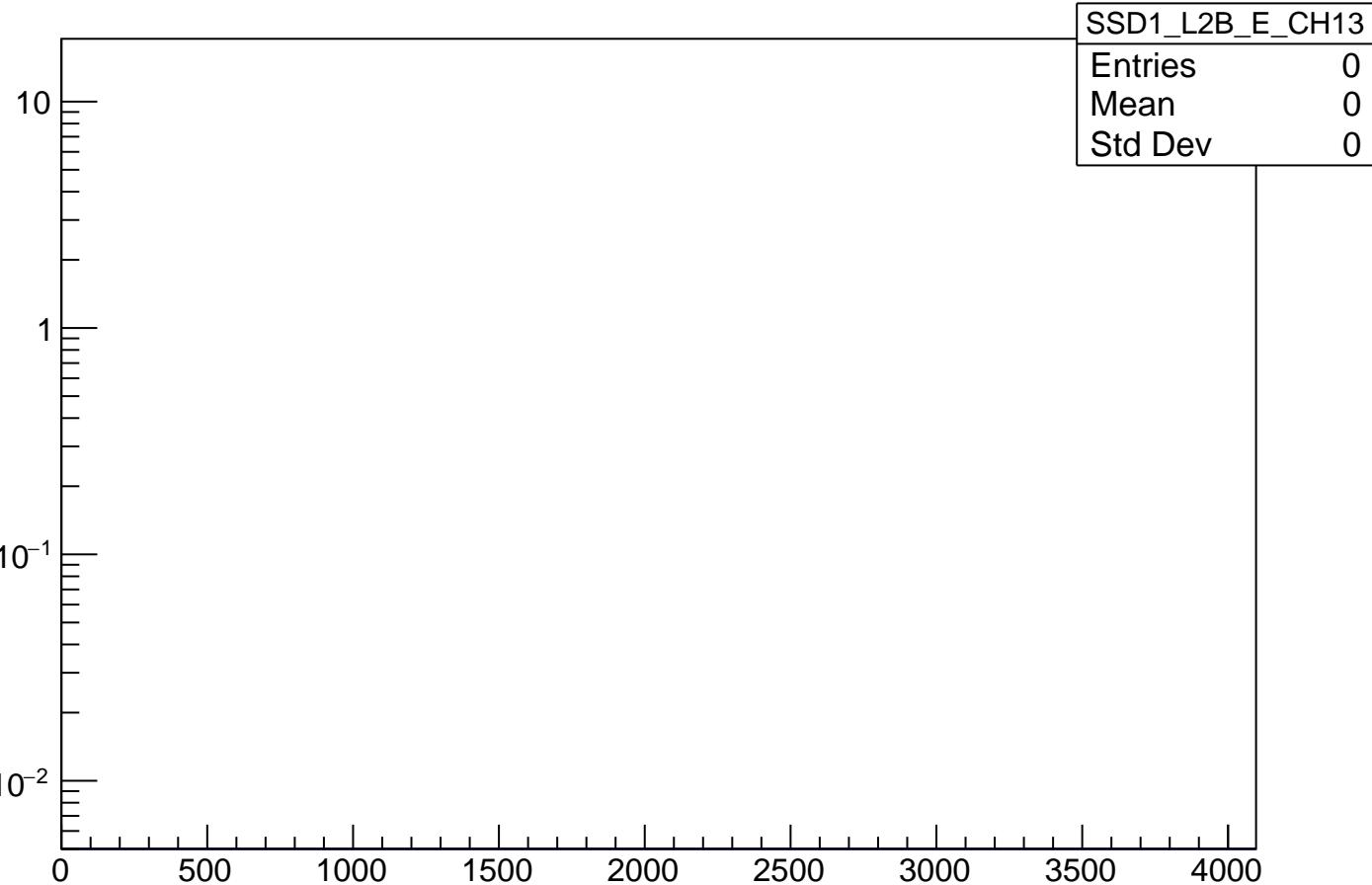
SSD1_L2B_E_CH11



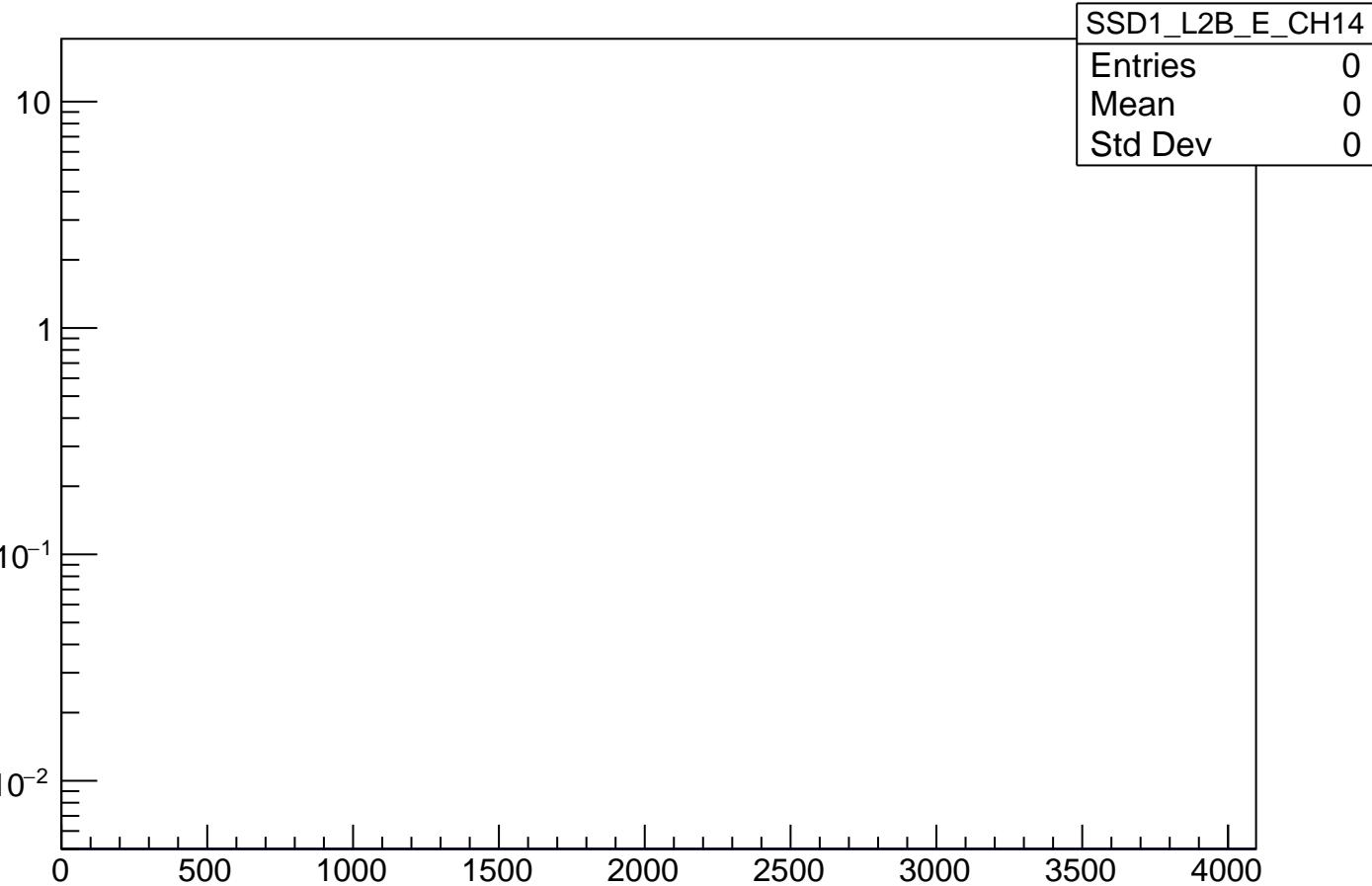
SSD1_L2B_E_CH12



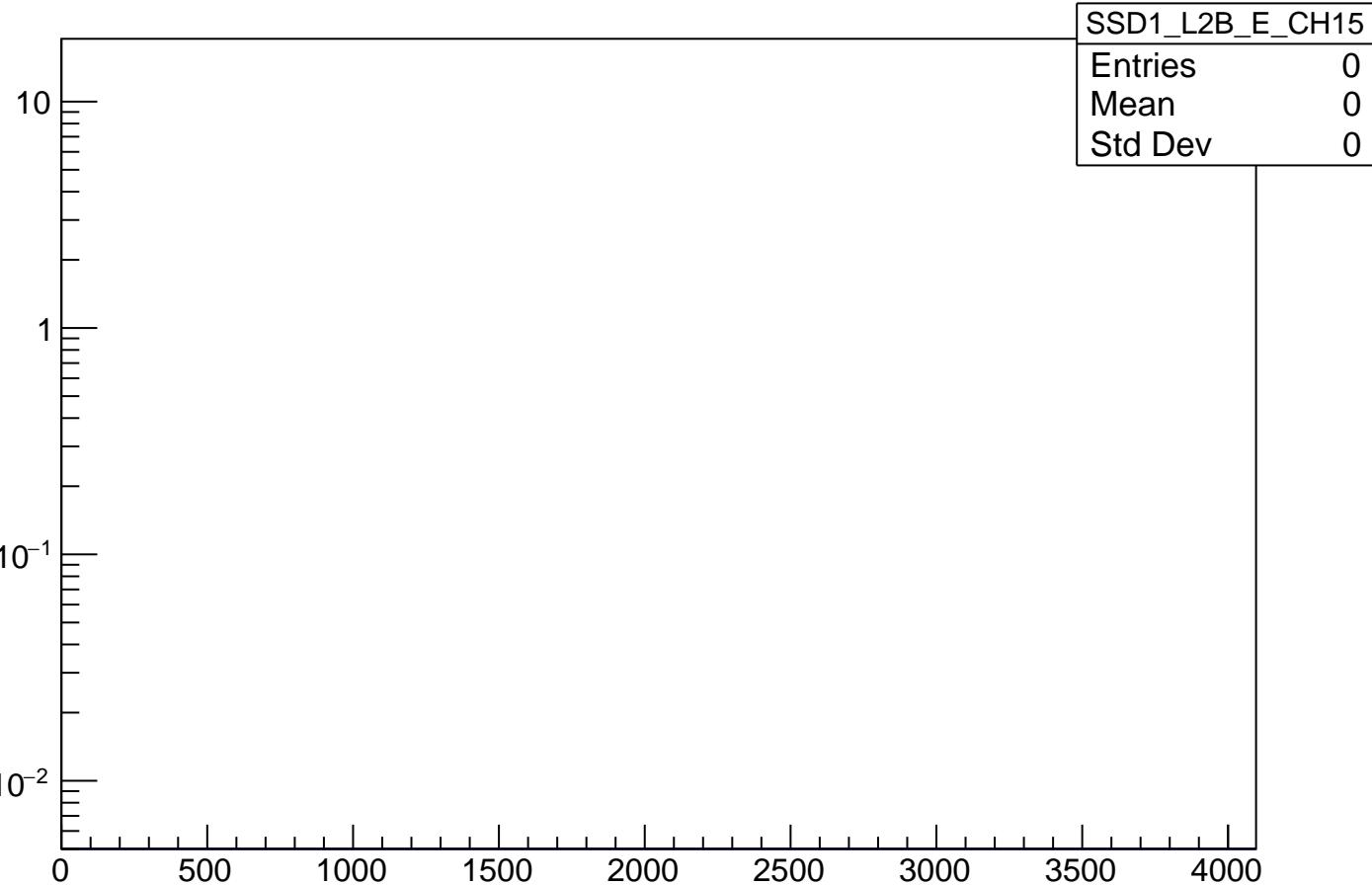
SSD1_L2B_E_CH13



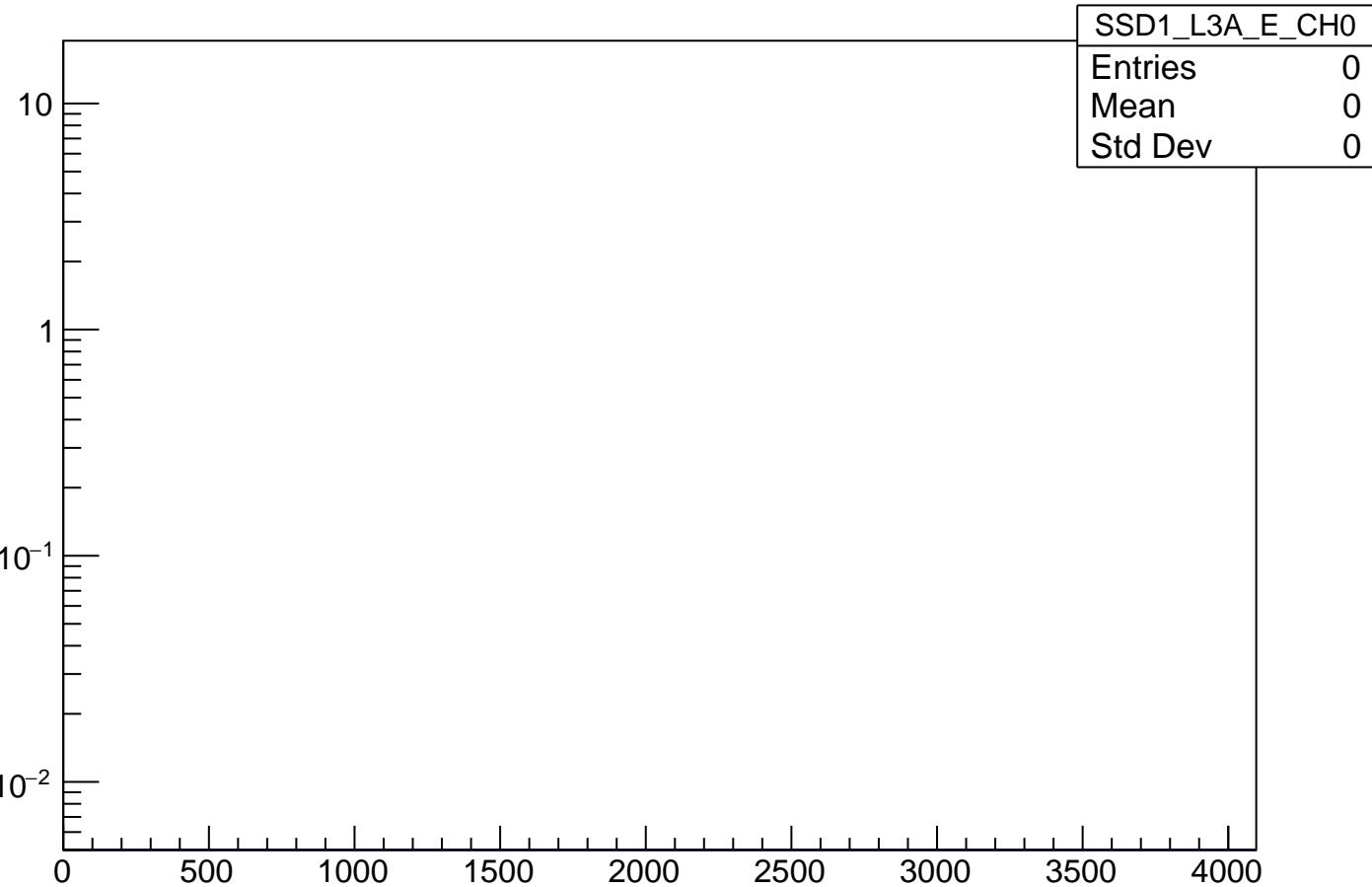
SSD1_L2B_E_CH14



SSD1_L2B_E_CH15

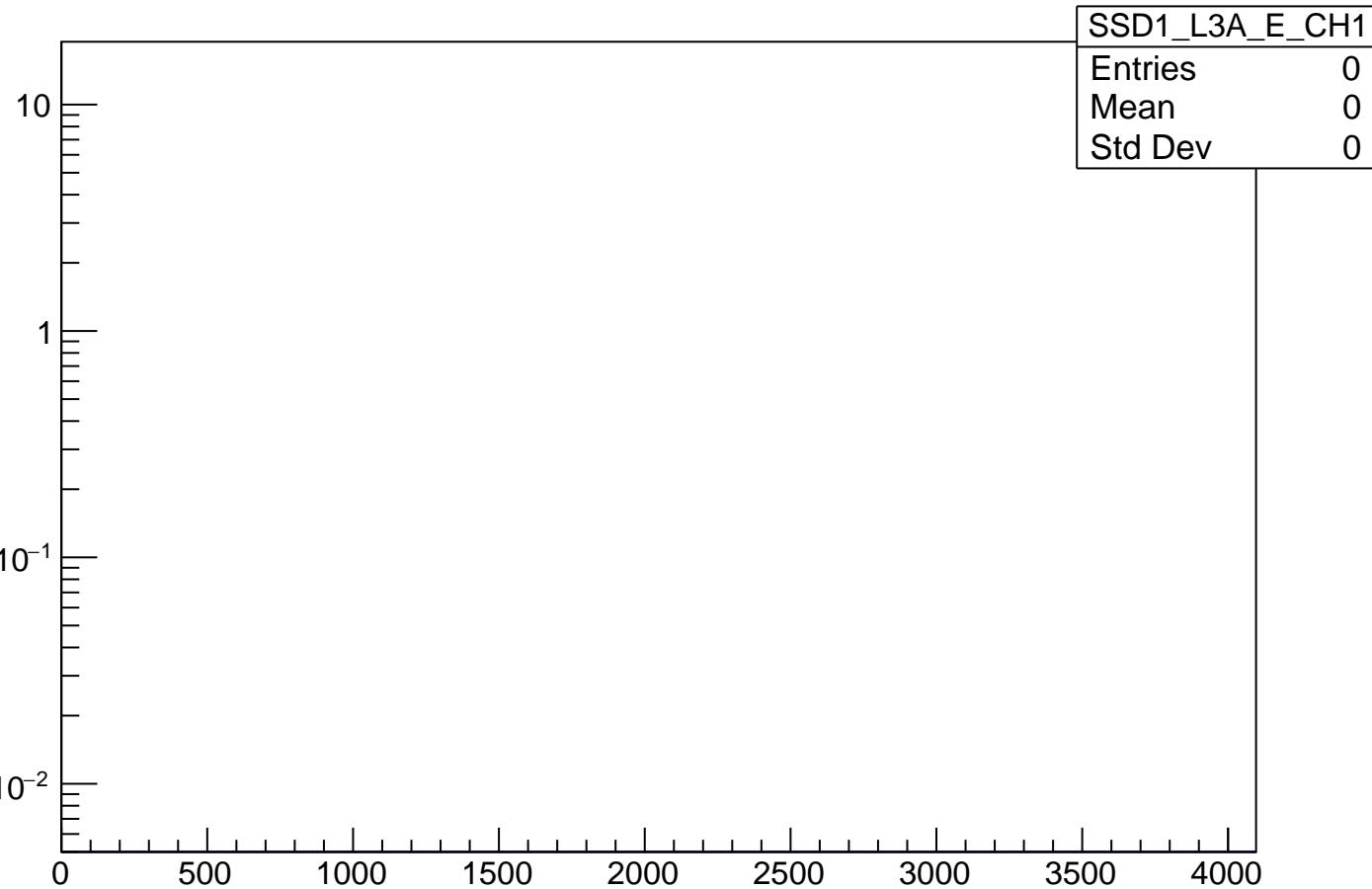


SSD1_L3A_E_CH0

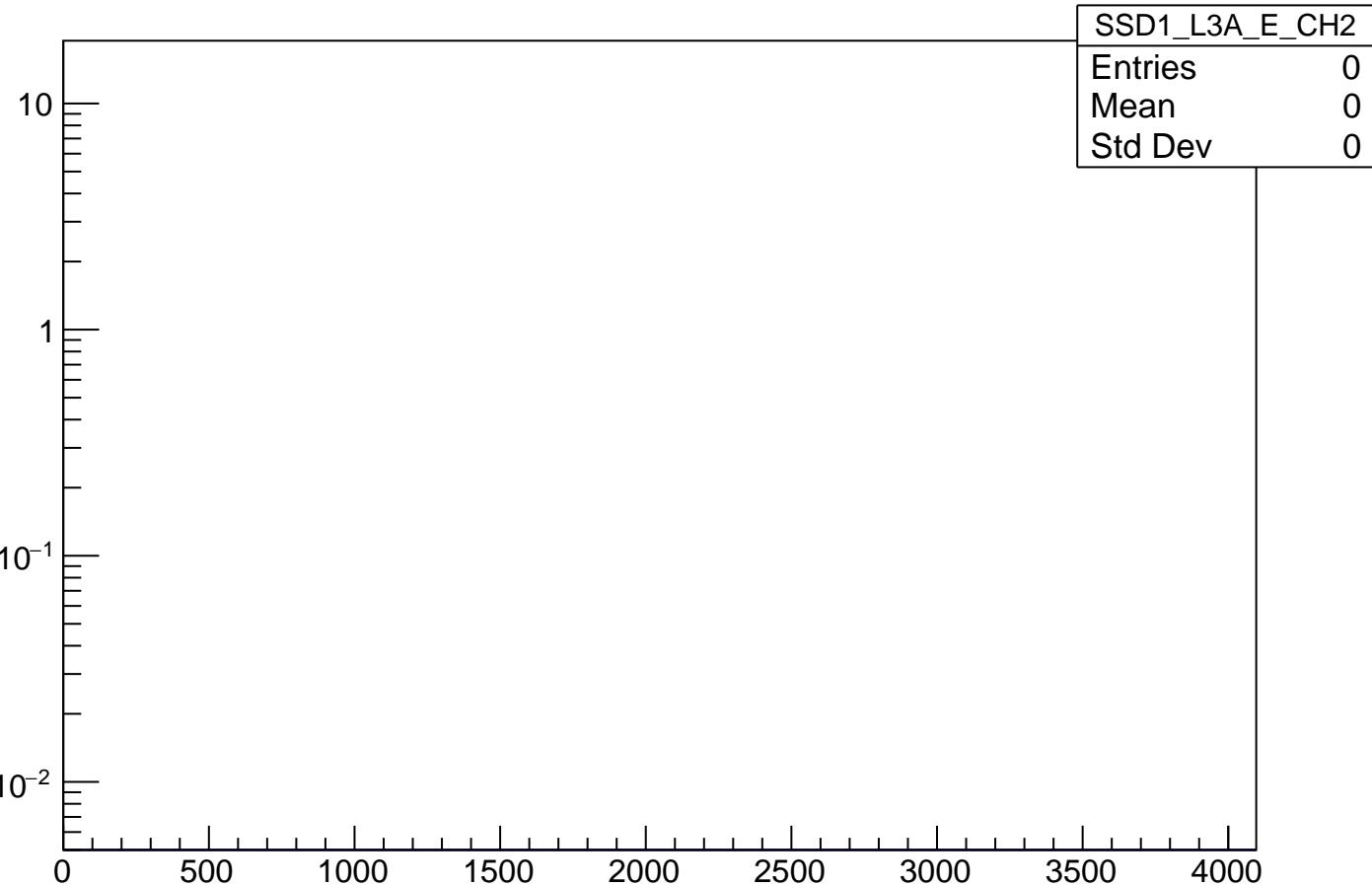


SSD1_L3A_E_CH0	
Entries	0
Mean	0
Std Dev	0

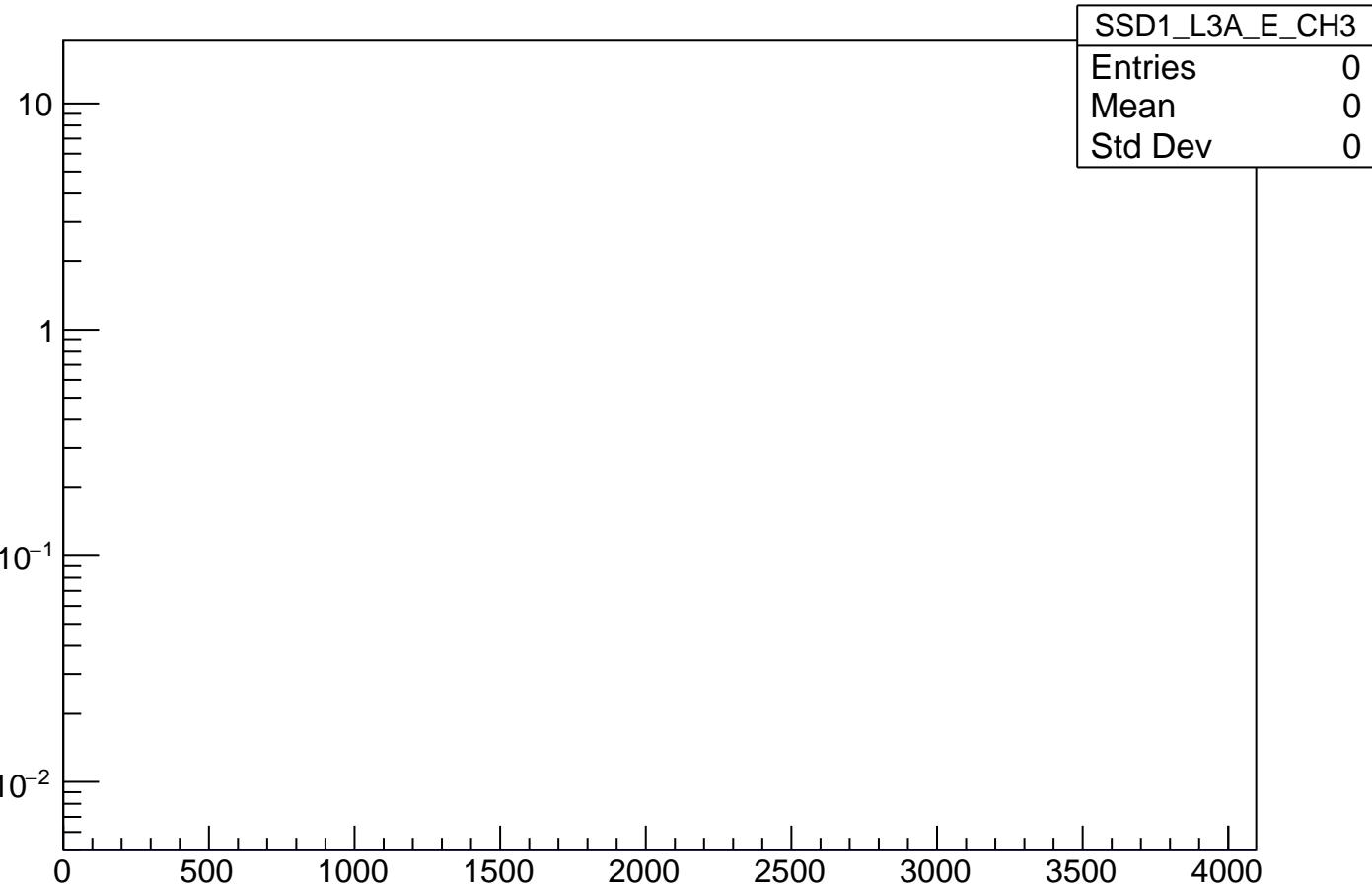
SSD1_L3A_E_CH1



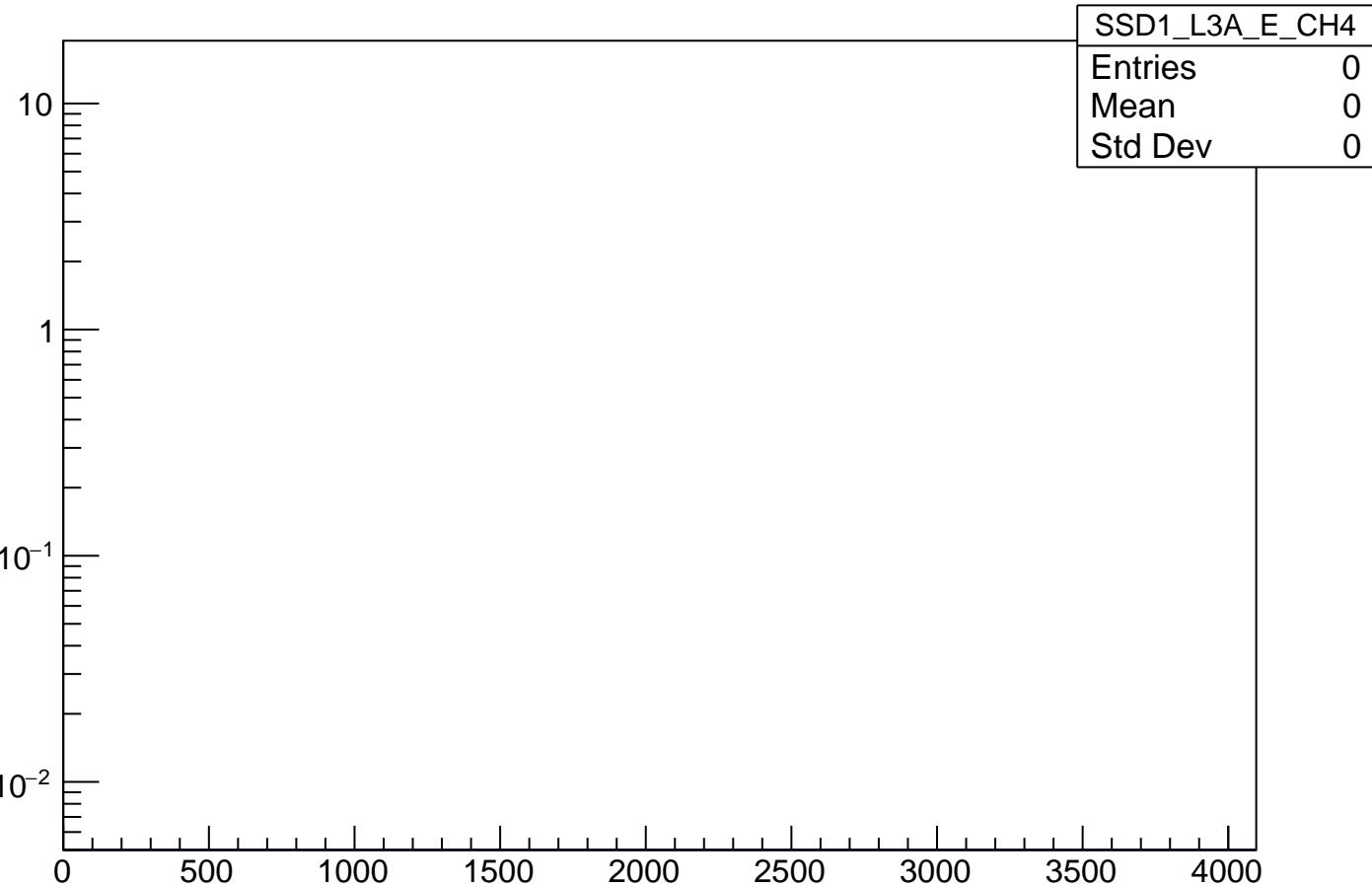
SSD1_L3A_E_CH2



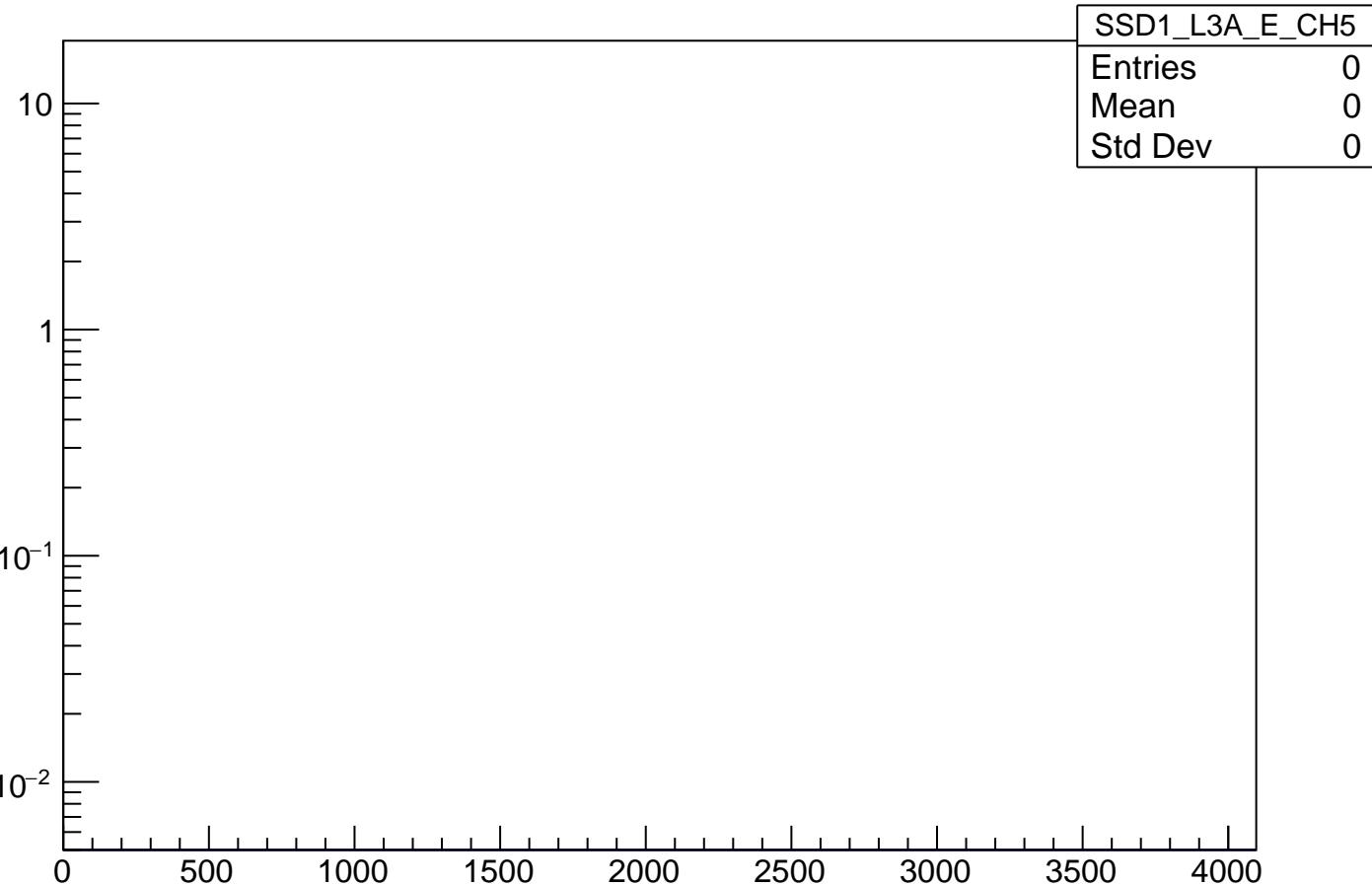
SSD1_L3A_E_CH3



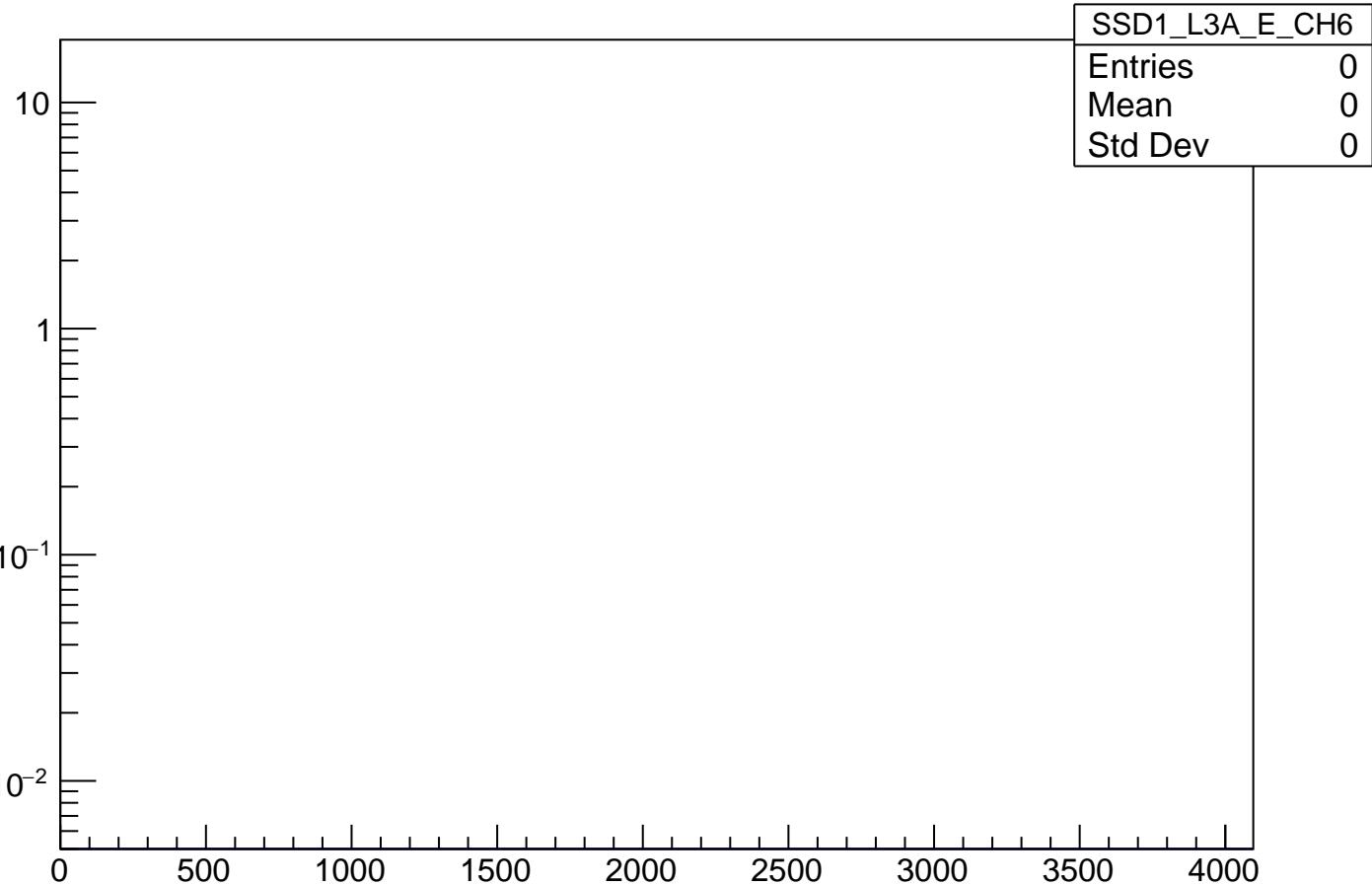
SSD1_L3A_E_CH4



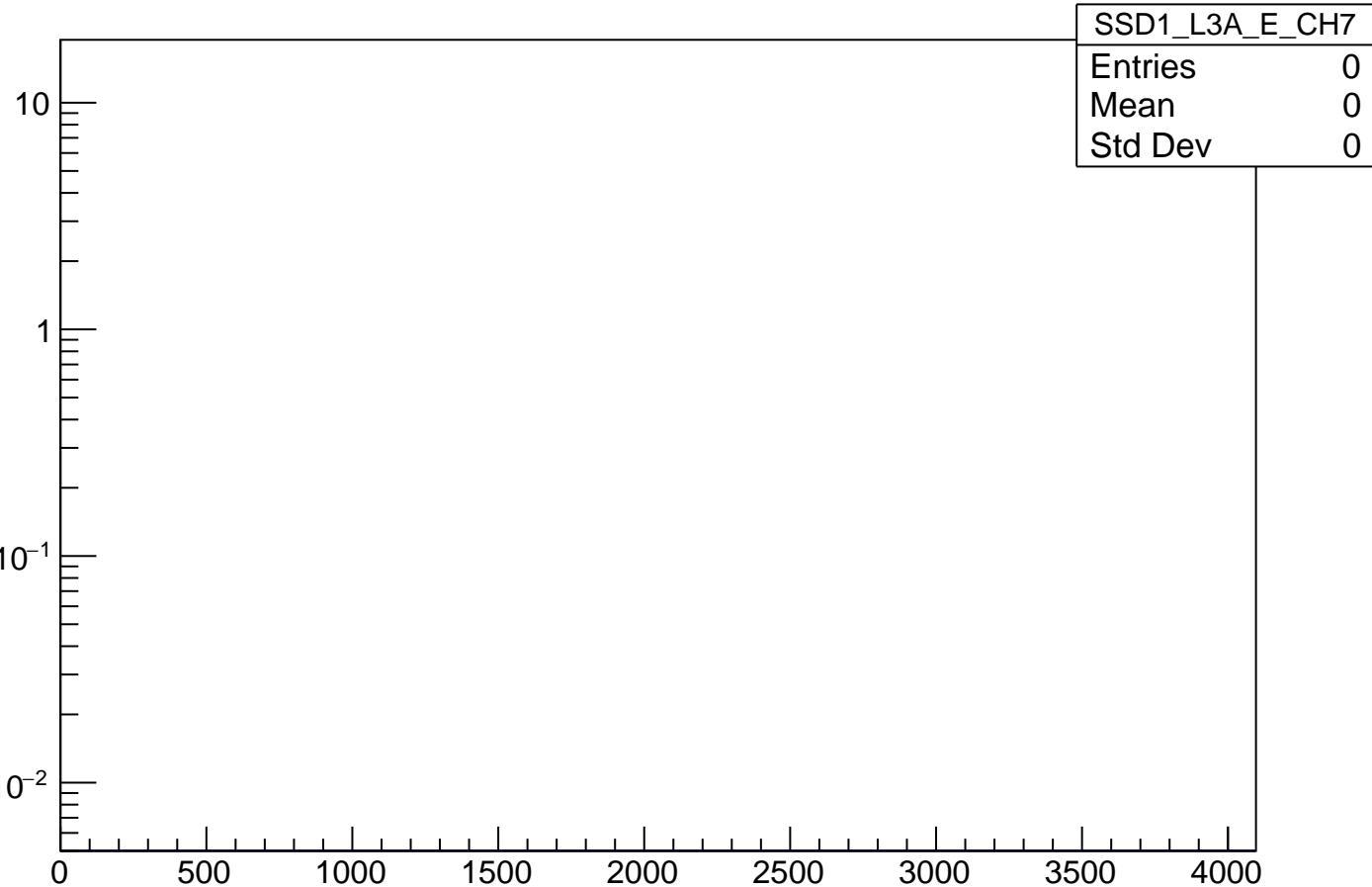
SSD1_L3A_E_CH5



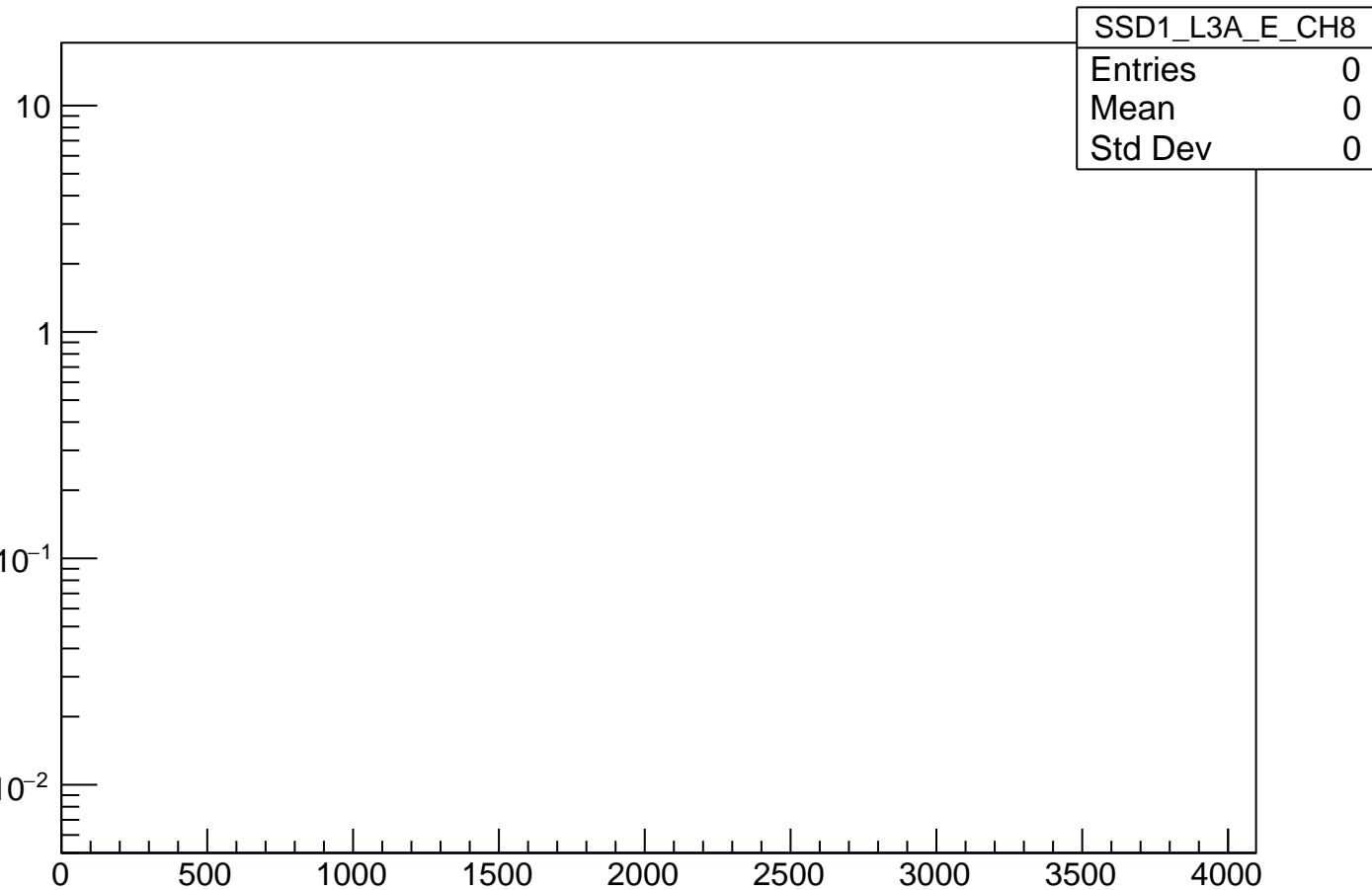
SSD1_L3A_E_CH6



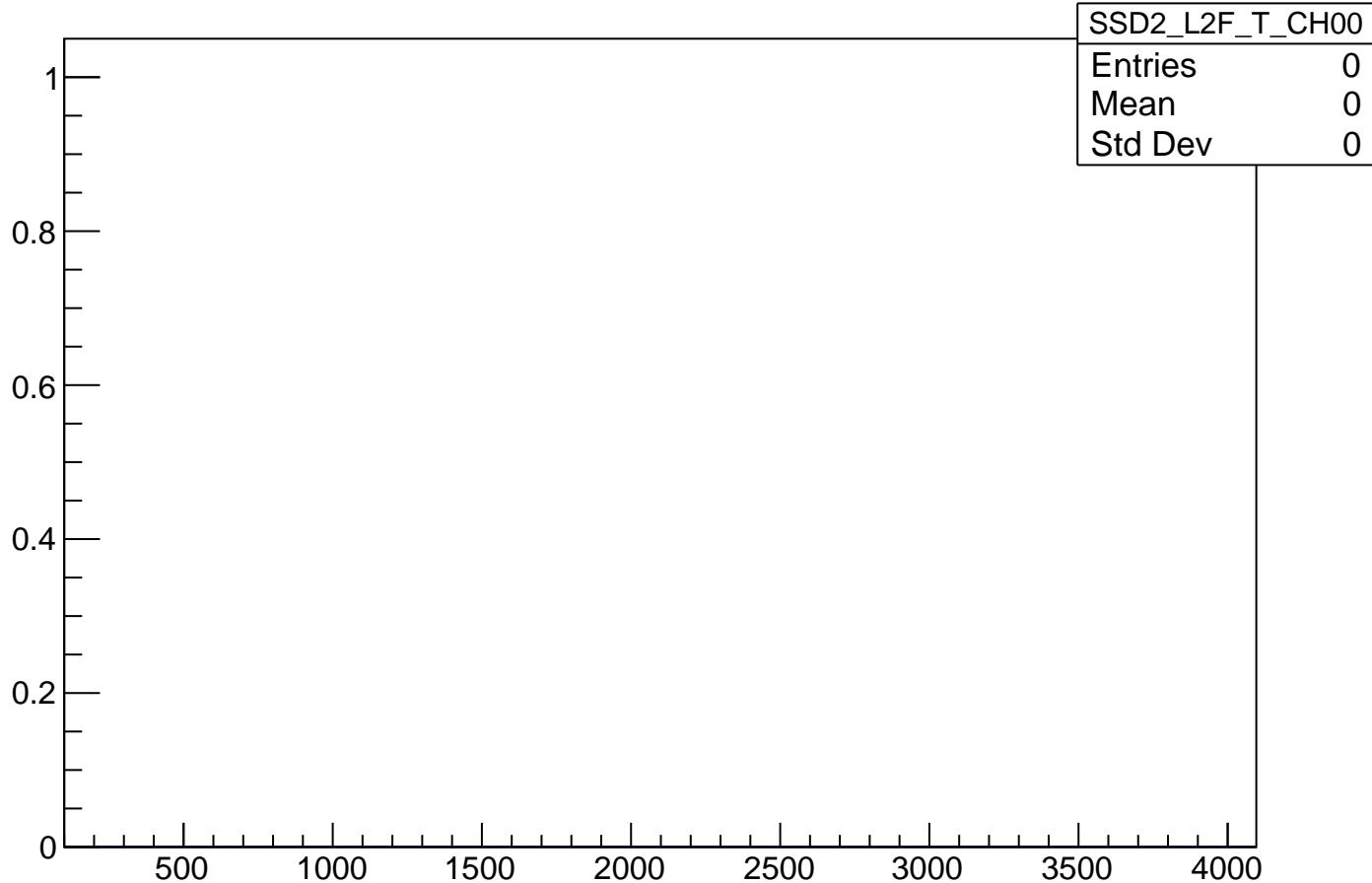
SSD1_L3A_E_CH7



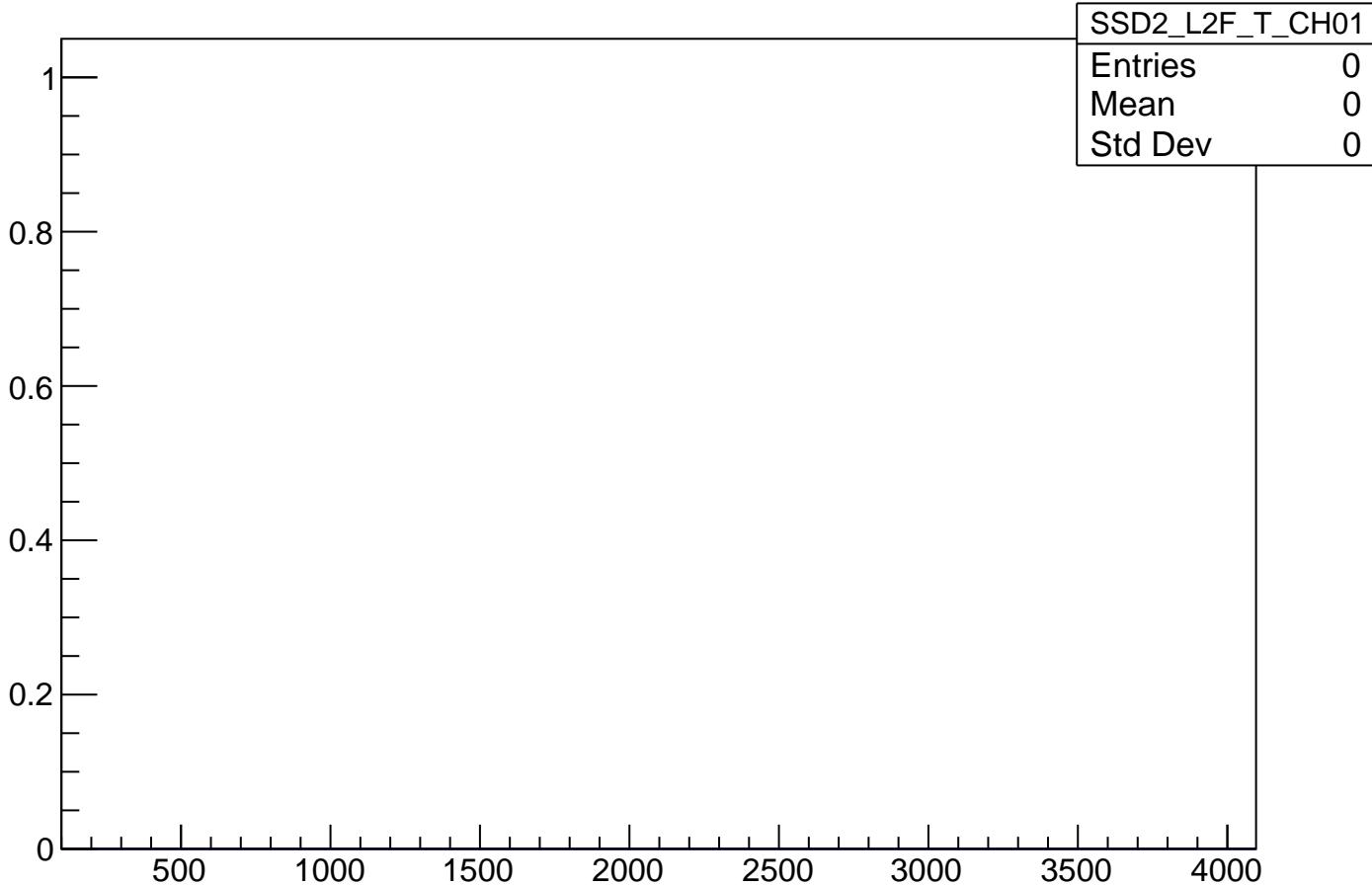
SSD1_L3A_E_CH8



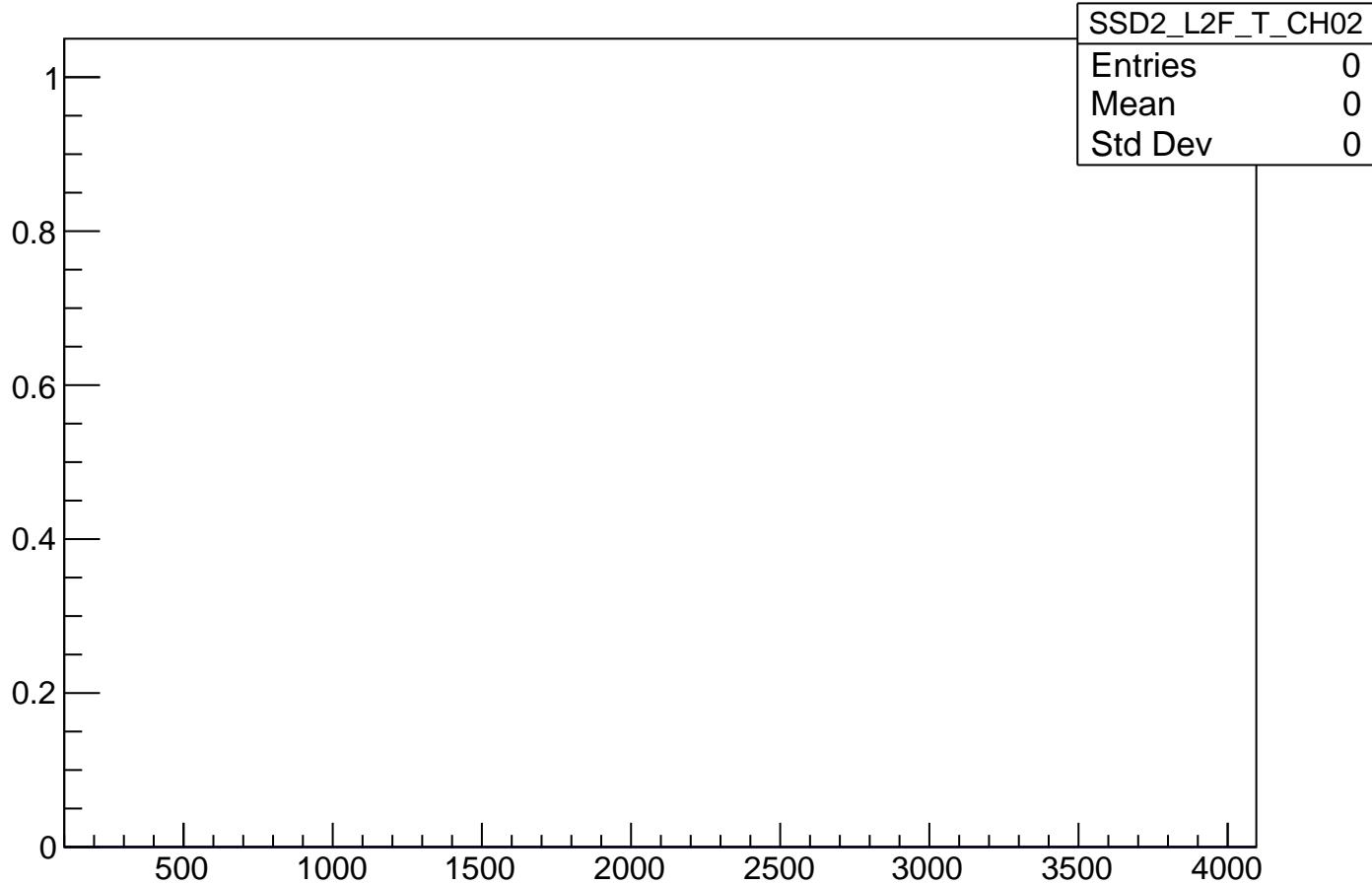
SSD2_L2F_T_CH00



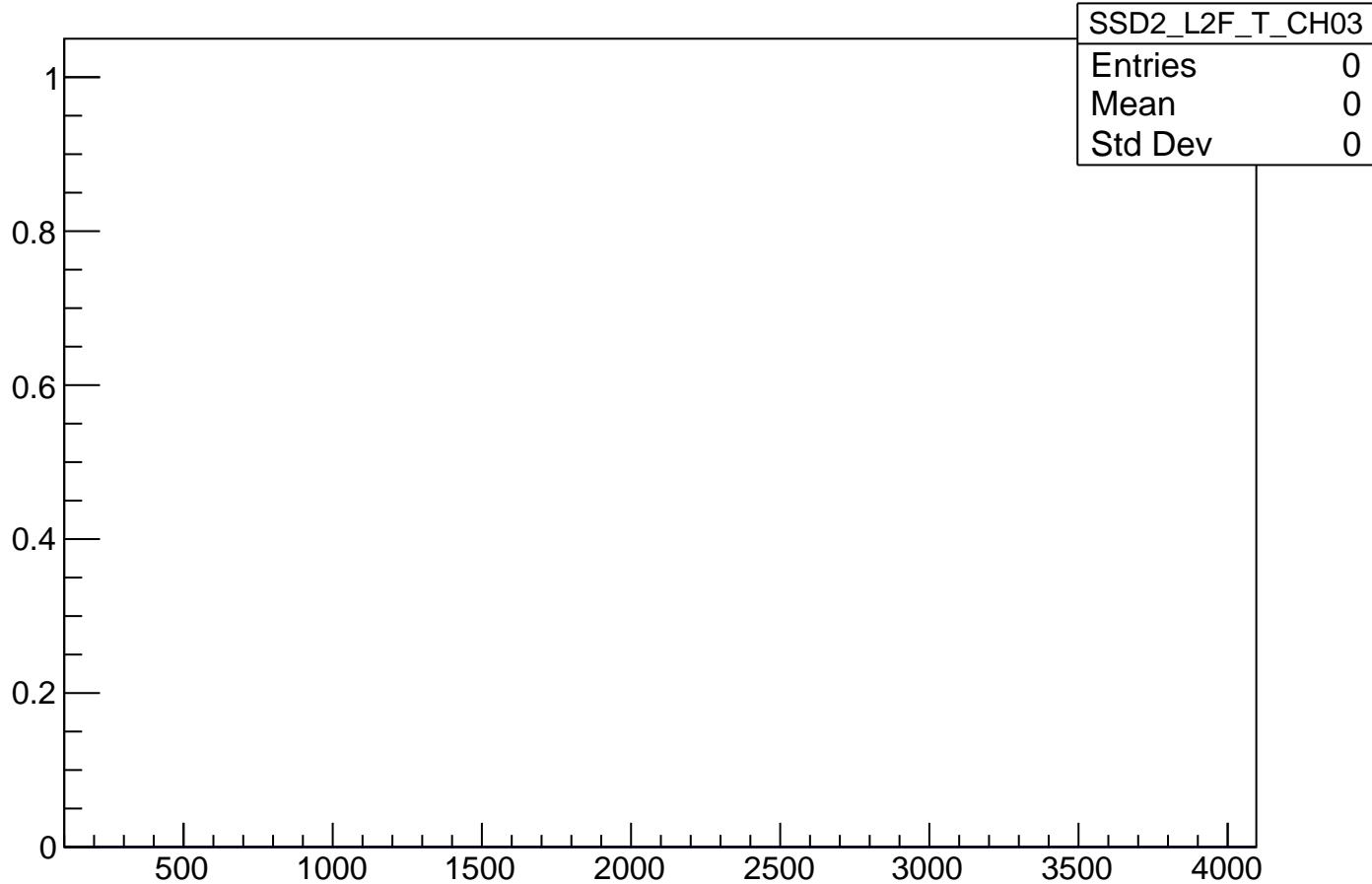
SSD2_L2F_T_CH01



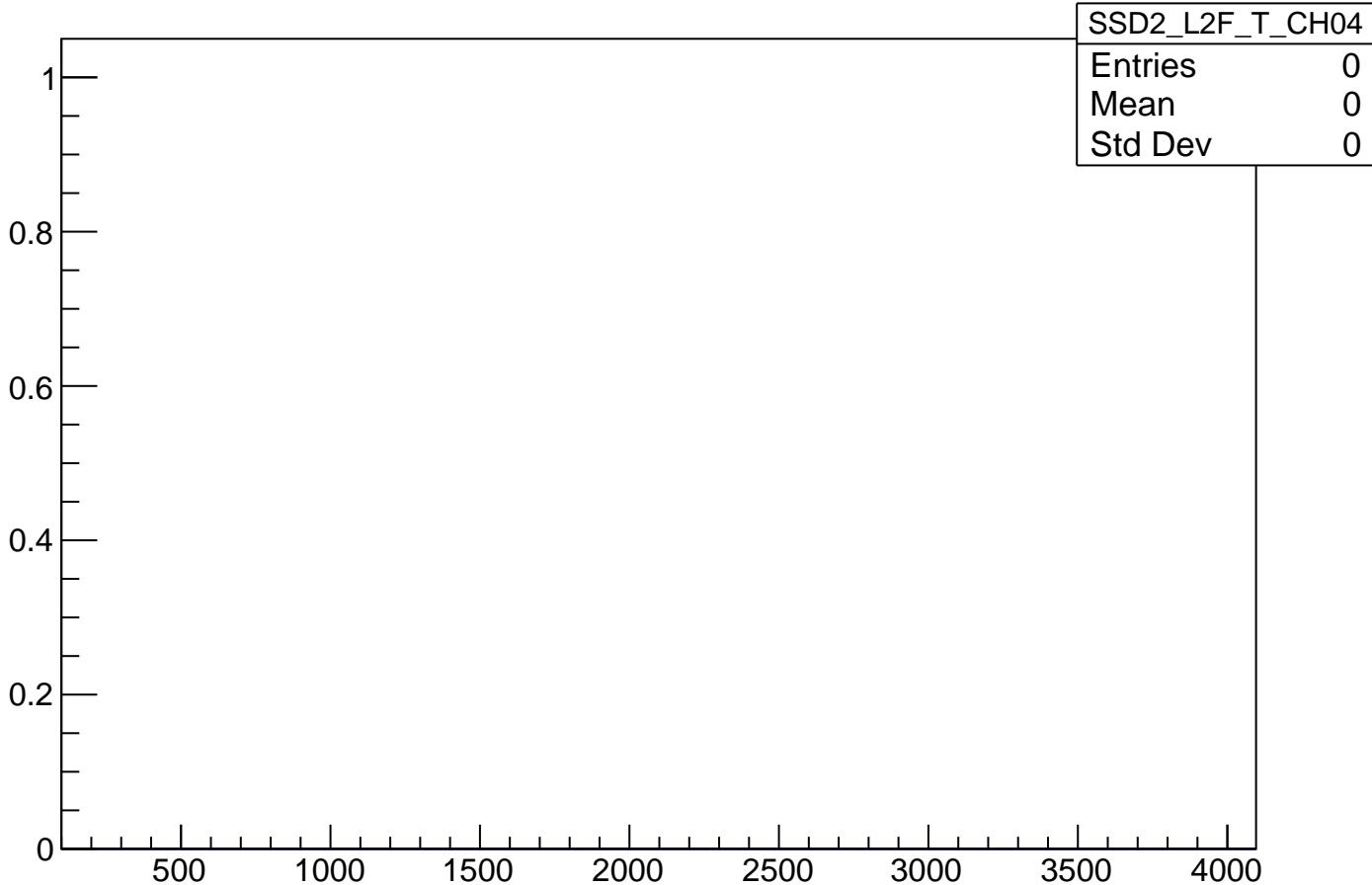
SSD2_L2F_T_CH02



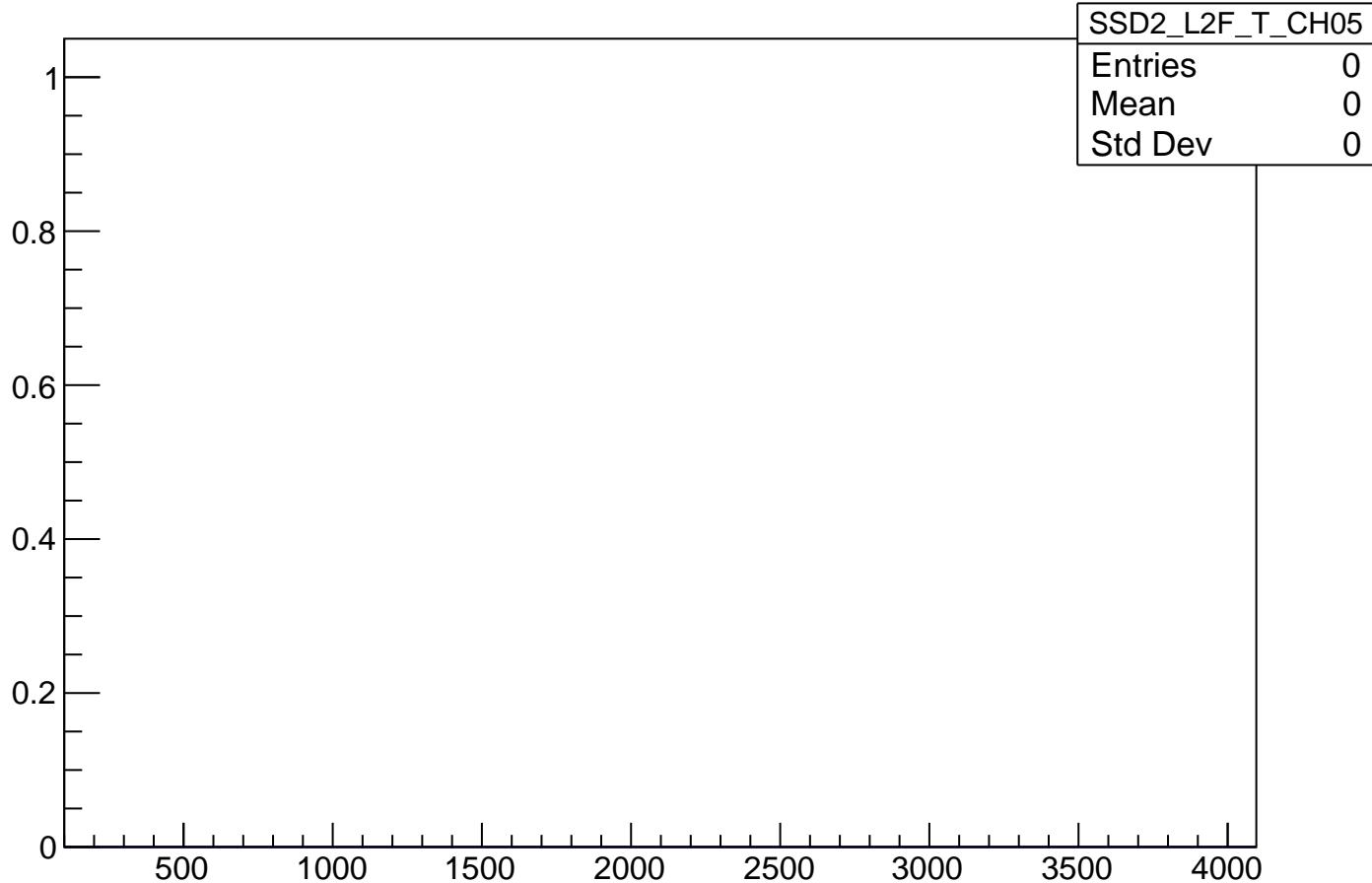
SSD2_L2F_T_CH03



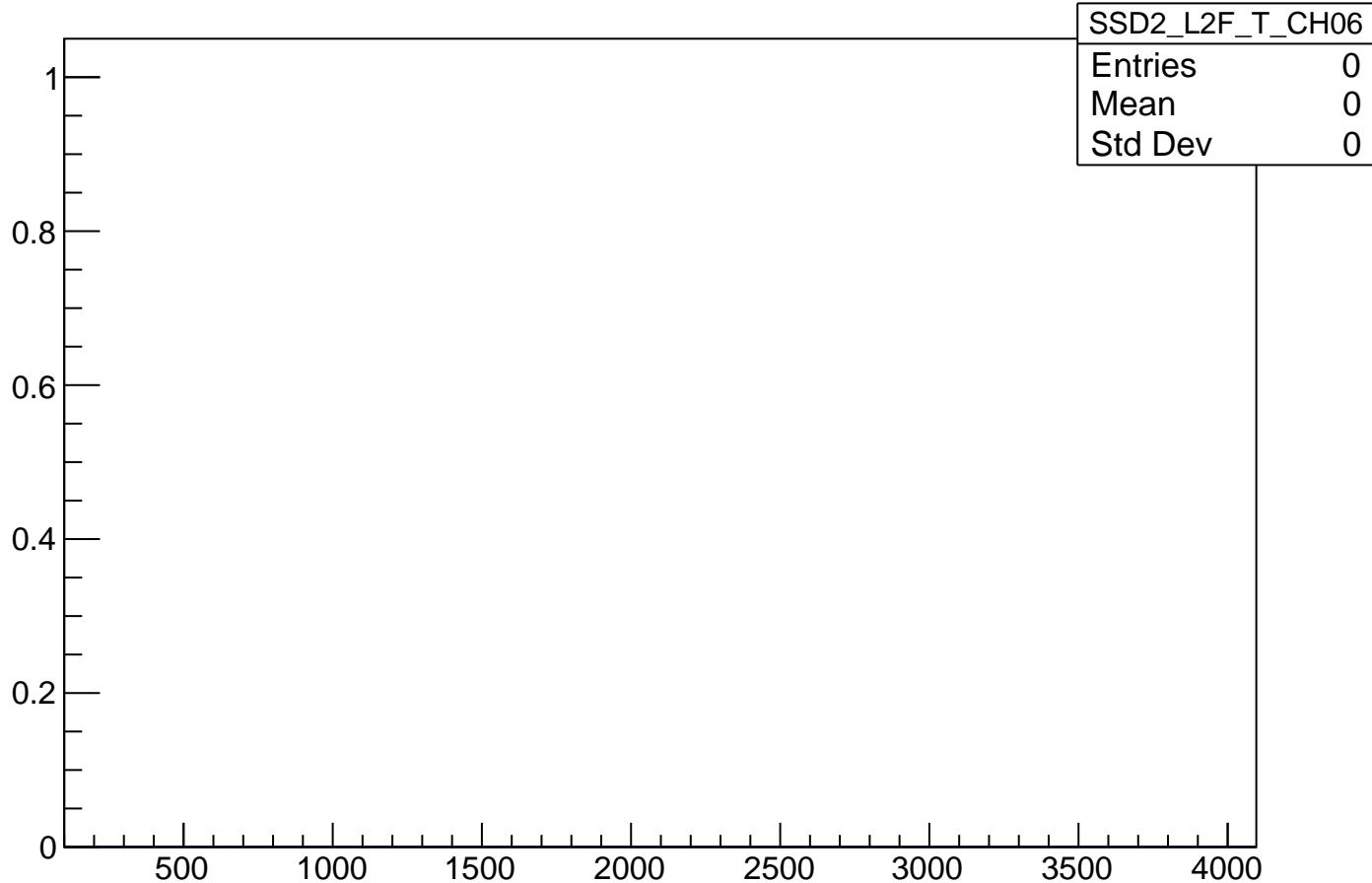
SSD2_L2F_T_CH04



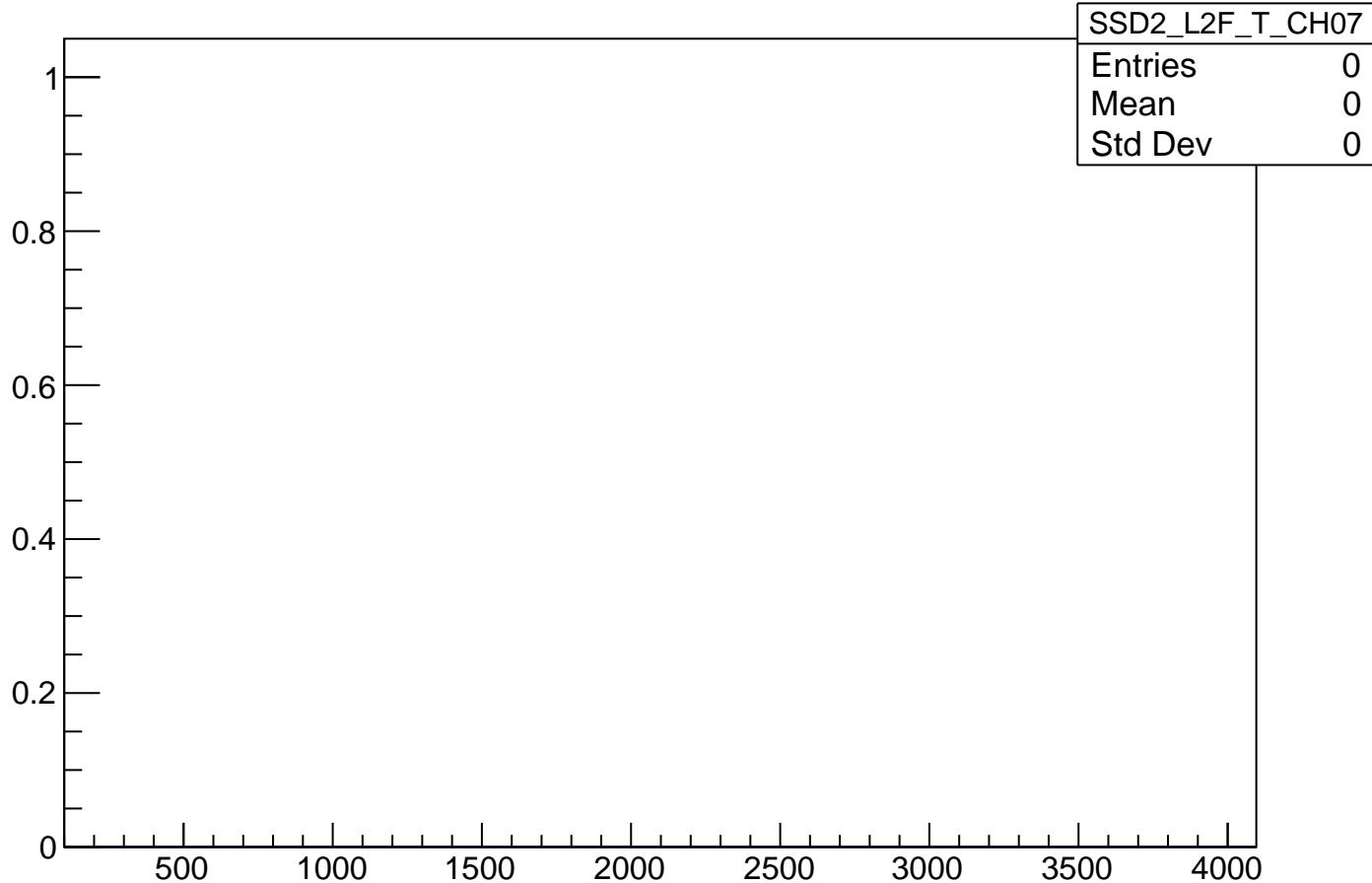
SSD2_L2F_T_CH05



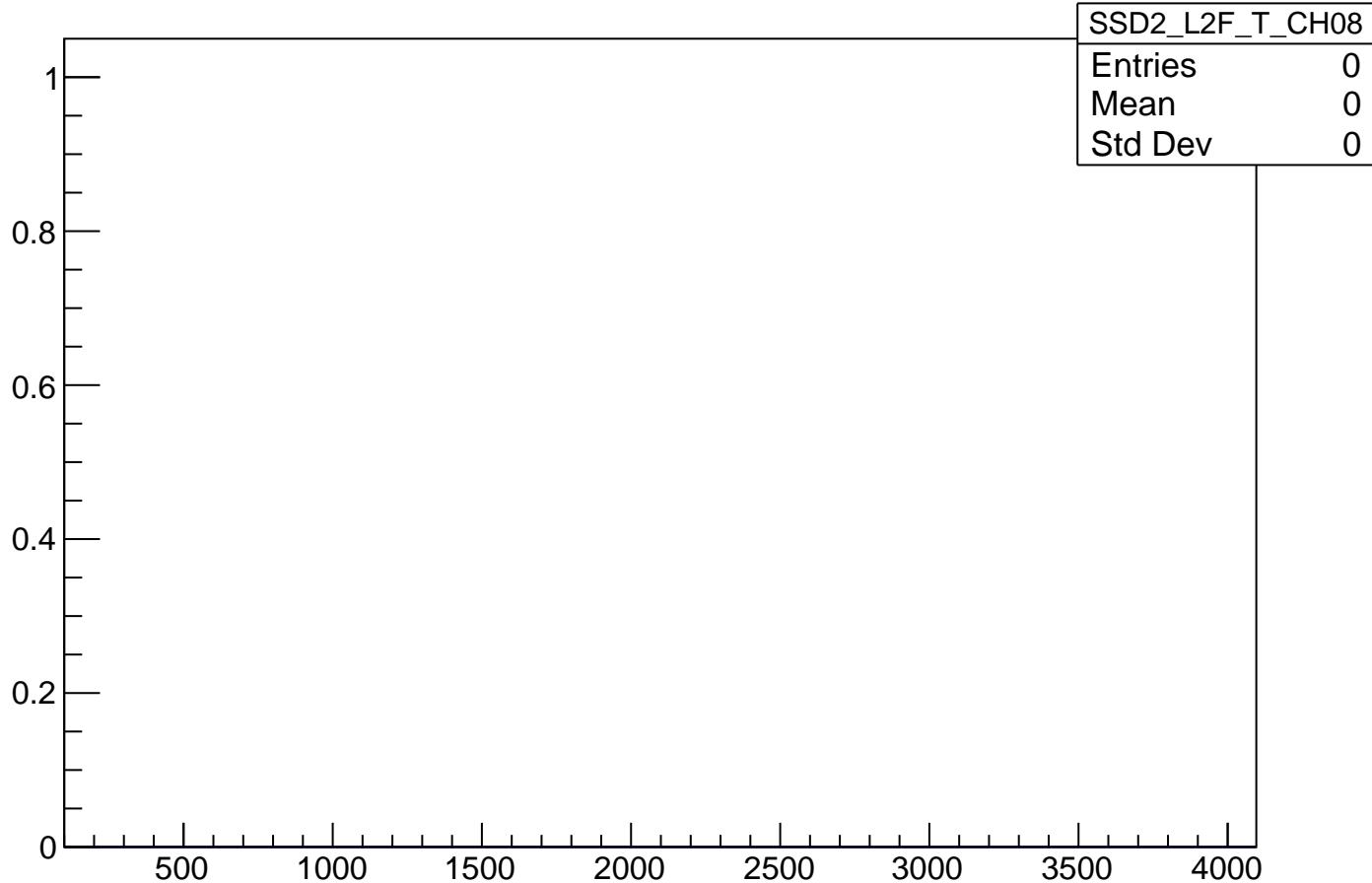
SSD2_L2F_T_CH06



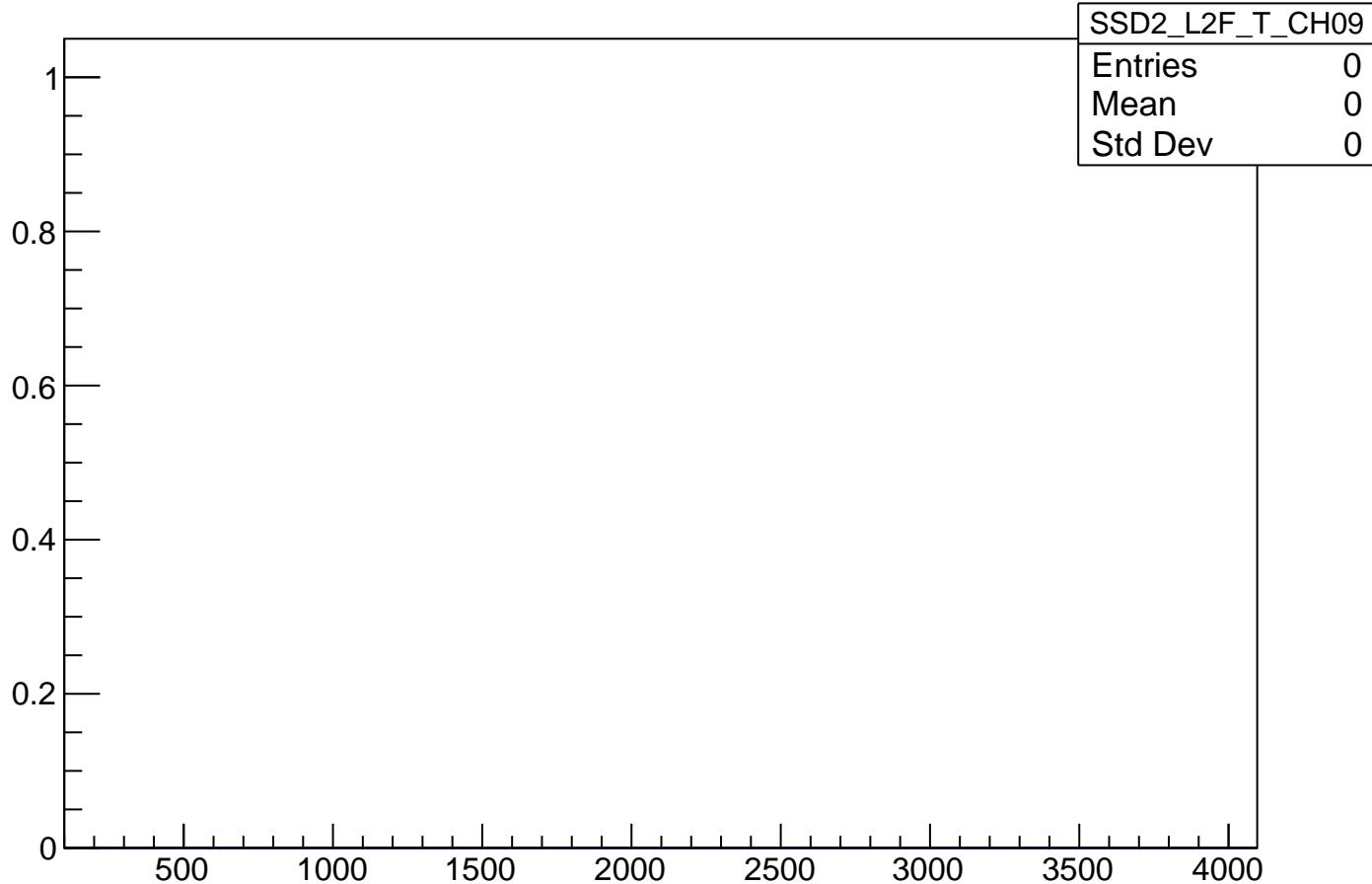
SSD2_L2F_T_CH07



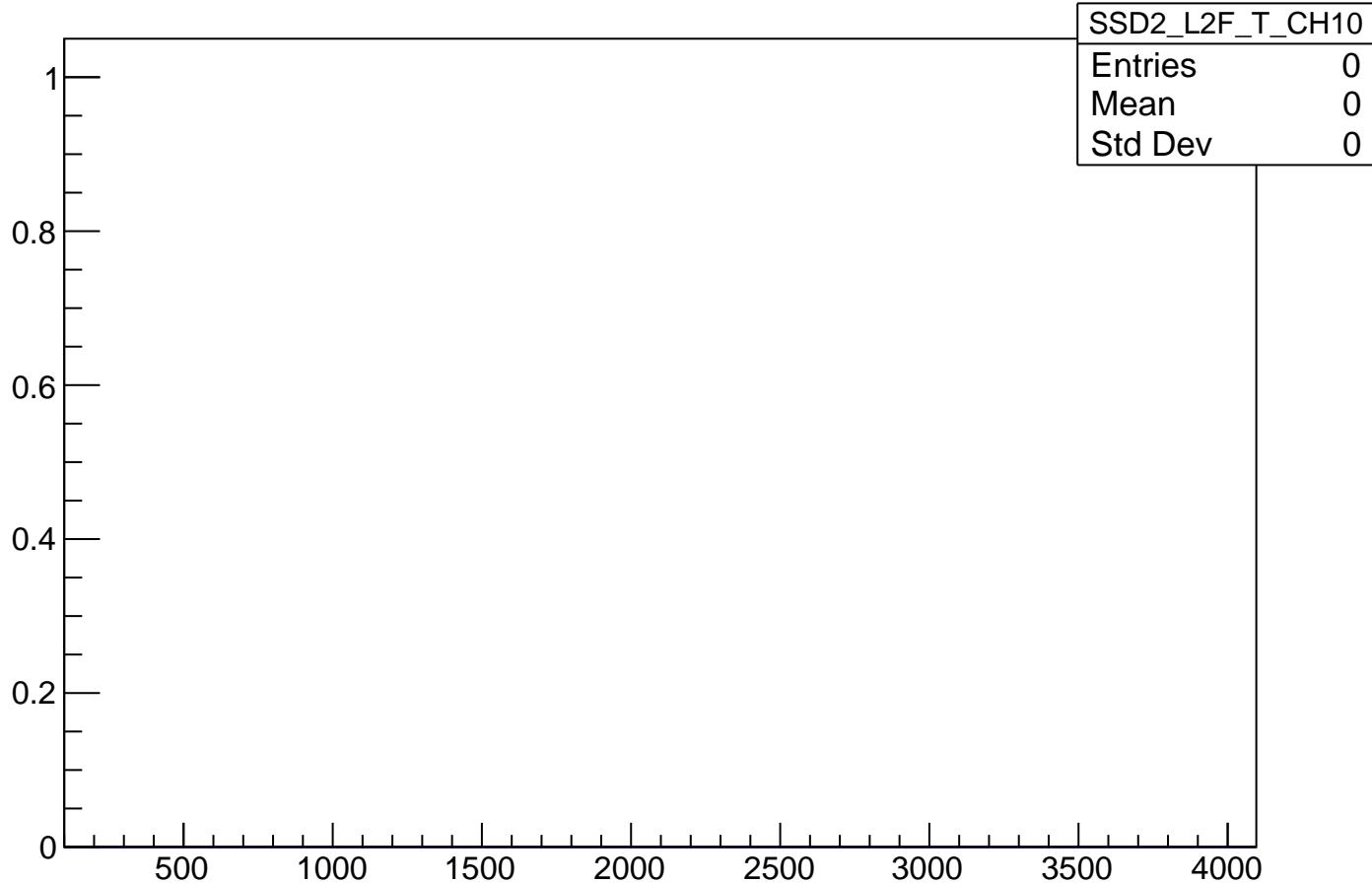
SSD2_L2F_T_CH08



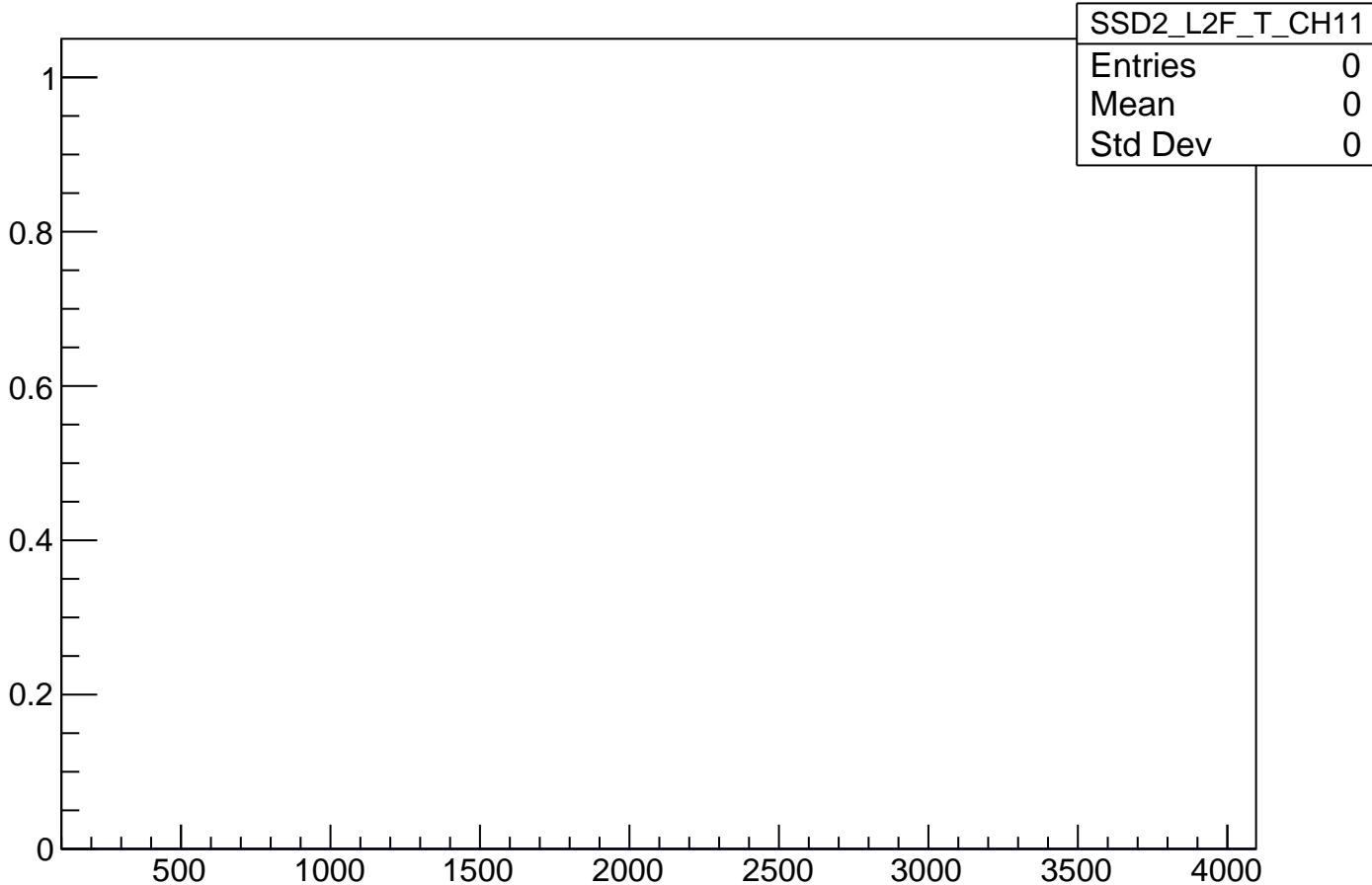
SSD2_L2F_T_CH09



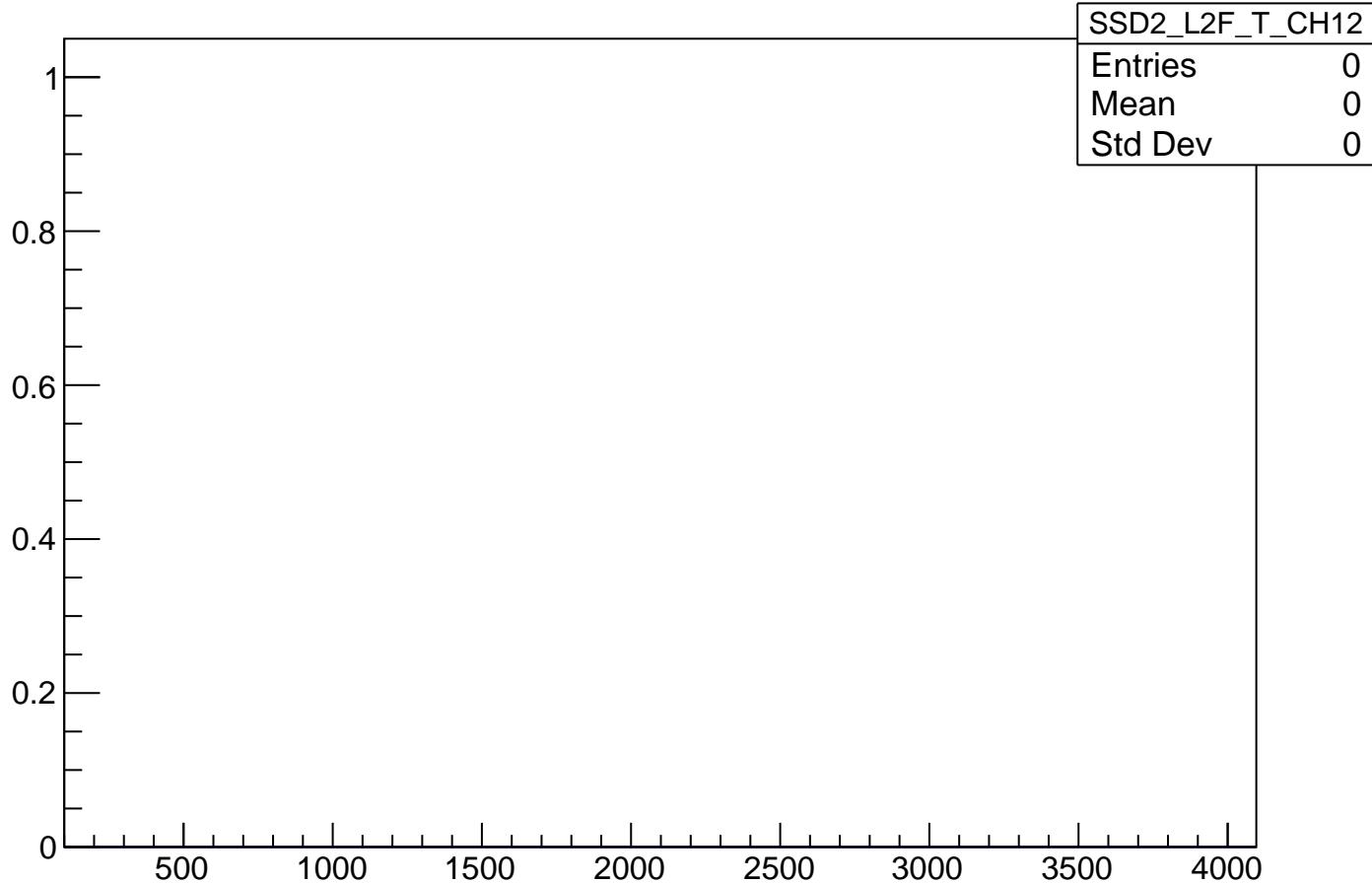
SSD2_L2F_T_CH10



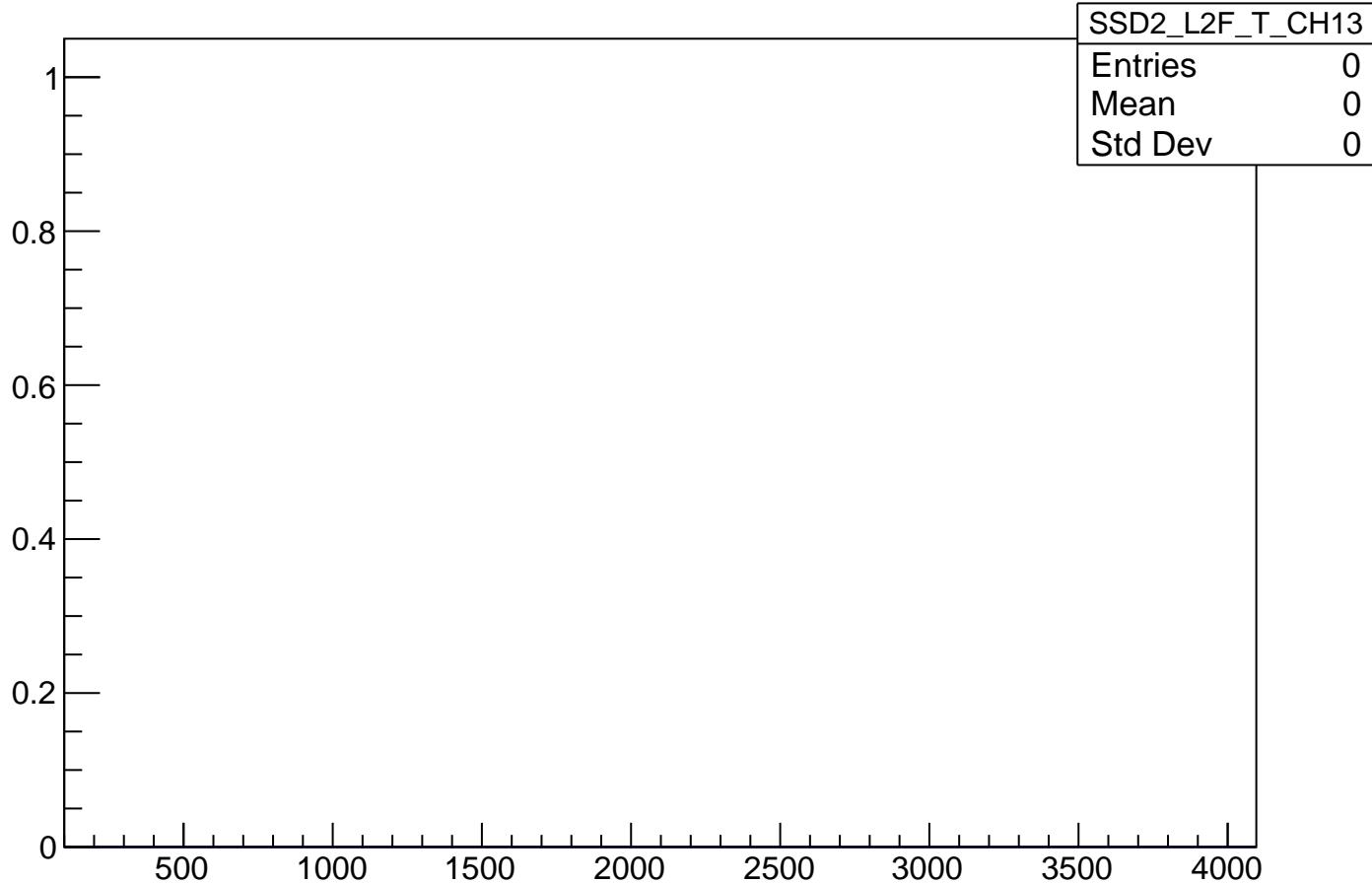
SSD2_L2F_T_CH11



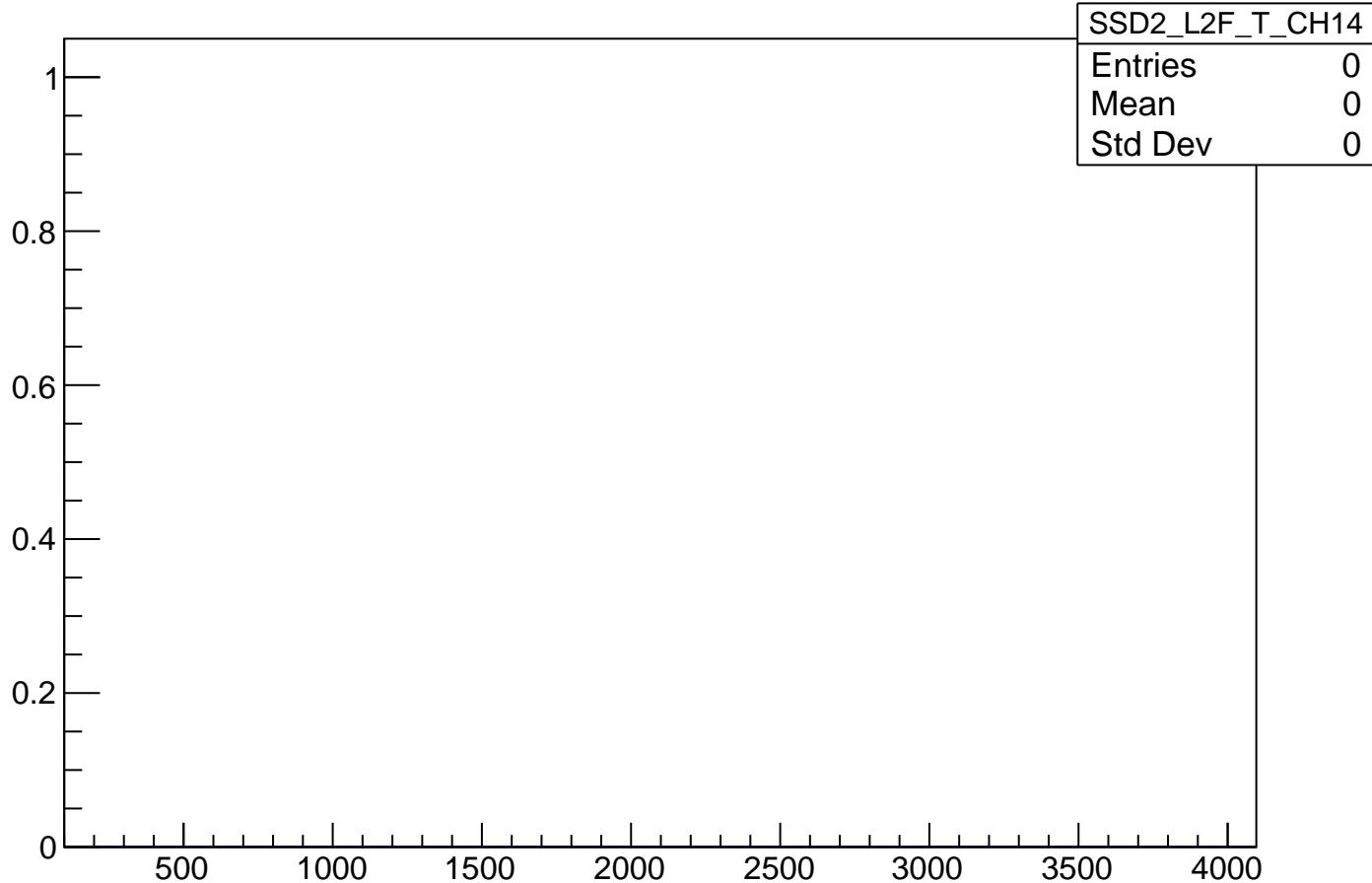
SSD2_L2F_T_CH12



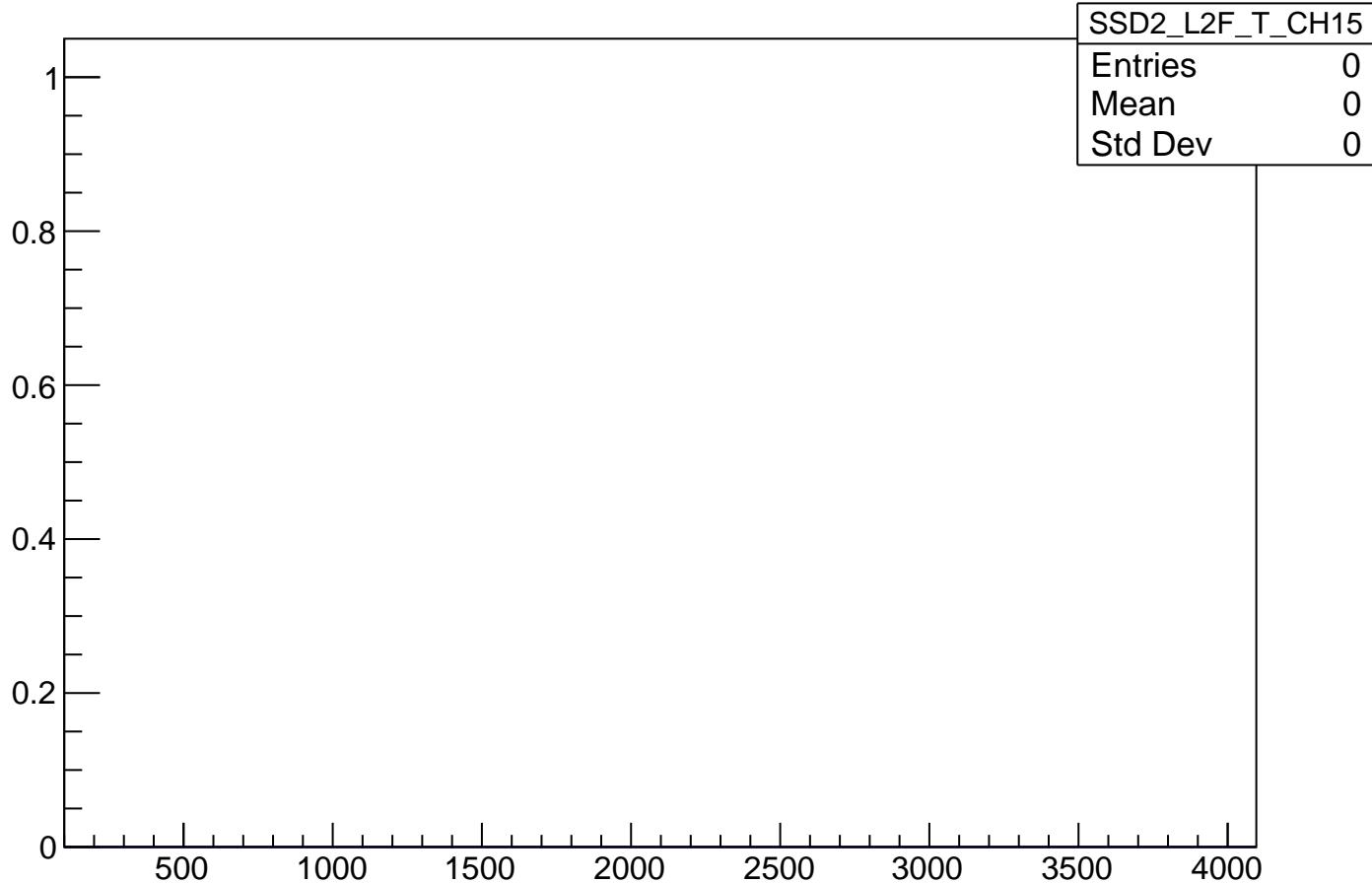
SSD2_L2F_T_CH13



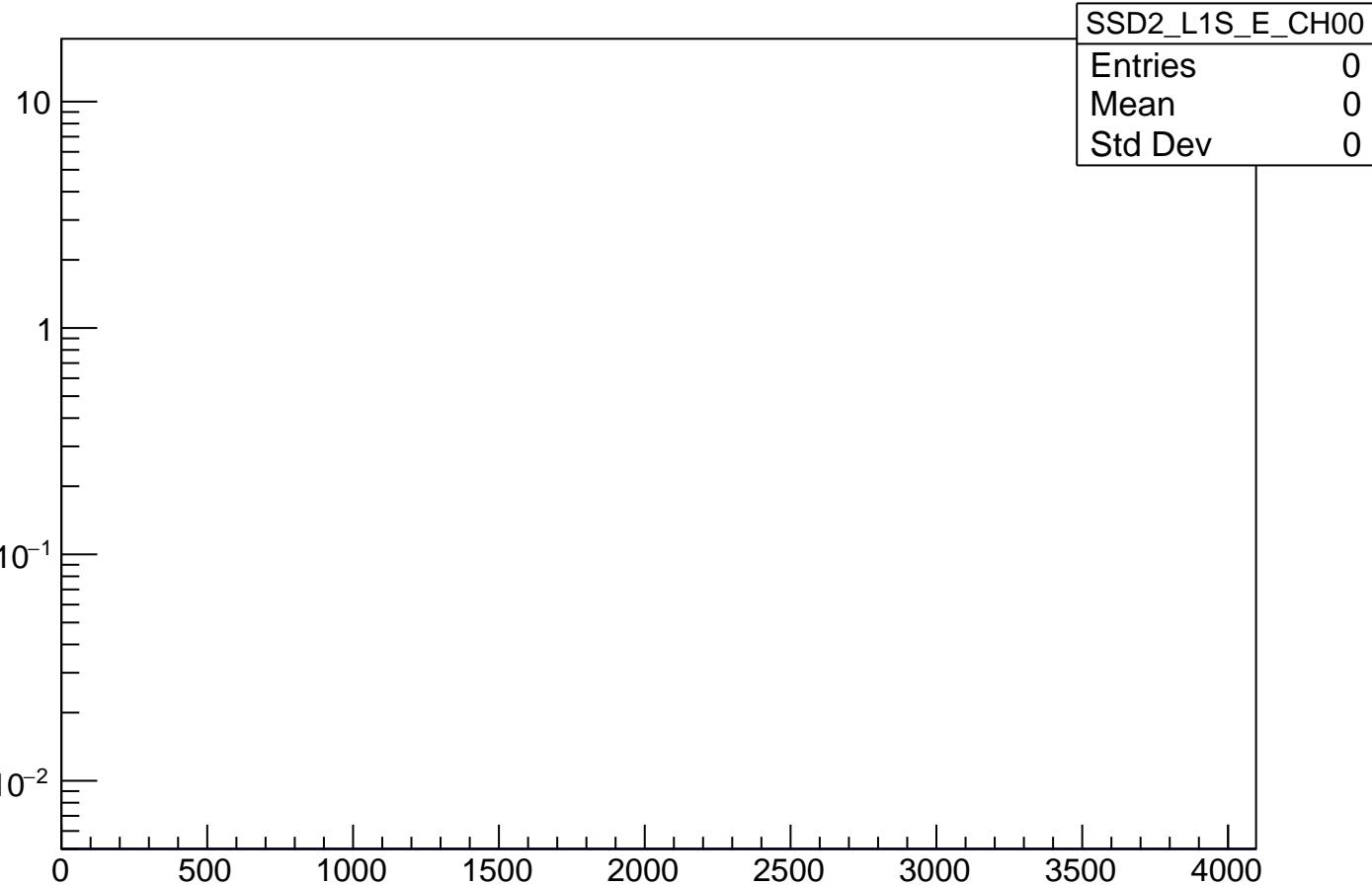
SSD2_L2F_T_CH14



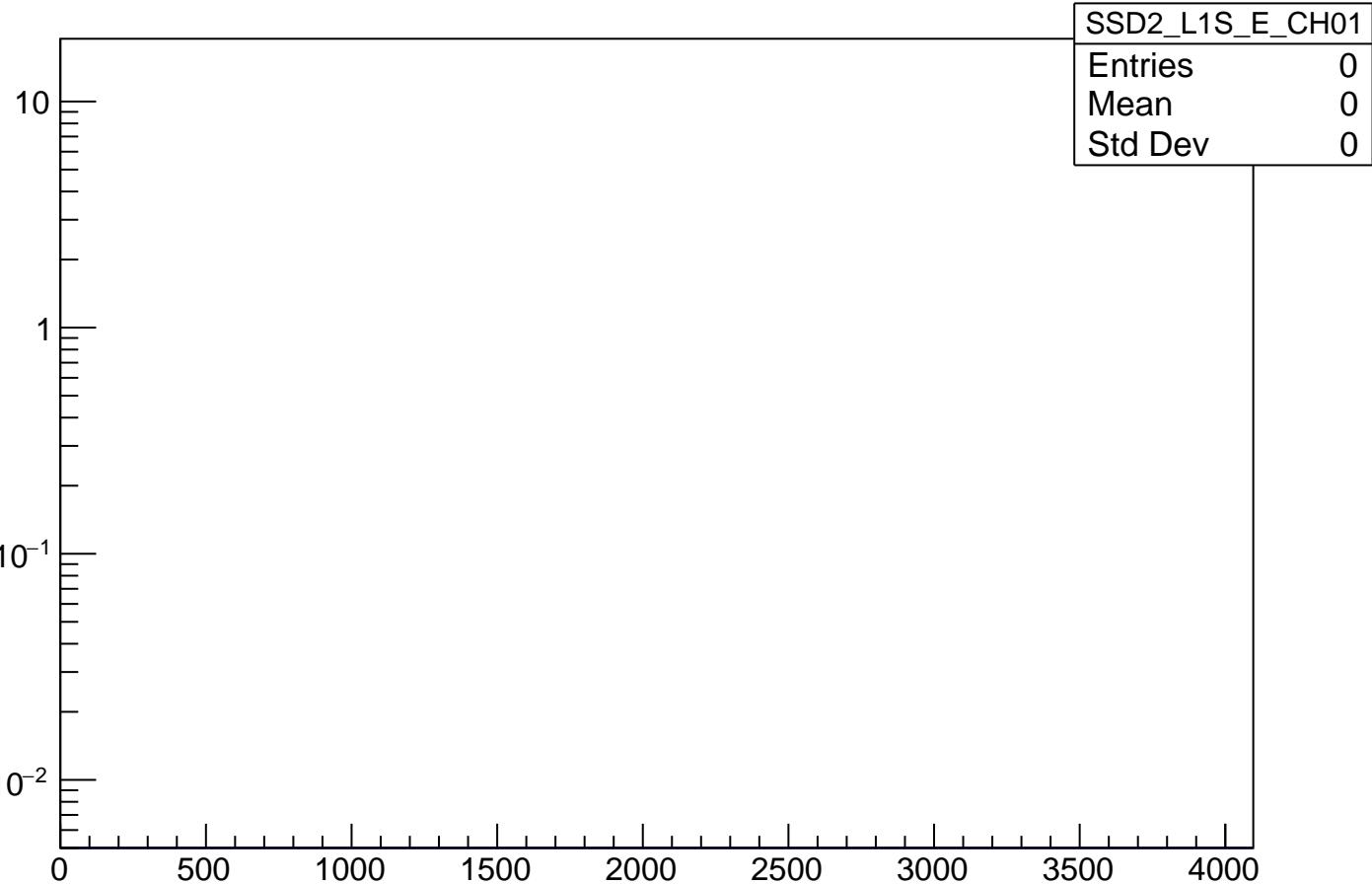
SSD2_L2F_T_CH15



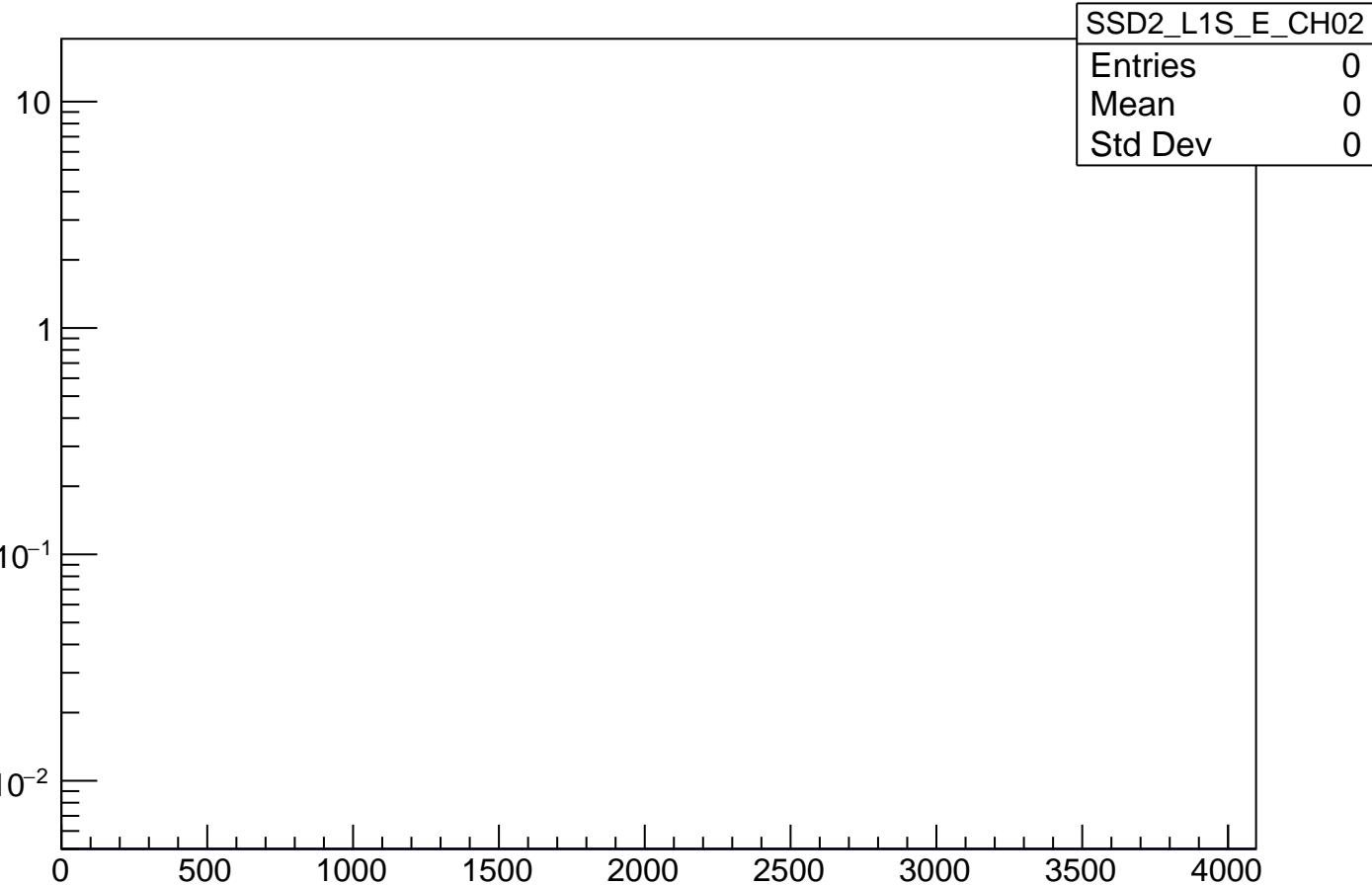
SSD2_L1S_E_CH00



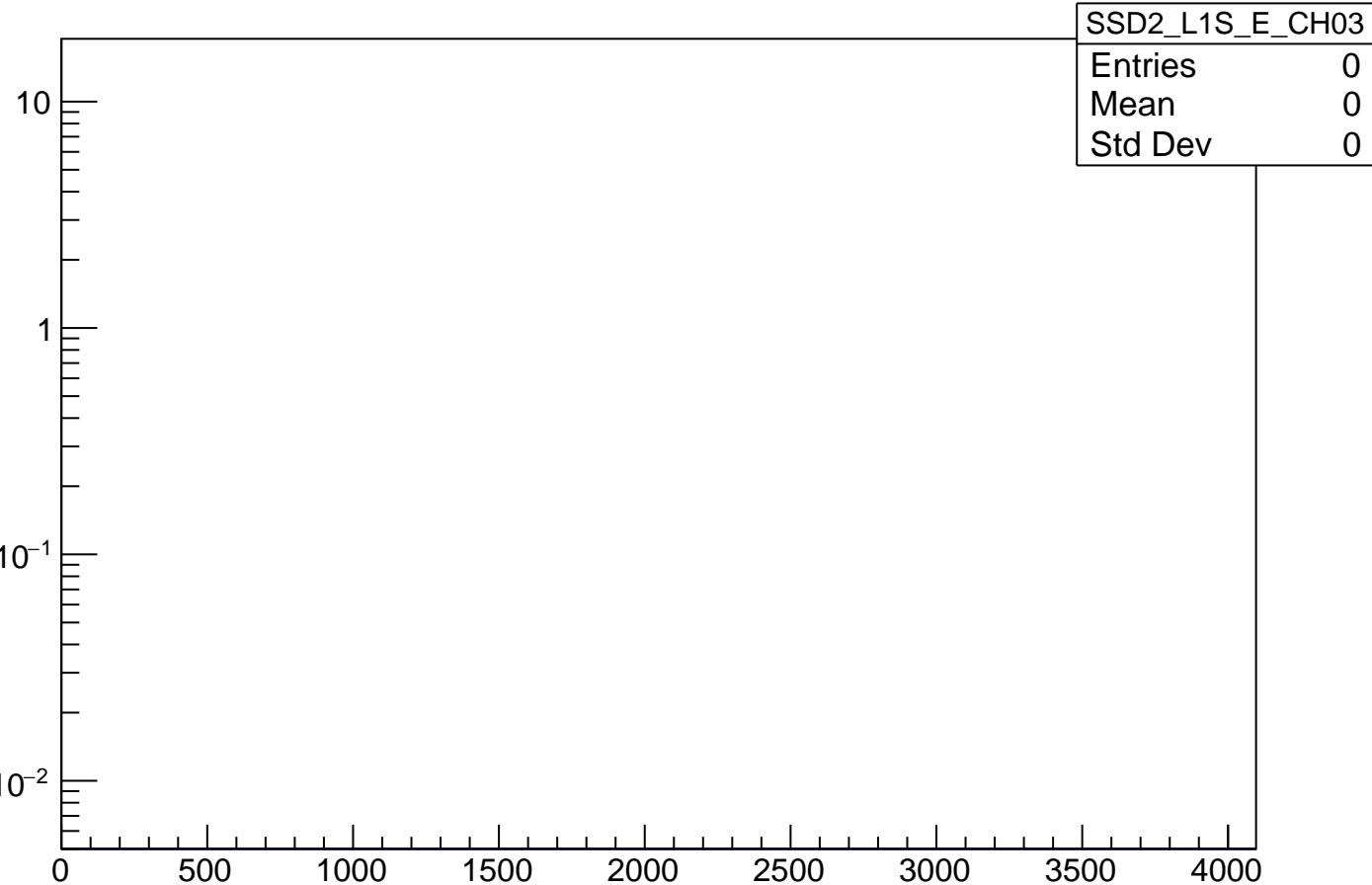
SSD2_L1S_E_CH01



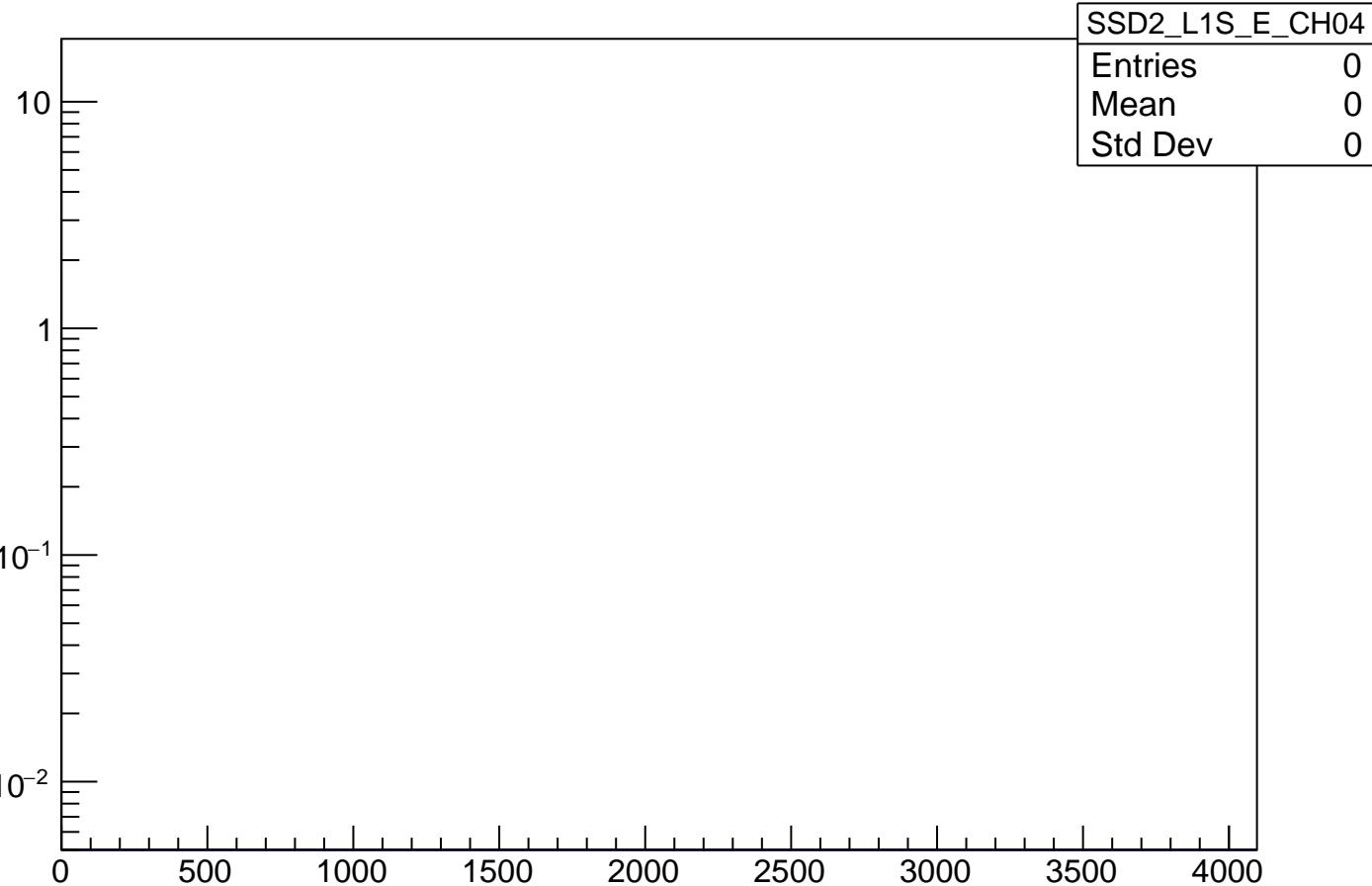
SSD2_L1S_E_CH02



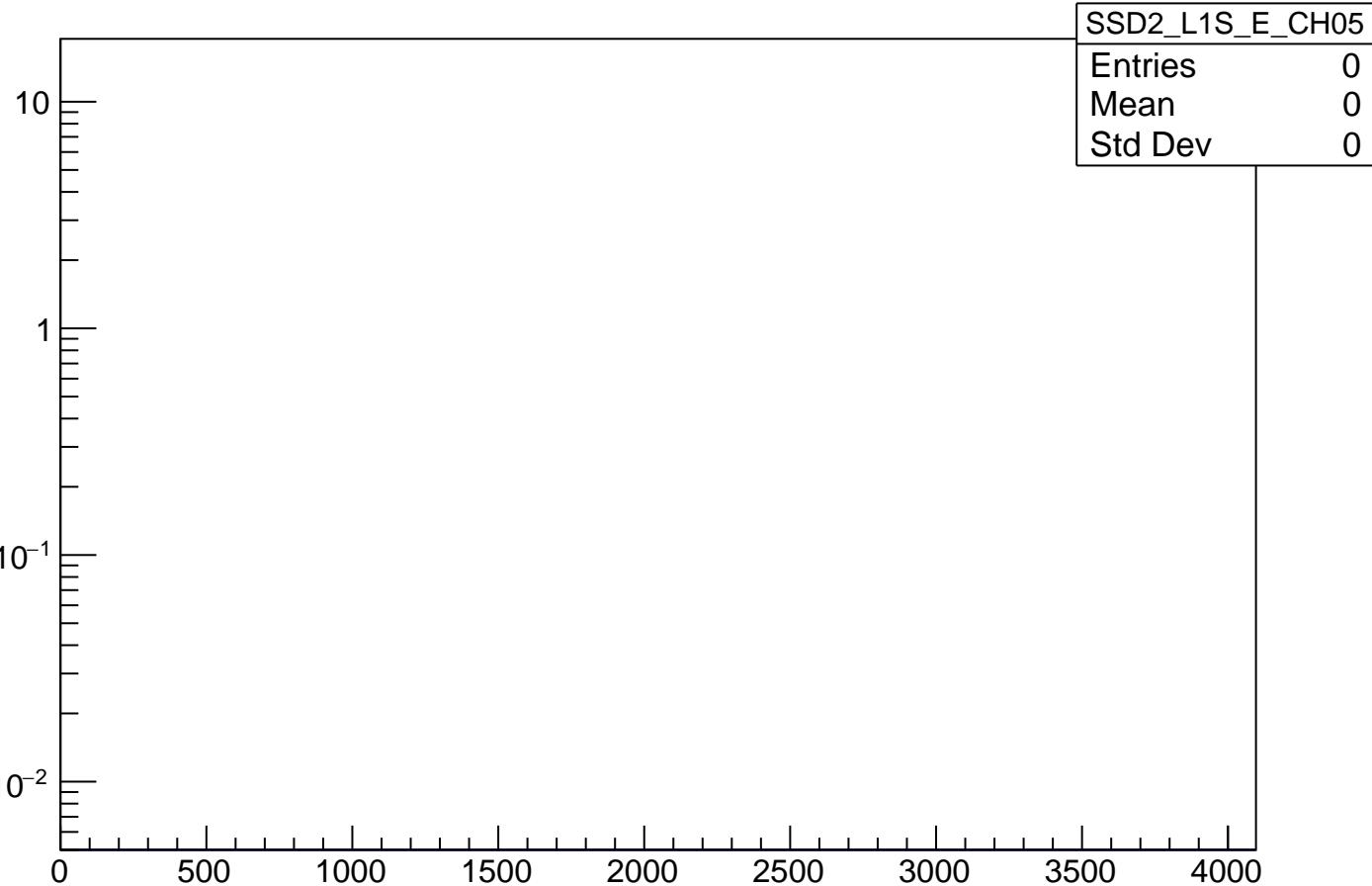
SSD2_L1S_E_CH03



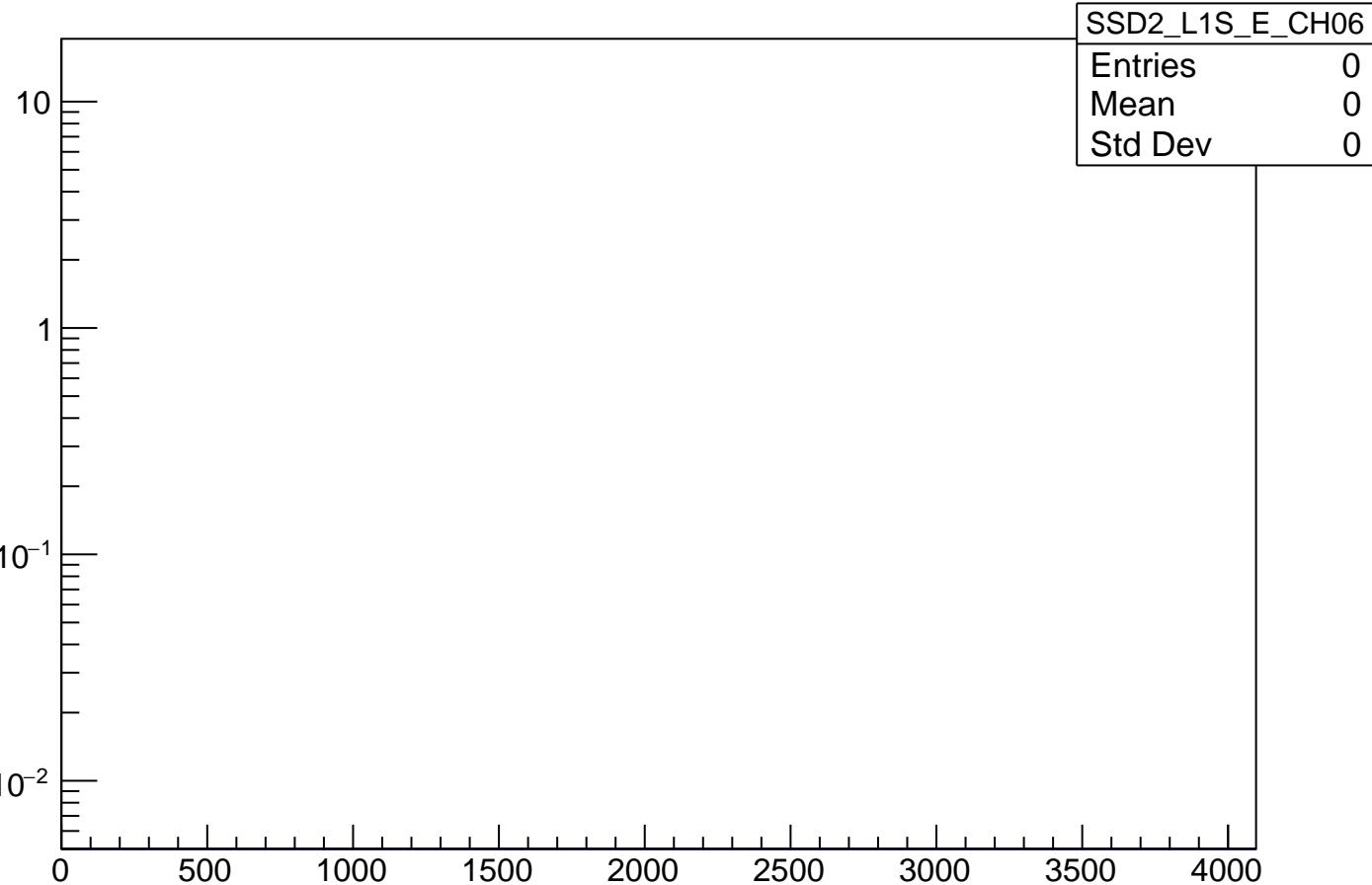
SSD2_L1S_E_CH04



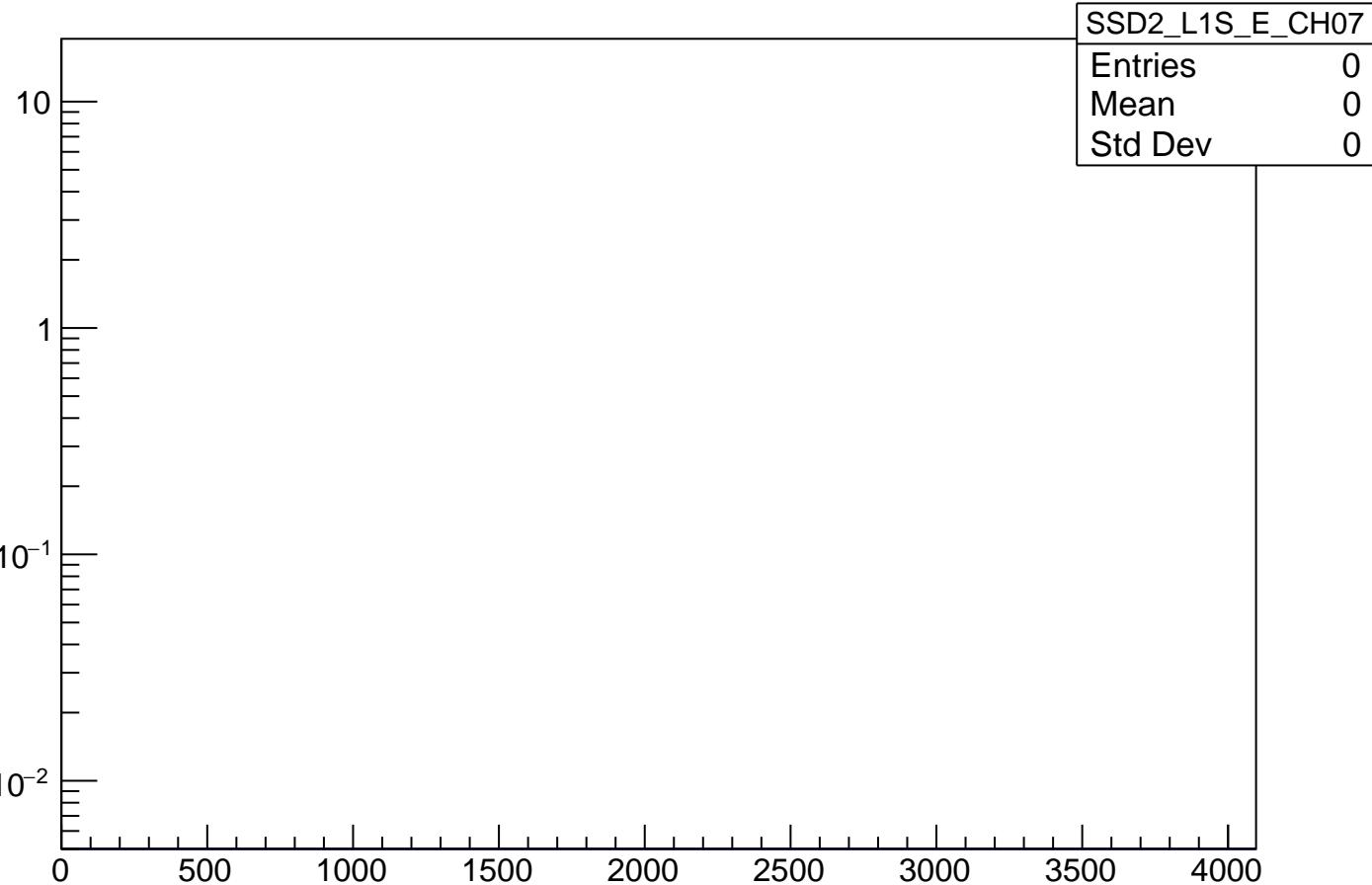
SSD2_L1S_E_CH05



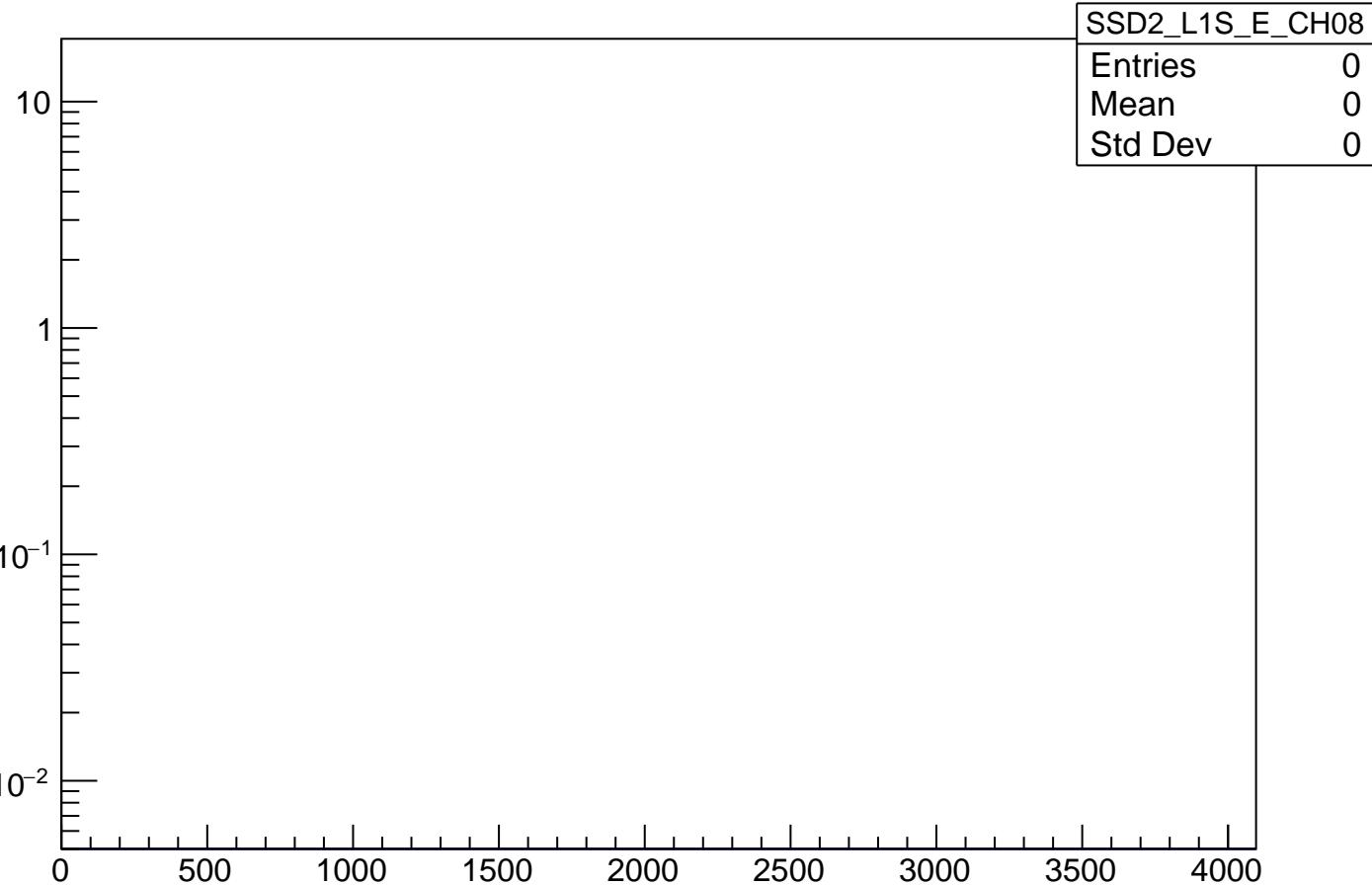
SSD2_L1S_E_CH06



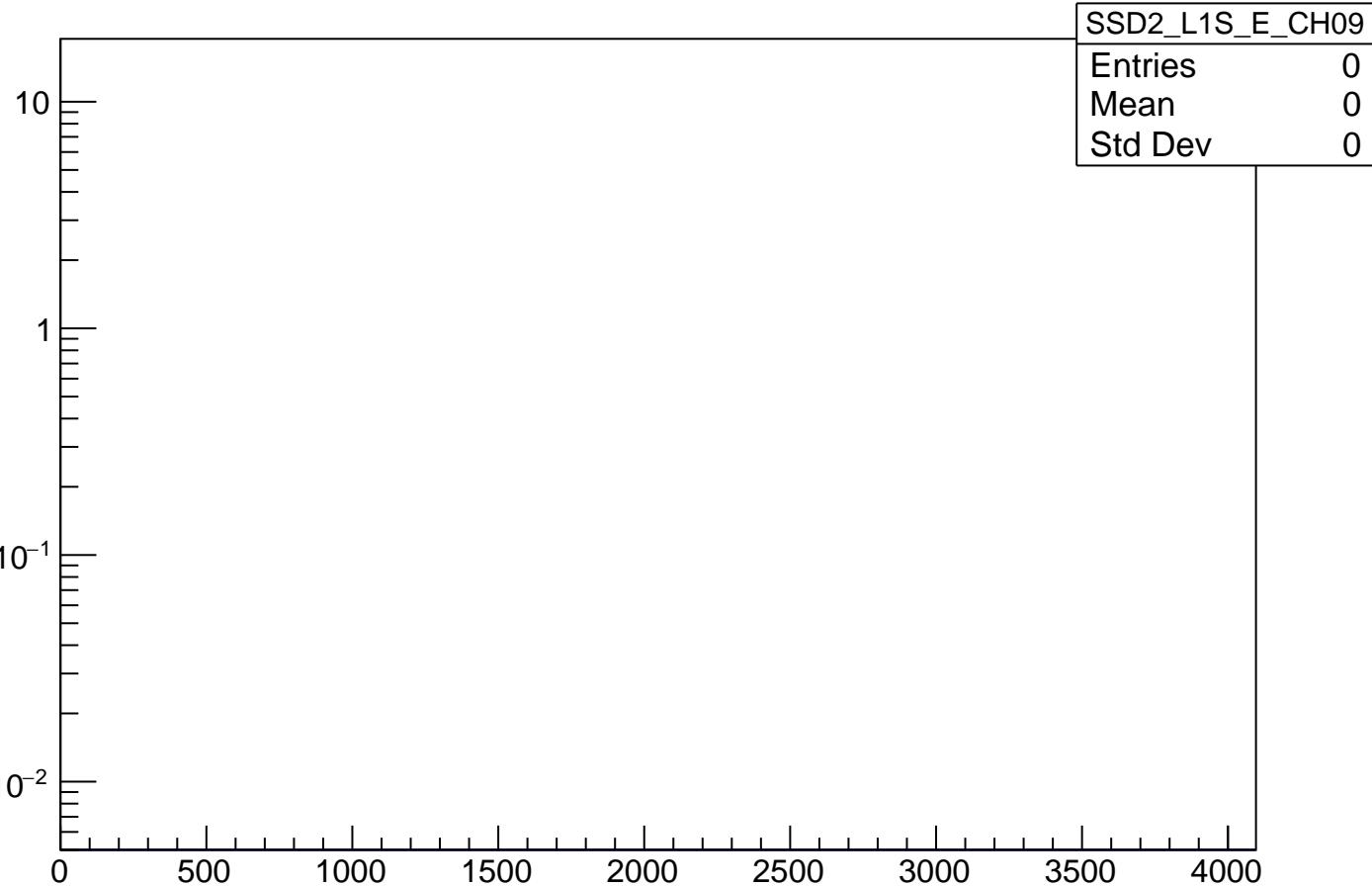
SSD2_L1S_E_CH07



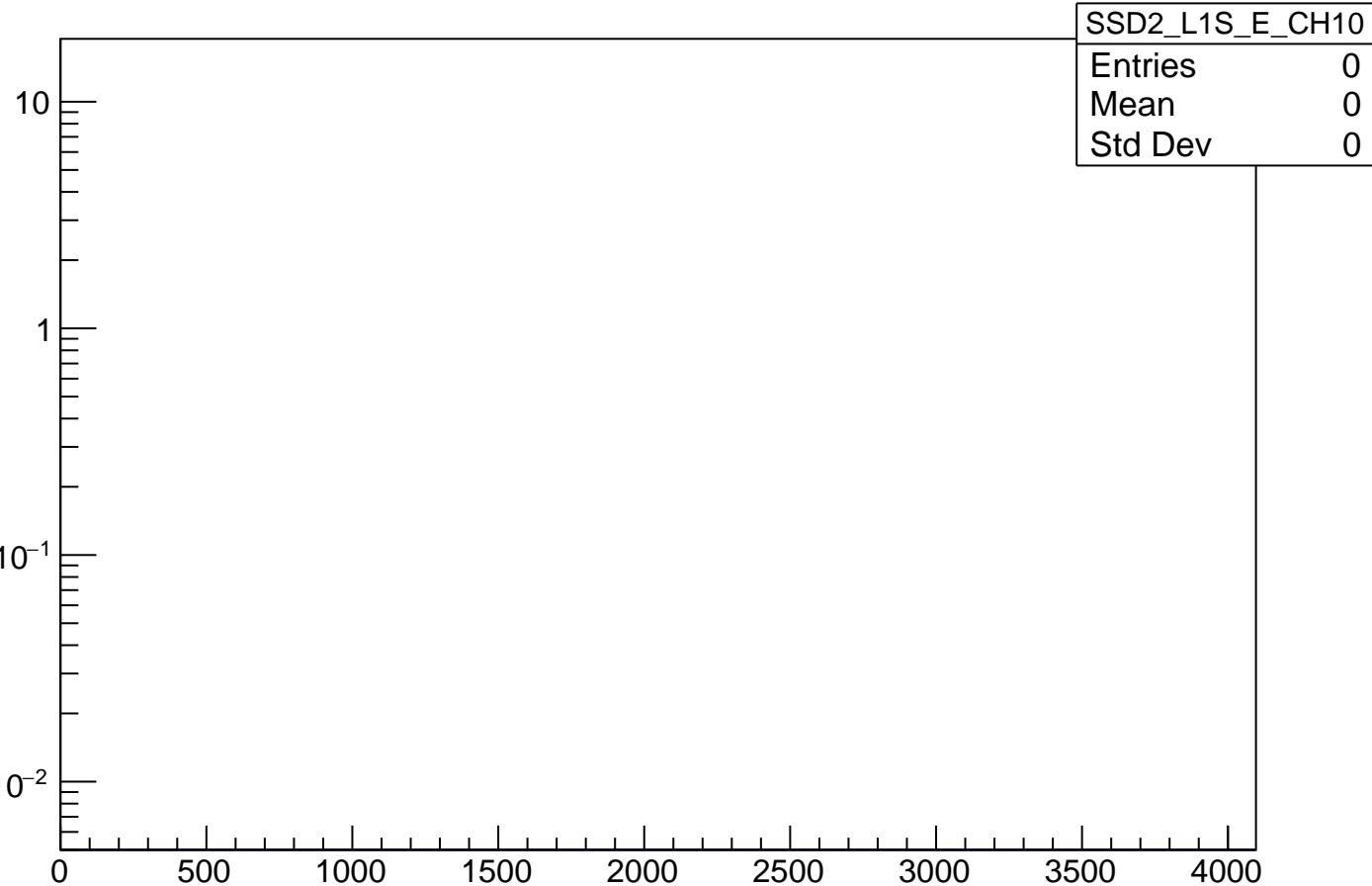
SSD2_L1S_E_CH08



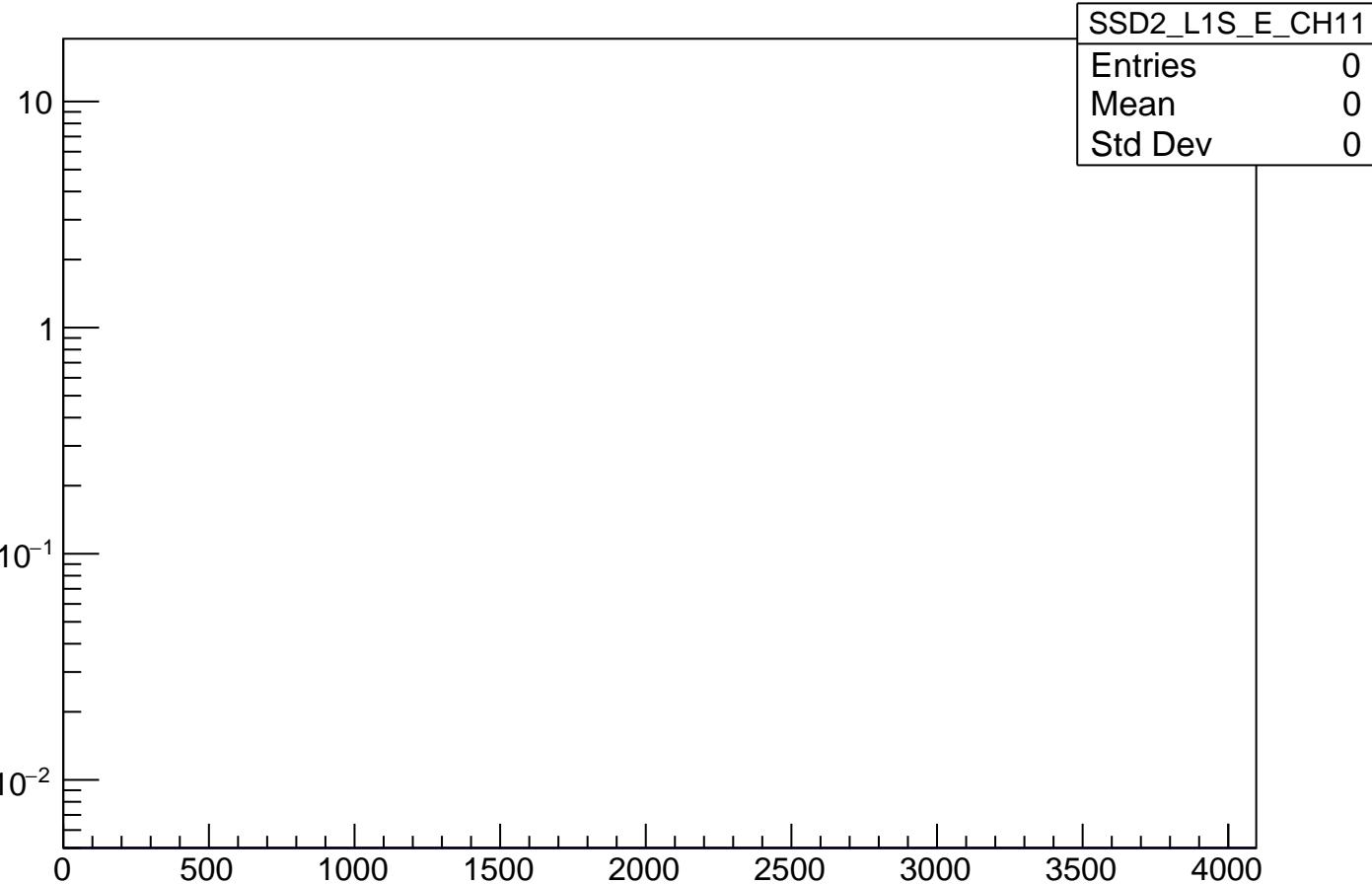
SSD2_L1S_E_CH09



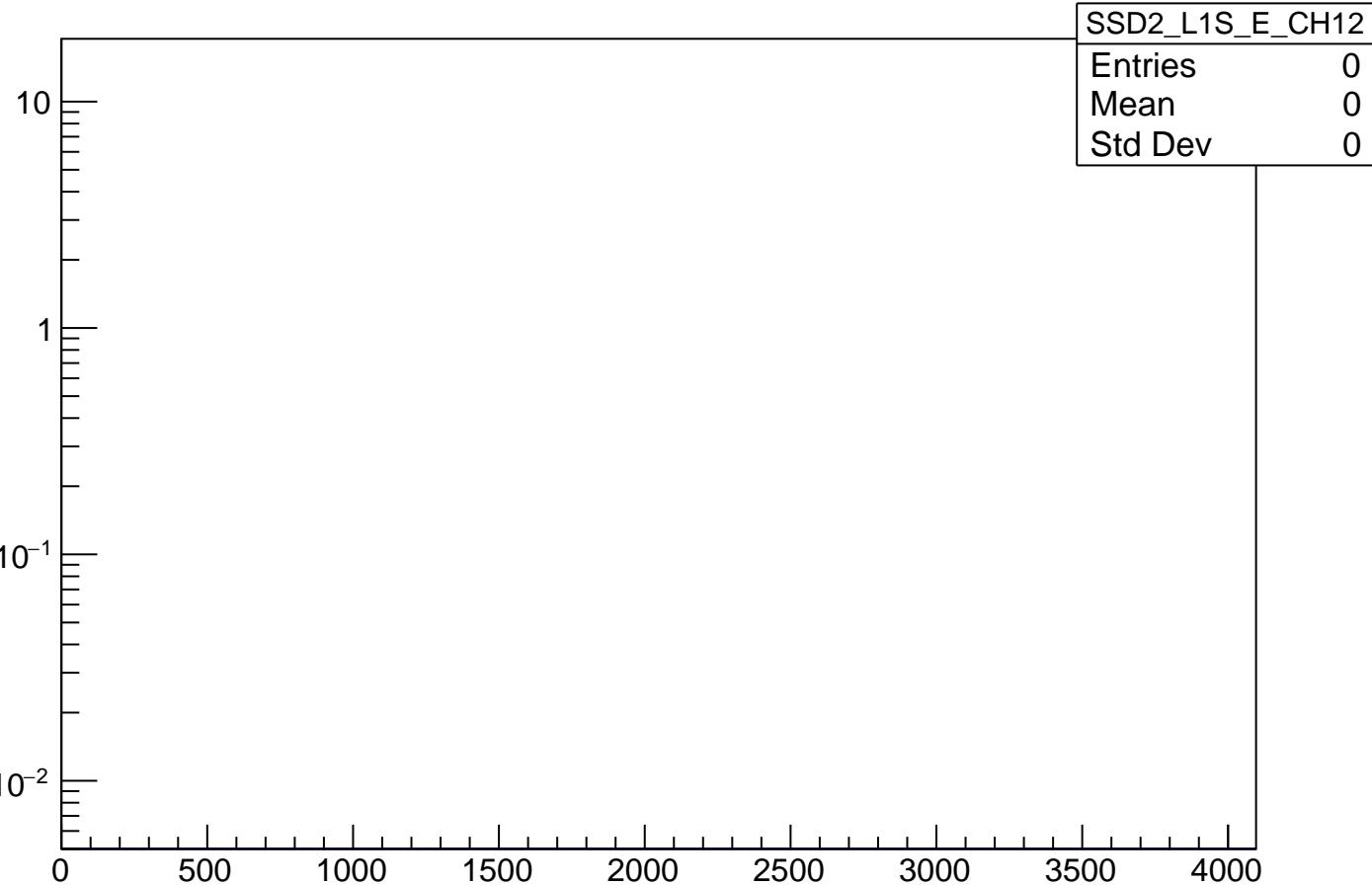
SSD2_L1S_E_CH10



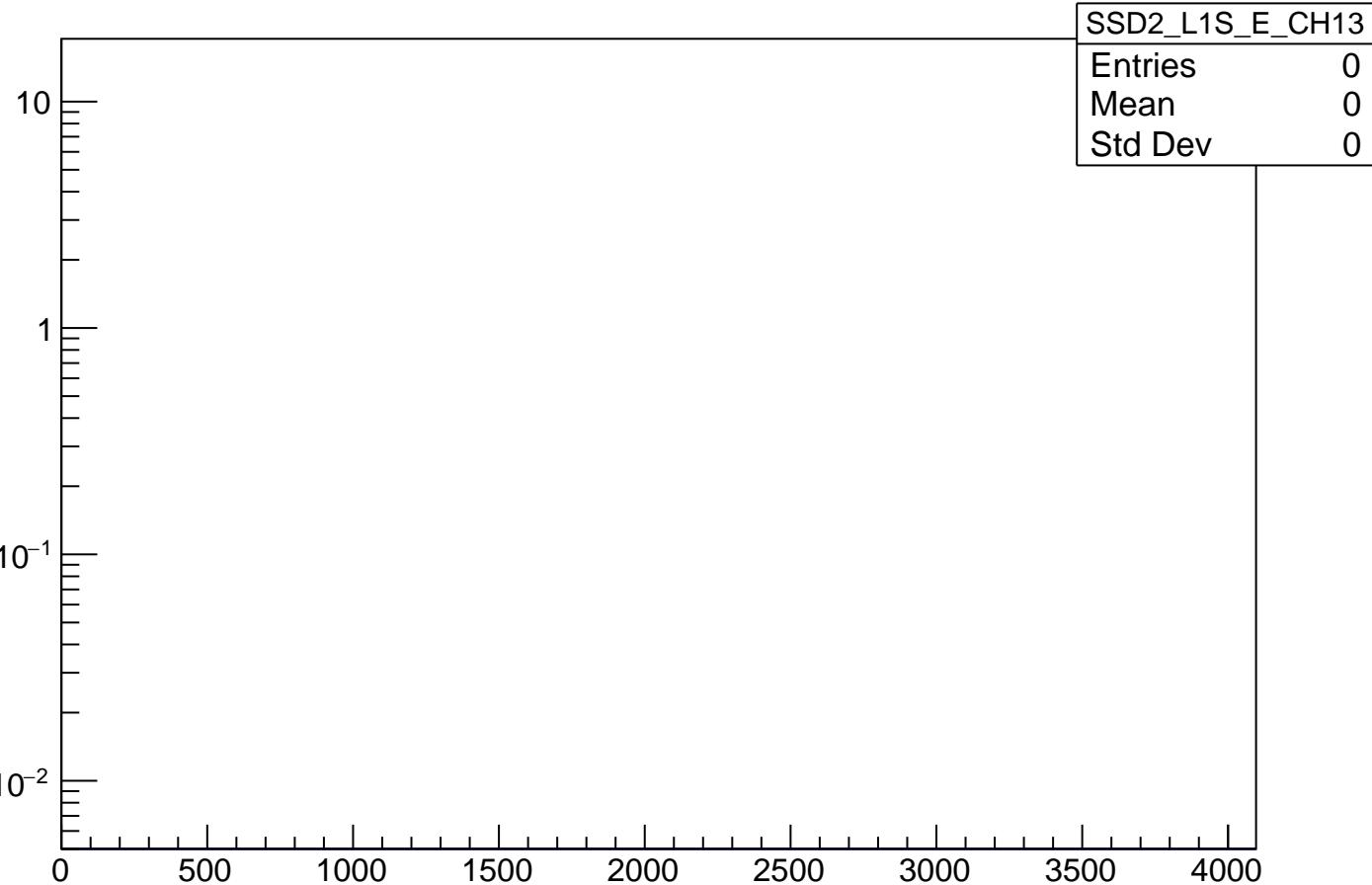
SSD2_L1S_E_CH11



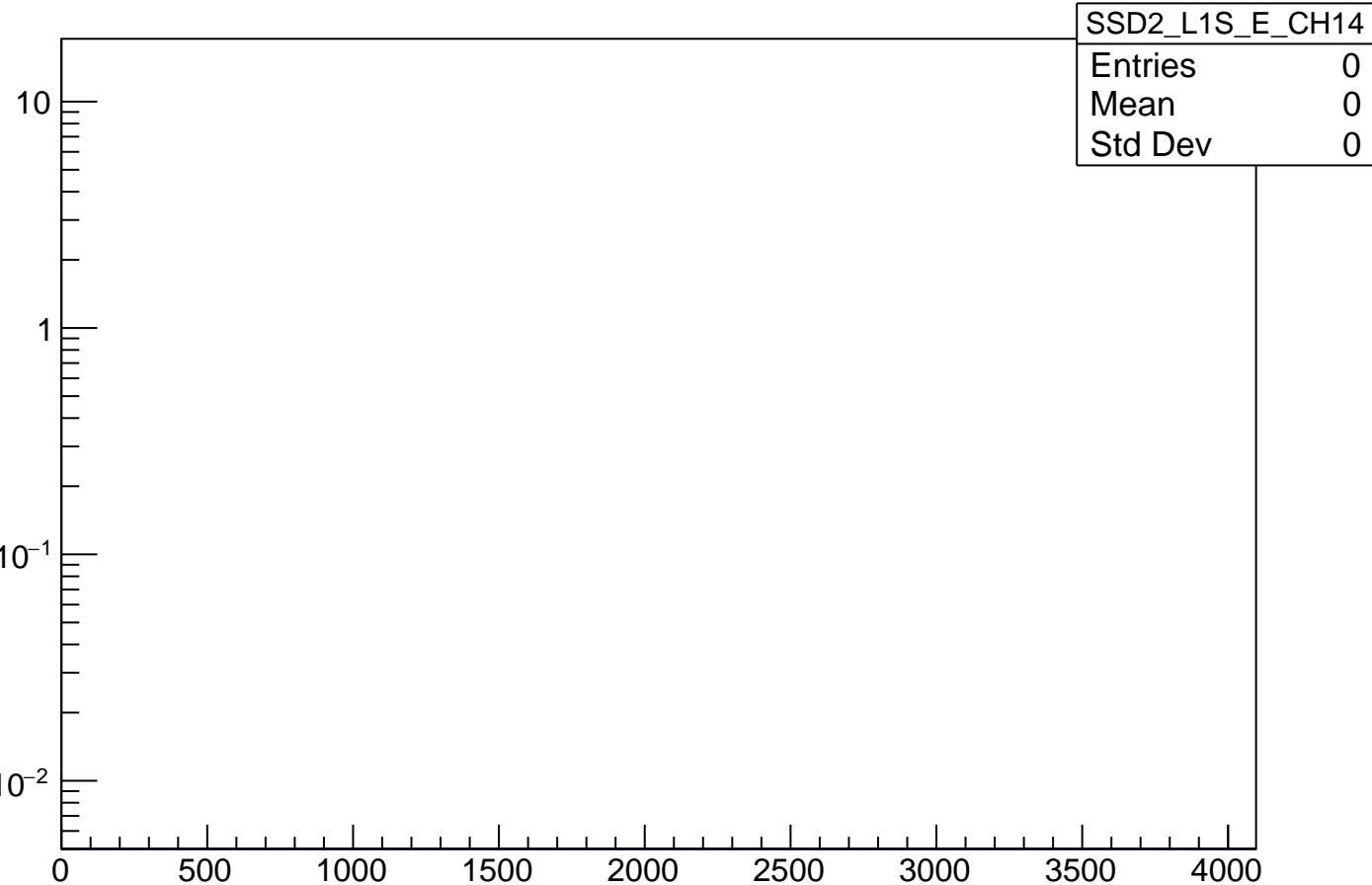
SSD2_L1S_E_CH12



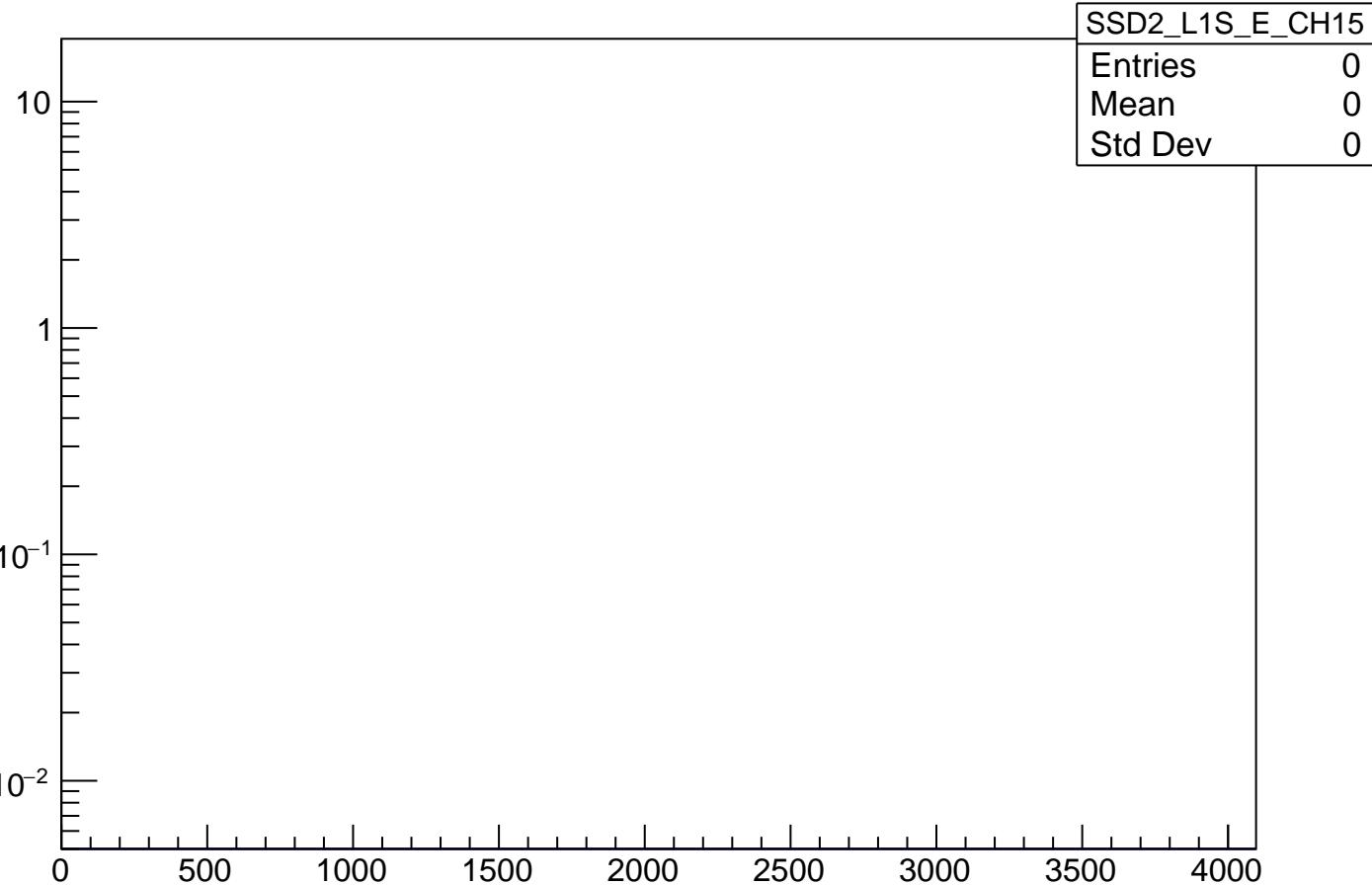
SSD2_L1S_E_CH13



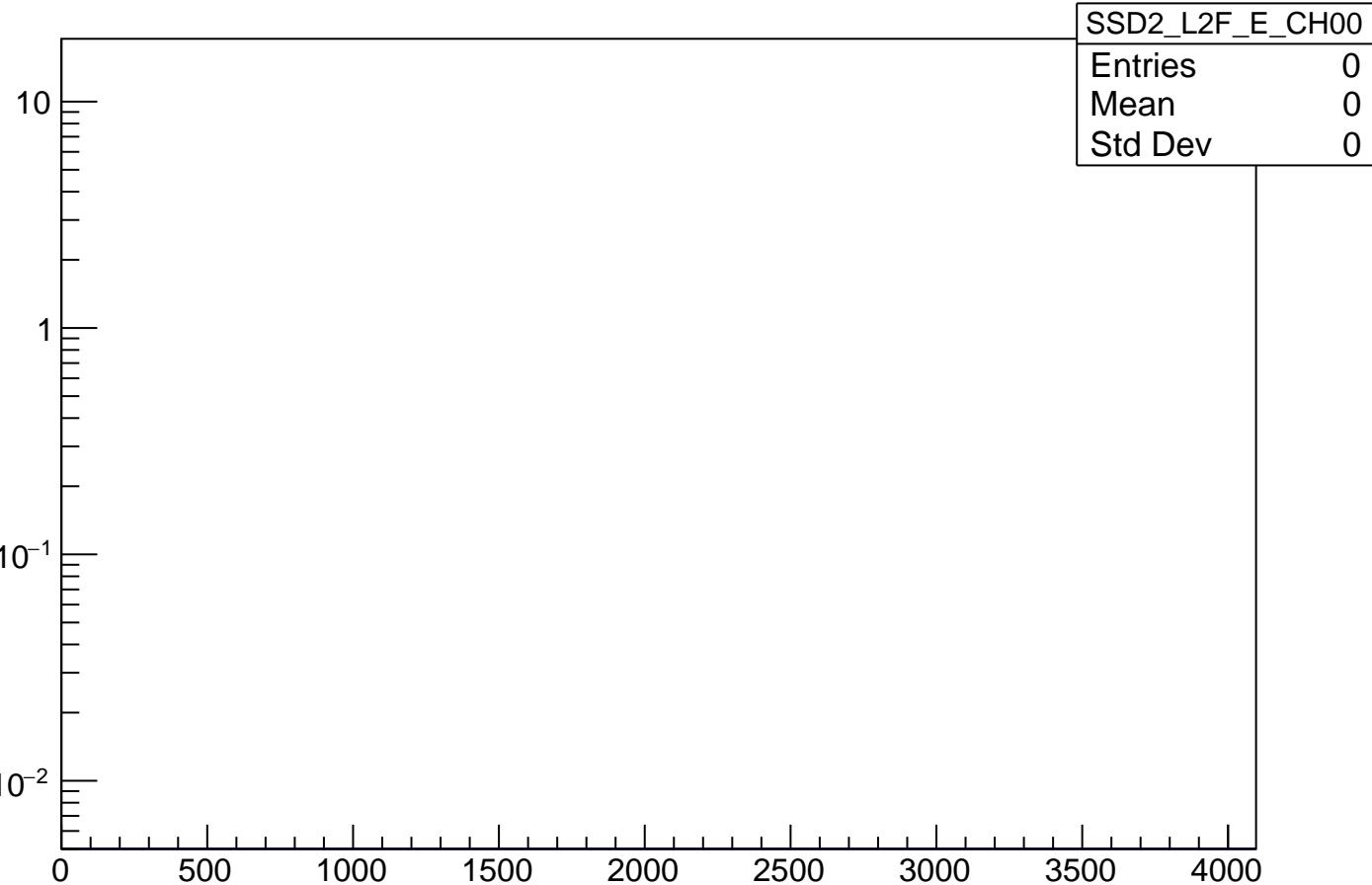
SSD2_L1S_E_CH14



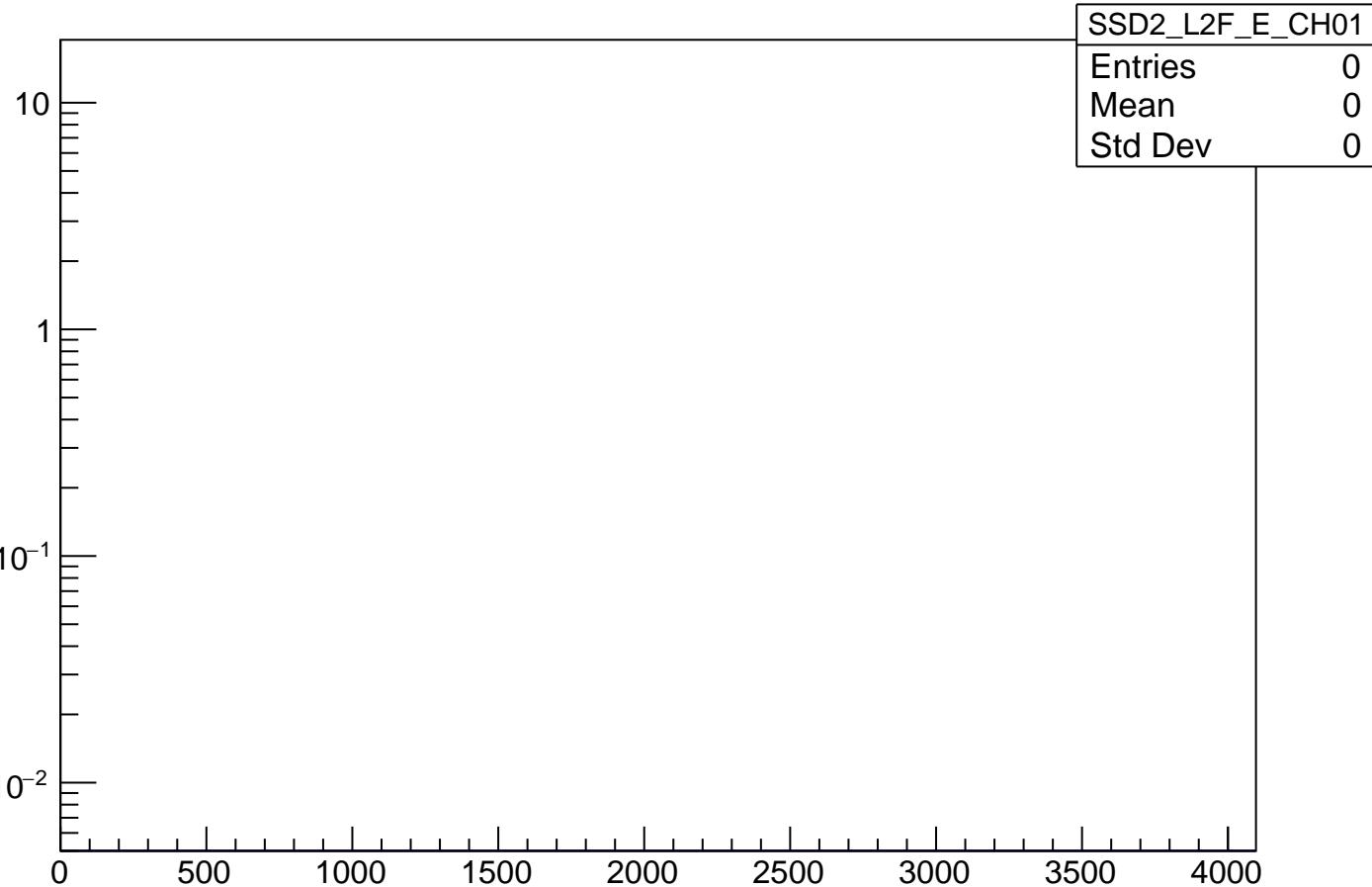
SSD2_L1S_E_CH15



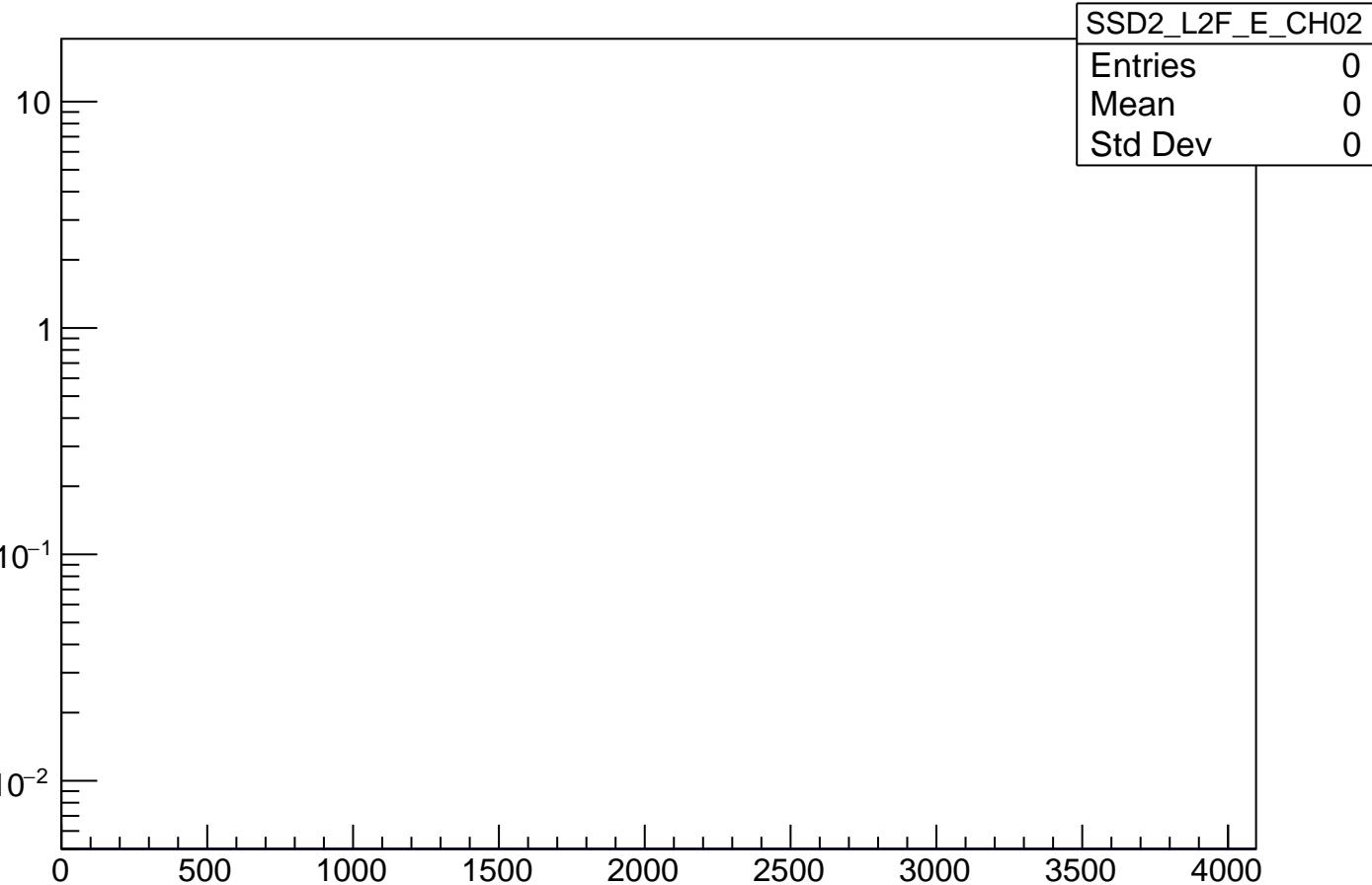
SSD2_L2F_E_CH00



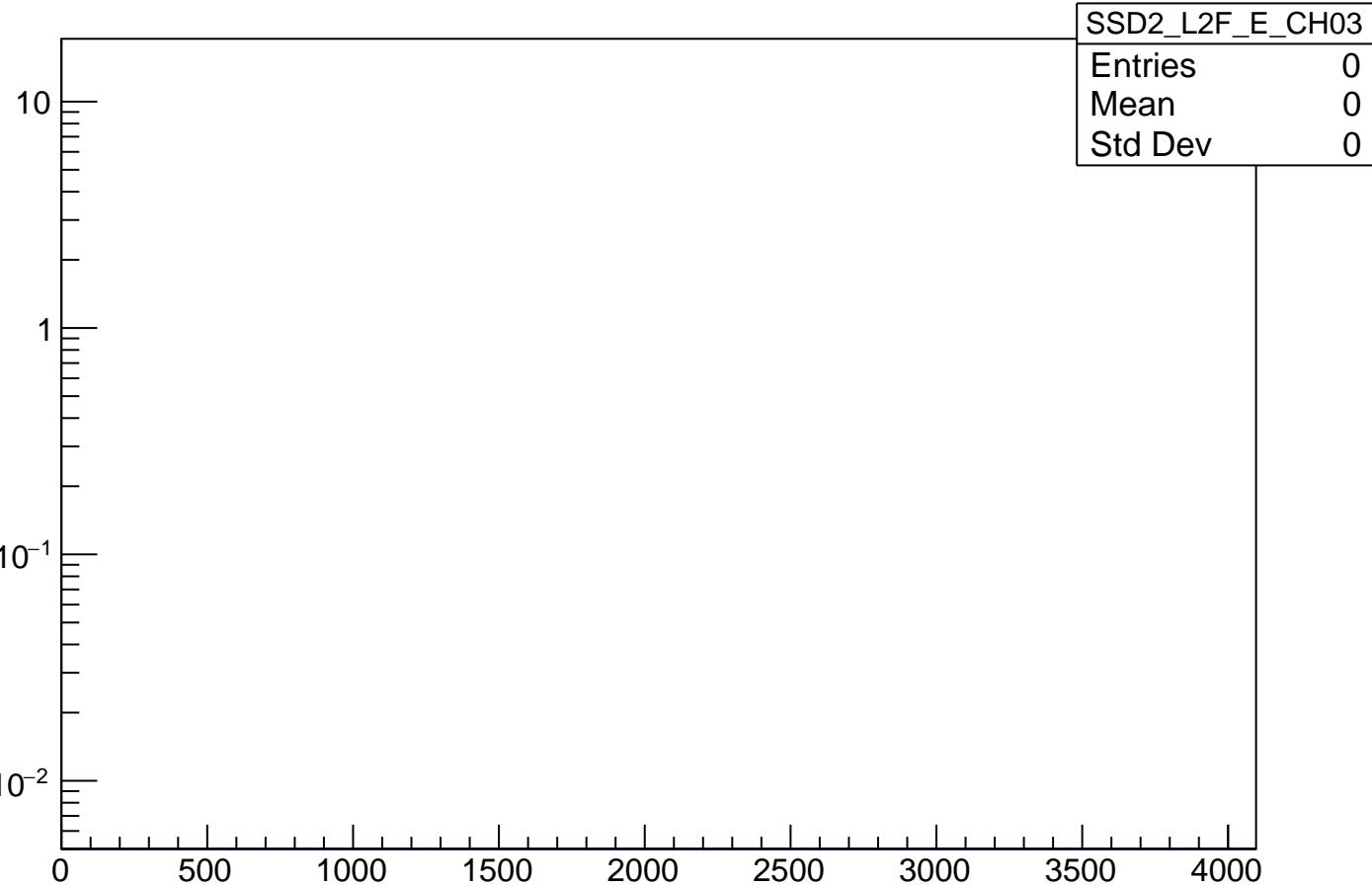
SSD2_L2F_E_CH01



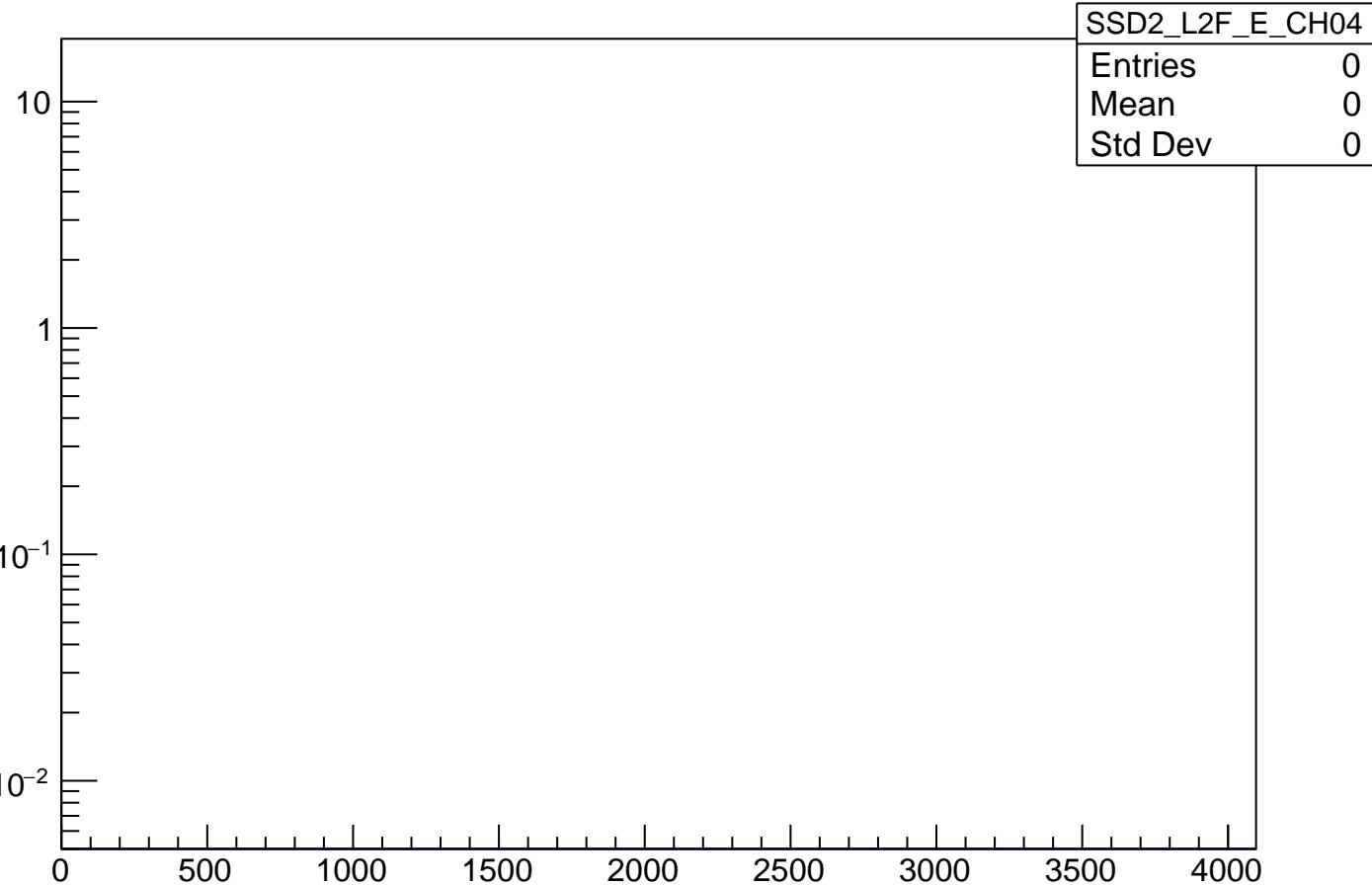
SSD2_L2F_E_CH02



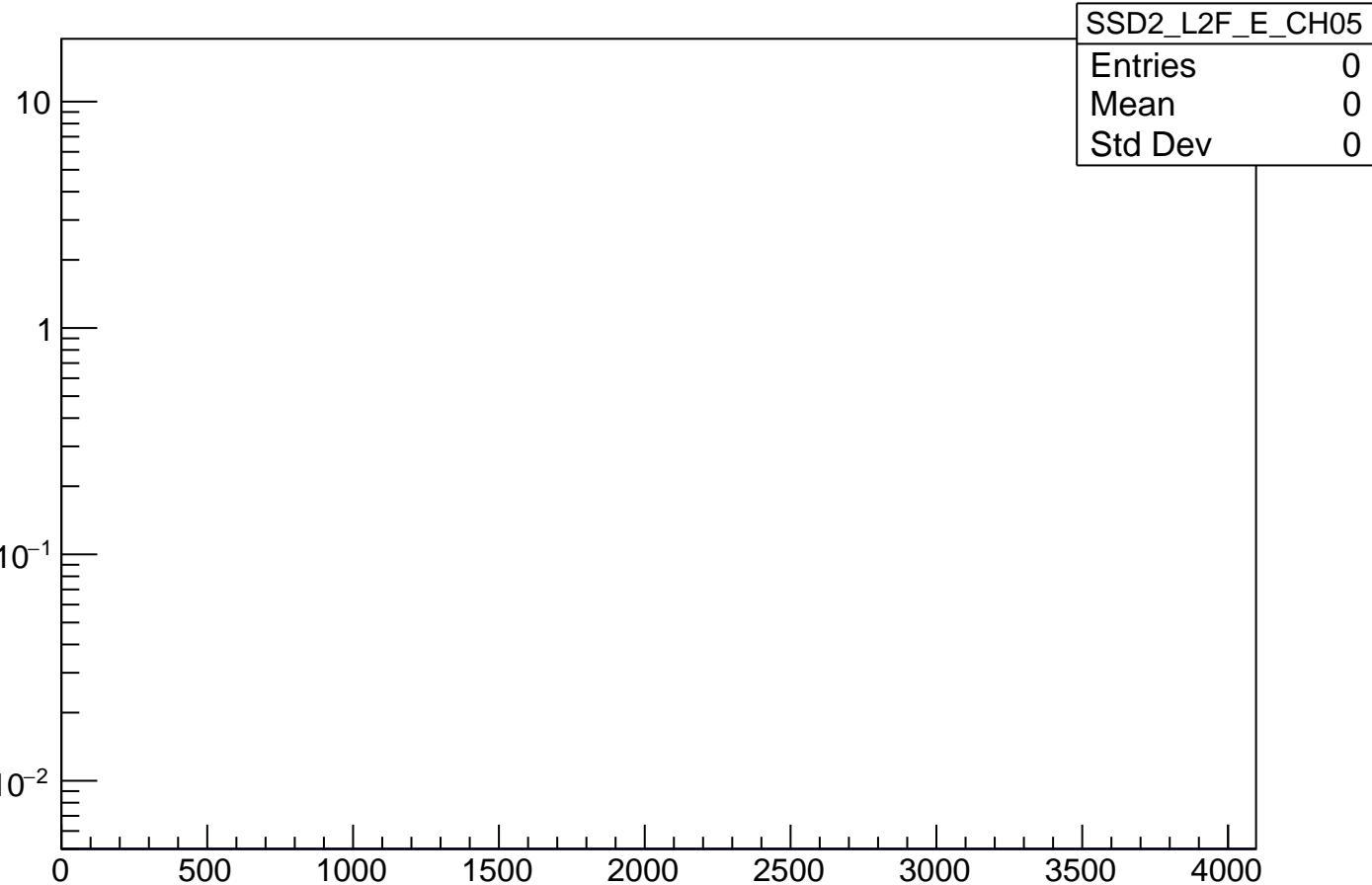
SSD2_L2F_E_CH03



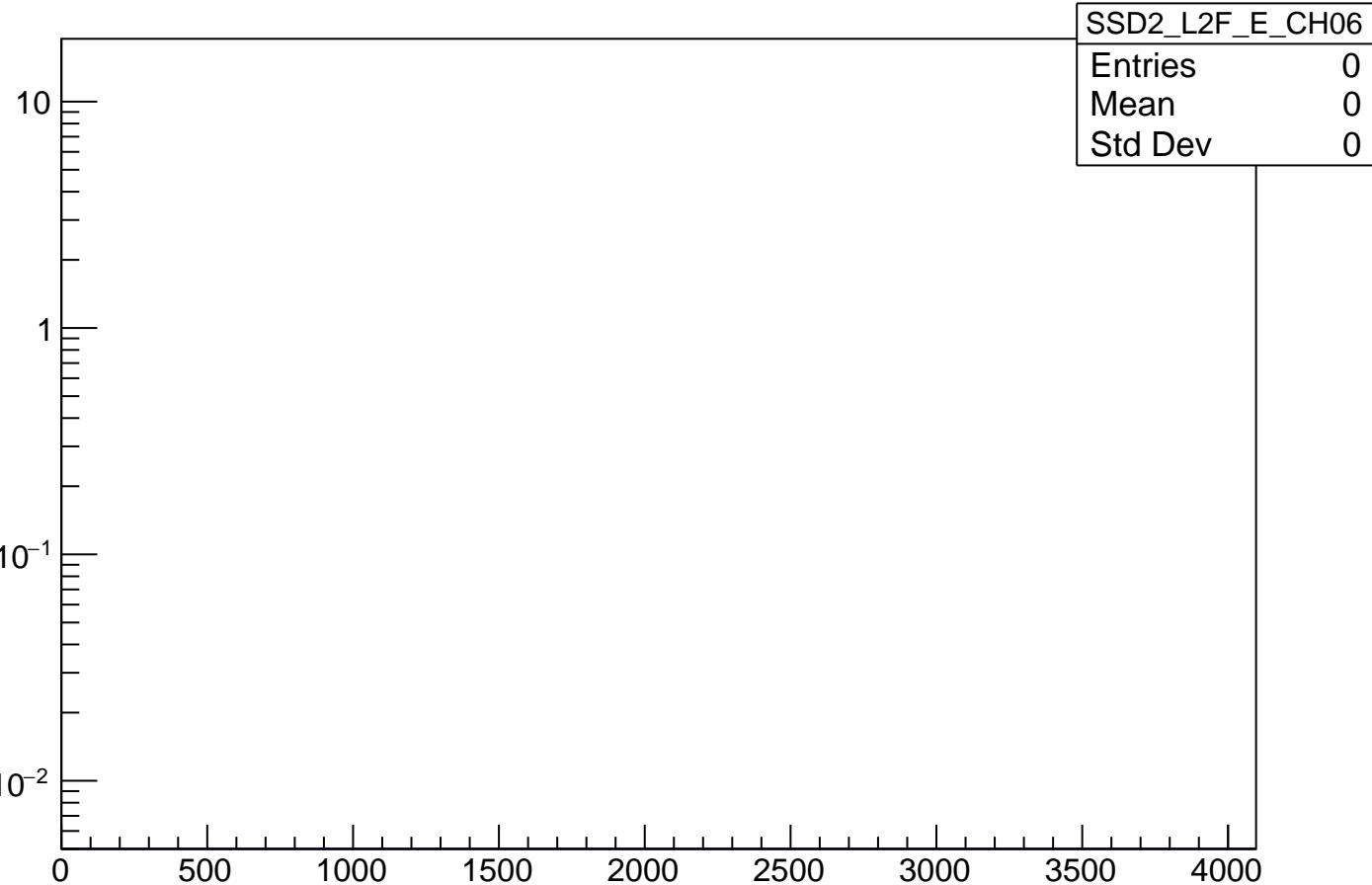
SSD2_L2F_E_CH04



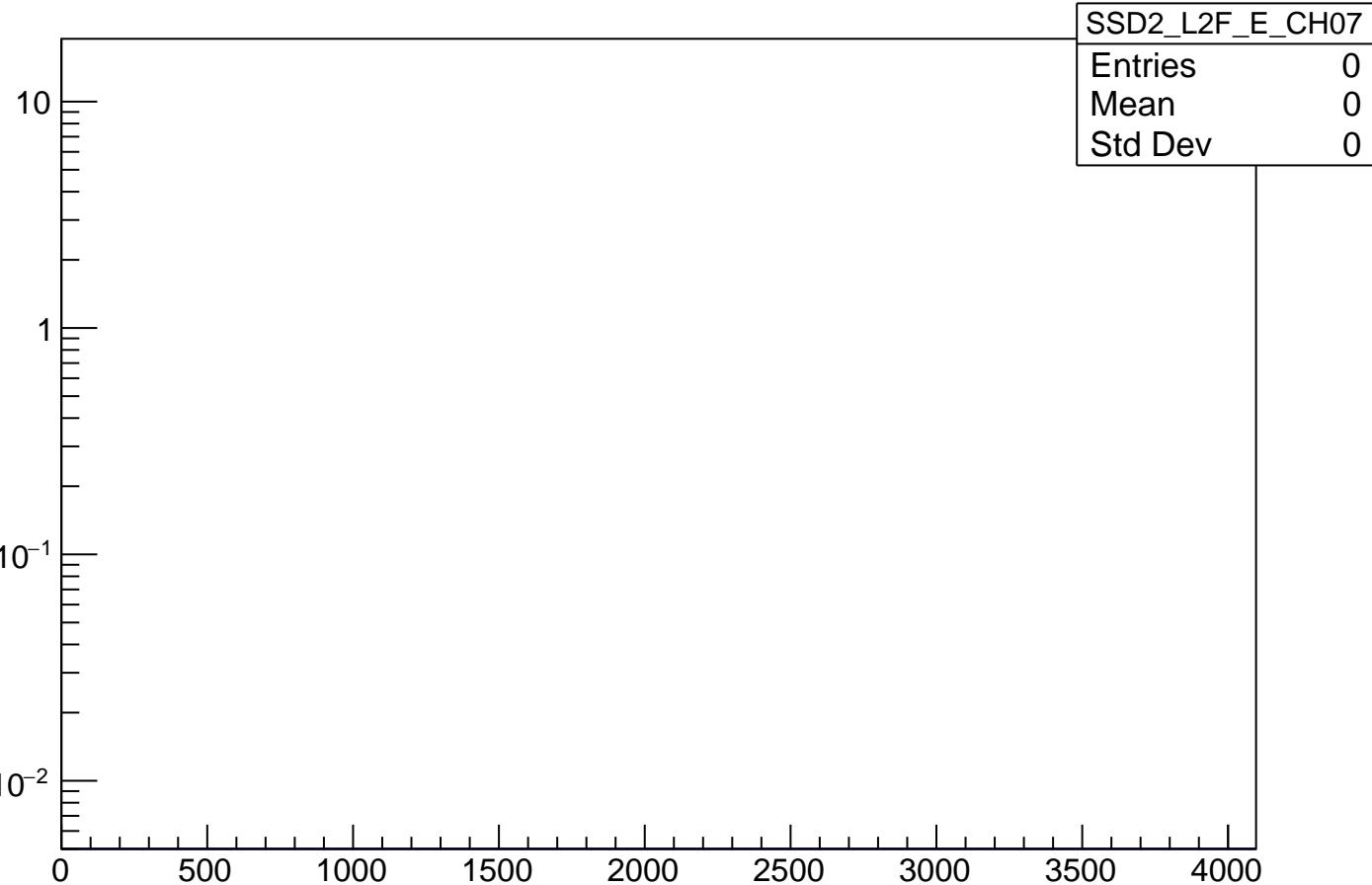
SSD2_L2F_E_CH05



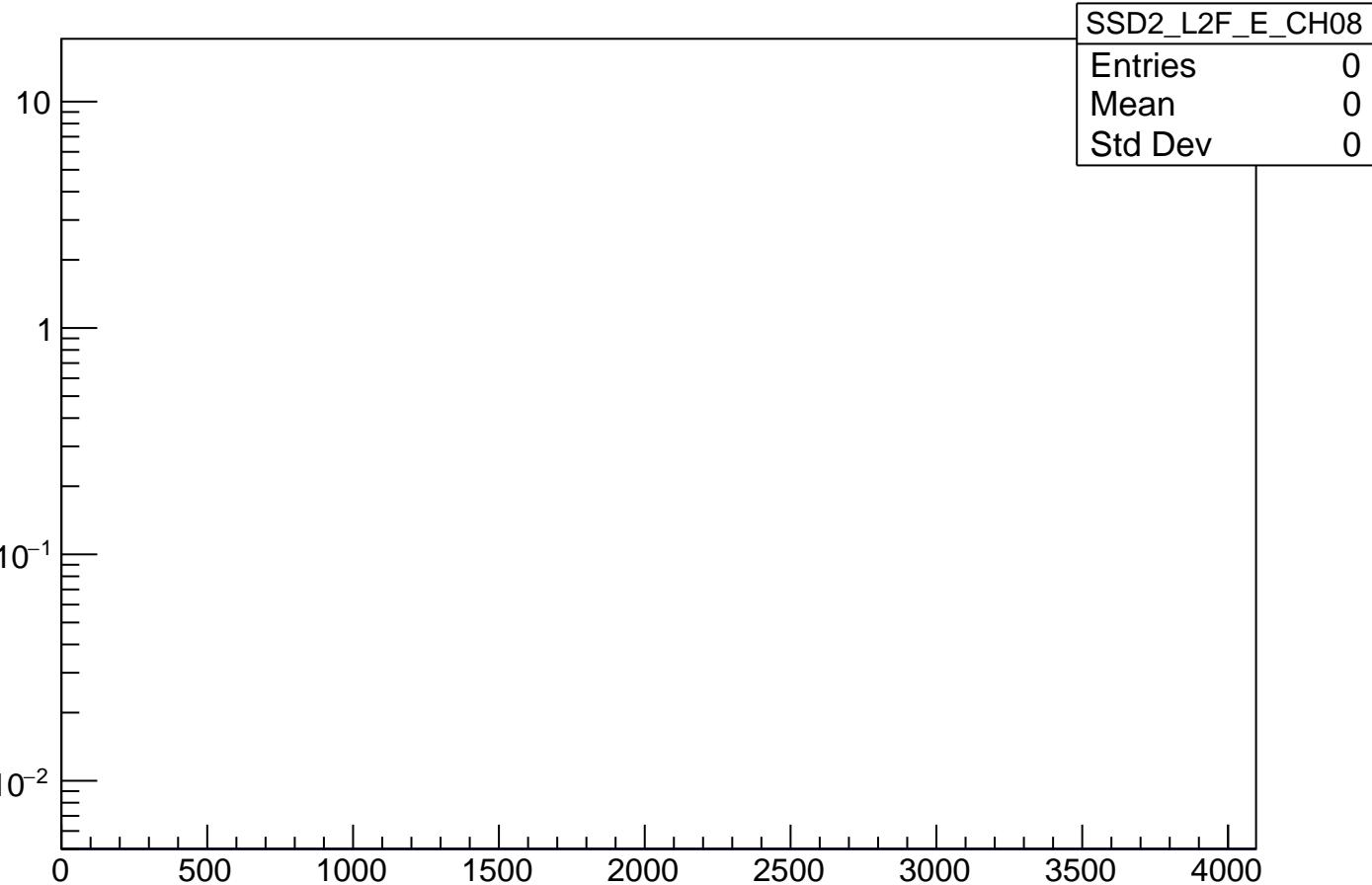
SSD2_L2F_E_CH06



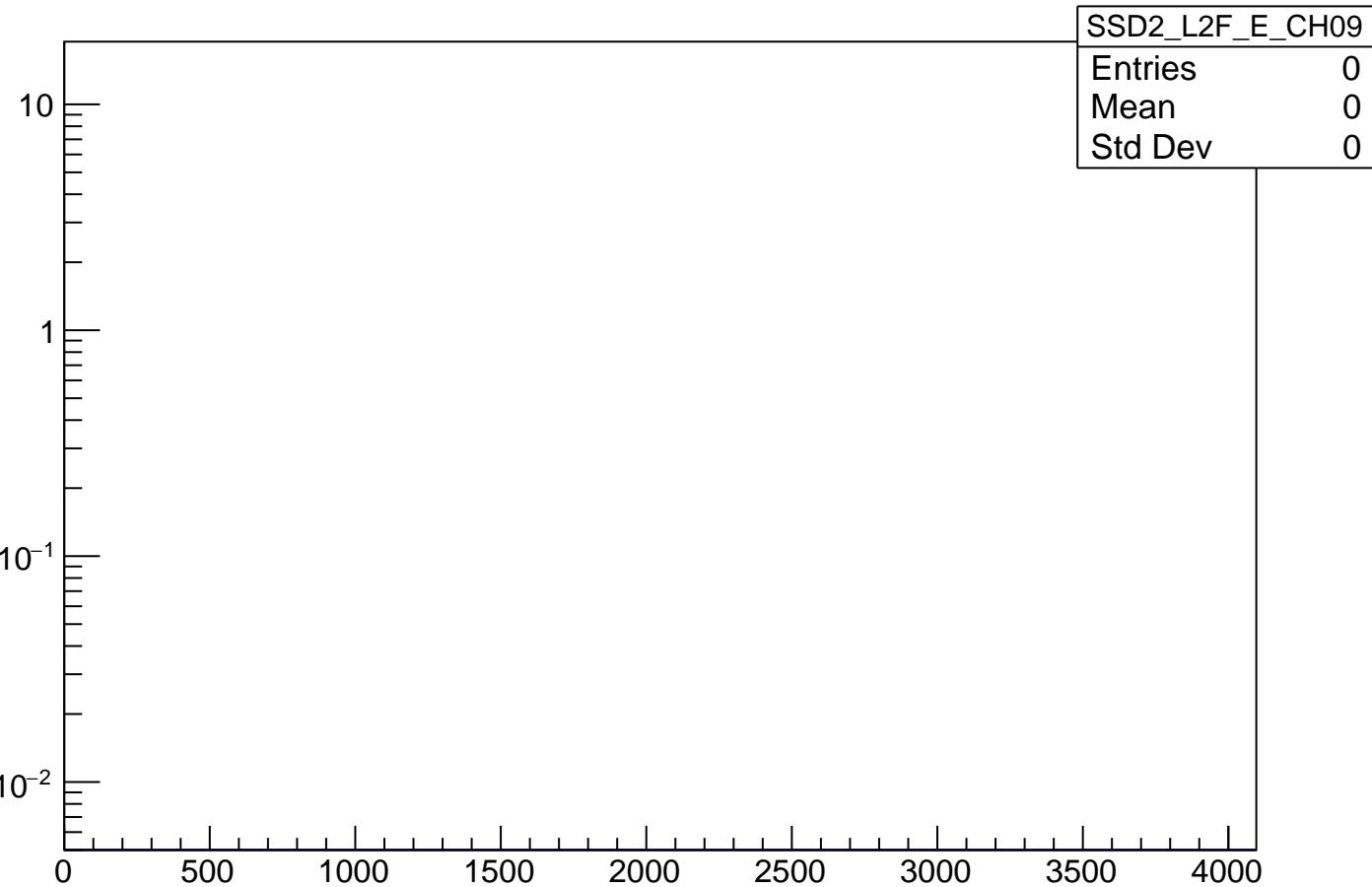
SSD2_L2F_E_CH07



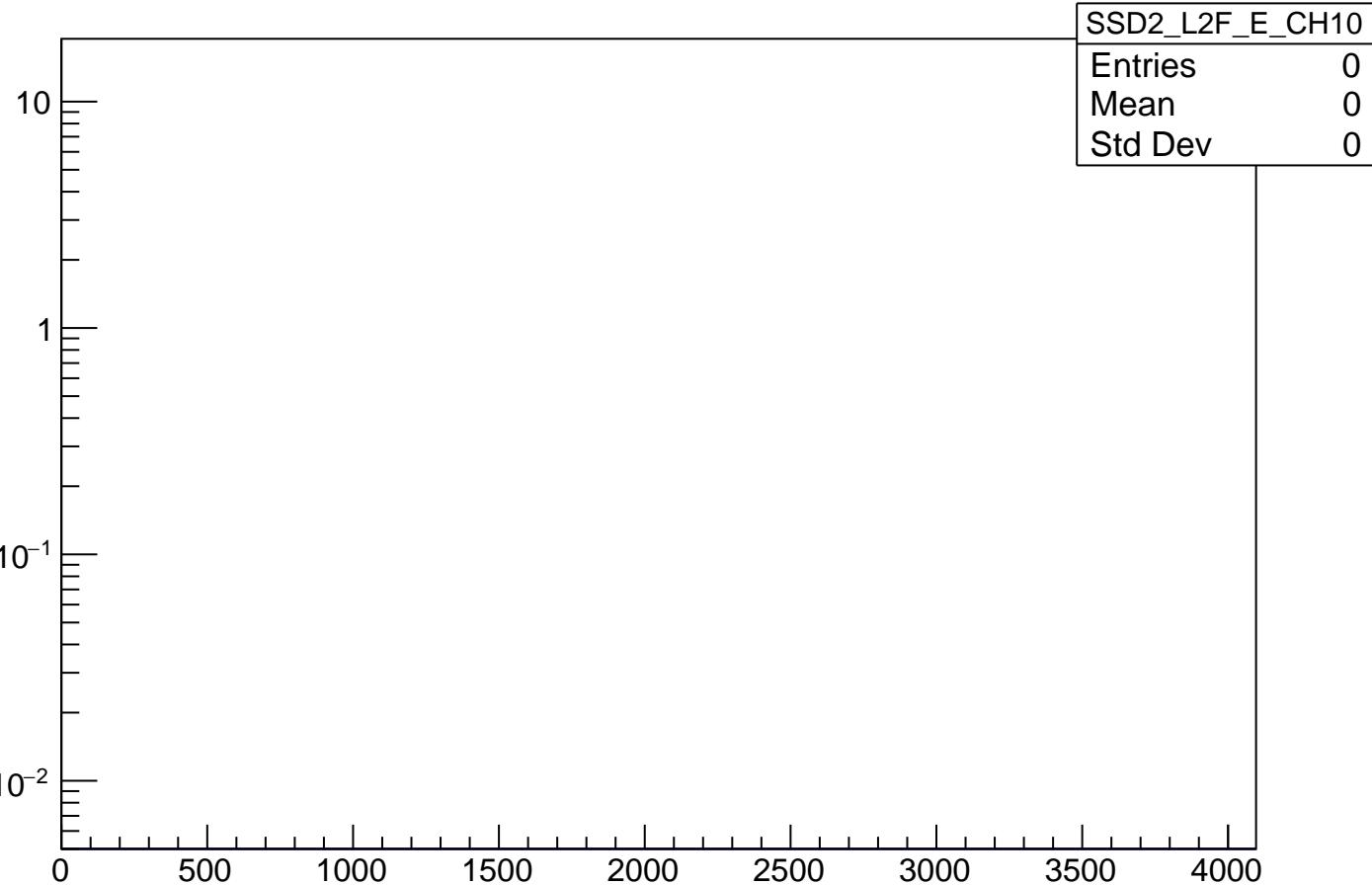
SSD2_L2F_E_CH08



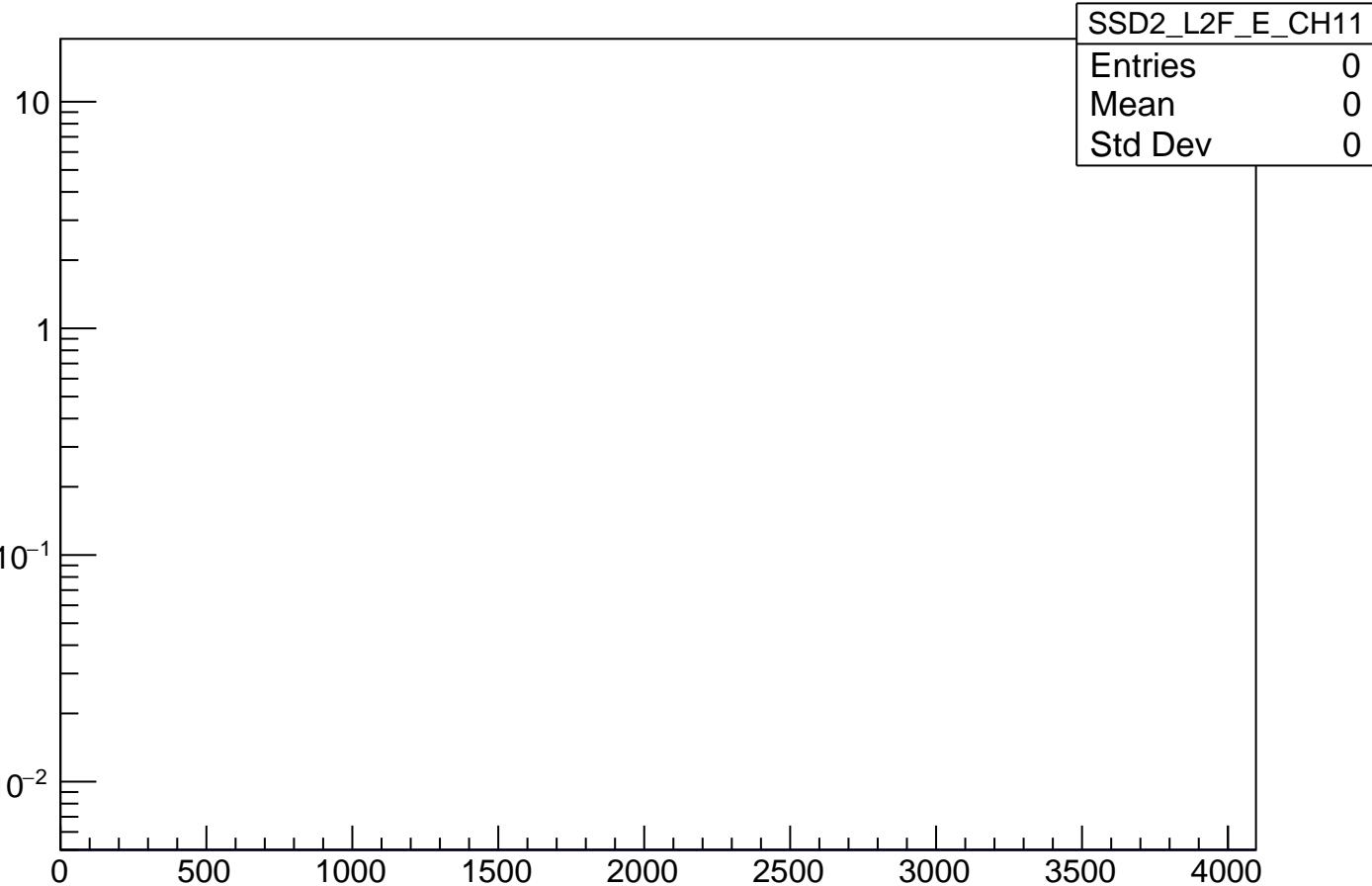
SSD2_L2F_E_CH09



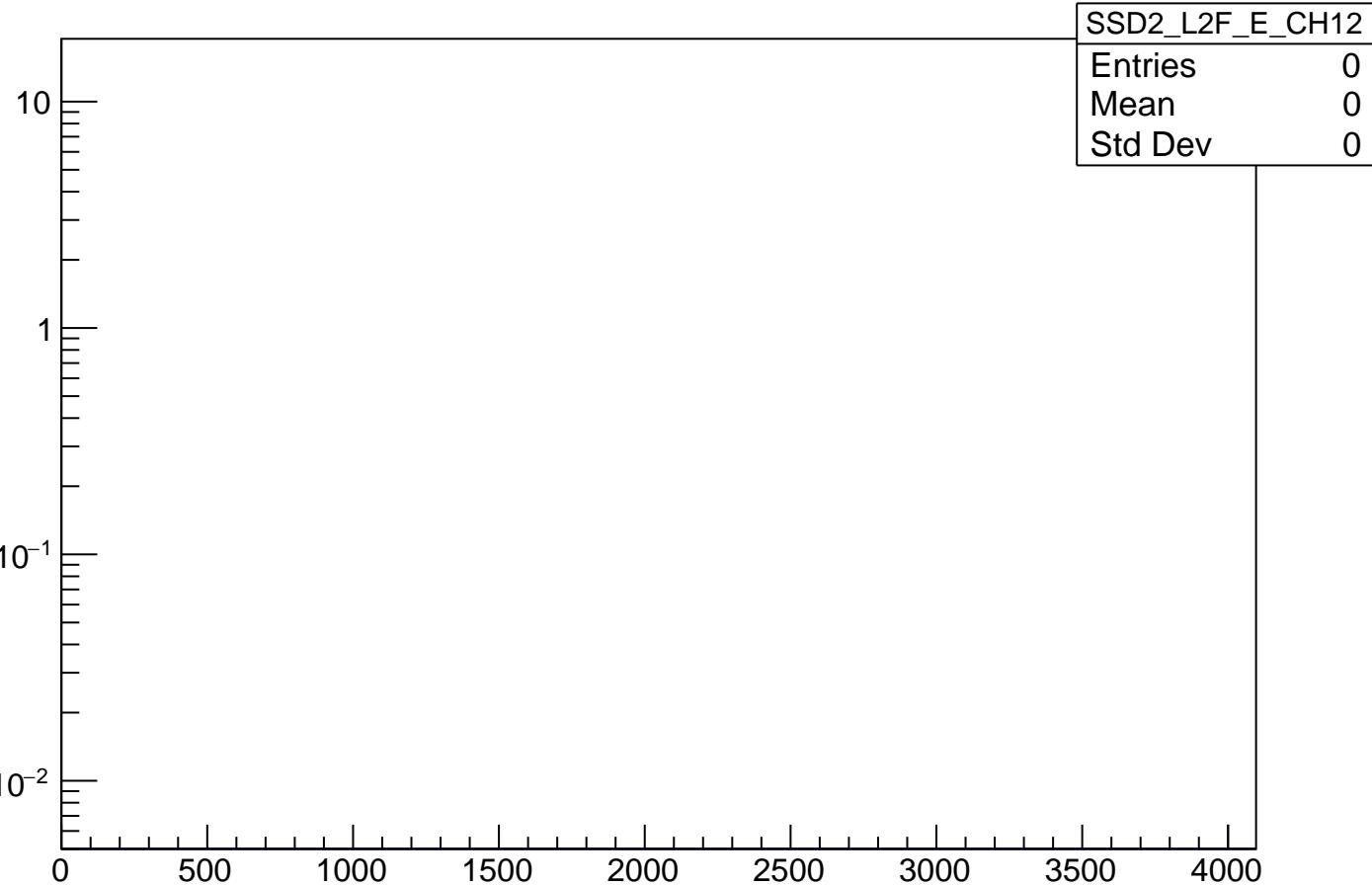
SSD2_L2F_E_CH10



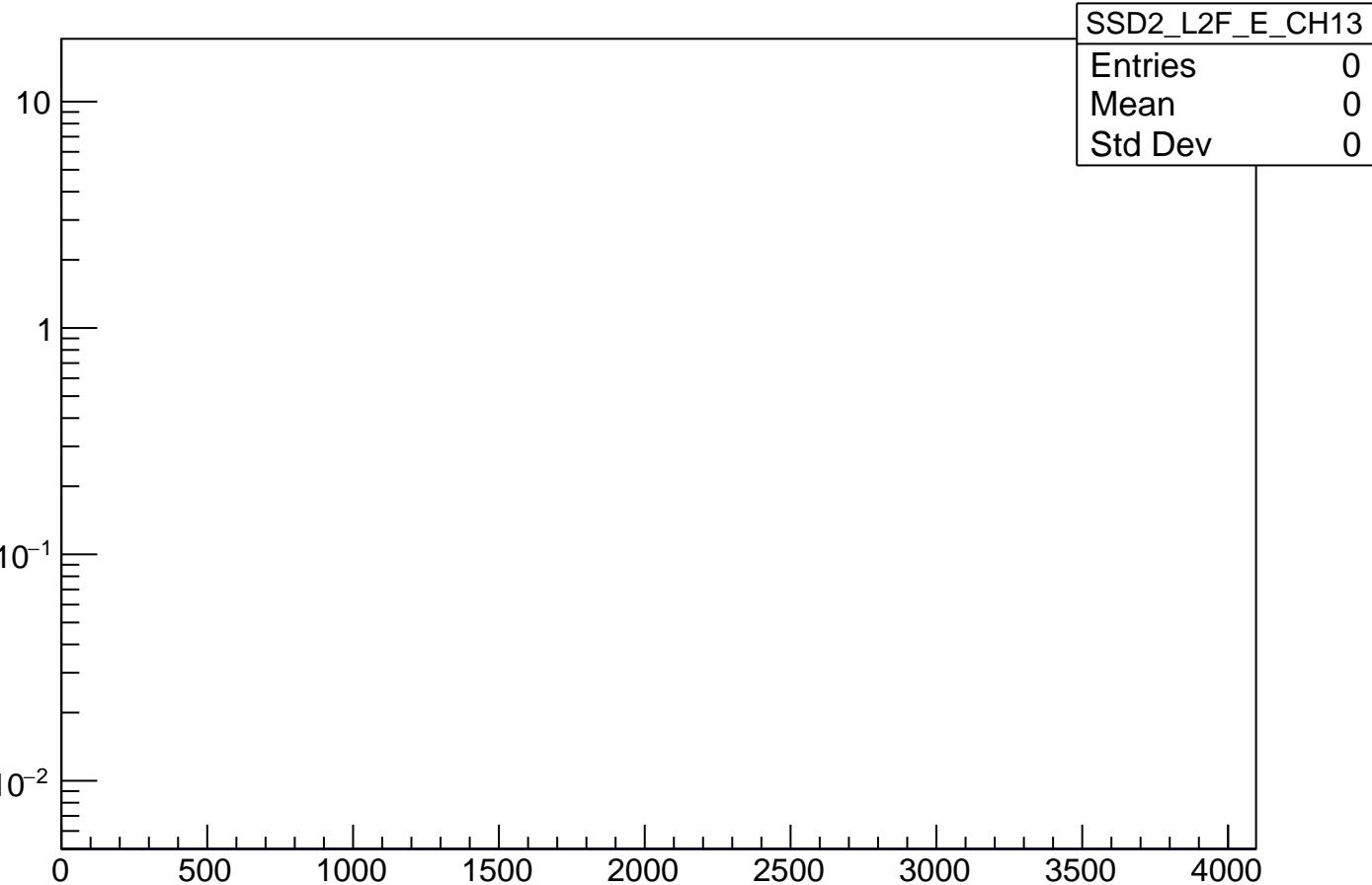
SSD2_L2F_E_CH11



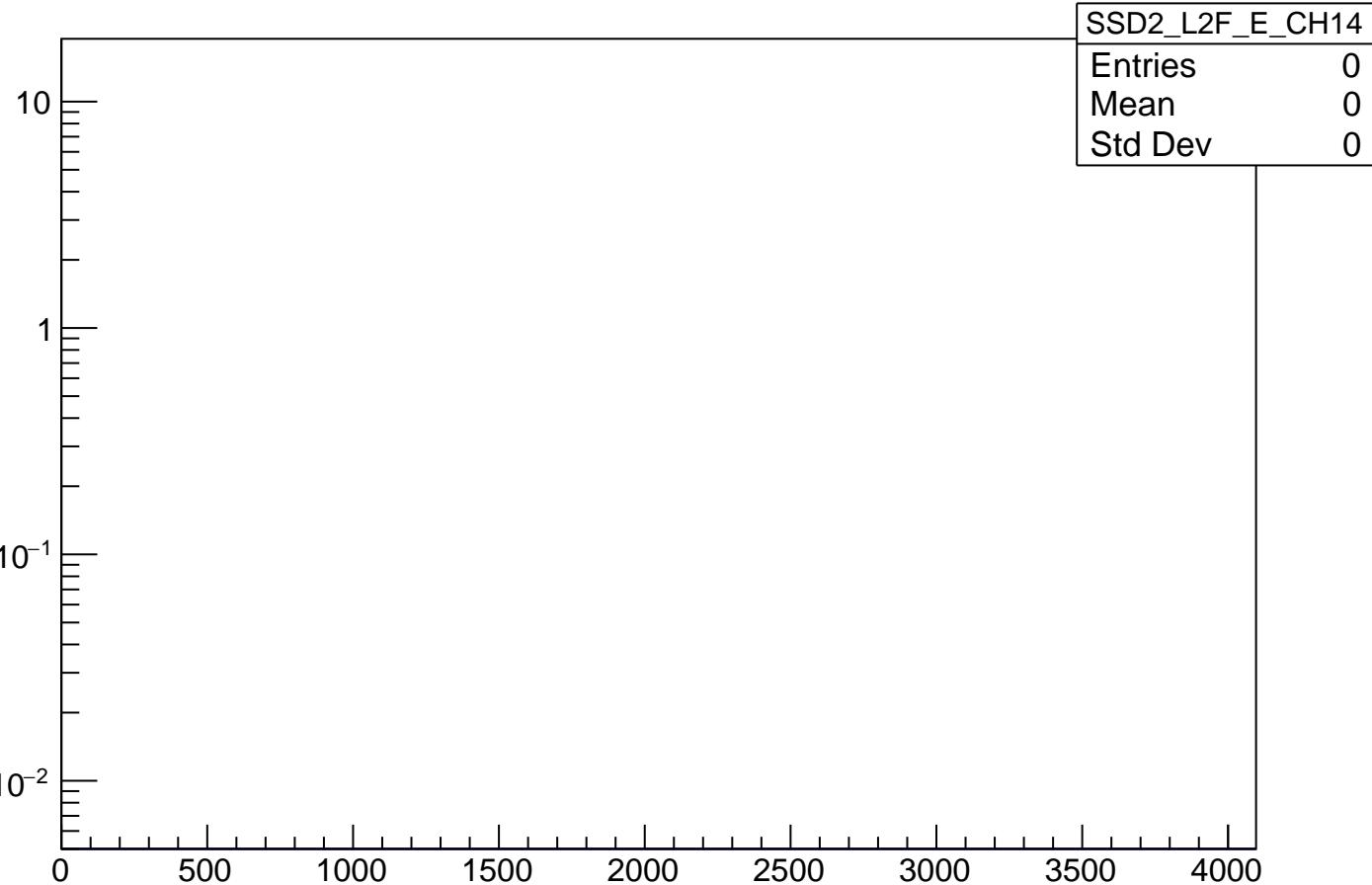
SSD2_L2F_E_CH12



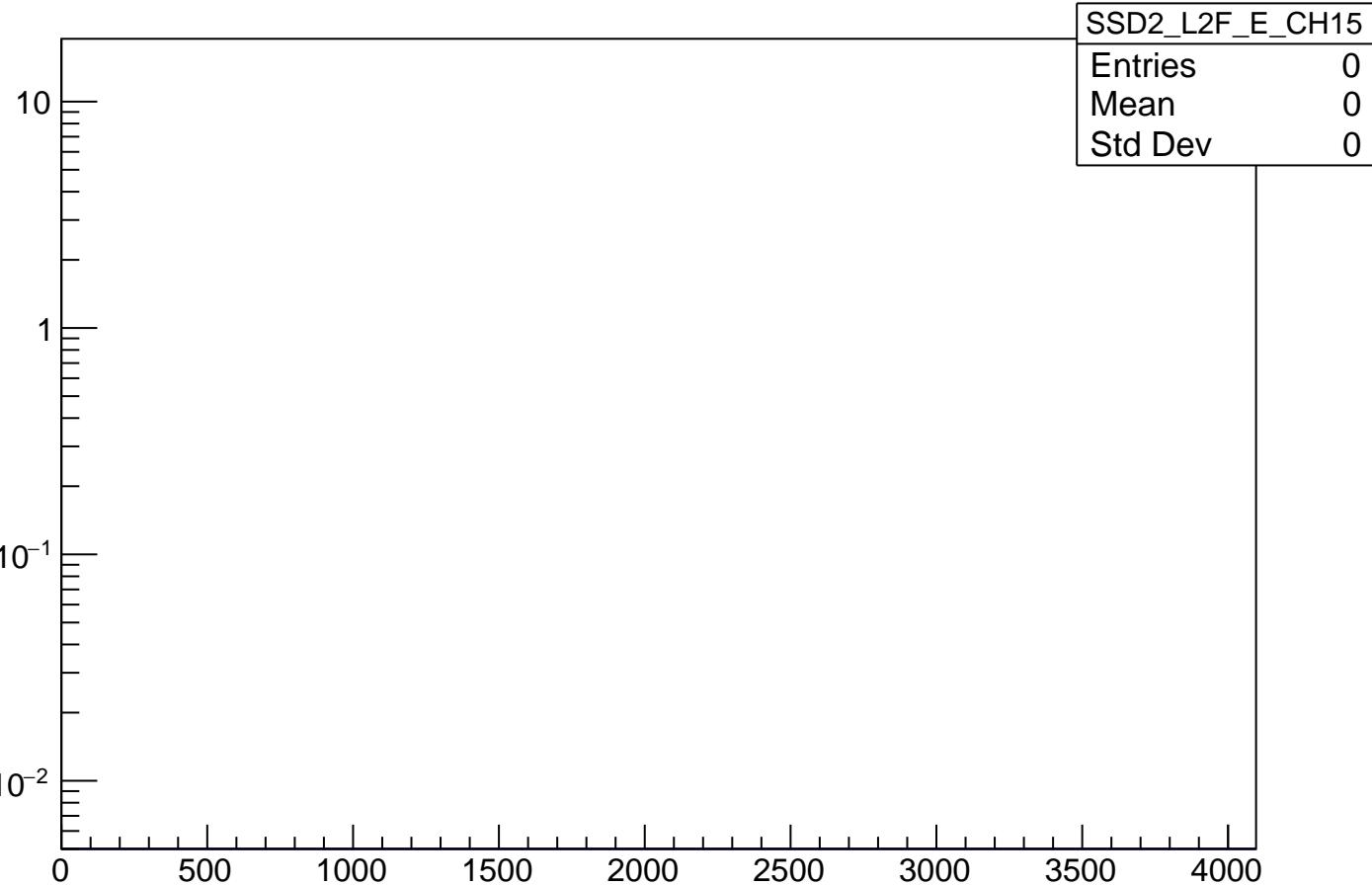
SSD2_L2F_E_CH13



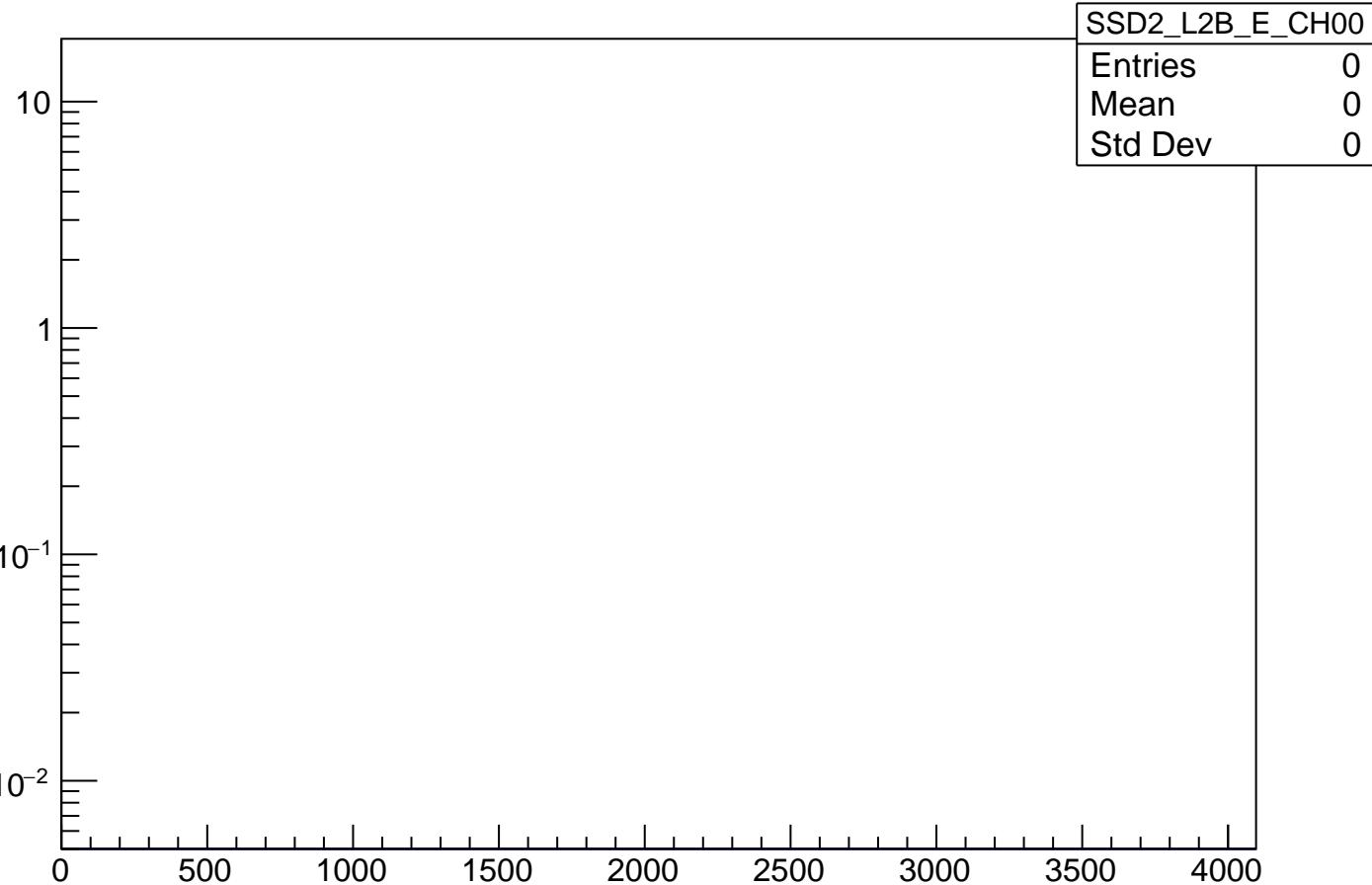
SSD2_L2F_E_CH14



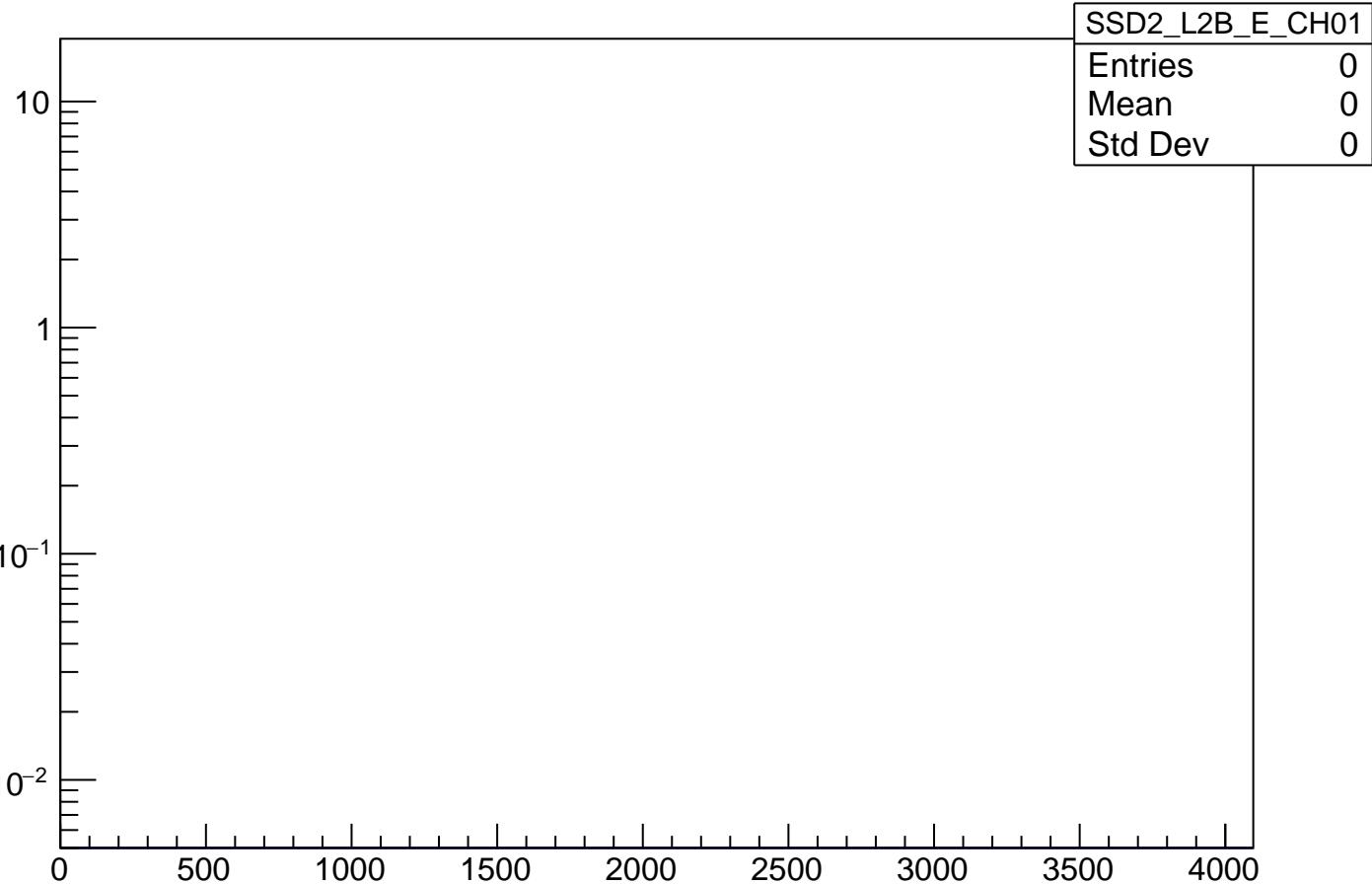
SSD2_L2F_E_CH15



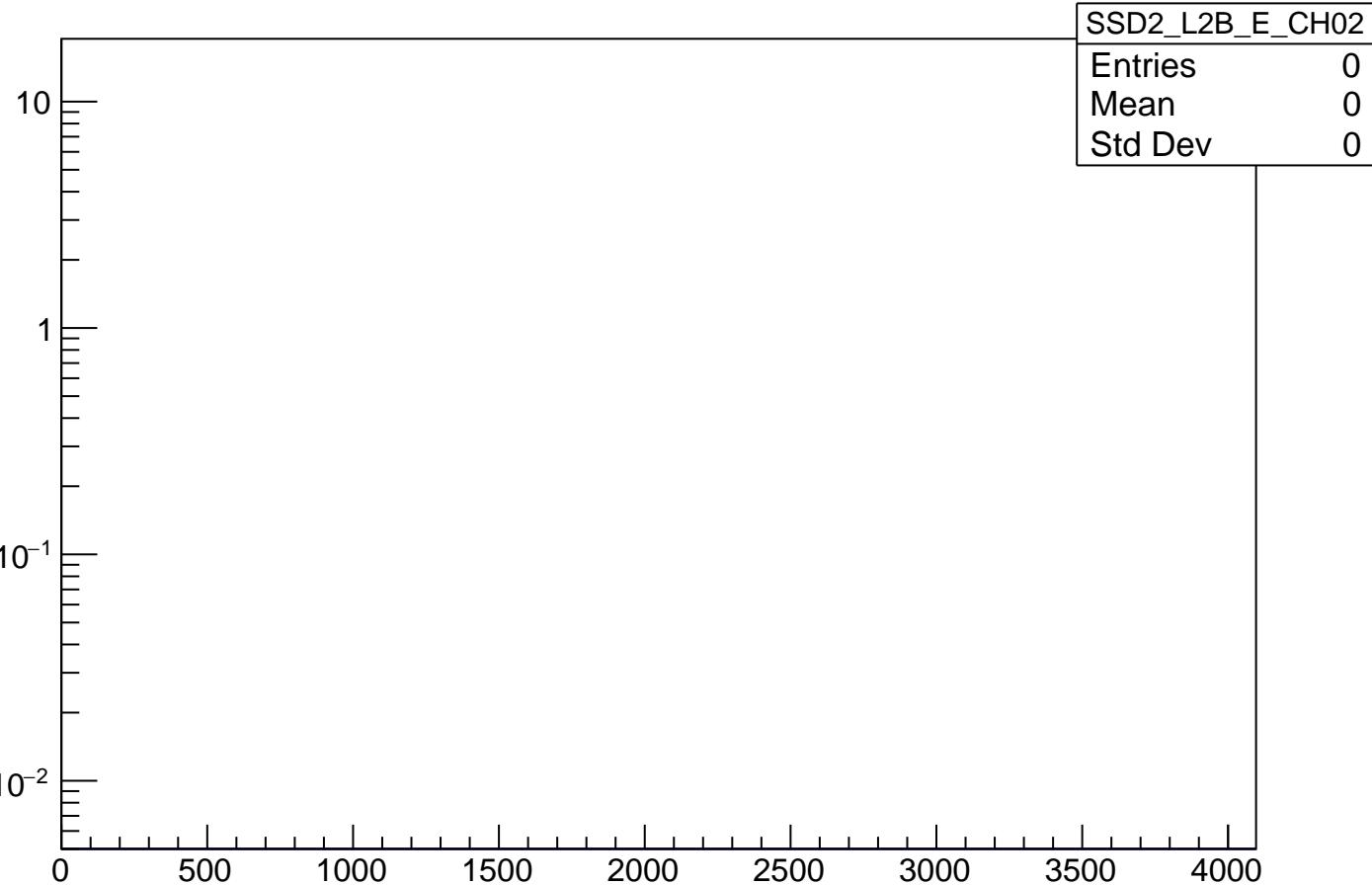
SSD2_L2B_E_CH00



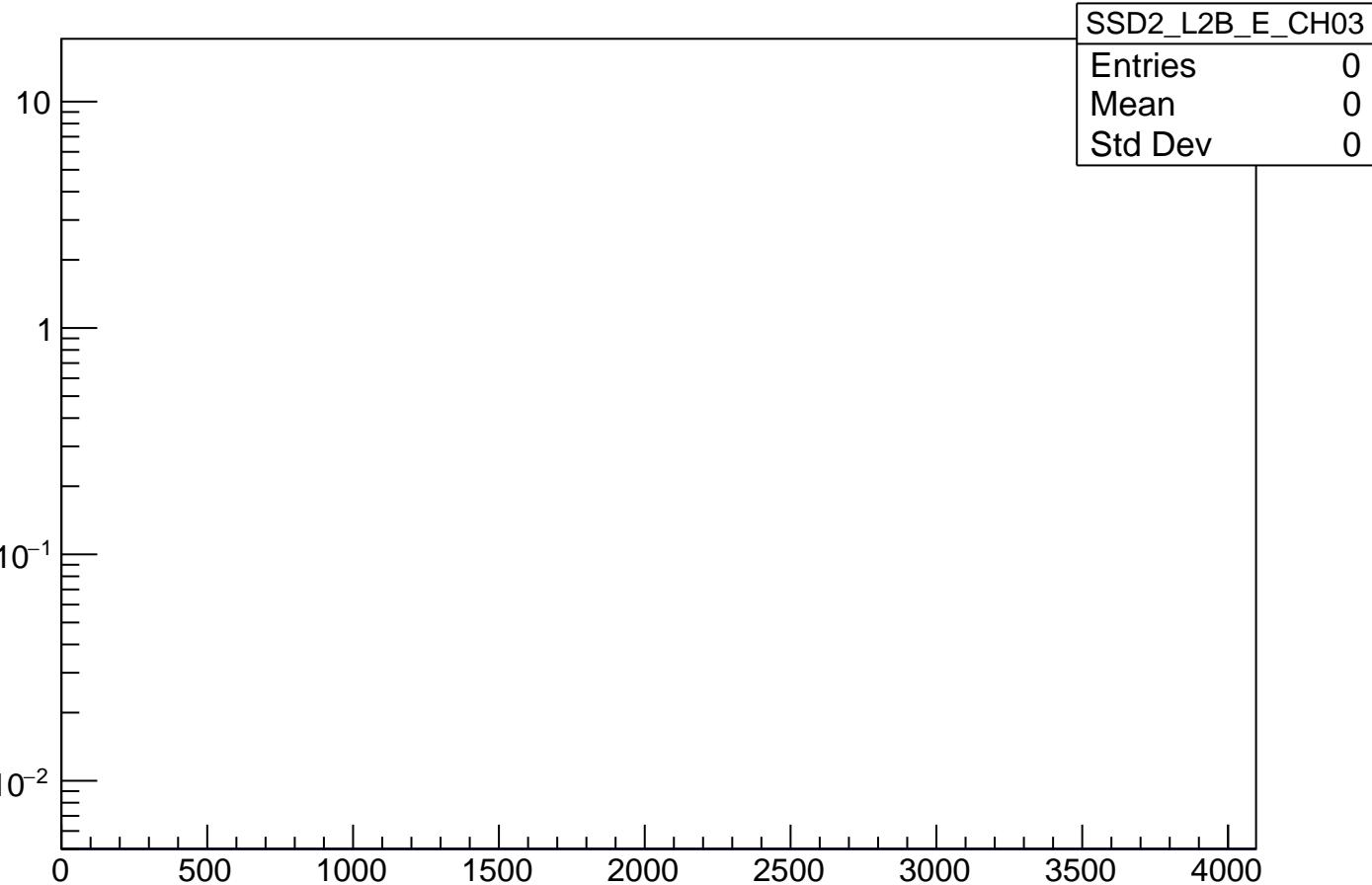
SSD2_L2B_E_CH01



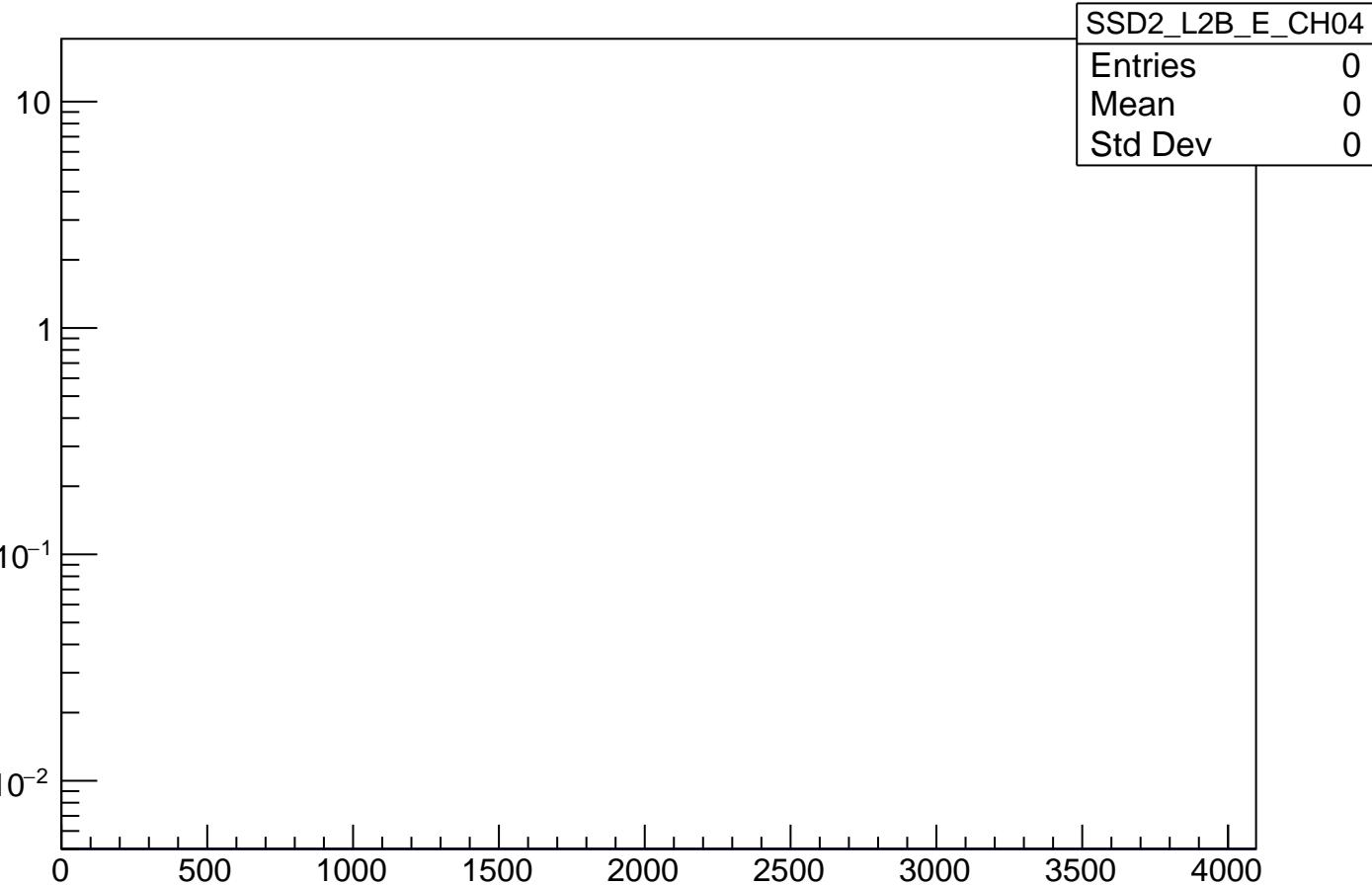
SSD2_L2B_E_CH02



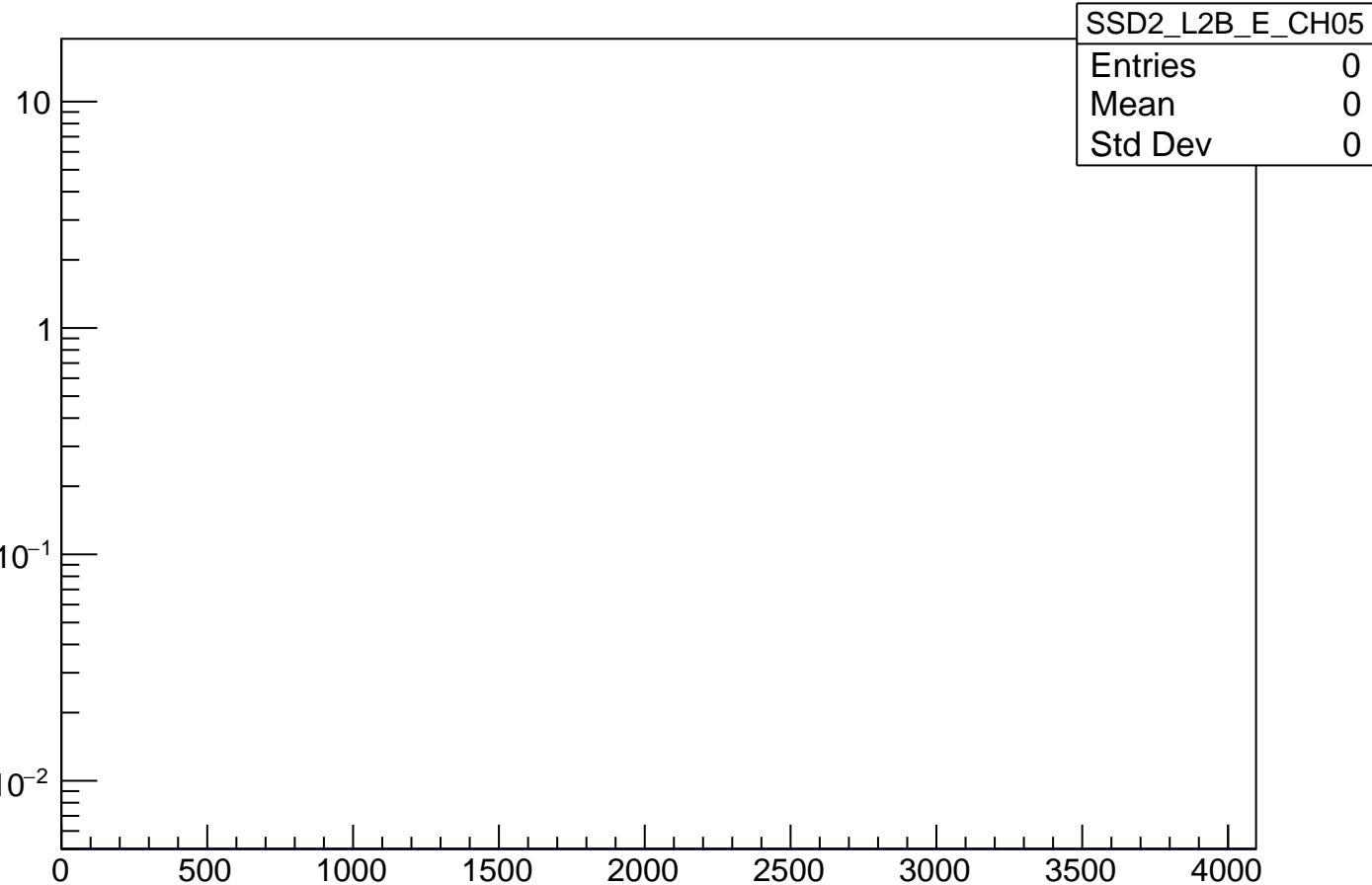
SSD2_L2B_E_CH03



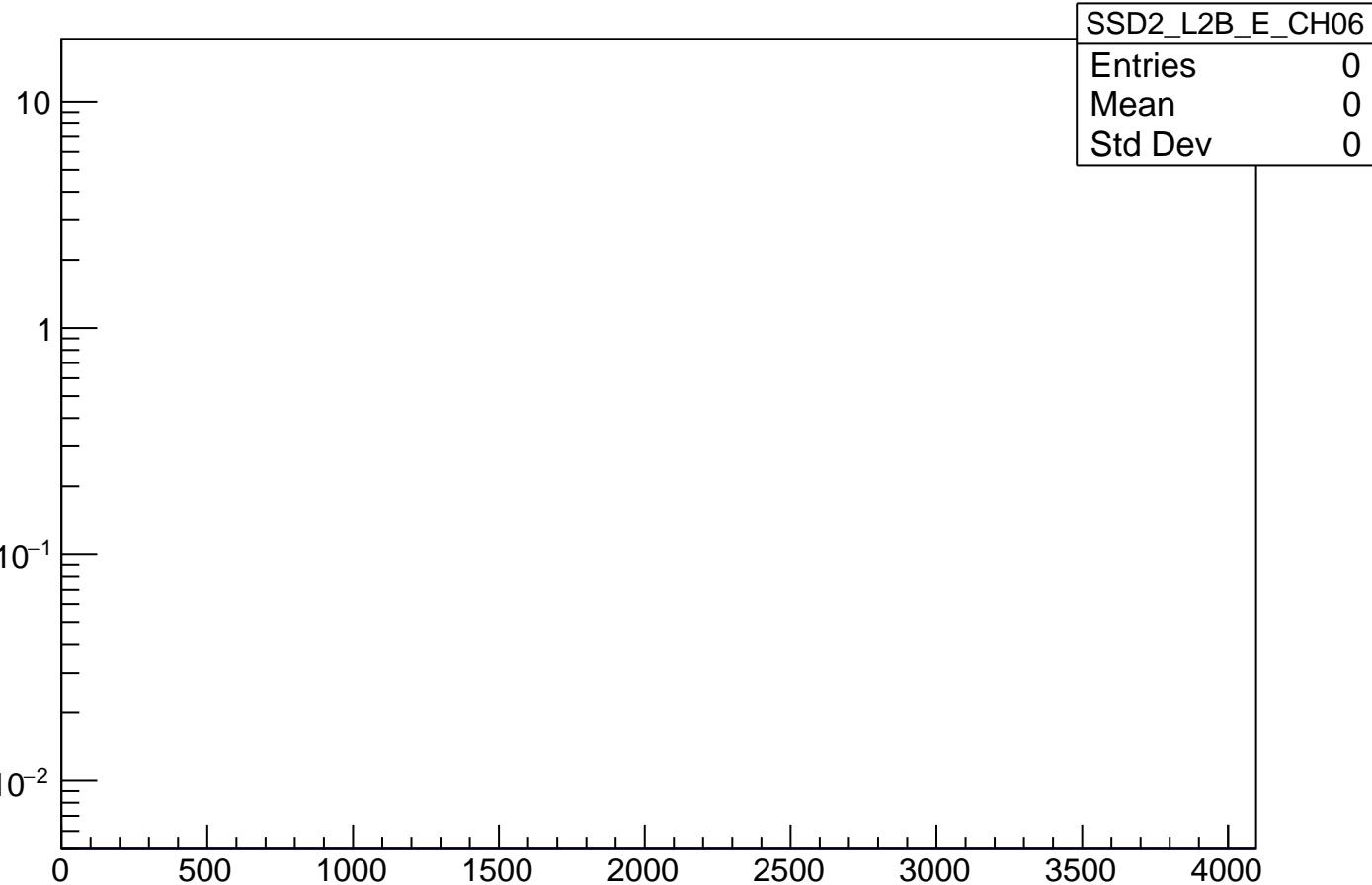
SSD2_L2B_E_CH04



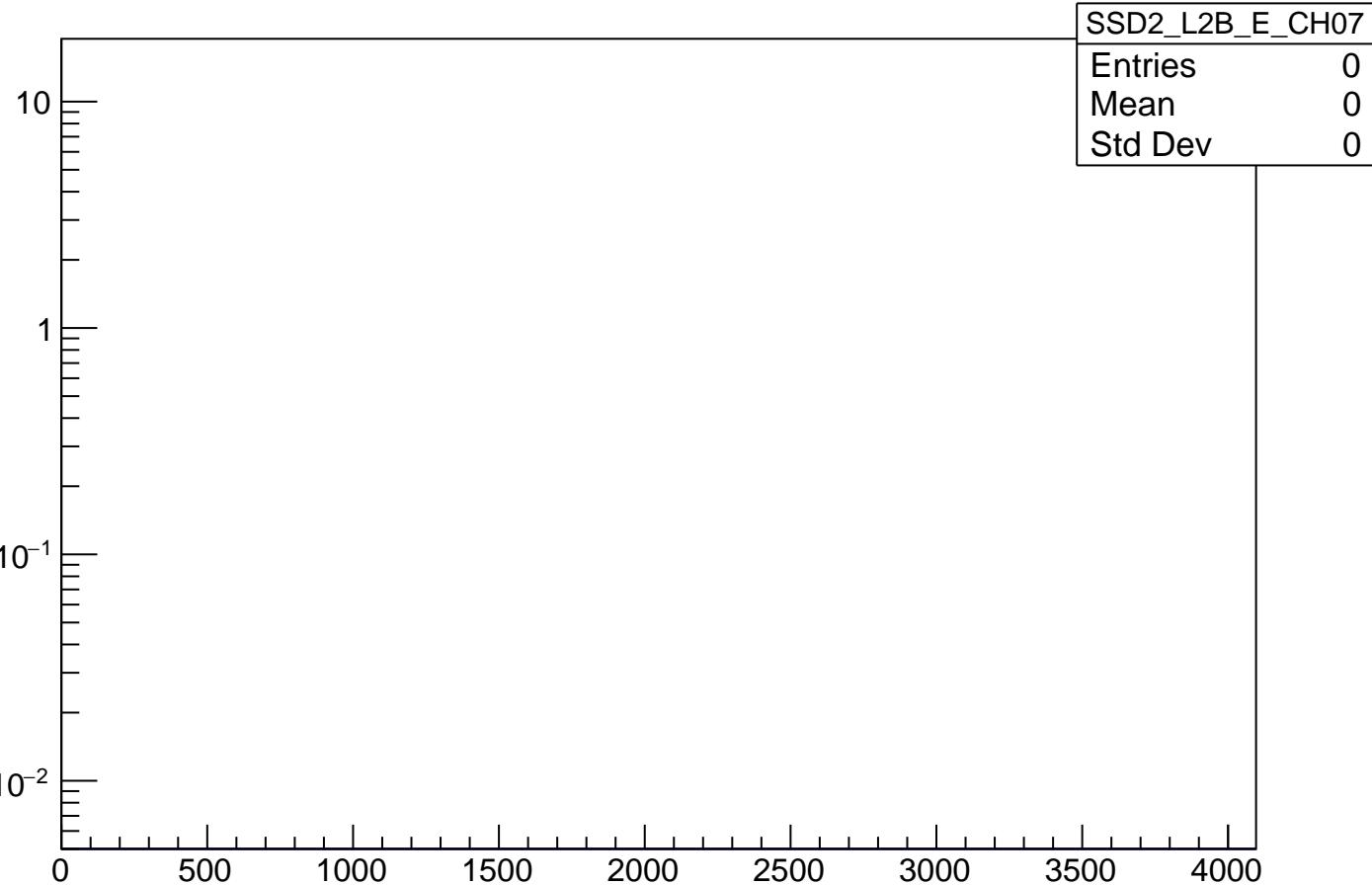
SSD2_L2B_E_CH05



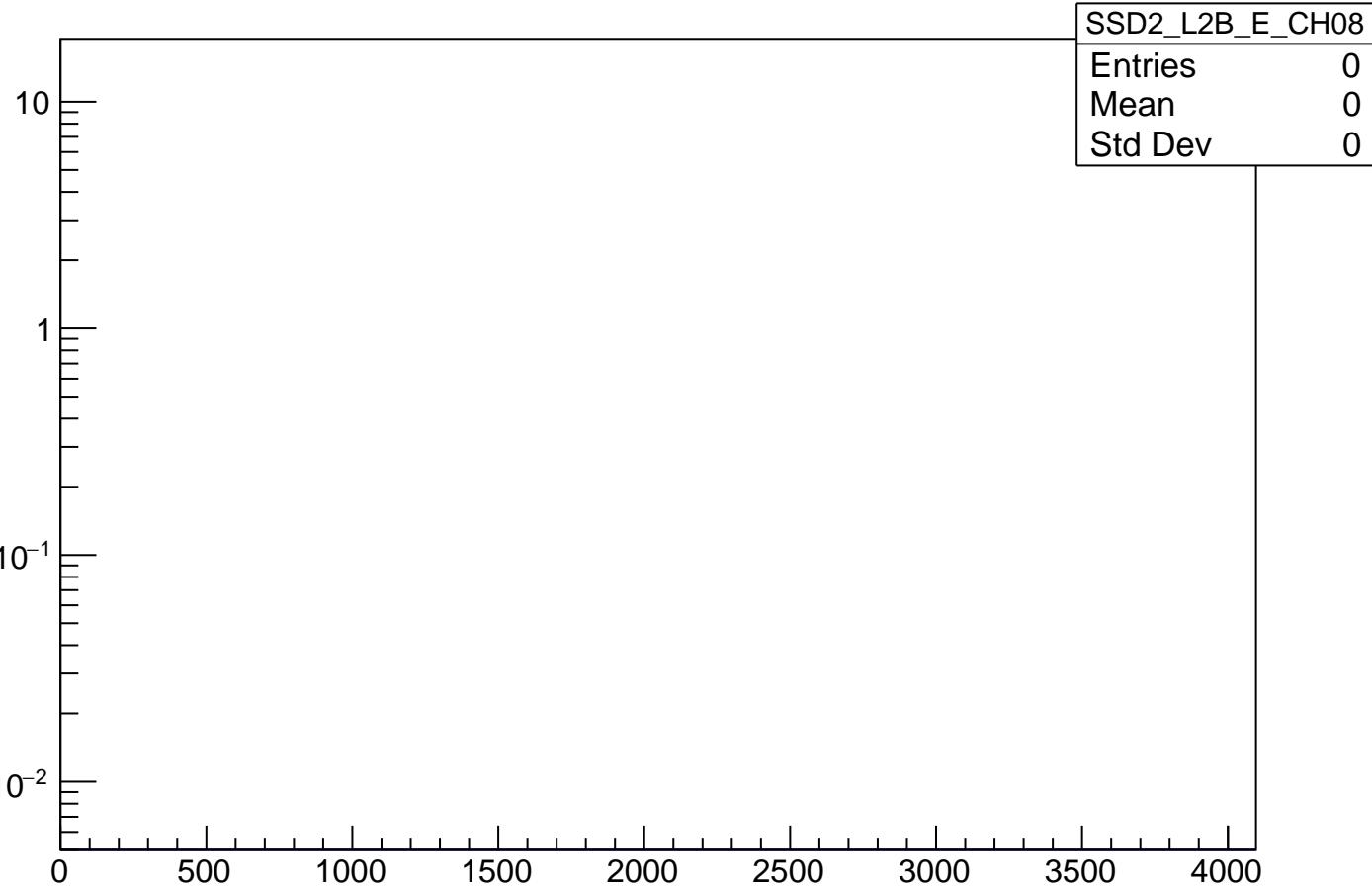
SSD2_L2B_E_CH06



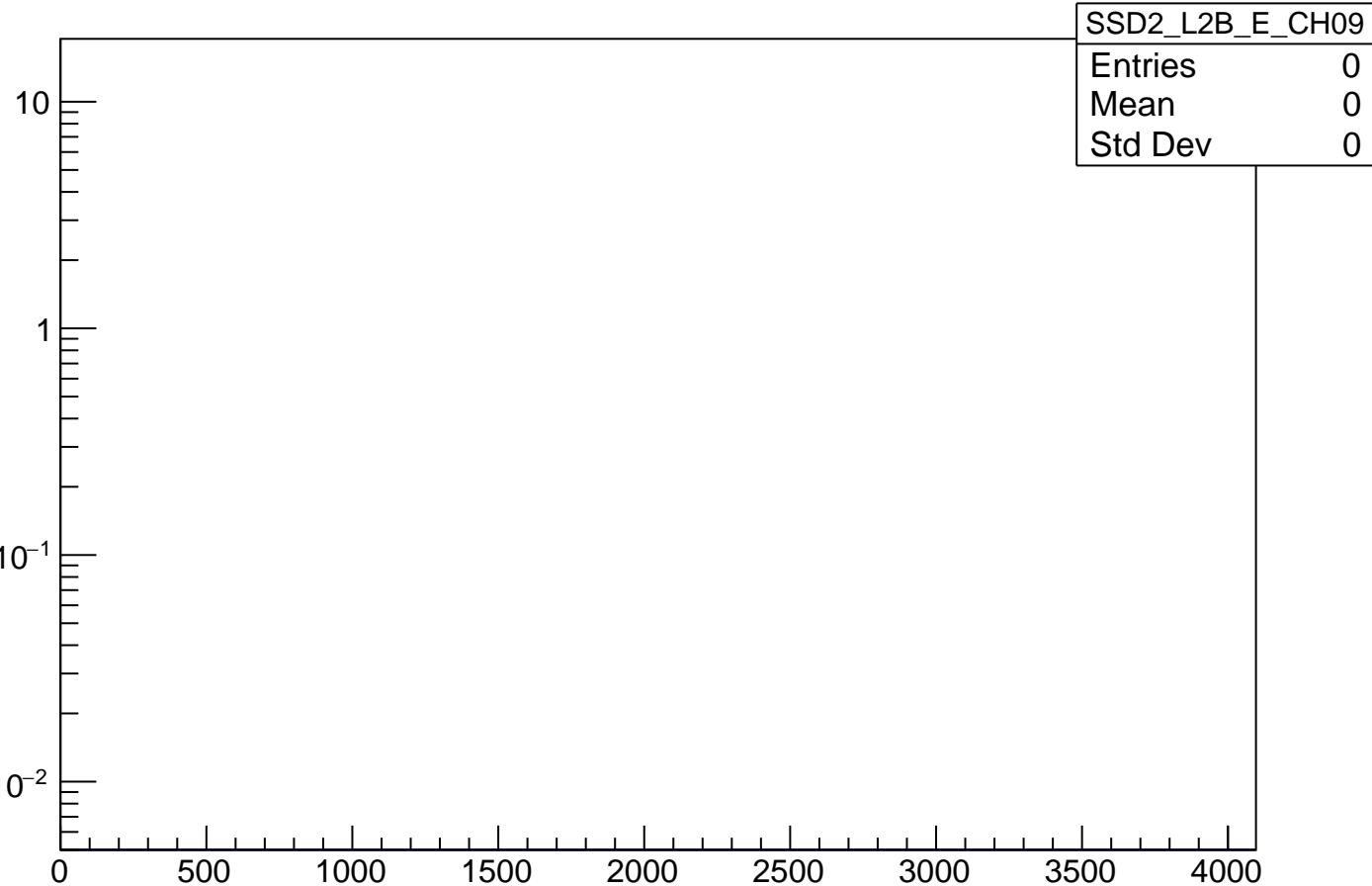
SSD2_L2B_E_CH07



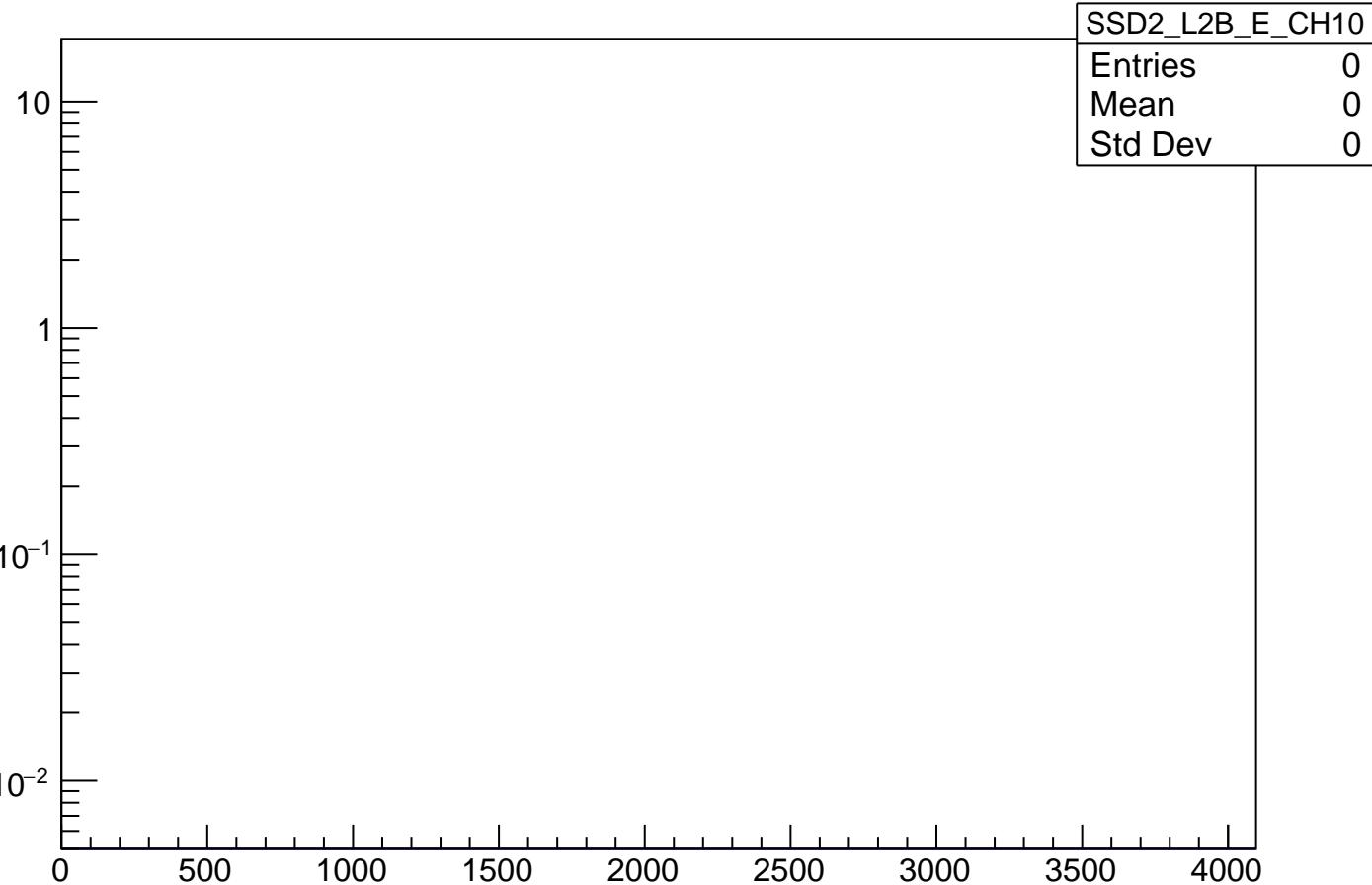
SSD2_L2B_E_CH08



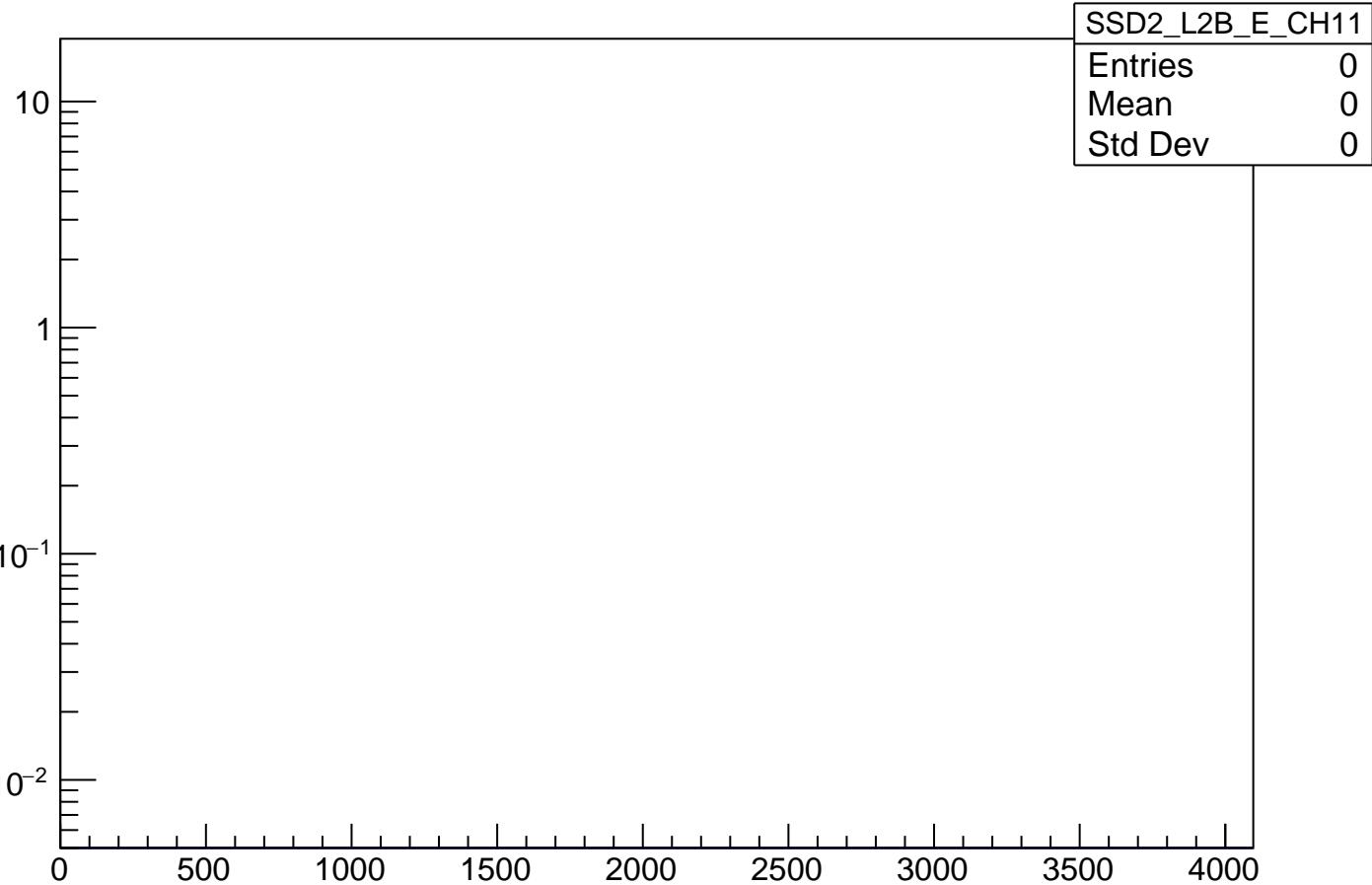
SSD2_L2B_E_CH09



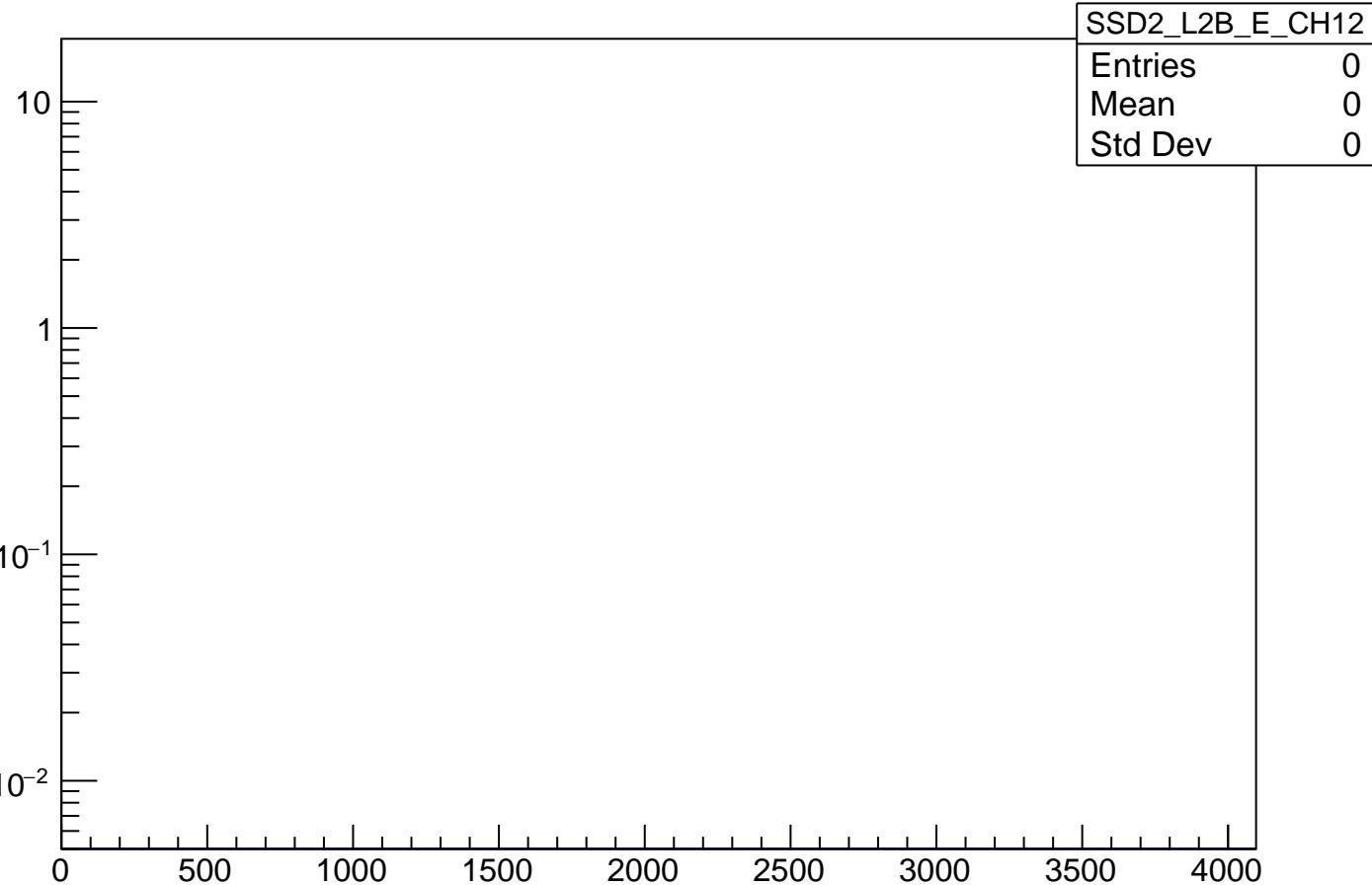
SSD2_L2B_E_CH10



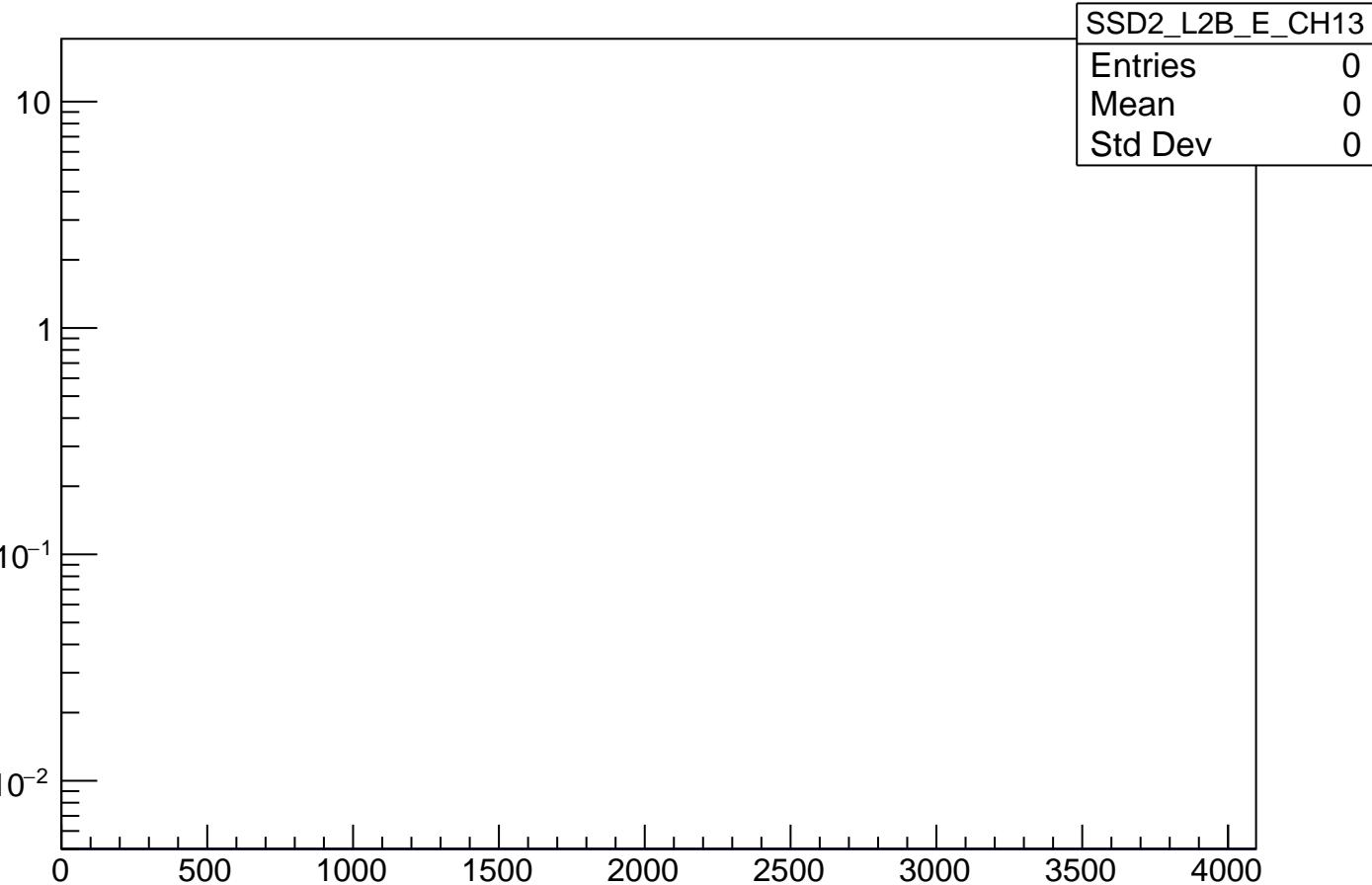
SSD2_L2B_E_CH11



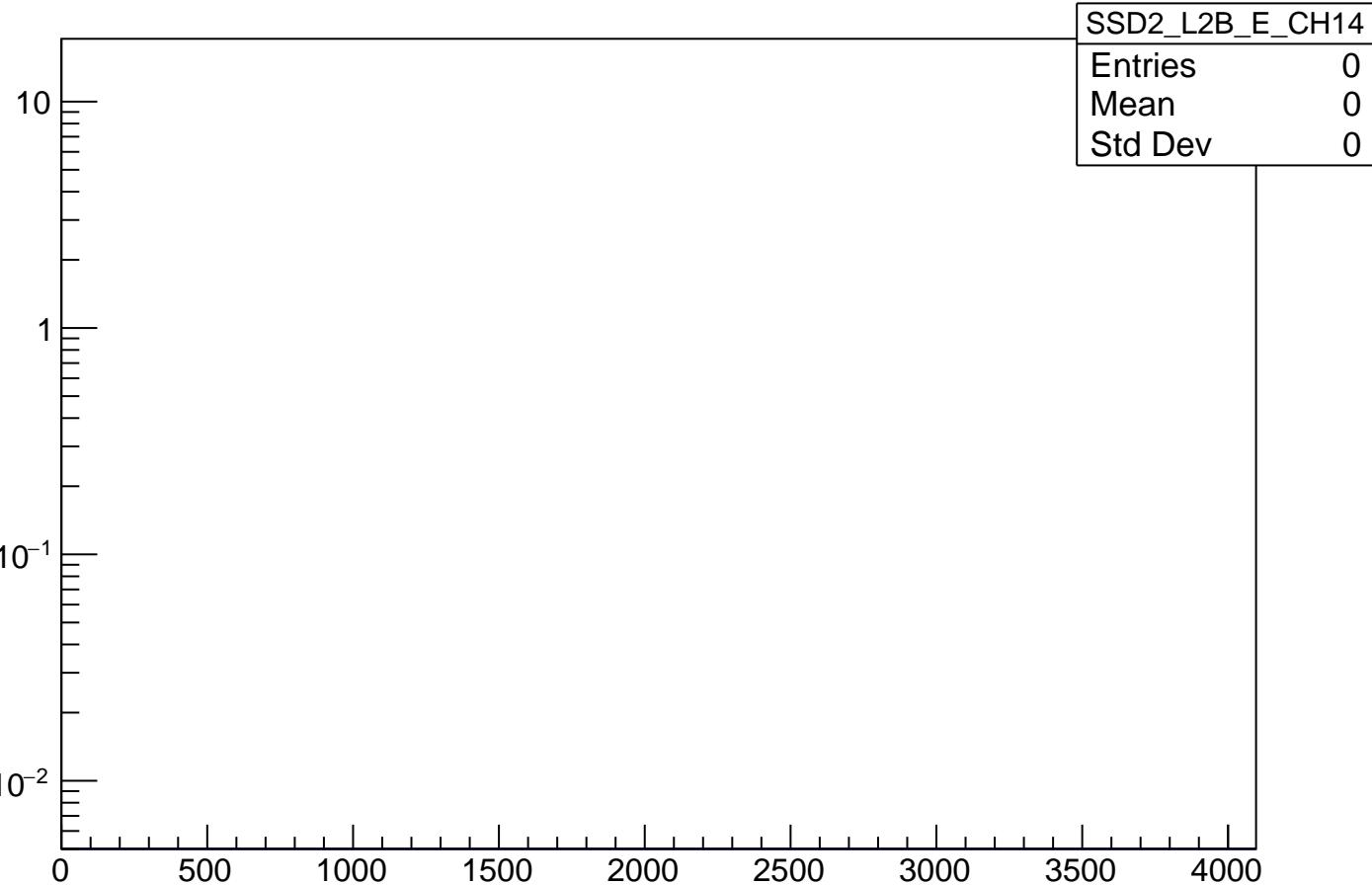
SSD2_L2B_E_CH12



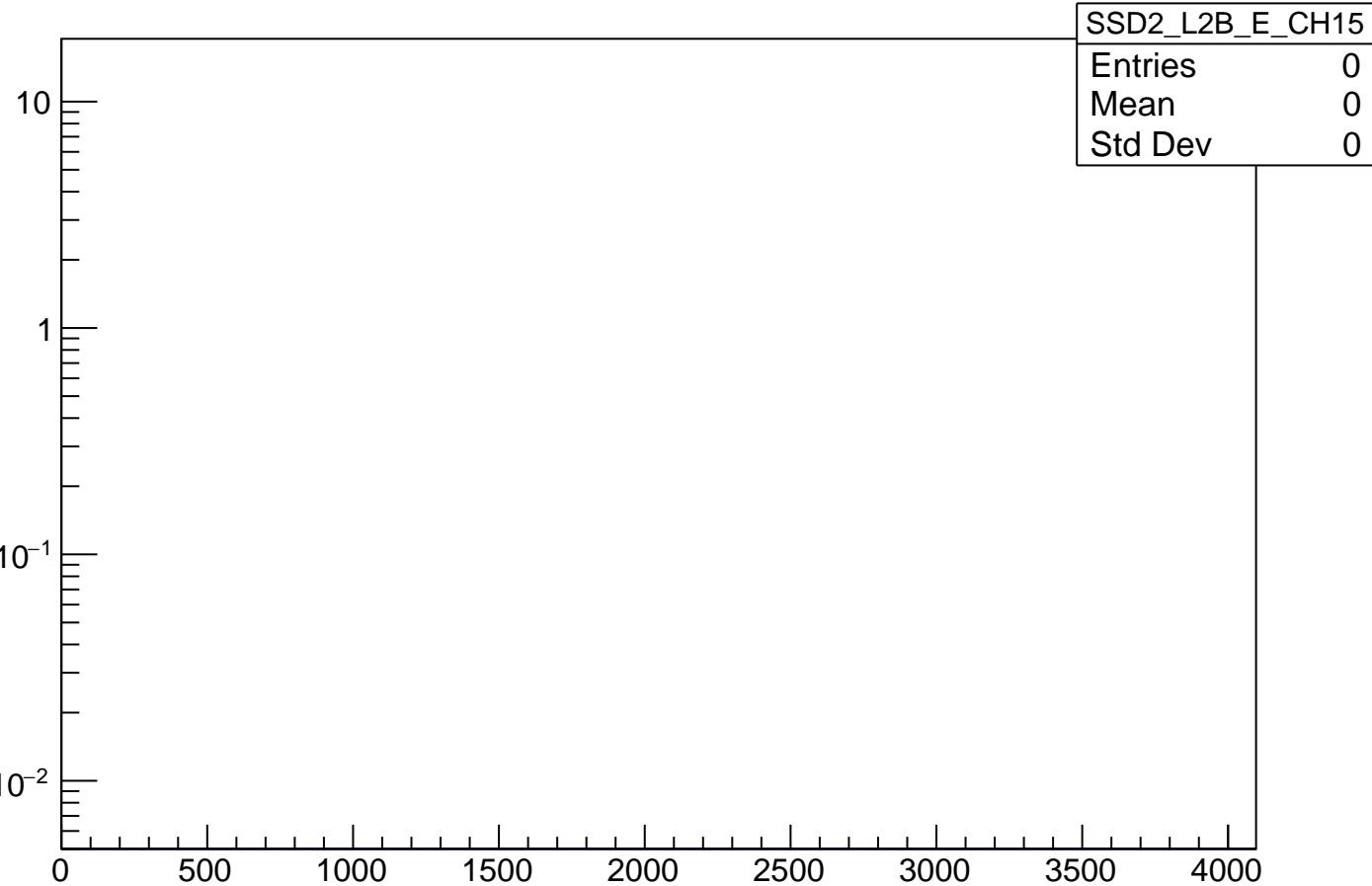
SSD2_L2B_E_CH13



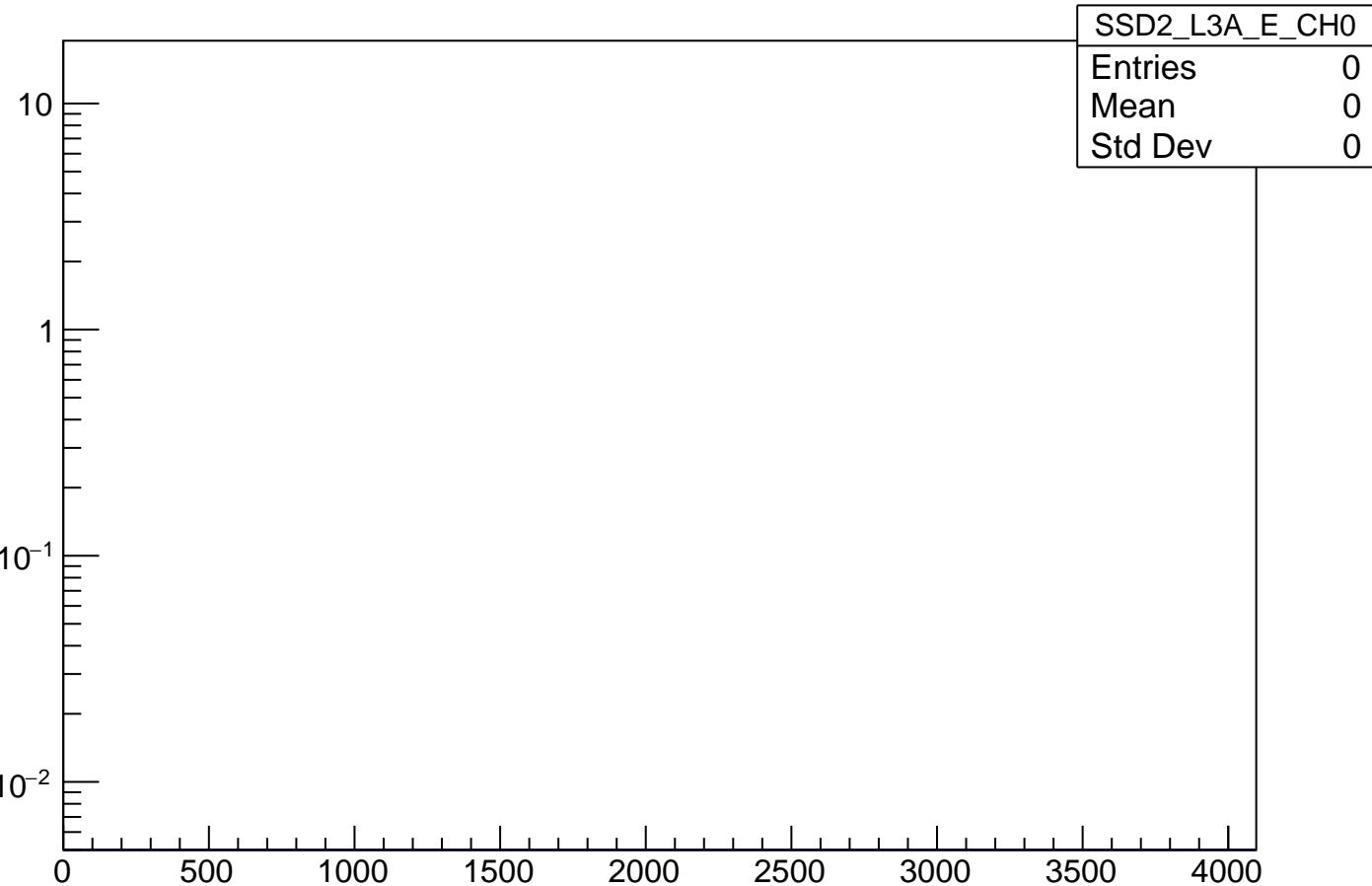
SSD2_L2B_E_CH14



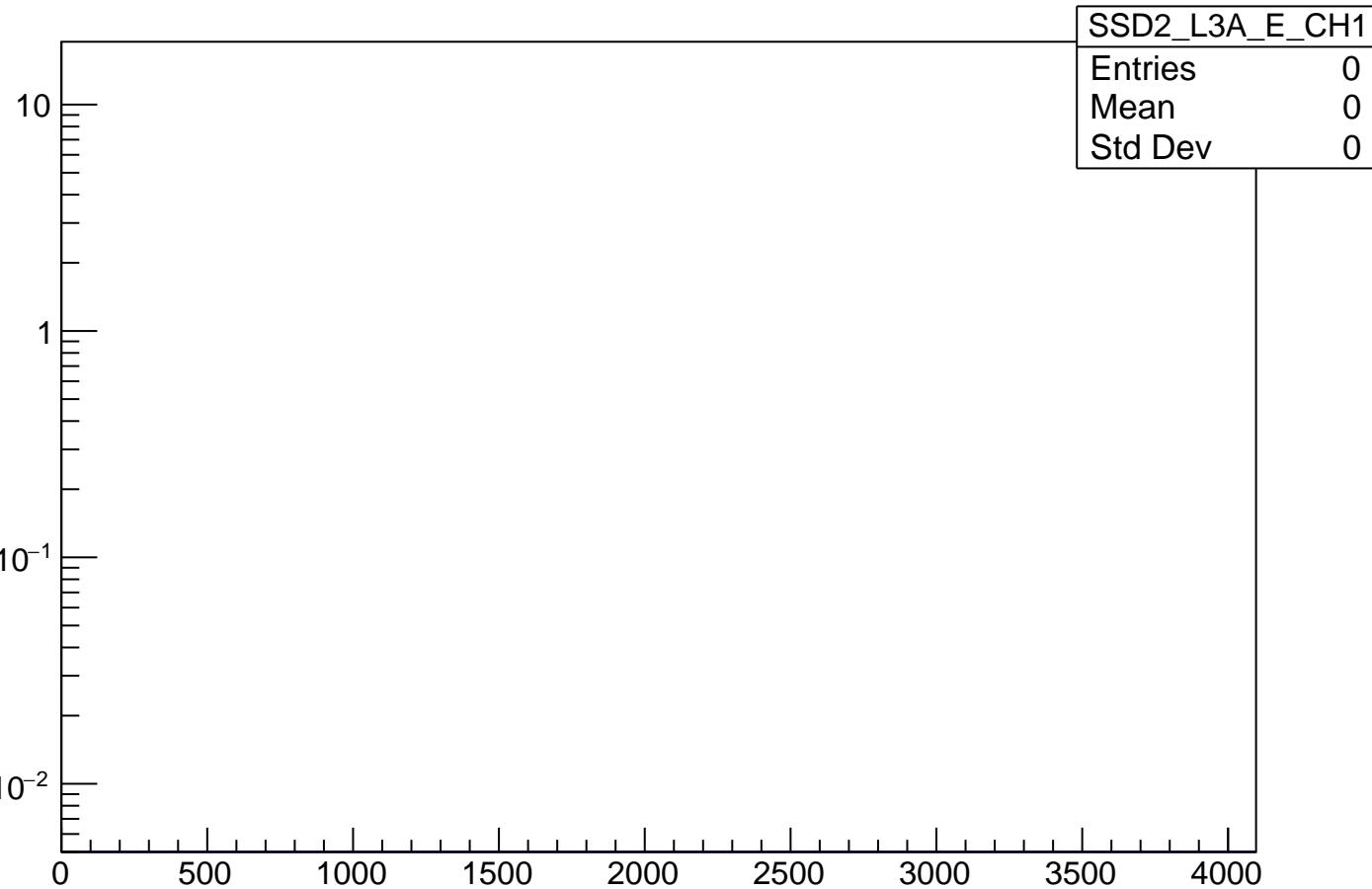
SSD2_L2B_E_CH15



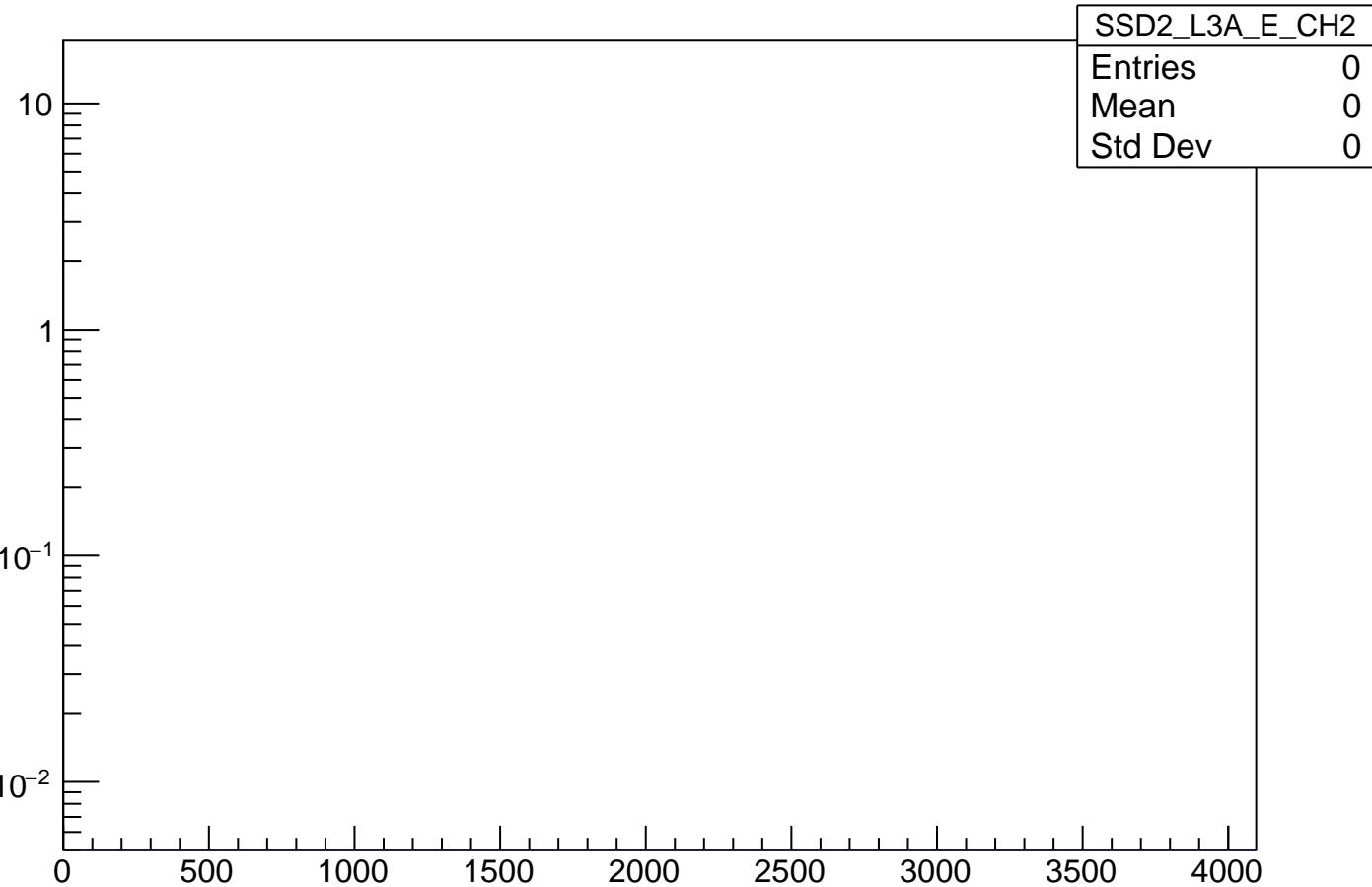
SSD2_L3A_E_CH0



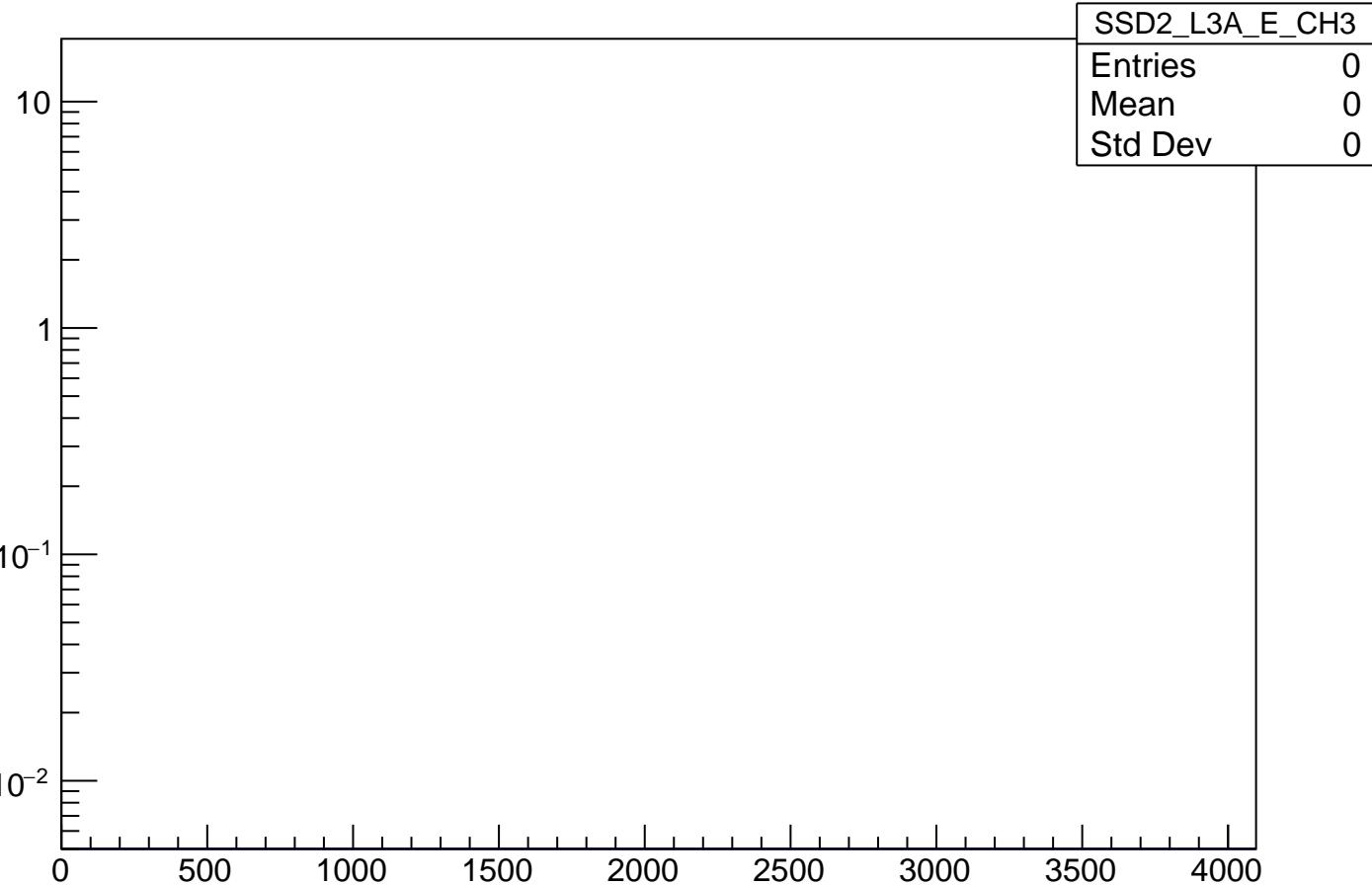
SSD2_L3A_E_CH1



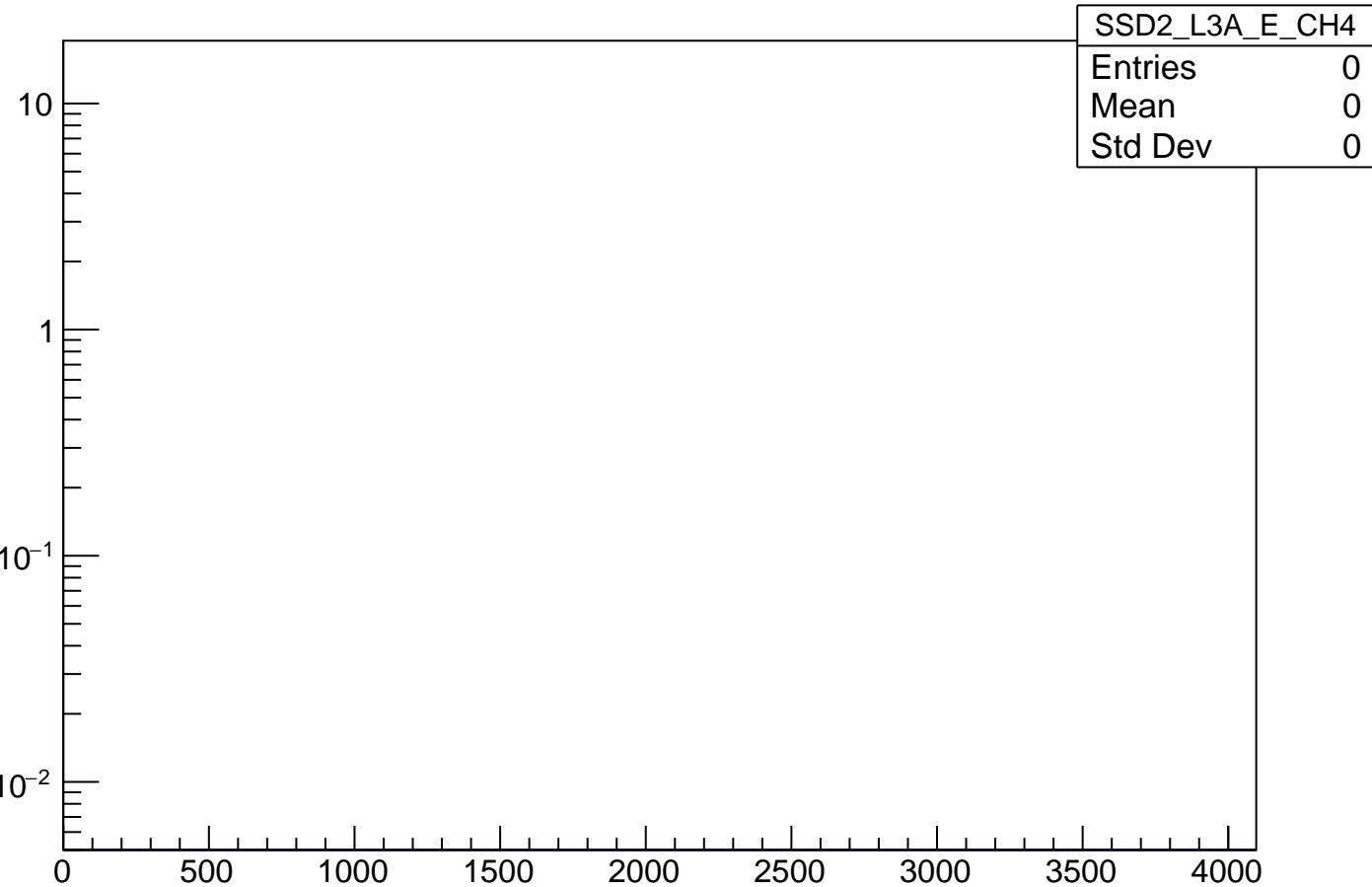
SSD2_L3A_E_CH2



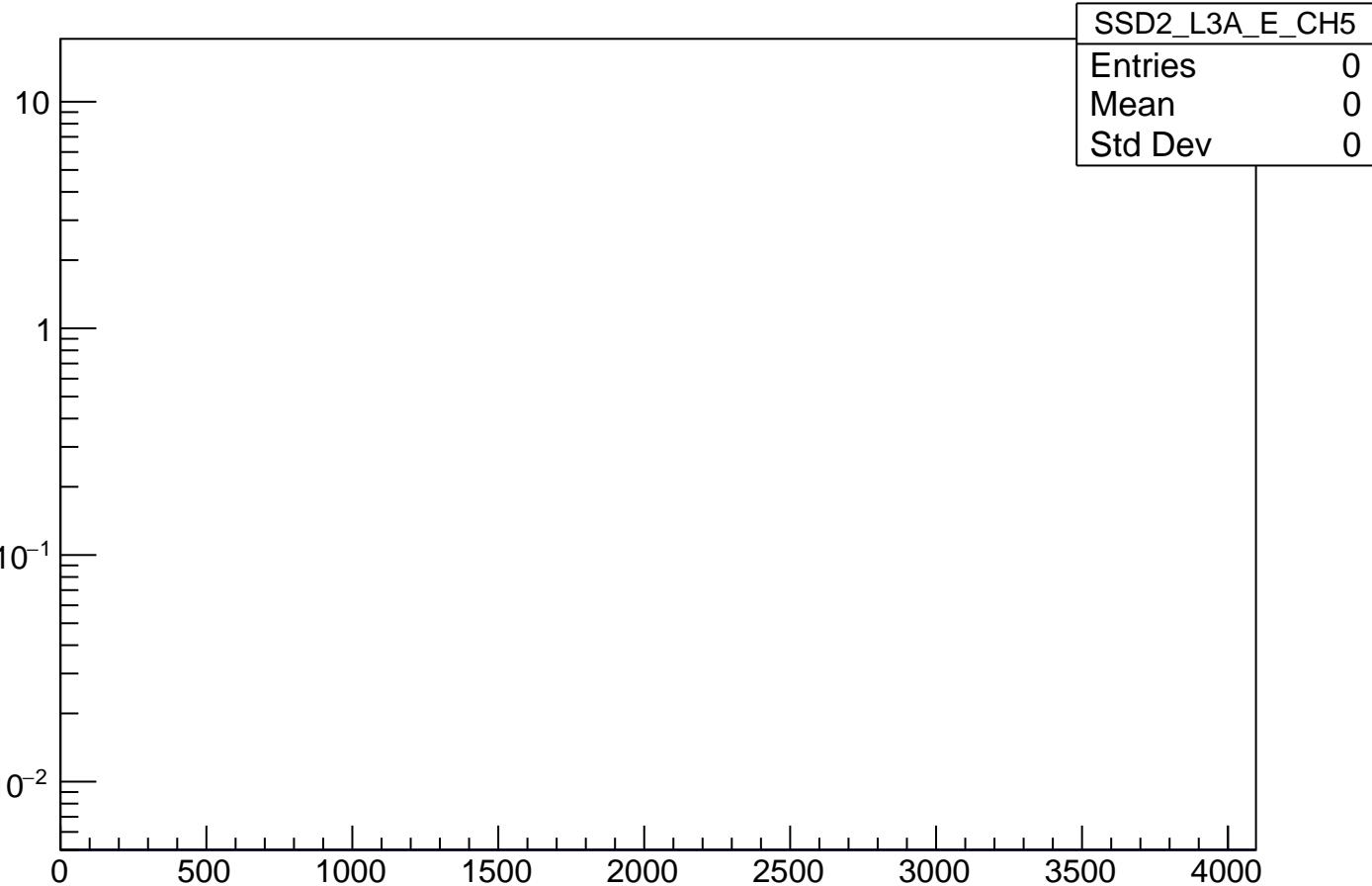
SSD2_L3A_E_CH3



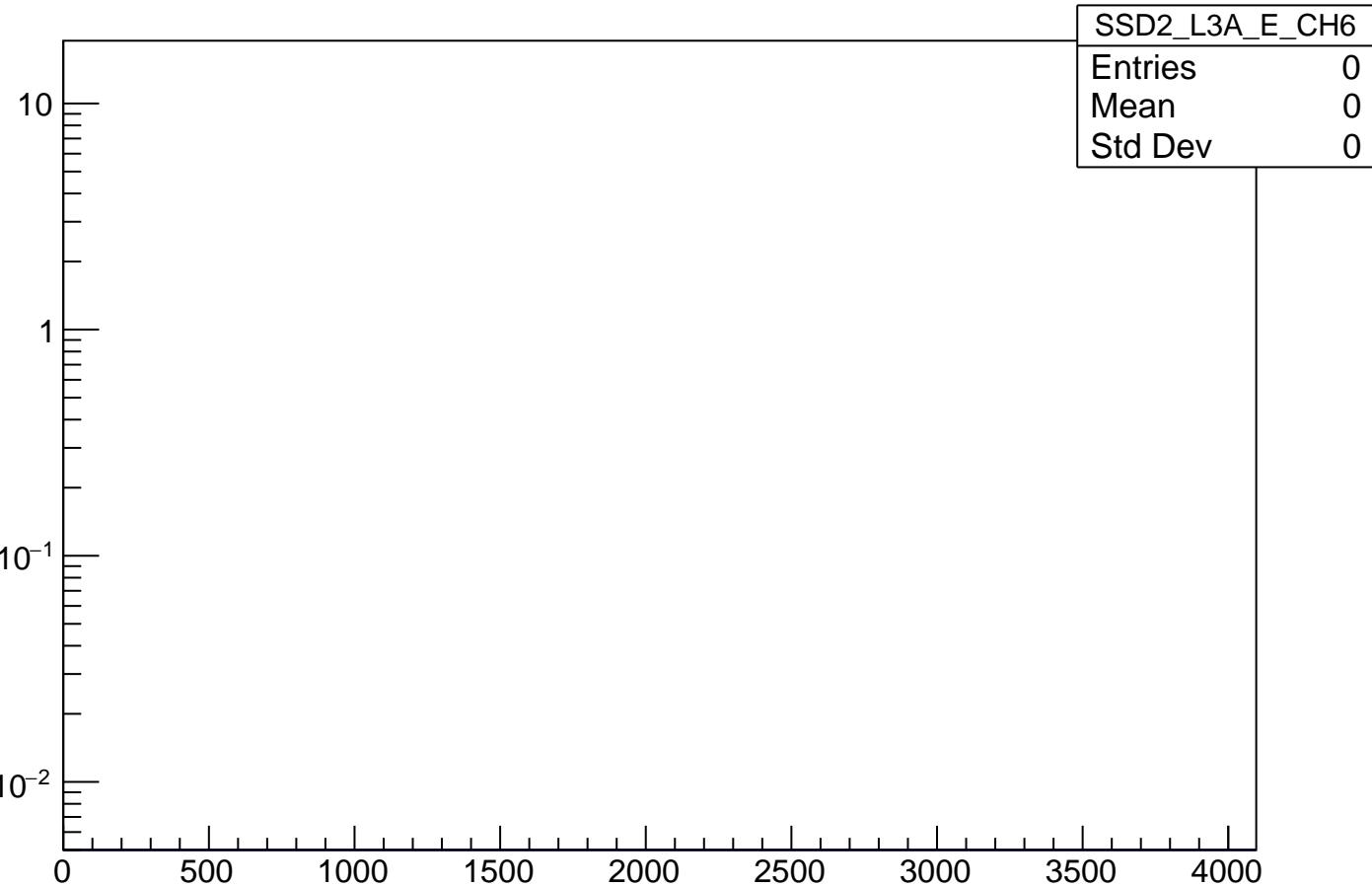
SSD2_L3A_E_CH4



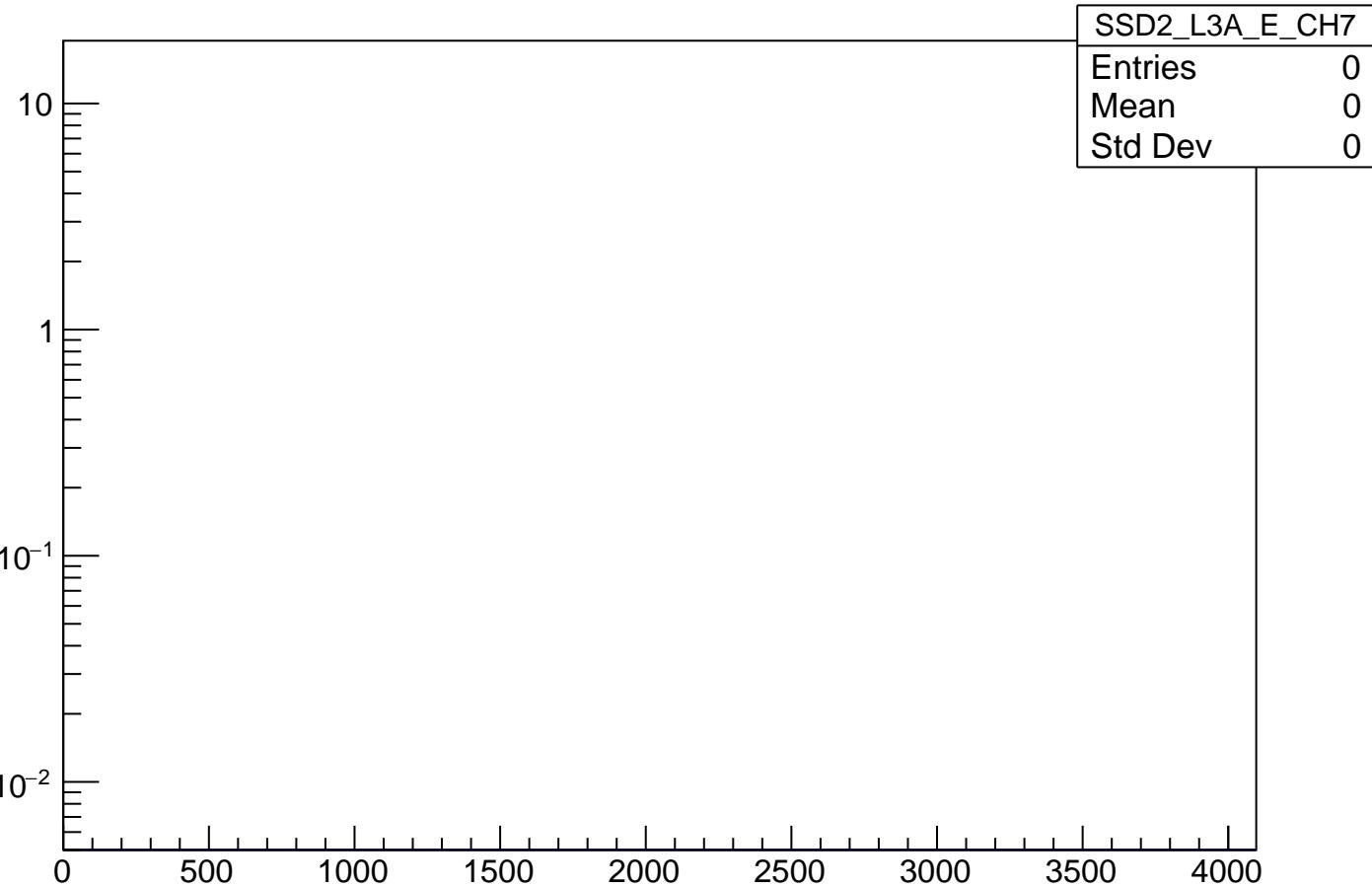
SSD2_L3A_E_CH5



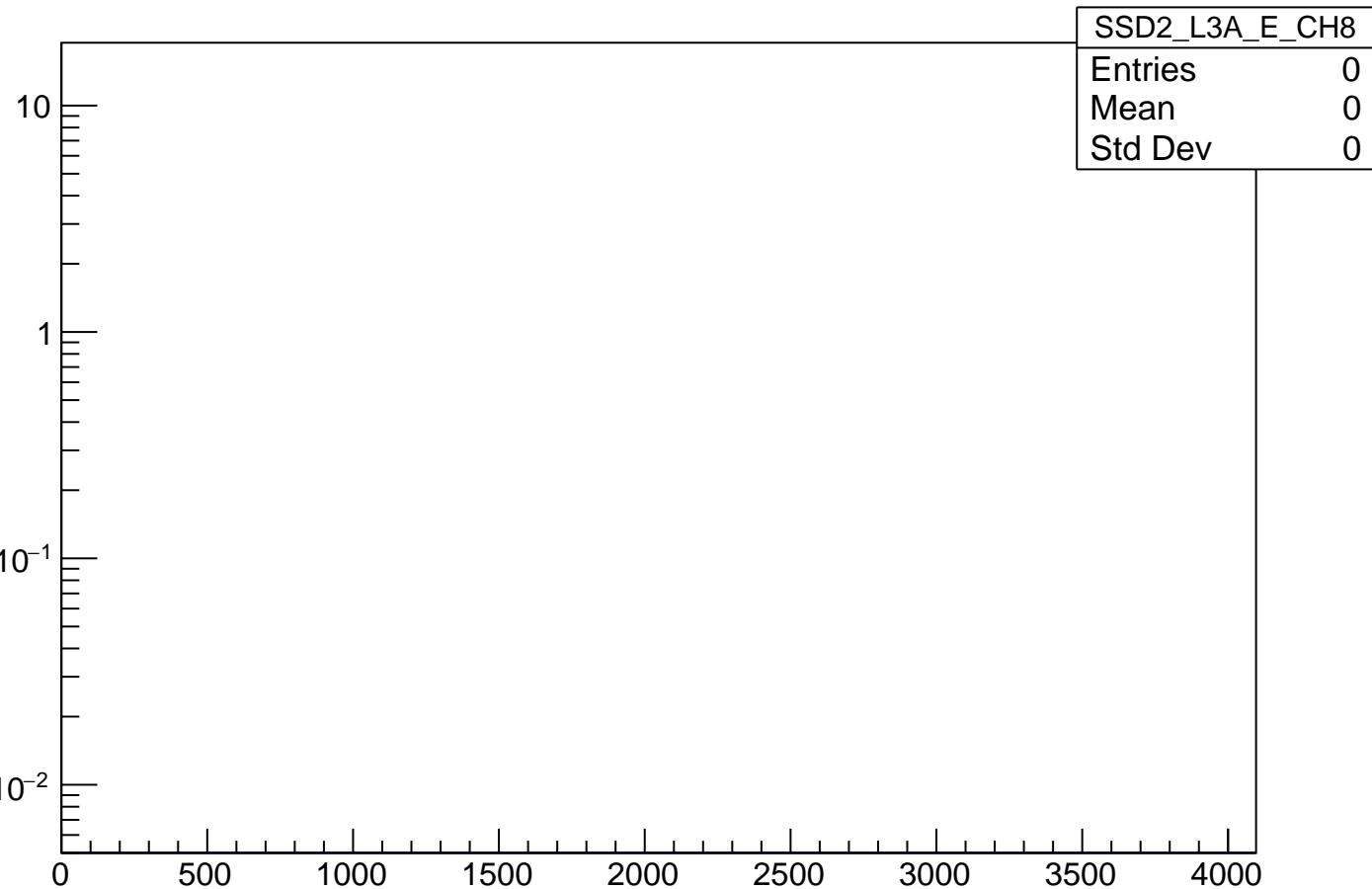
SSD2_L3A_E_CH6



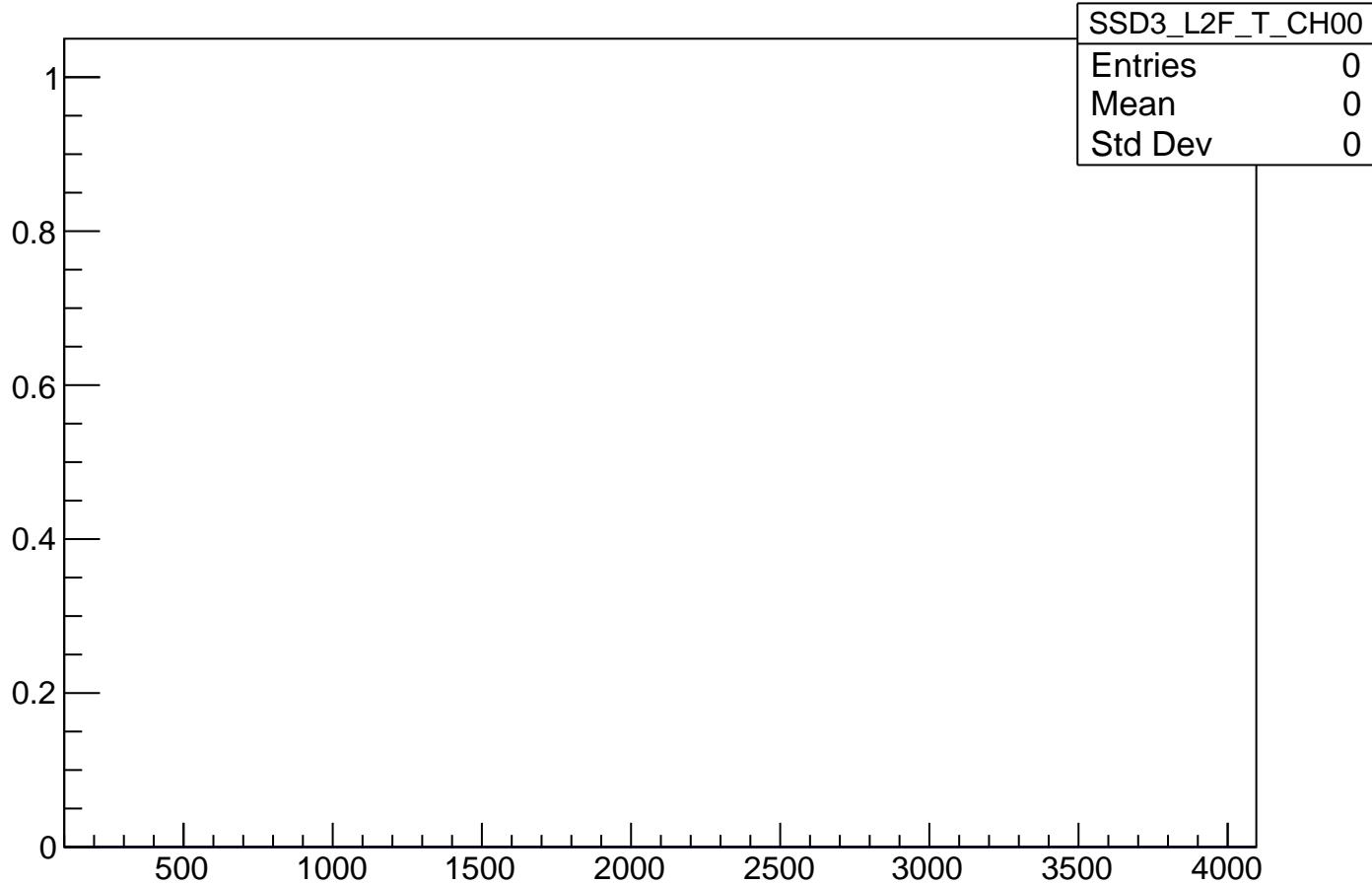
SSD2_L3A_E_CH7



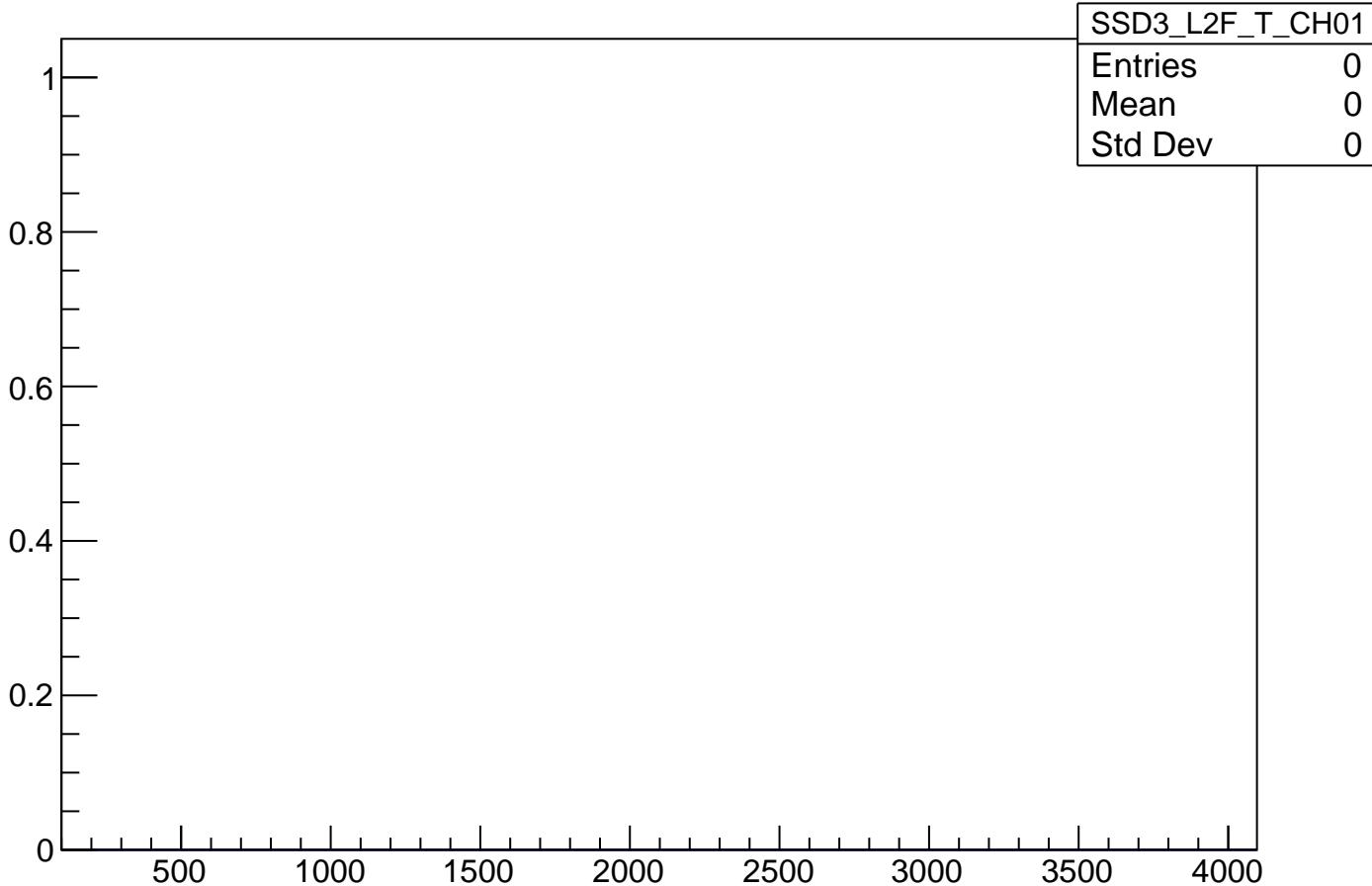
SSD2_L3A_E_CH8



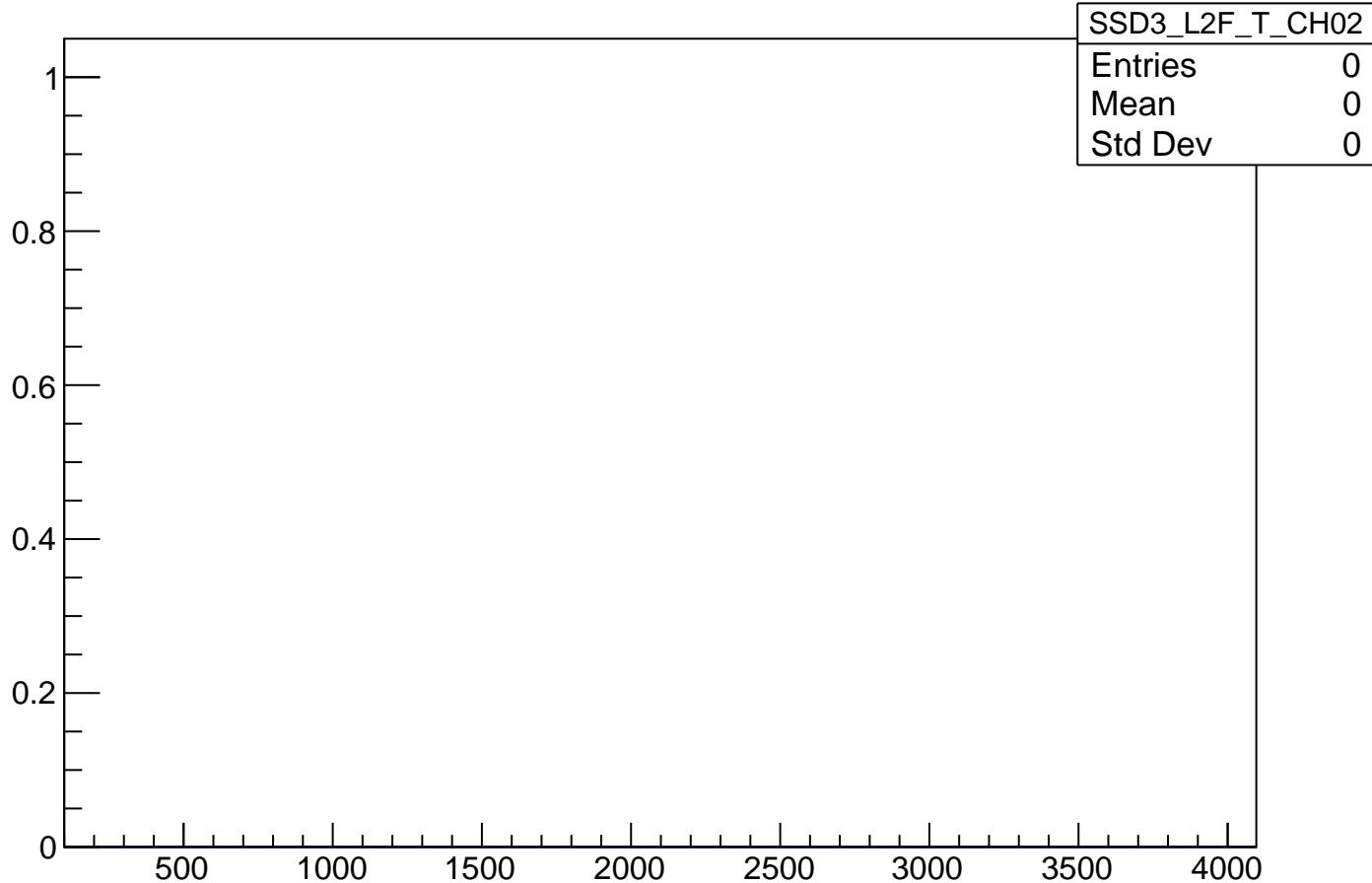
SSD3_L2F_T_CH00



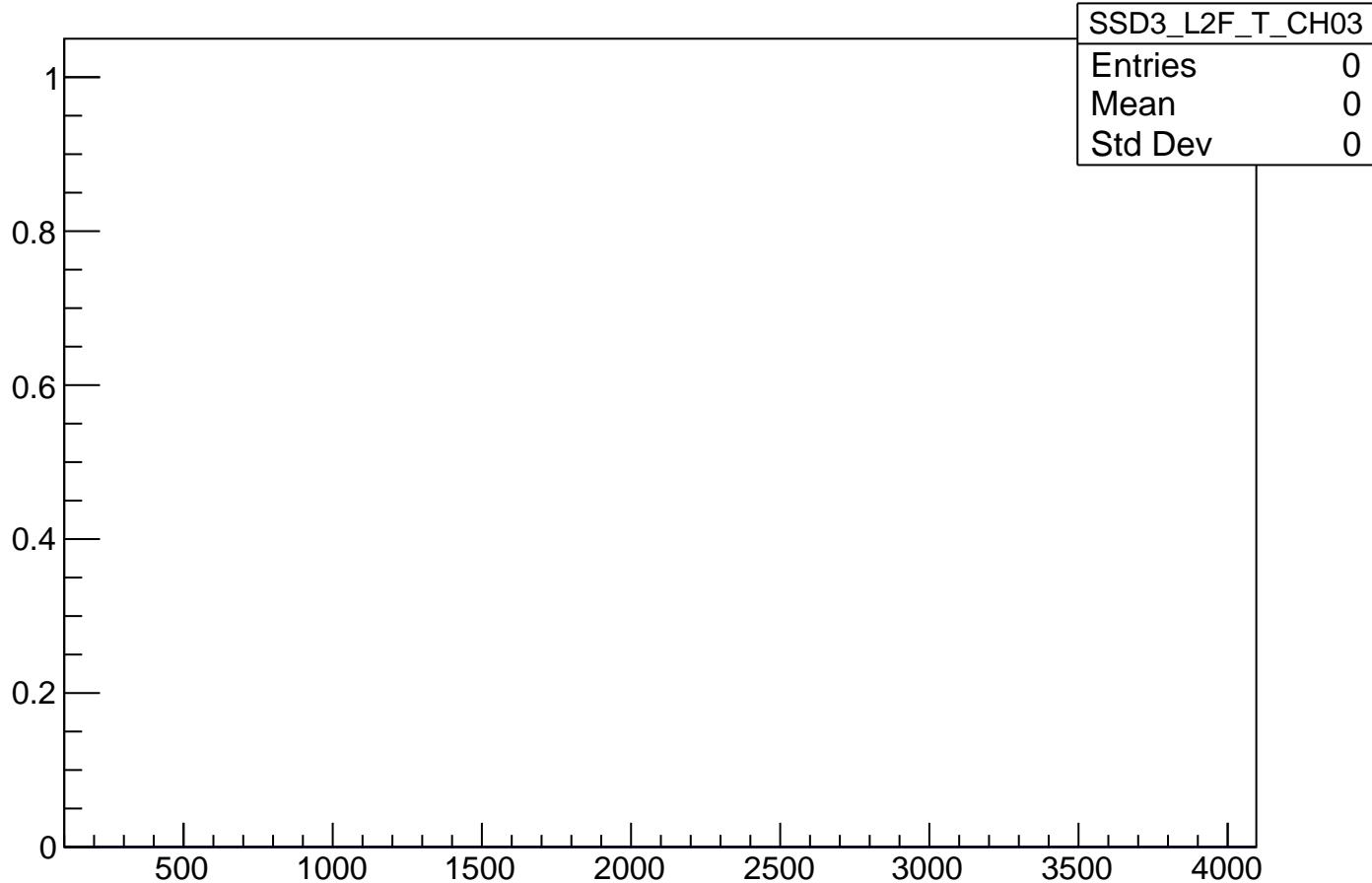
SSD3_L2F_T_CH01



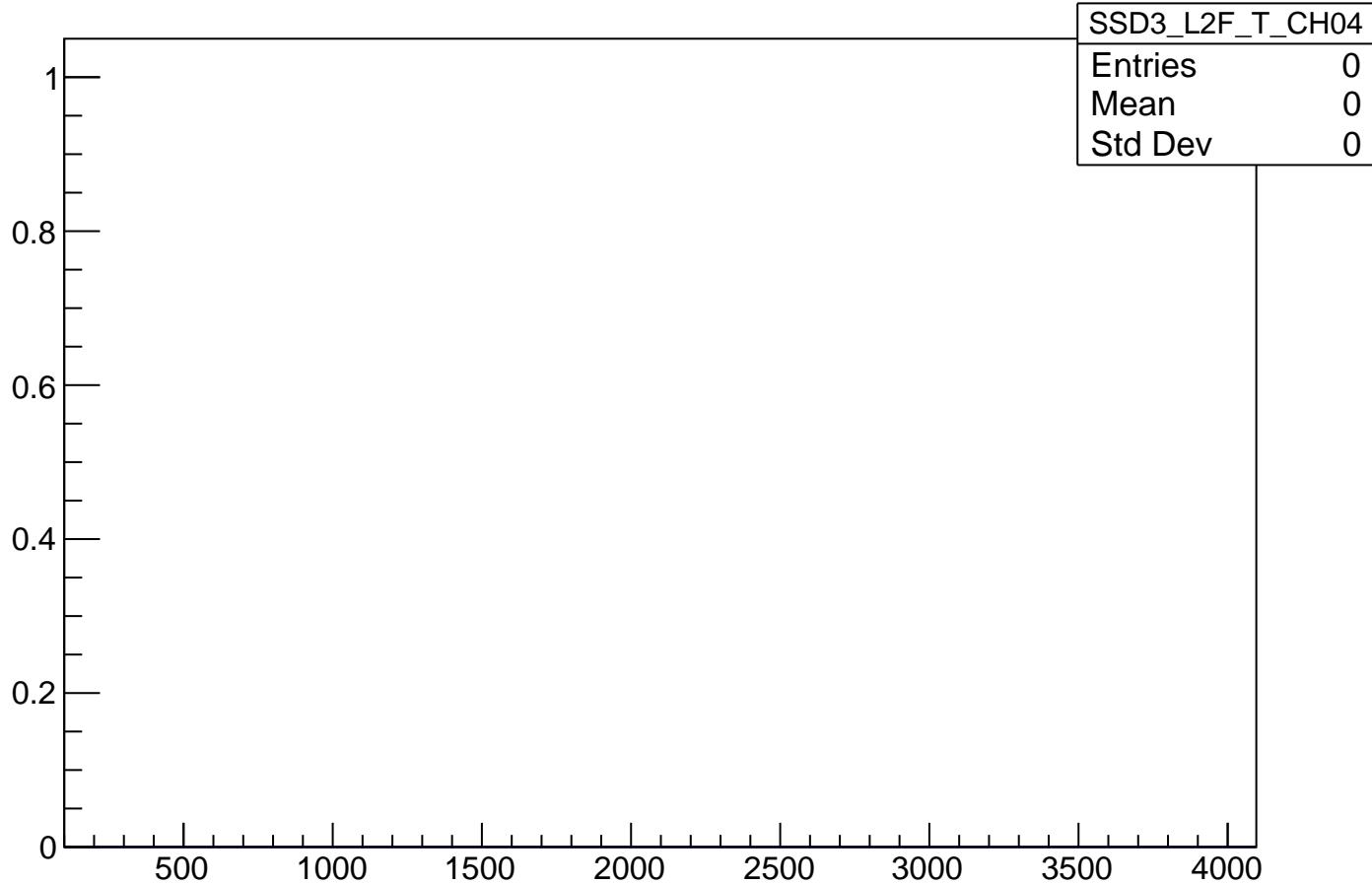
SSD3_L2F_T_CH02



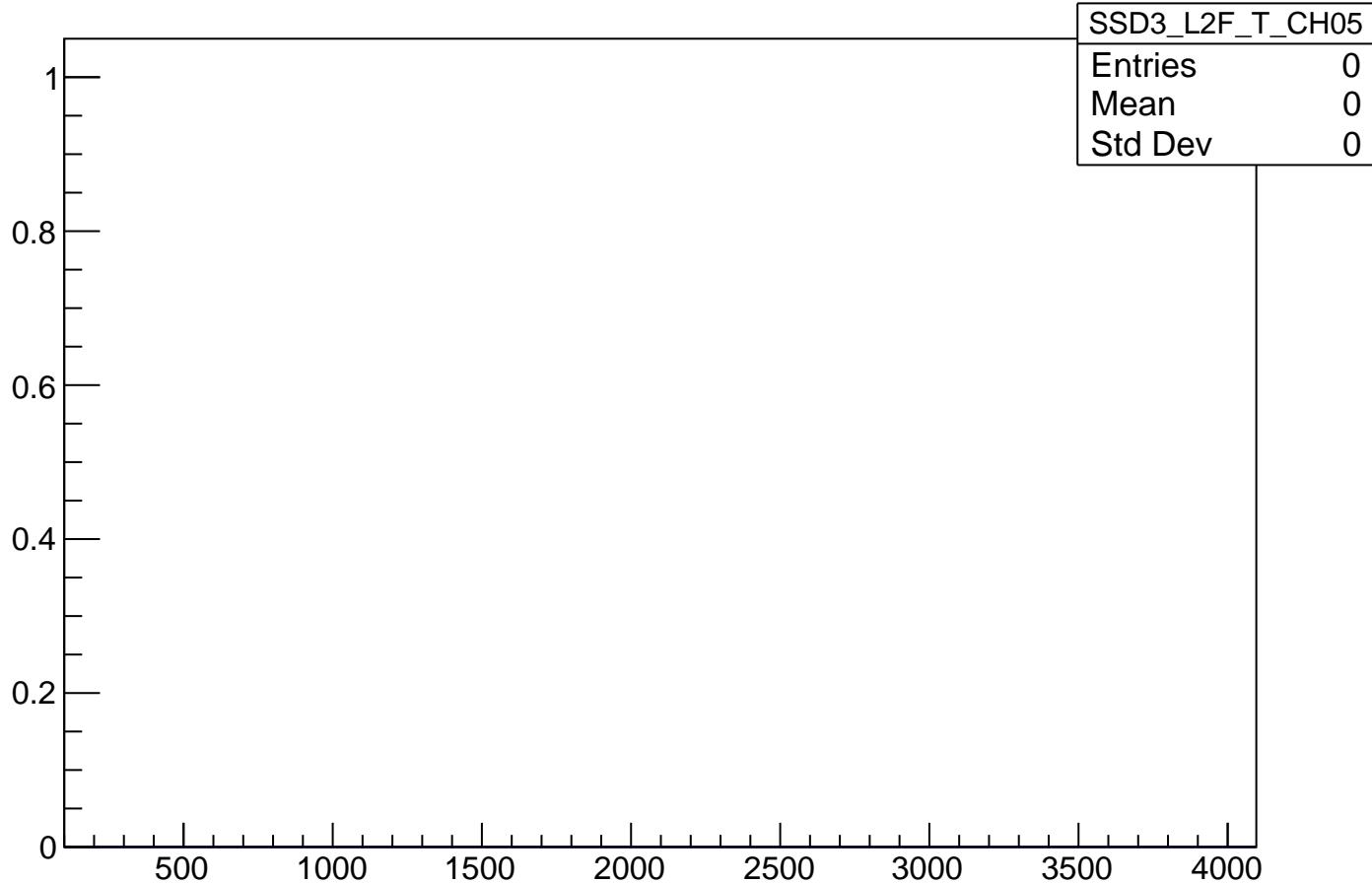
SSD3_L2F_T_CH03



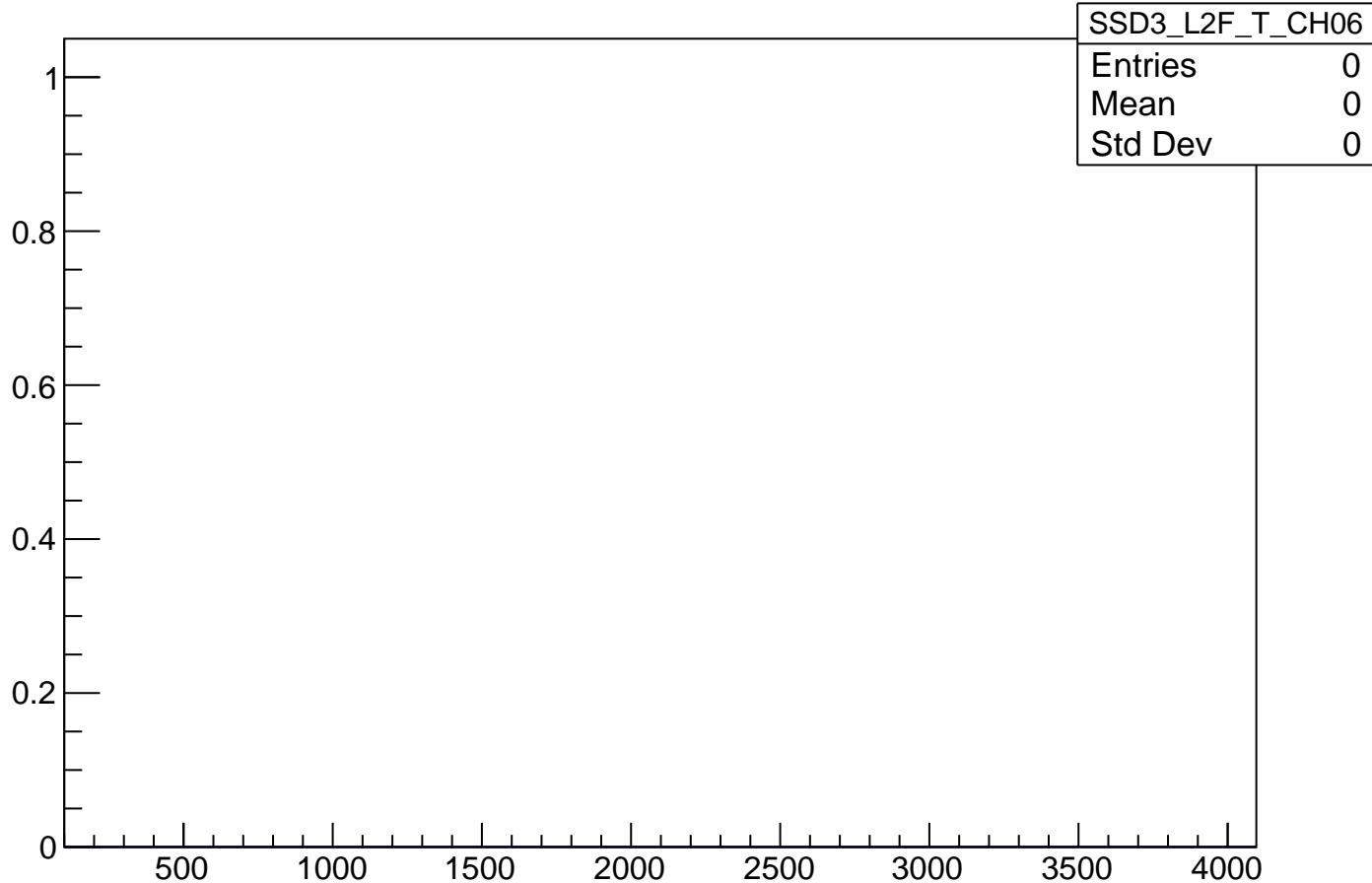
SSD3_L2F_T_CH04



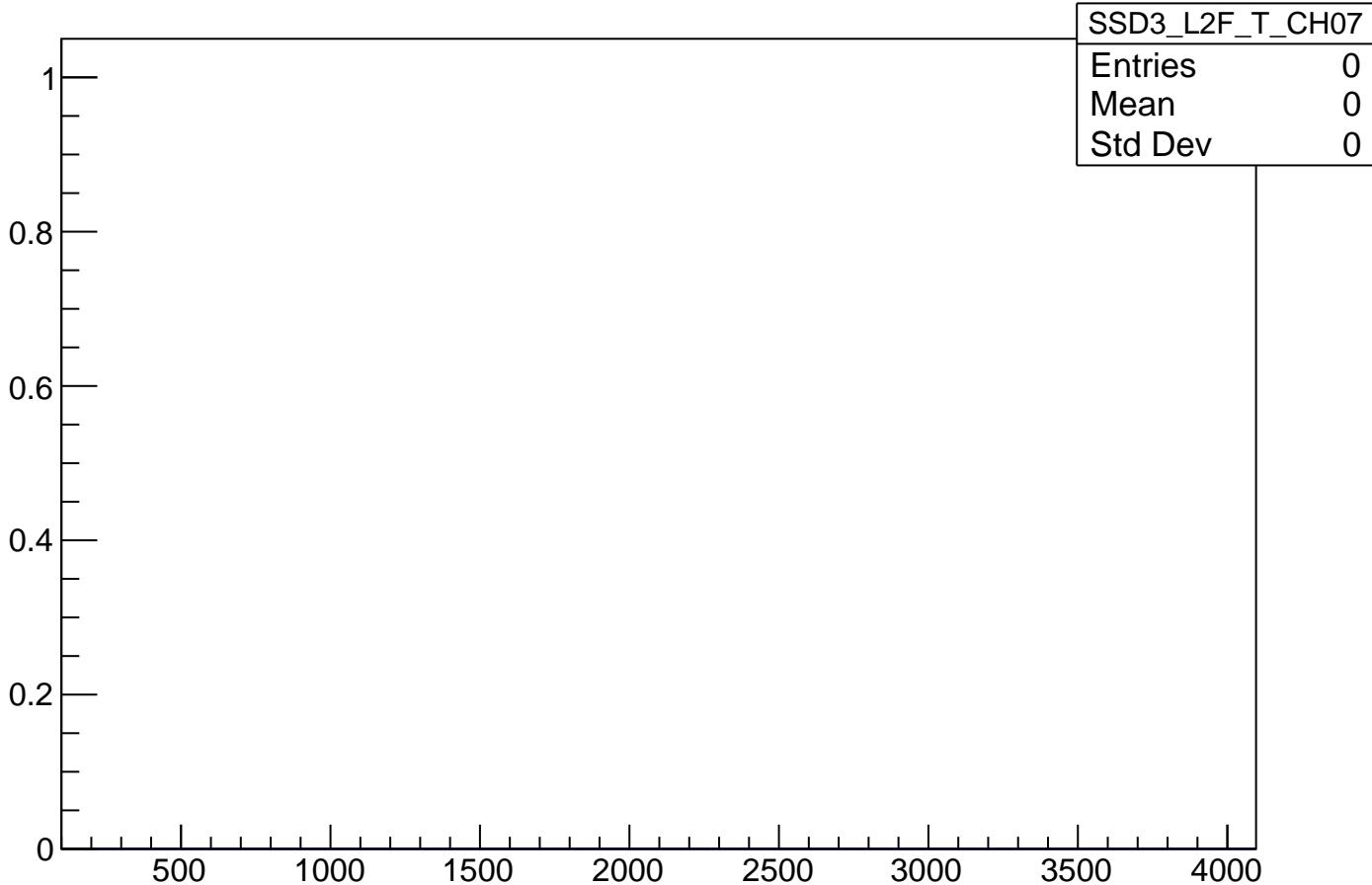
SSD3_L2F_T_CH05



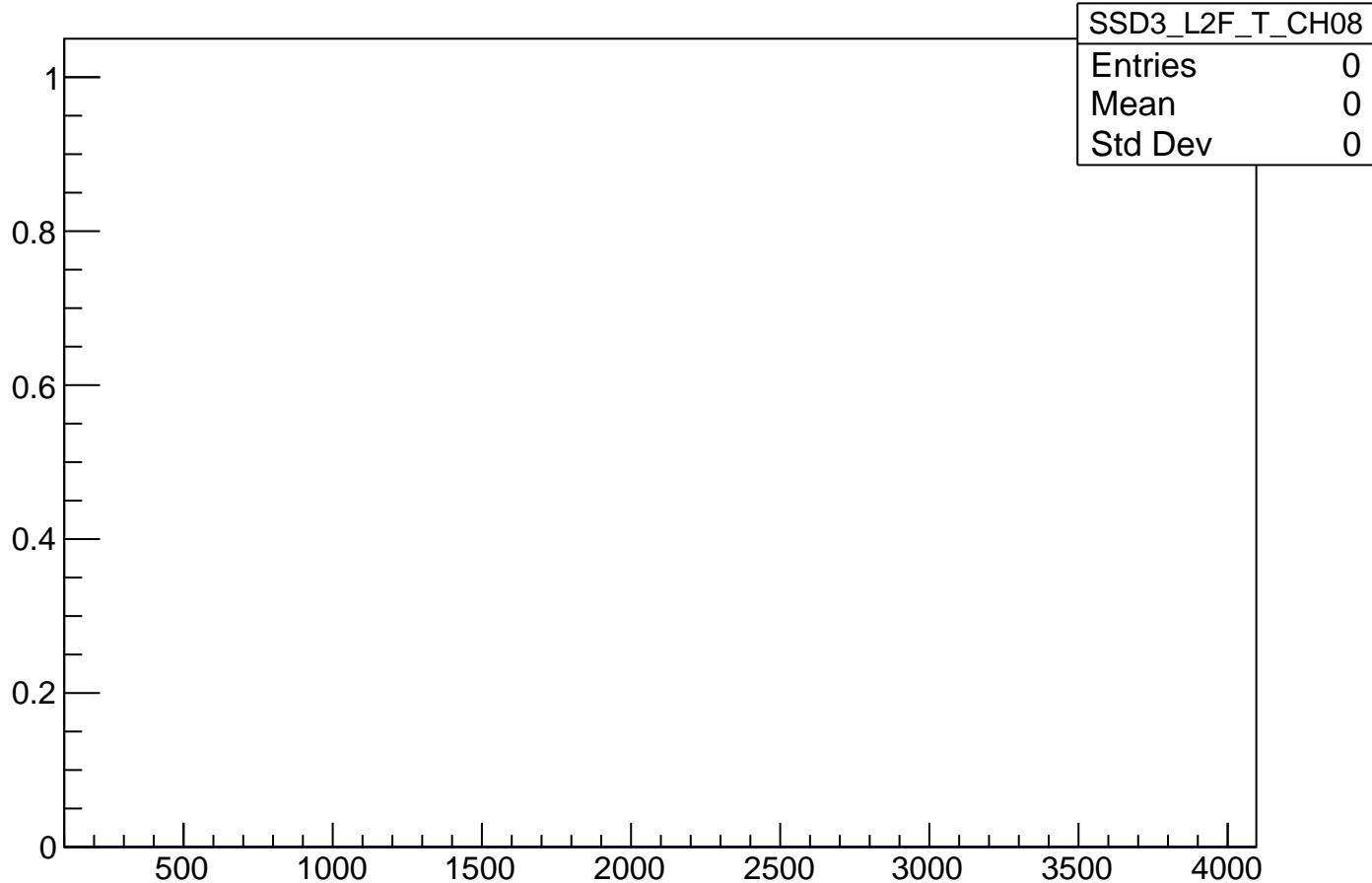
SSD3_L2F_T_CH06



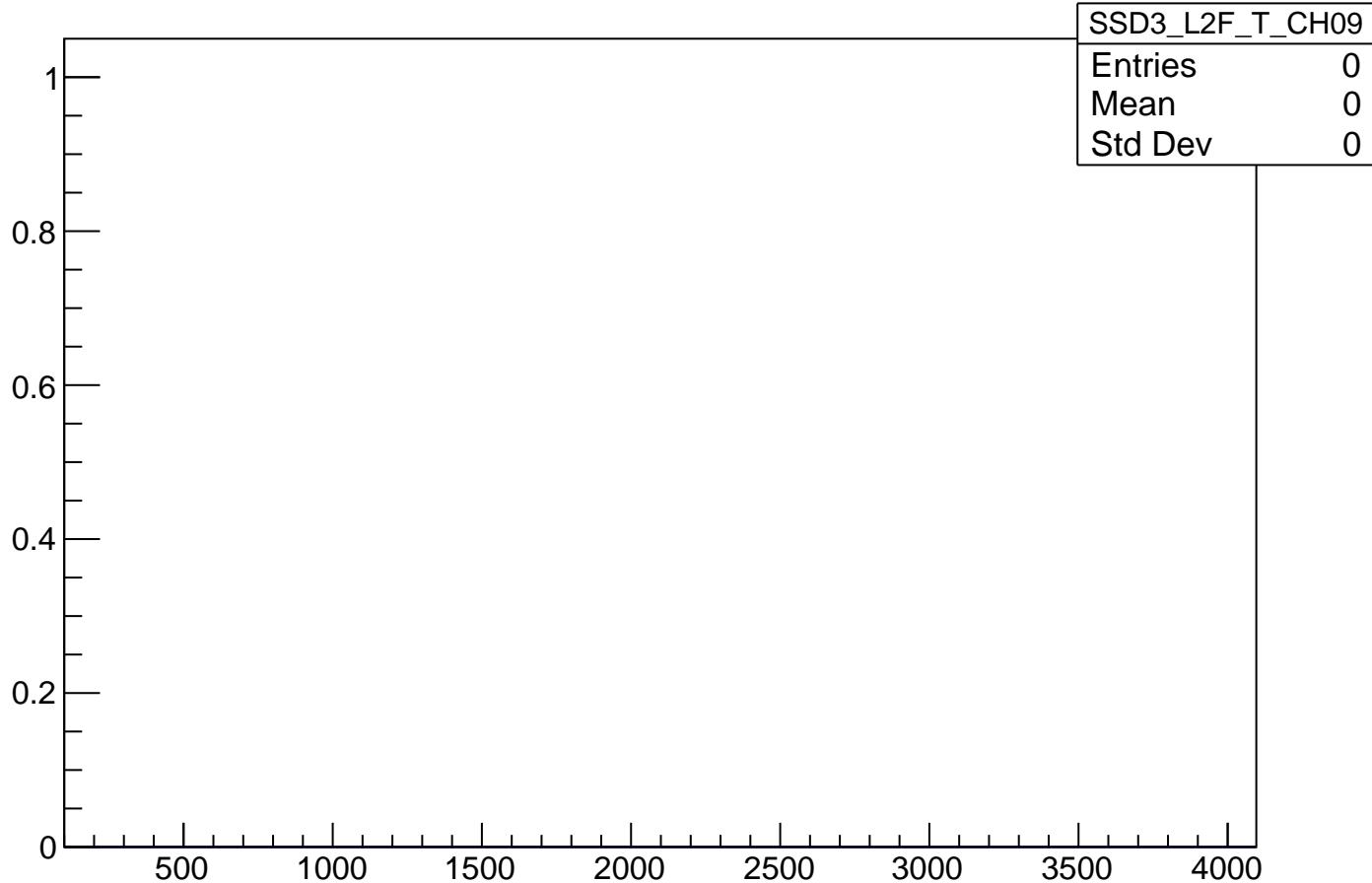
SSD3_L2F_T_CH07



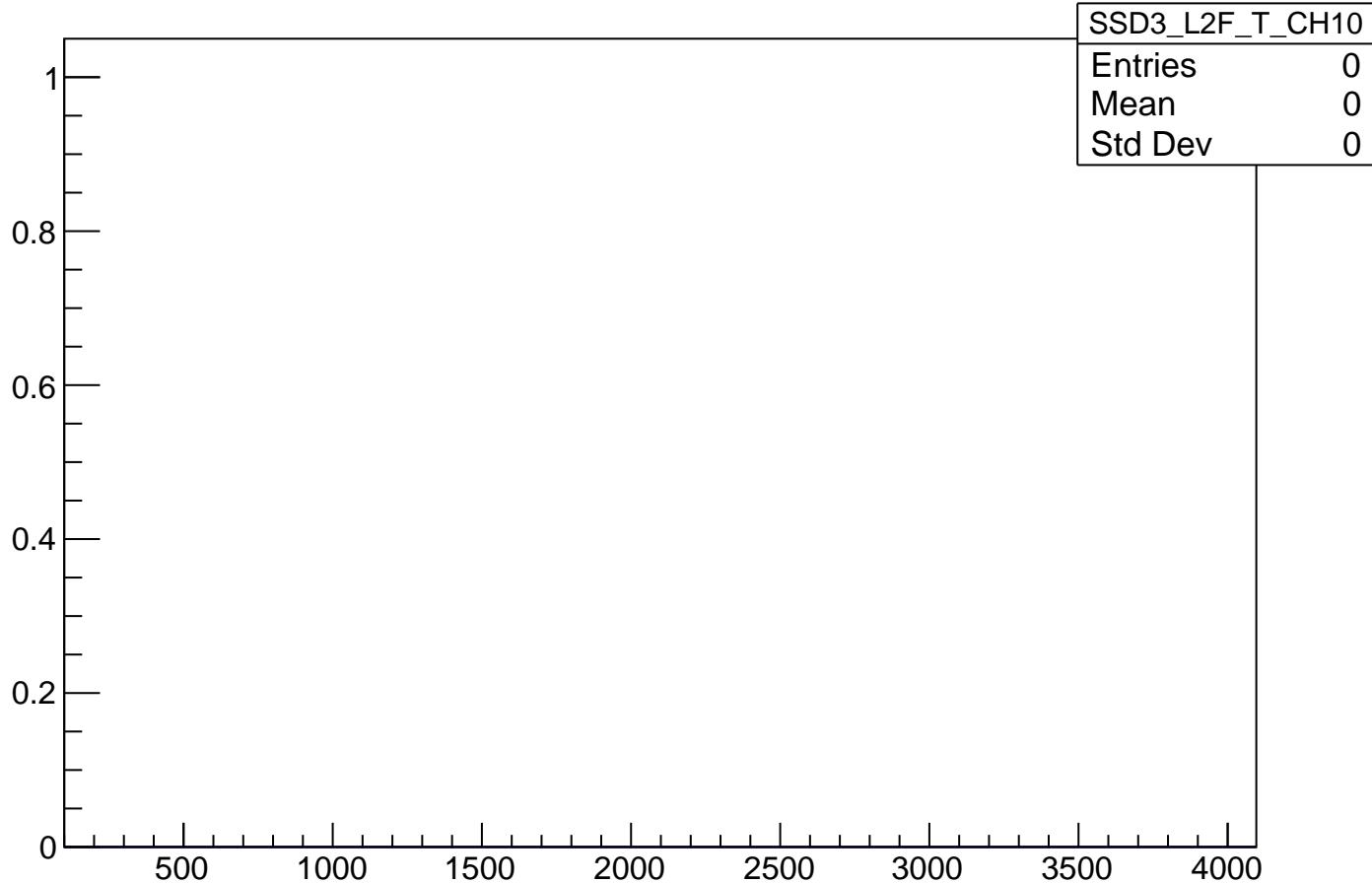
SSD3_L2F_T_CH08



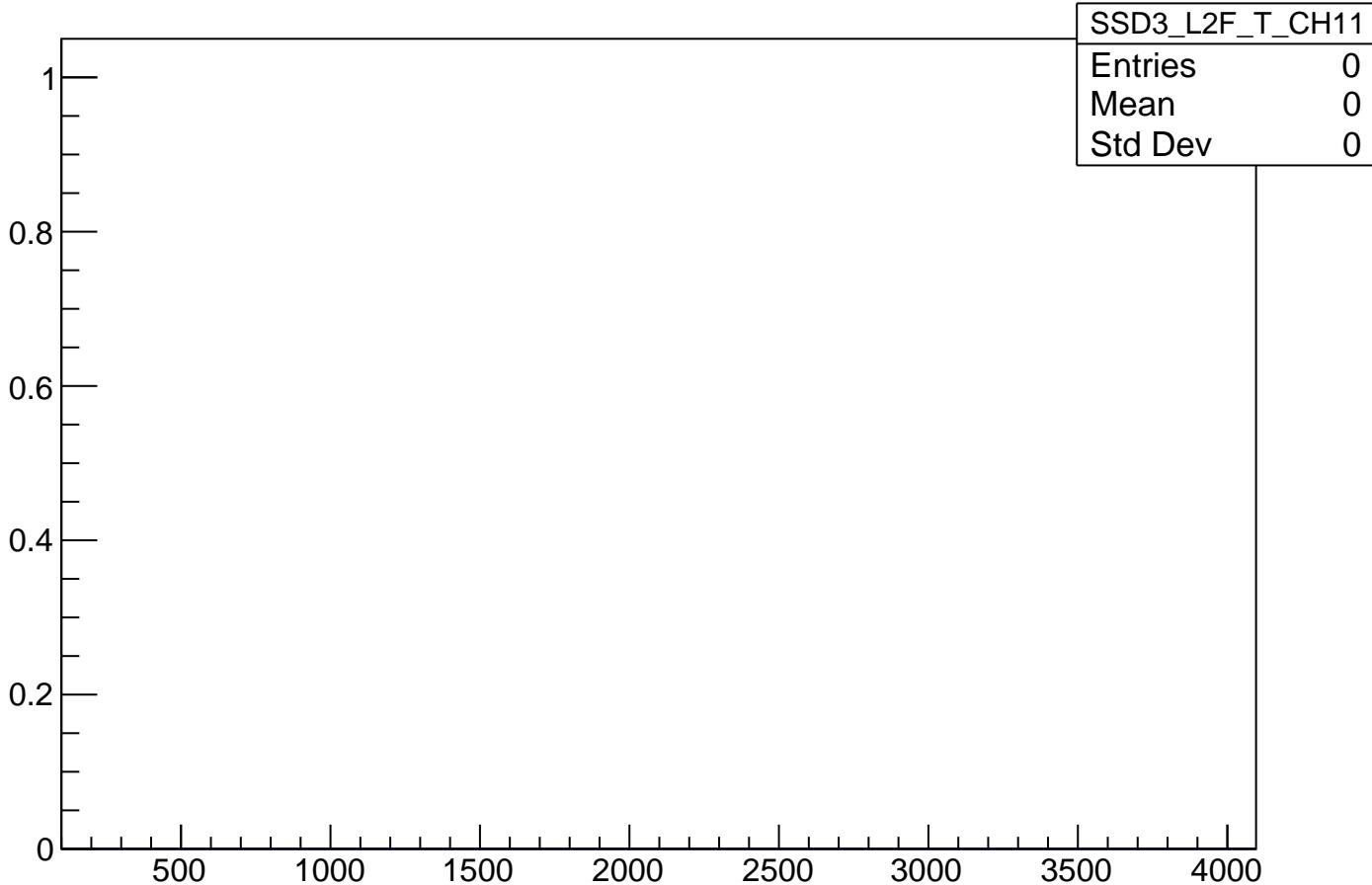
SSD3_L2F_T_CH09



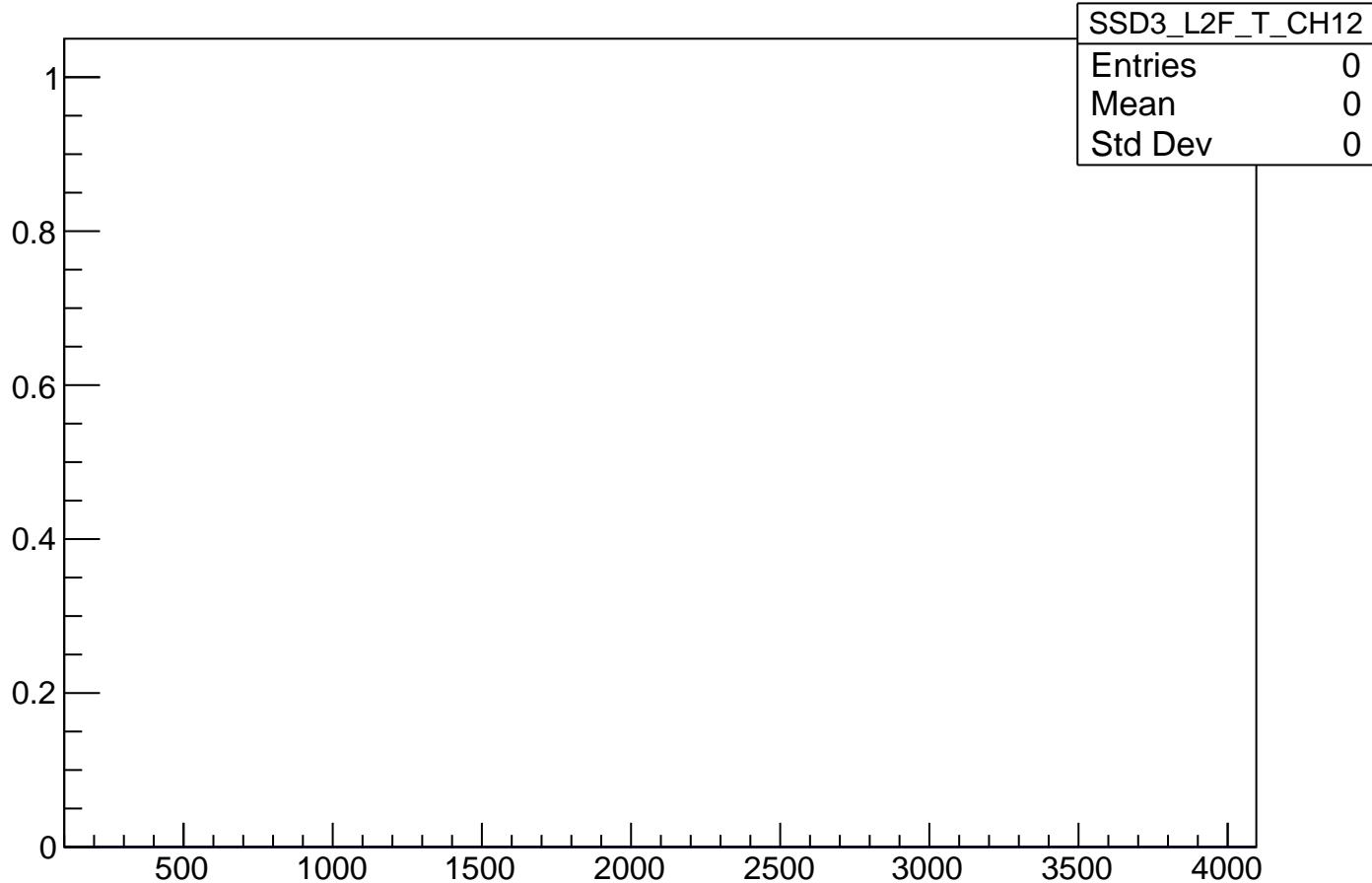
SSD3_L2F_T_CH10



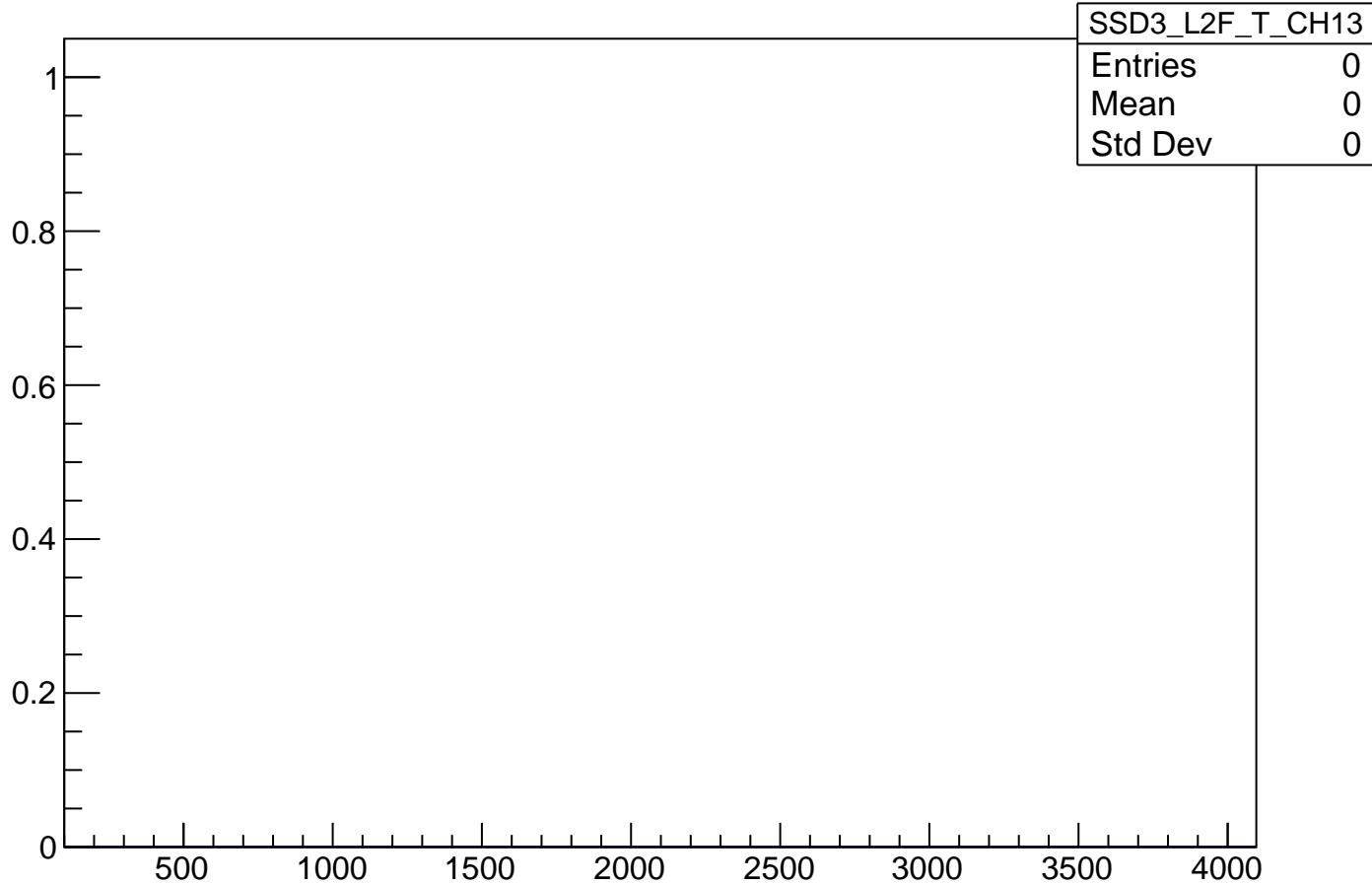
SSD3_L2F_T_CH11



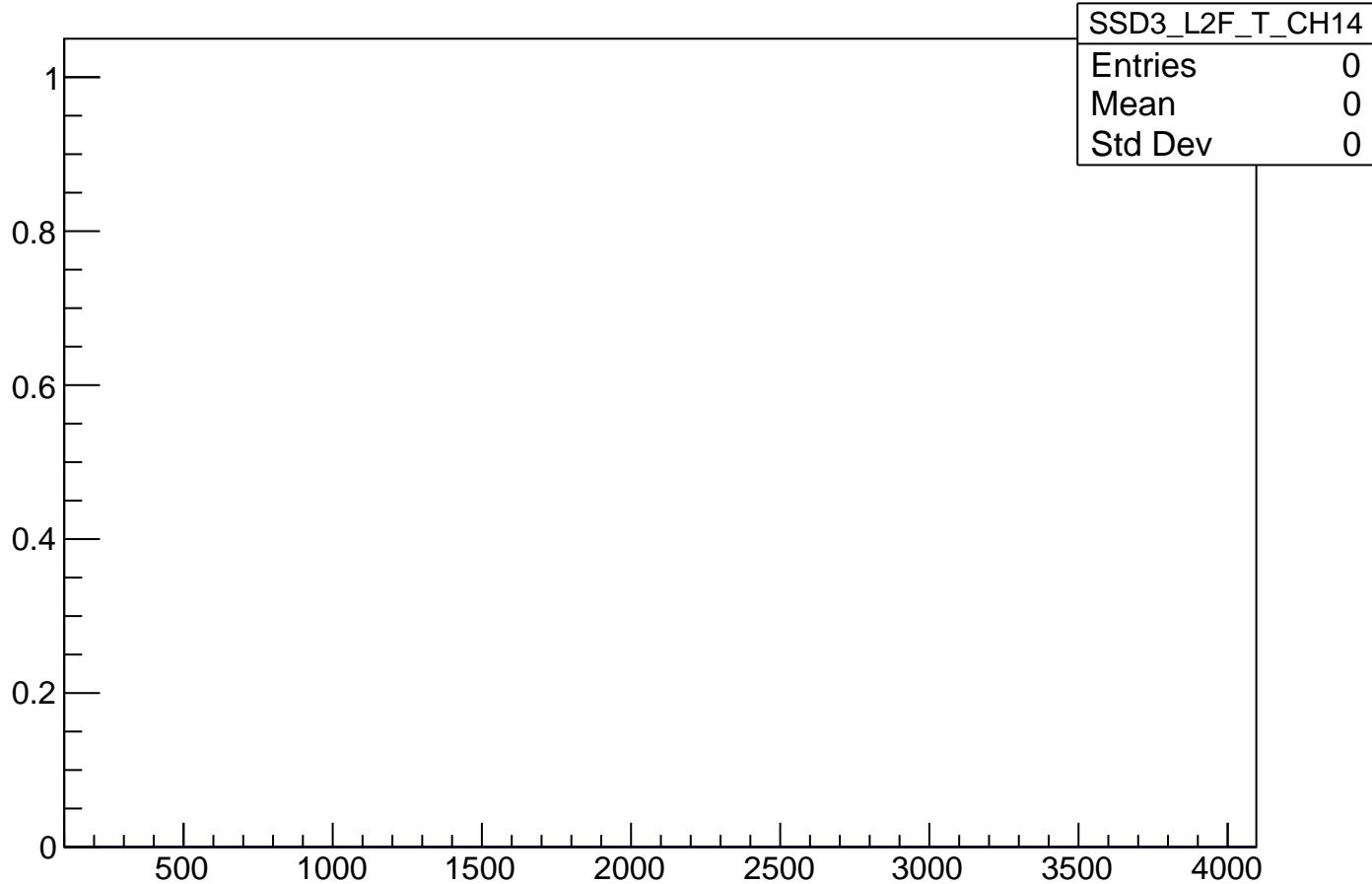
SSD3_L2F_T_CH12



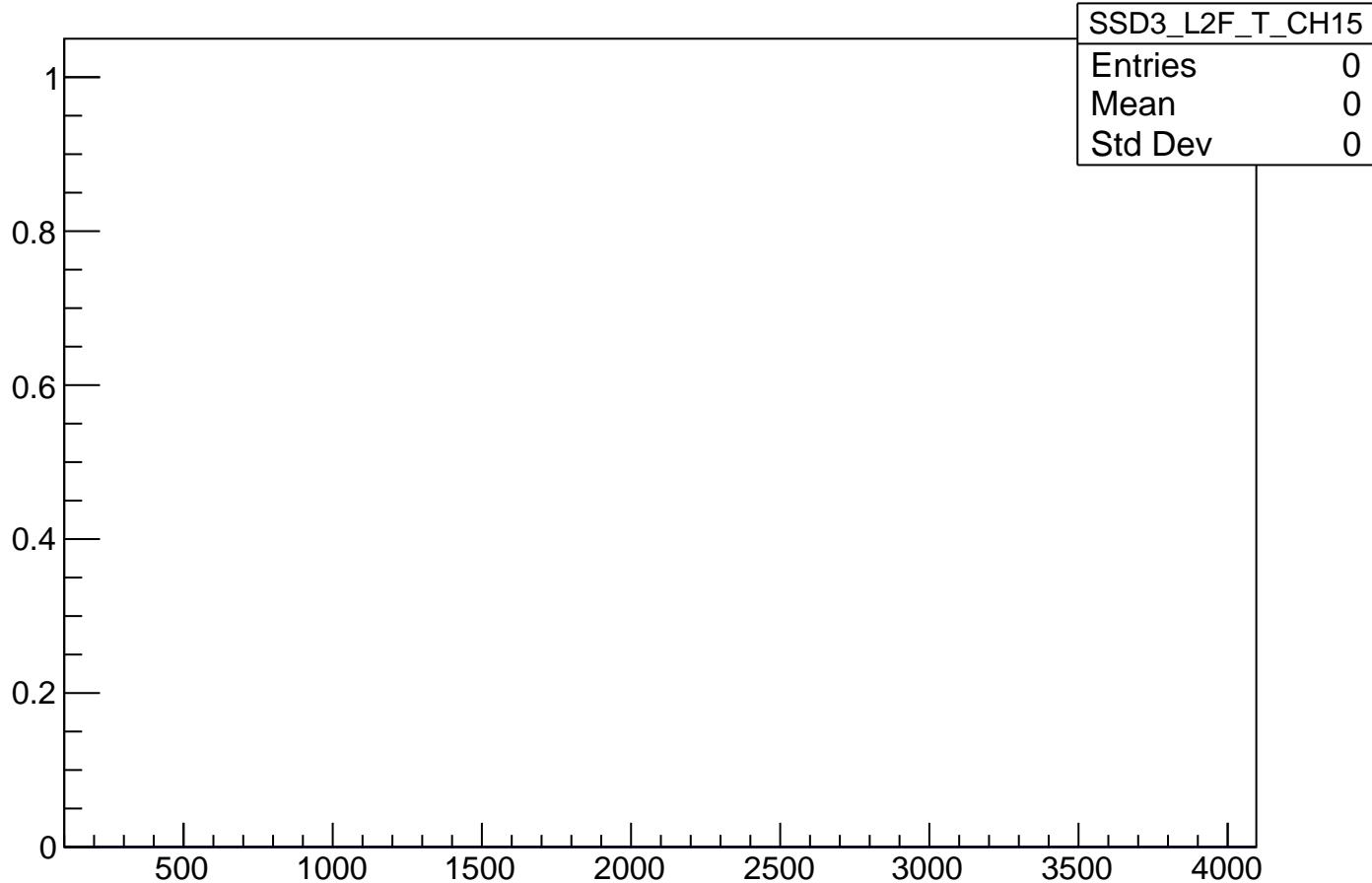
SSD3_L2F_T_CH13



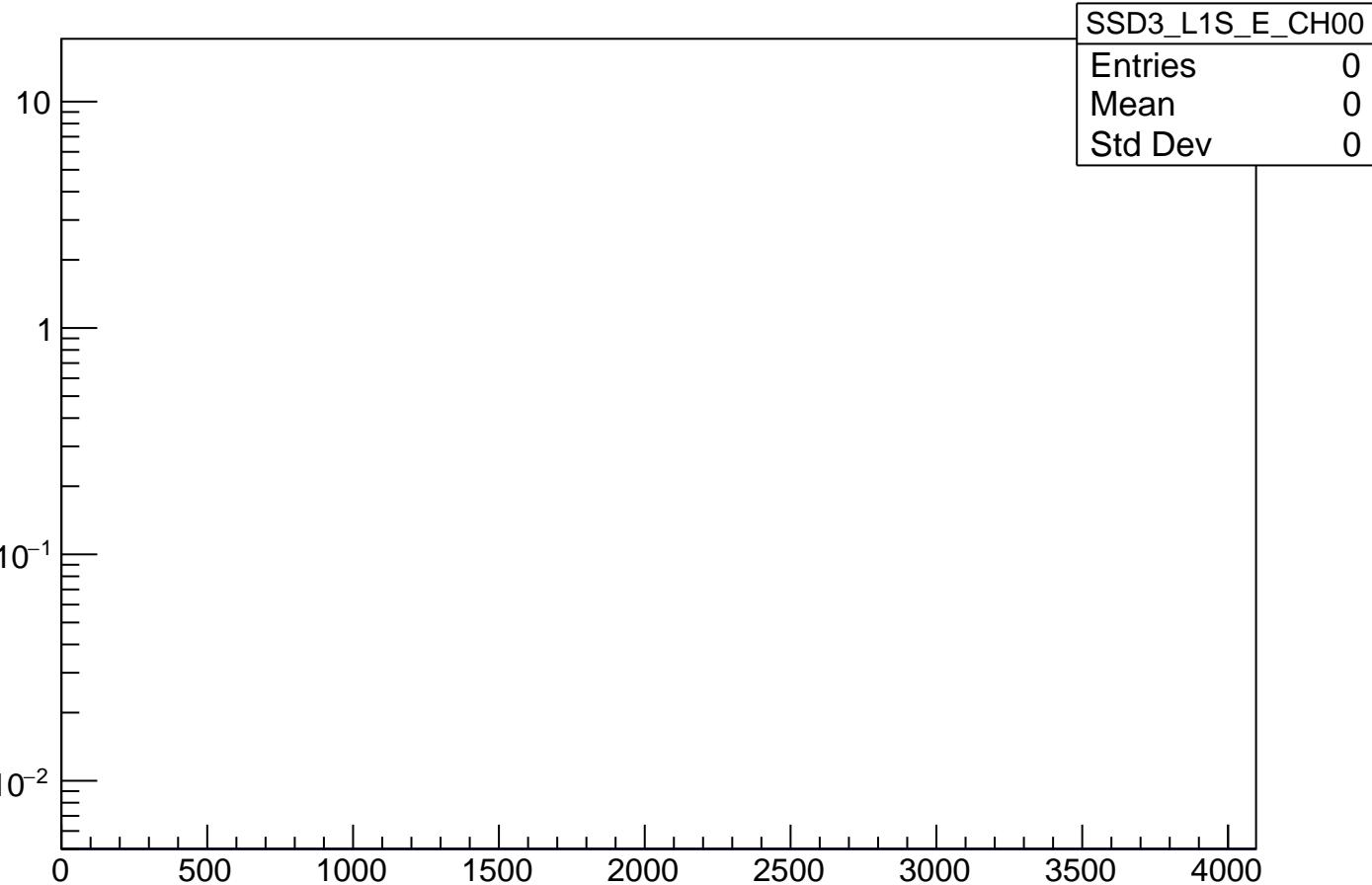
SSD3_L2F_T_CH14



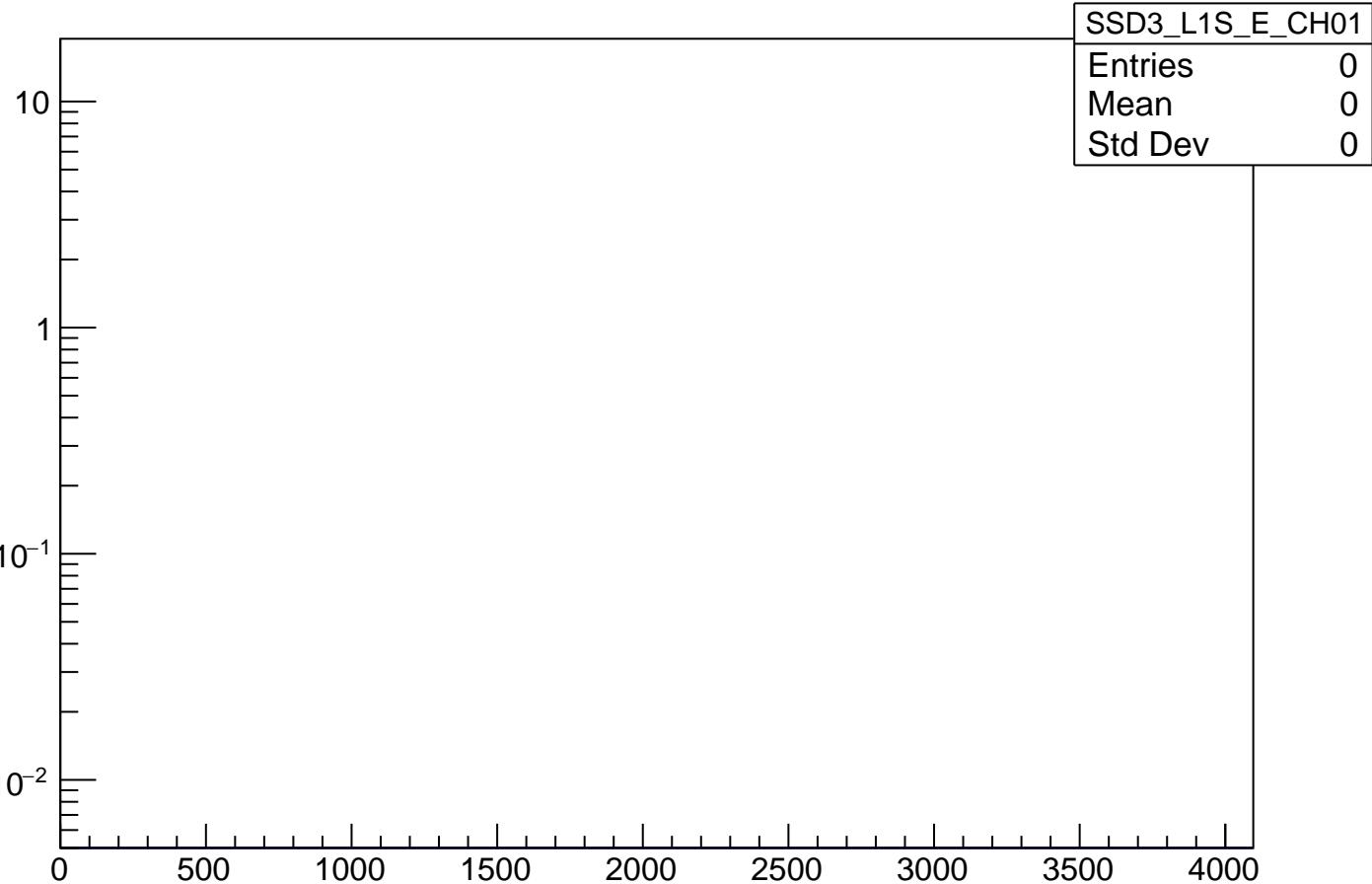
SSD3_L2F_T_CH15



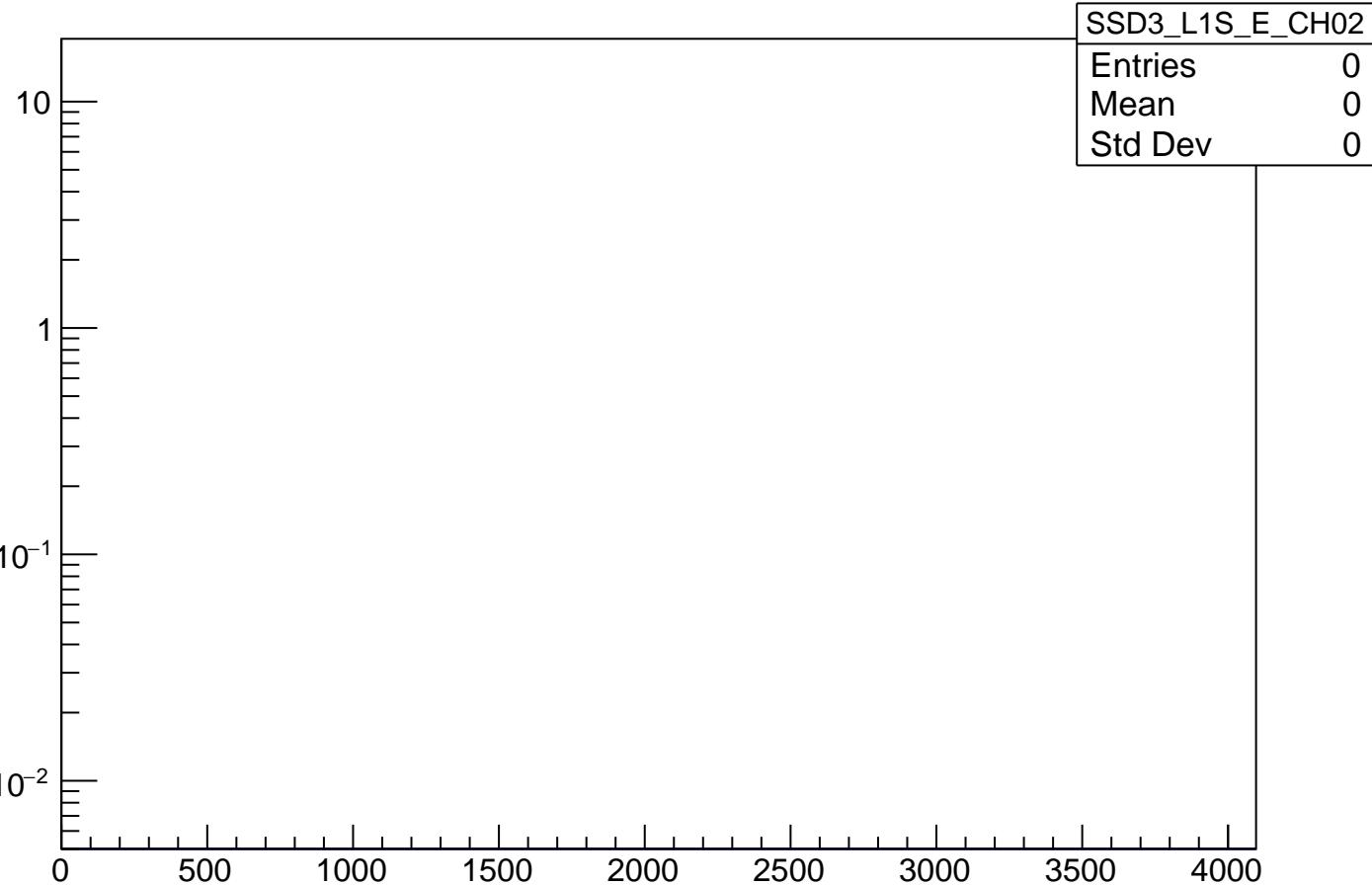
SSD3_L1S_E_CH00



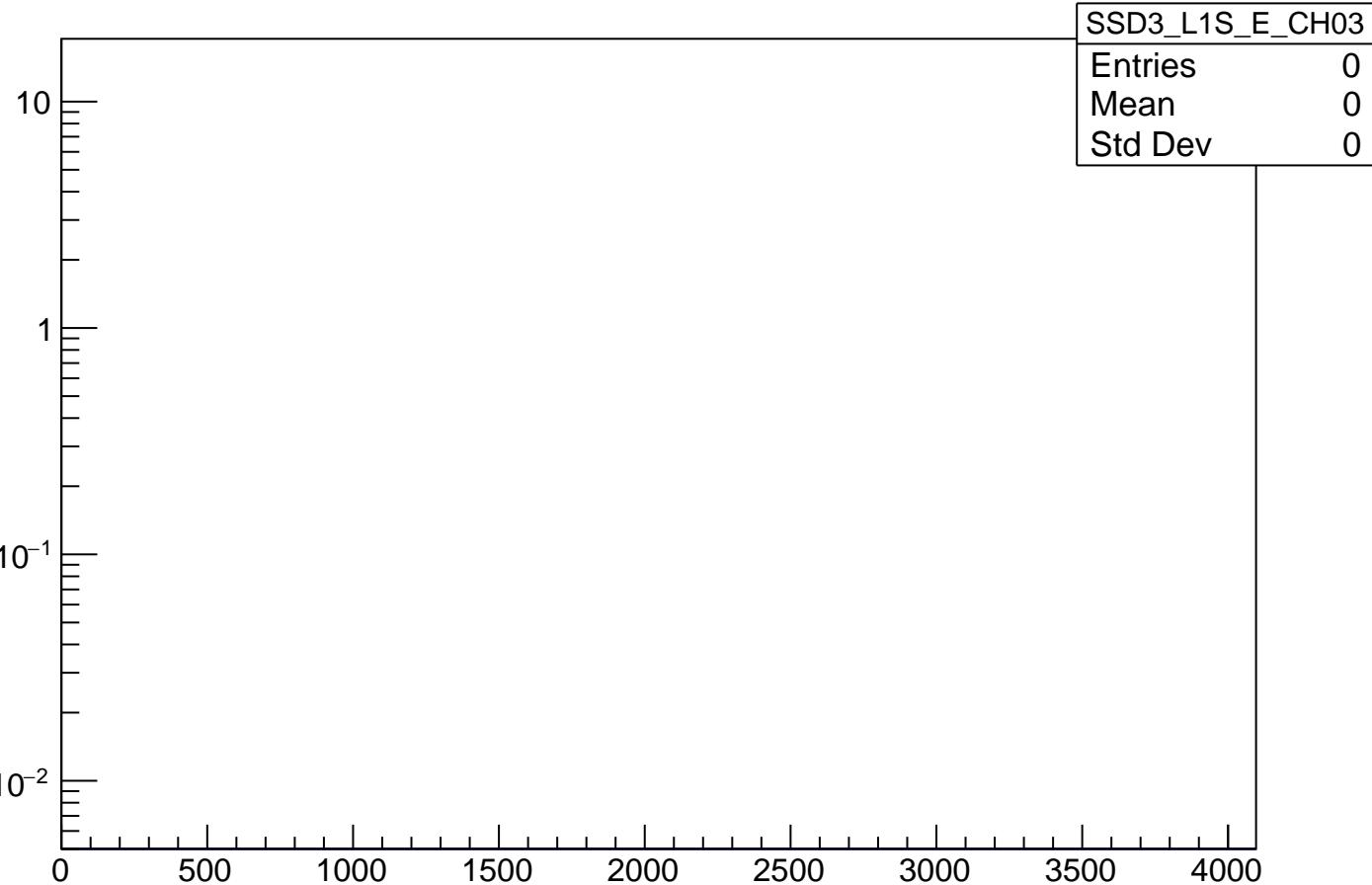
SSD3_L1S_E_CH01



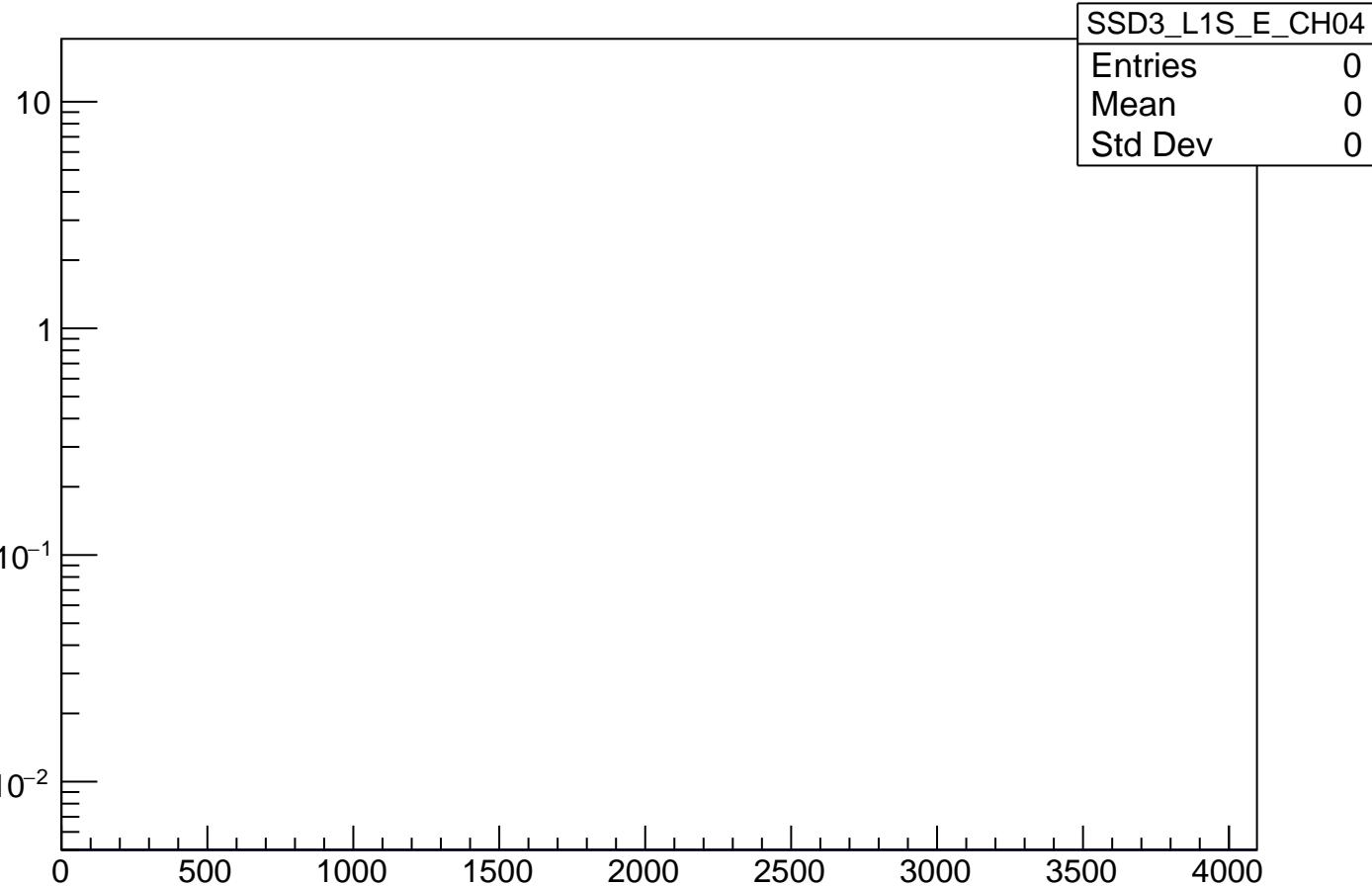
SSD3_L1S_E_CH02



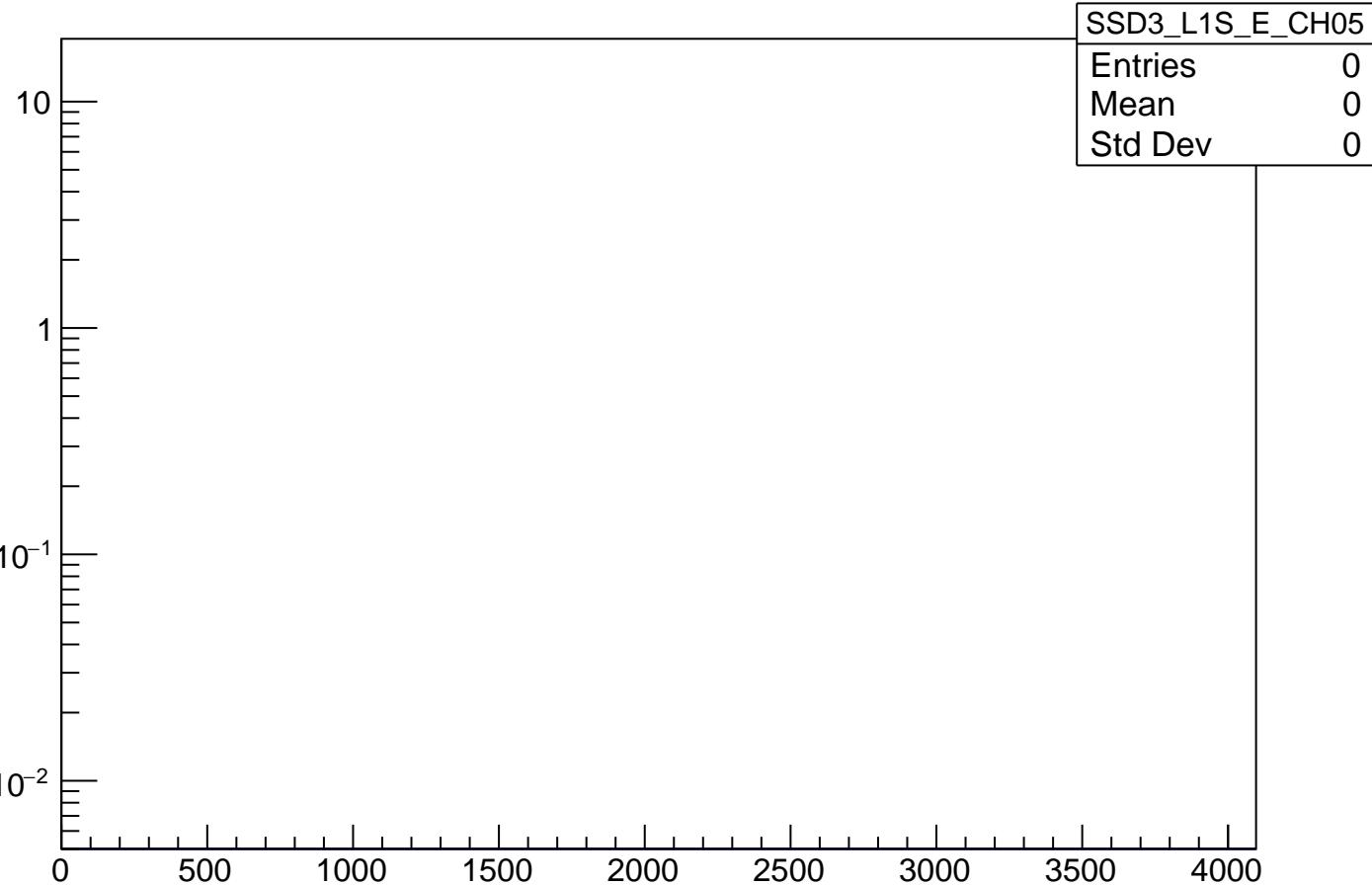
SSD3_L1S_E_CH03



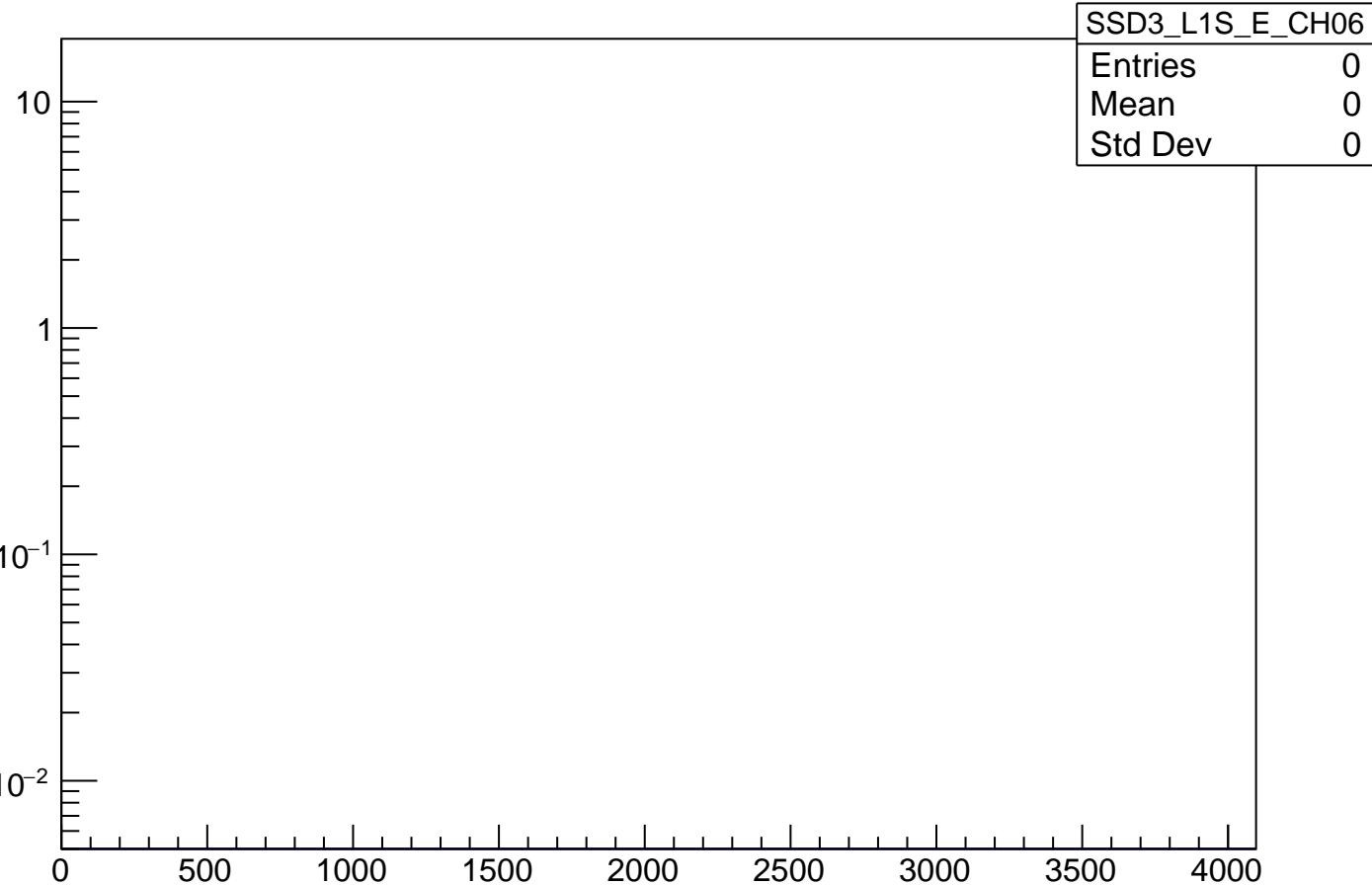
SSD3_L1S_E_CH04



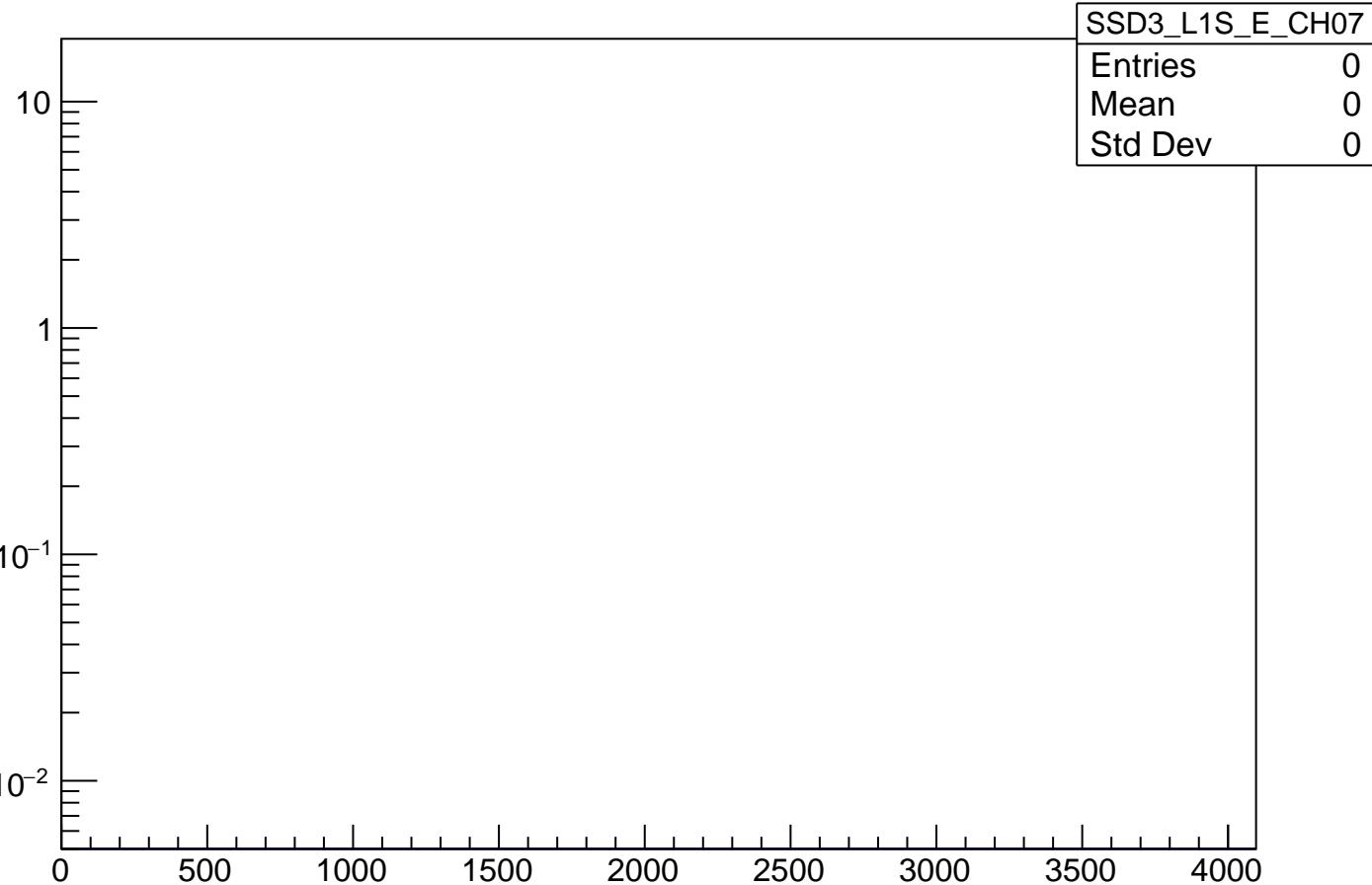
SSD3_L1S_E_CH05



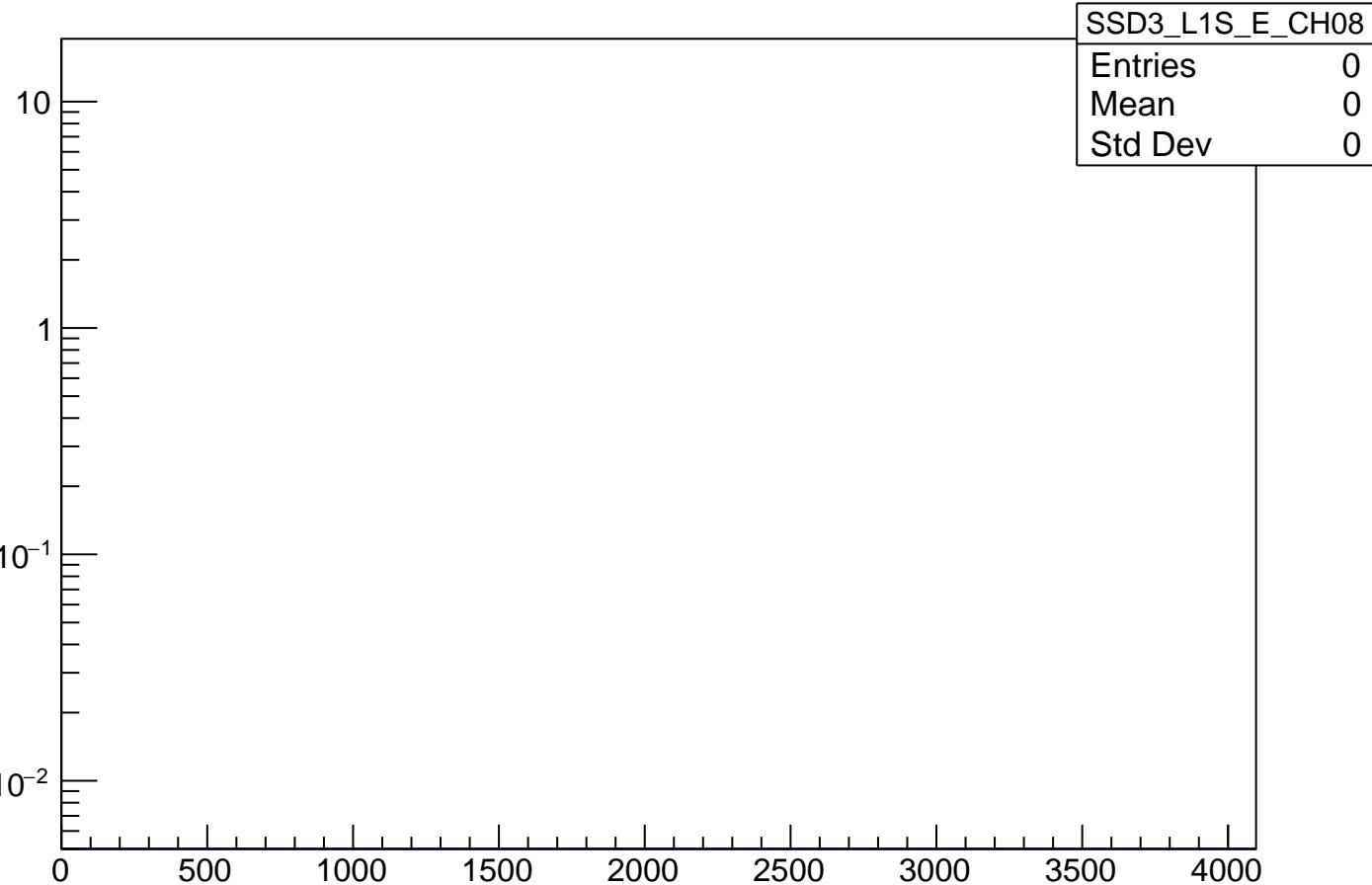
SSD3_L1S_E_CH06



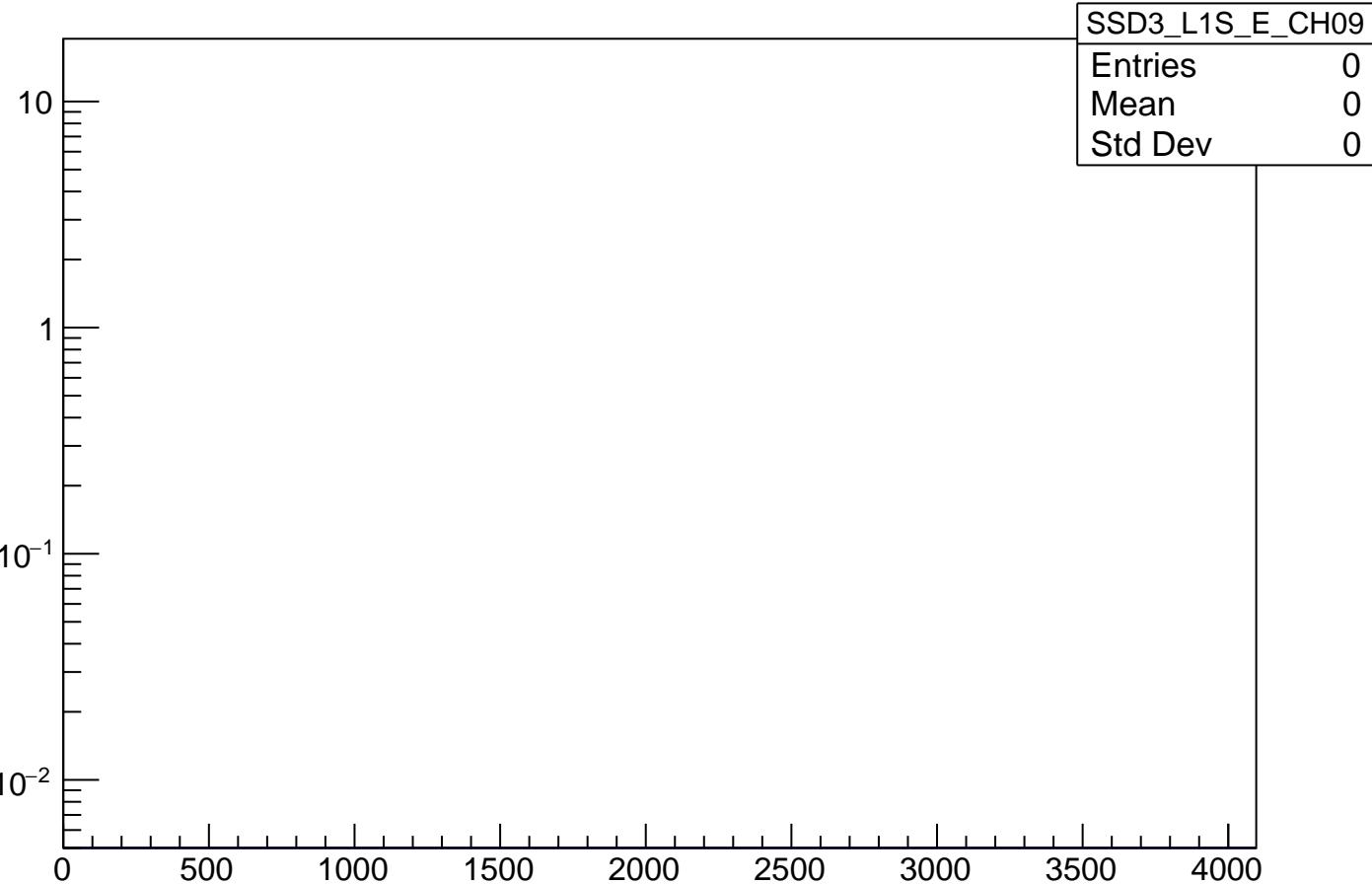
SSD3_L1S_E_CH07



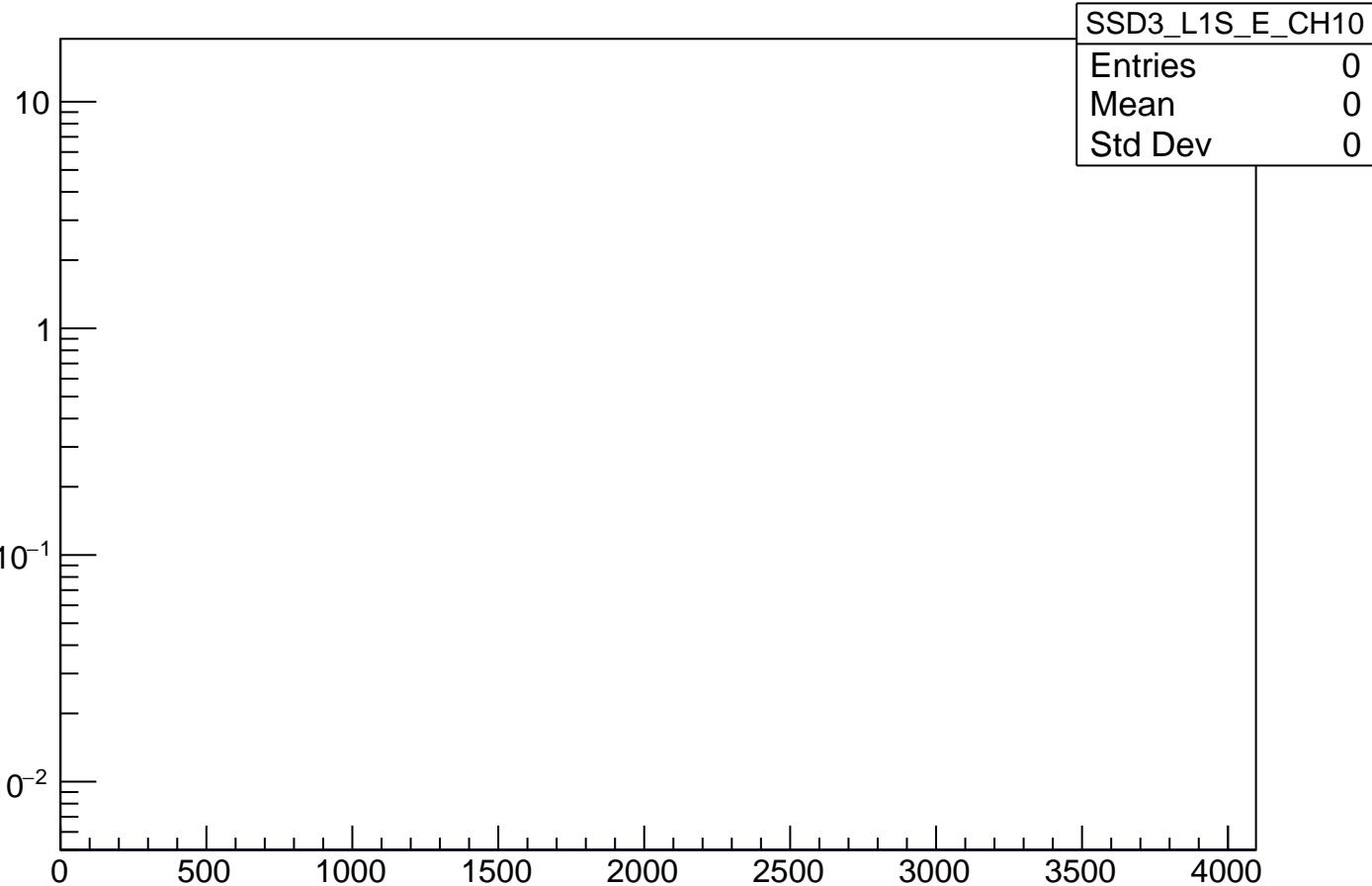
SSD3_L1S_E_CH08



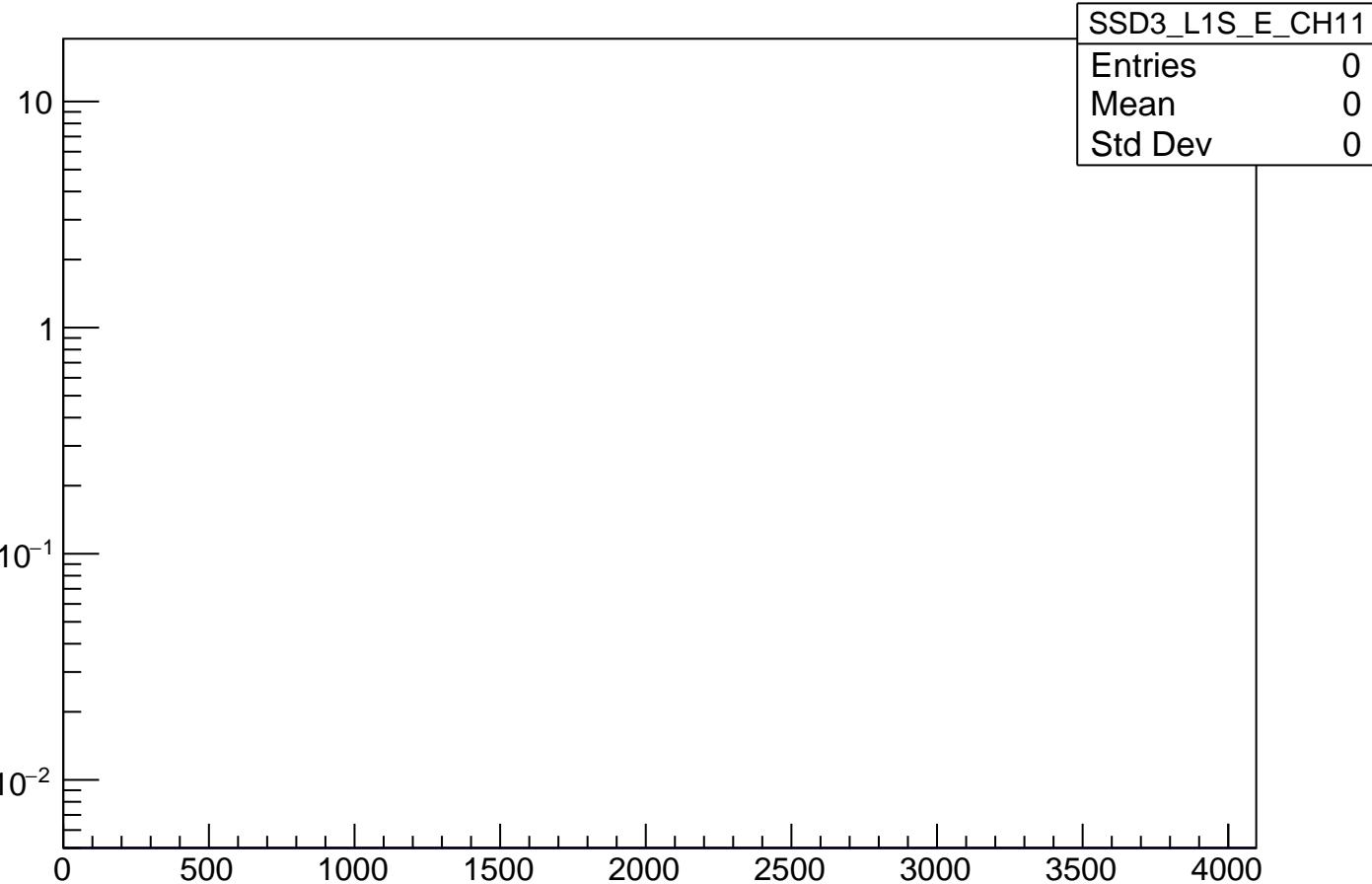
SSD3_L1S_E_CH09



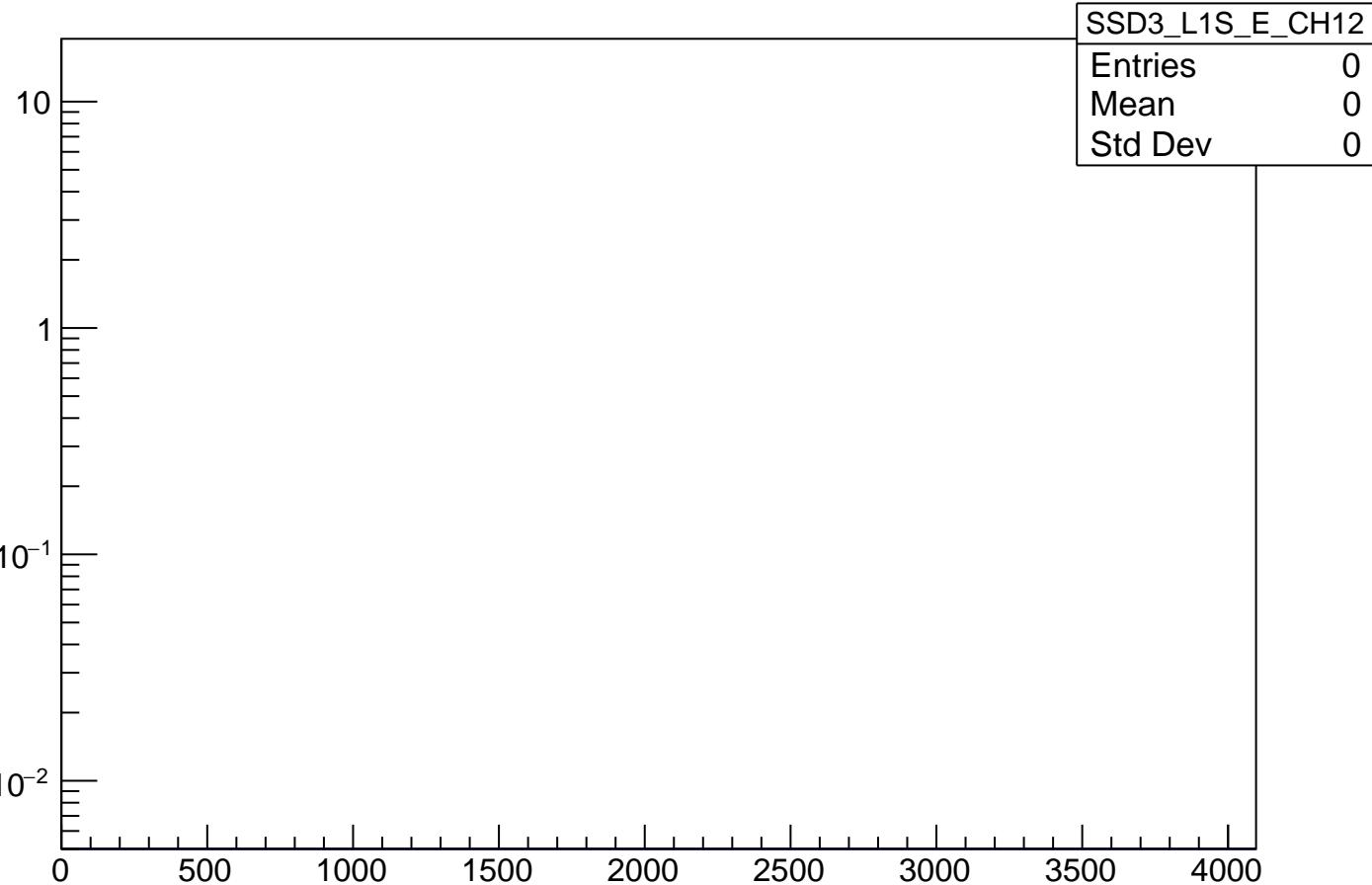
SSD3_L1S_E_CH10



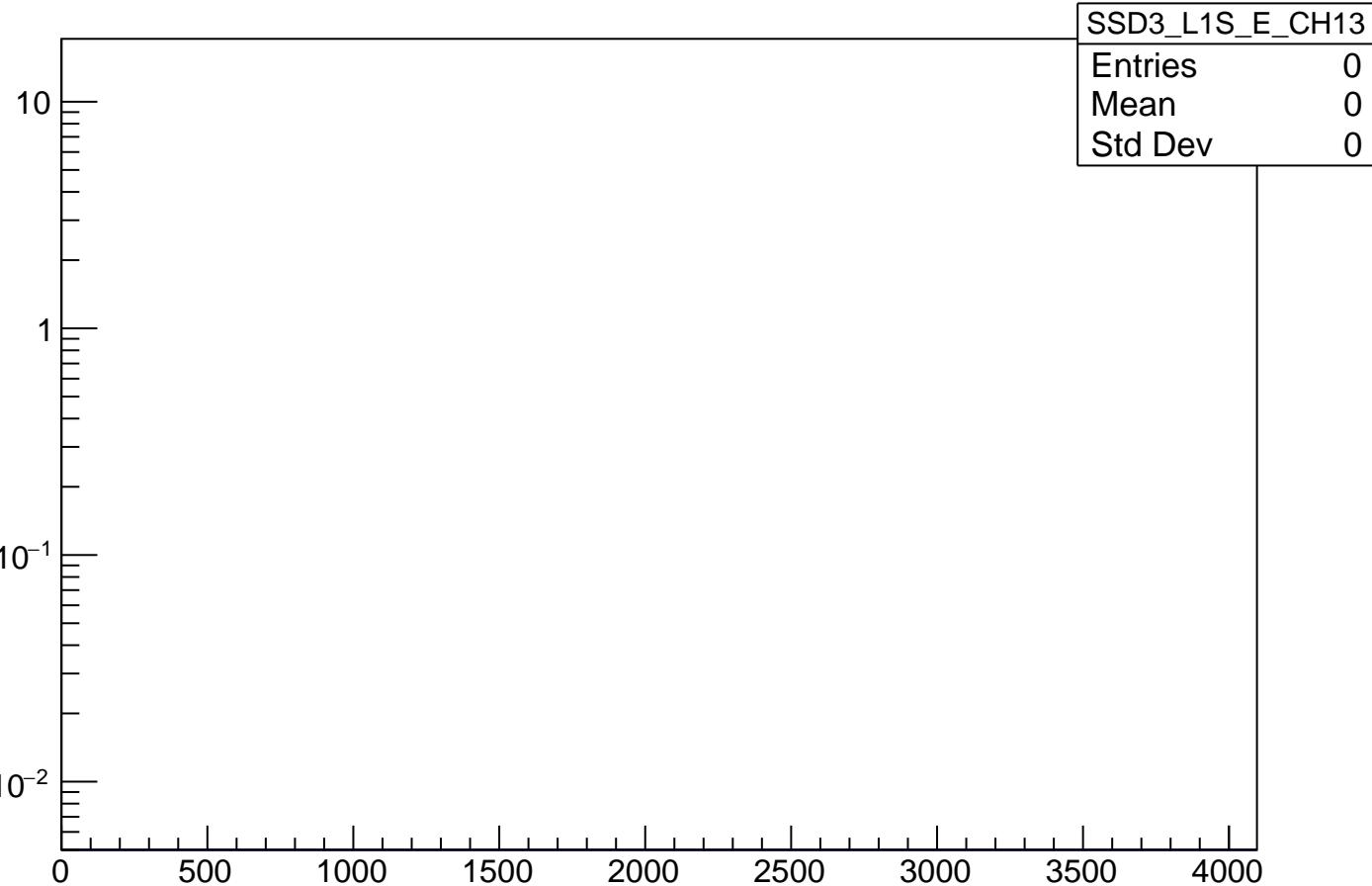
SSD3_L1S_E_CH11



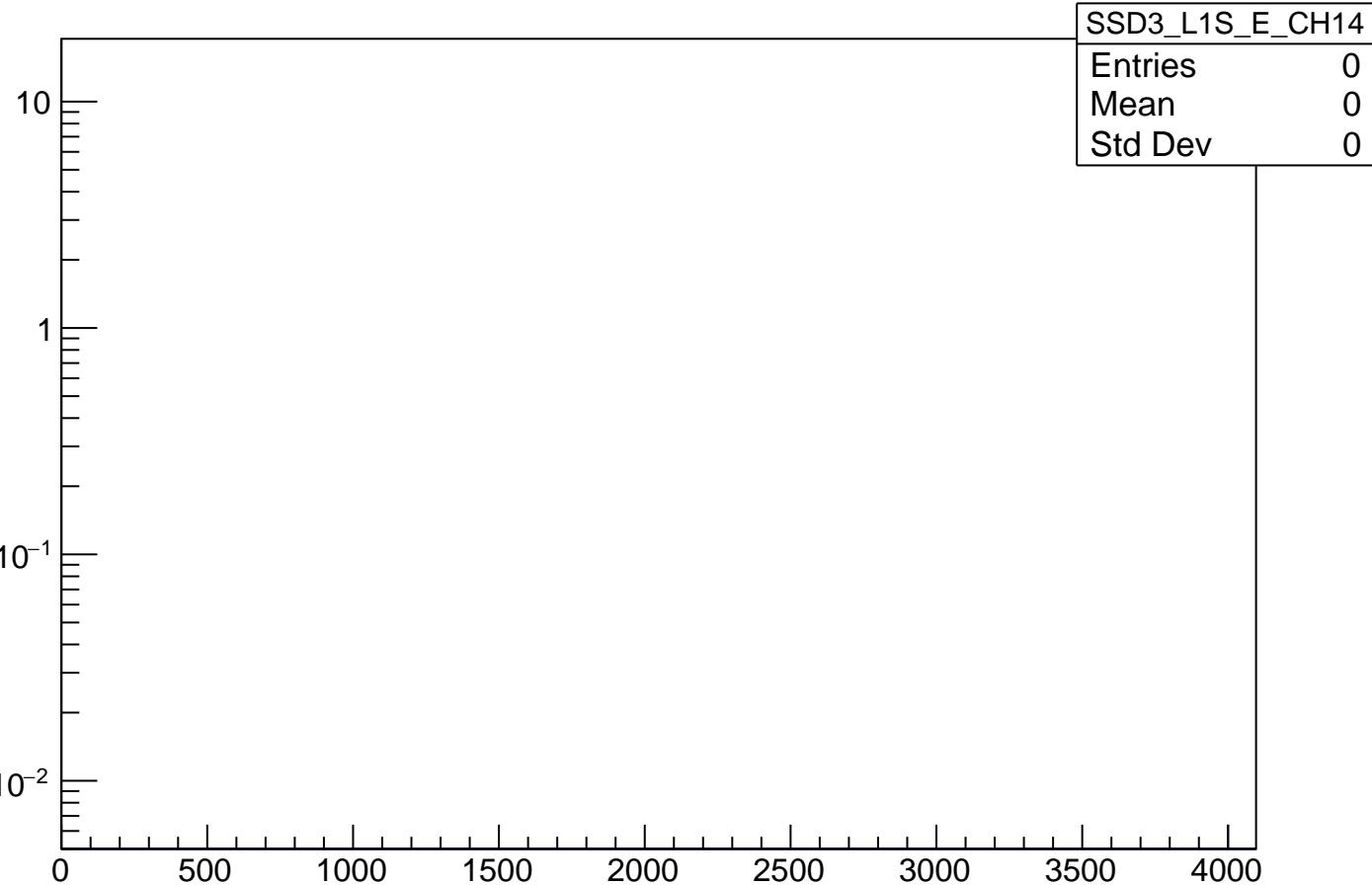
SSD3_L1S_E_CH12



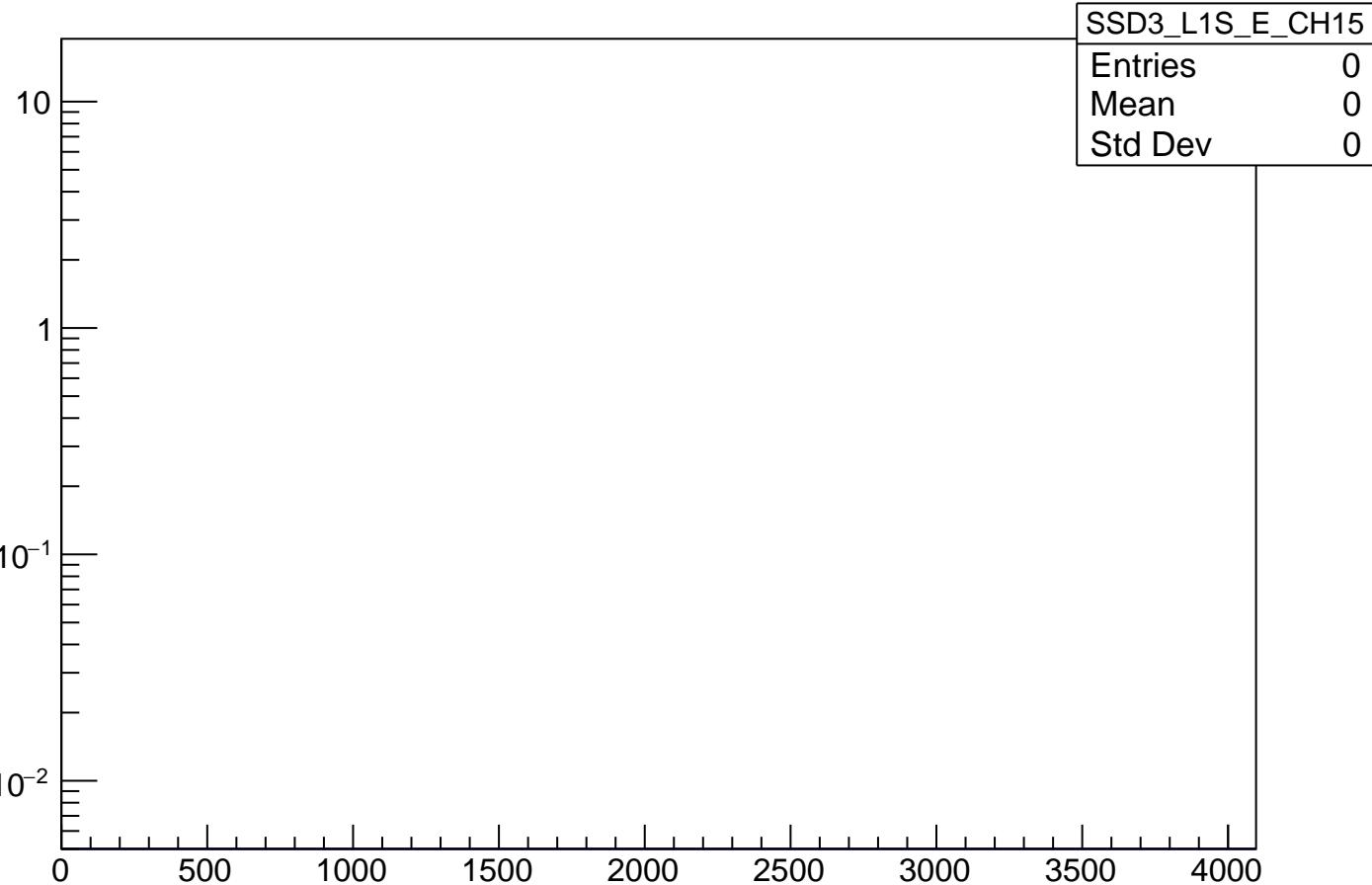
SSD3_L1S_E_CH13



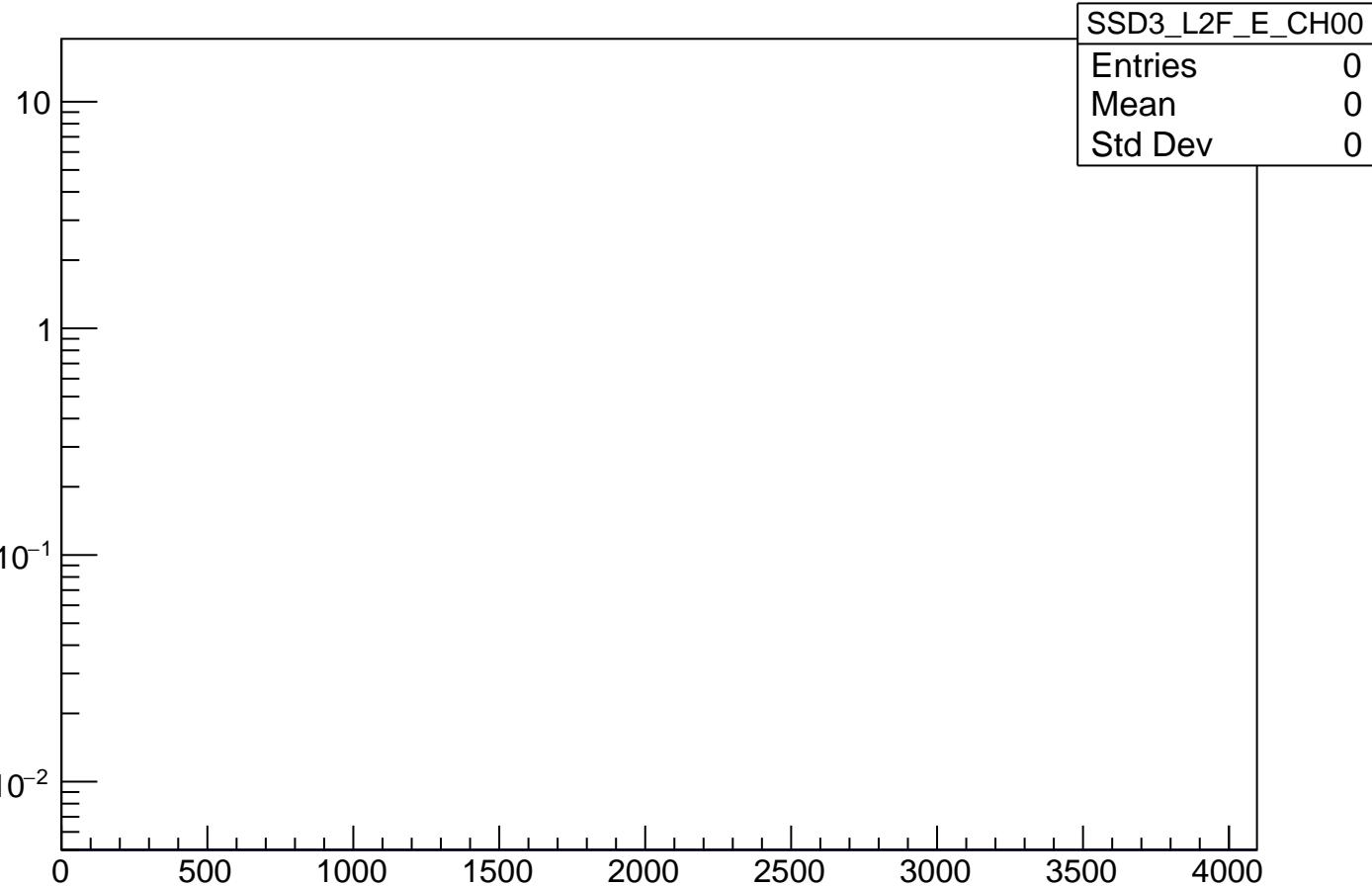
SSD3_L1S_E_CH14



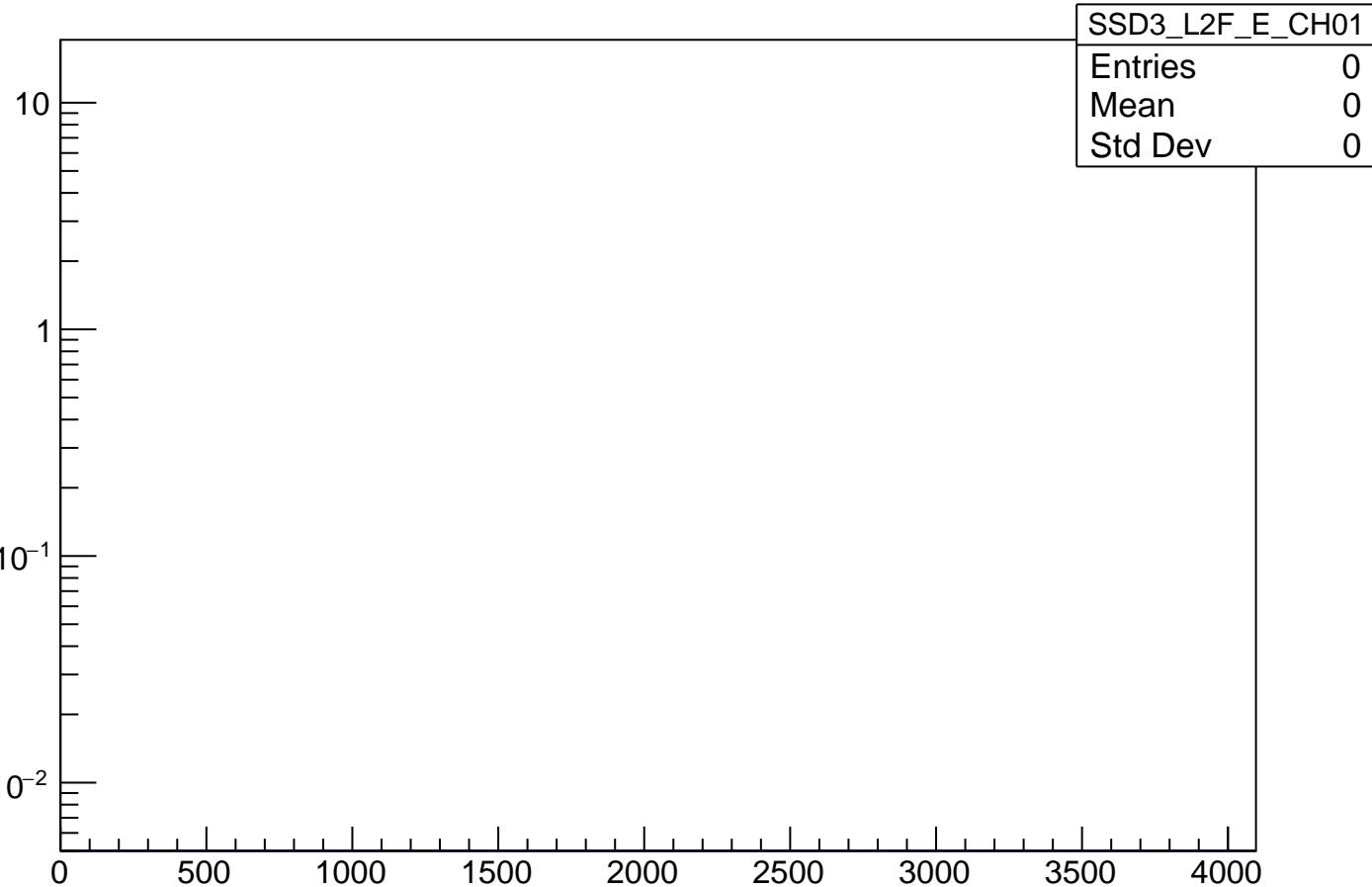
SSD3_L1S_E_CH15



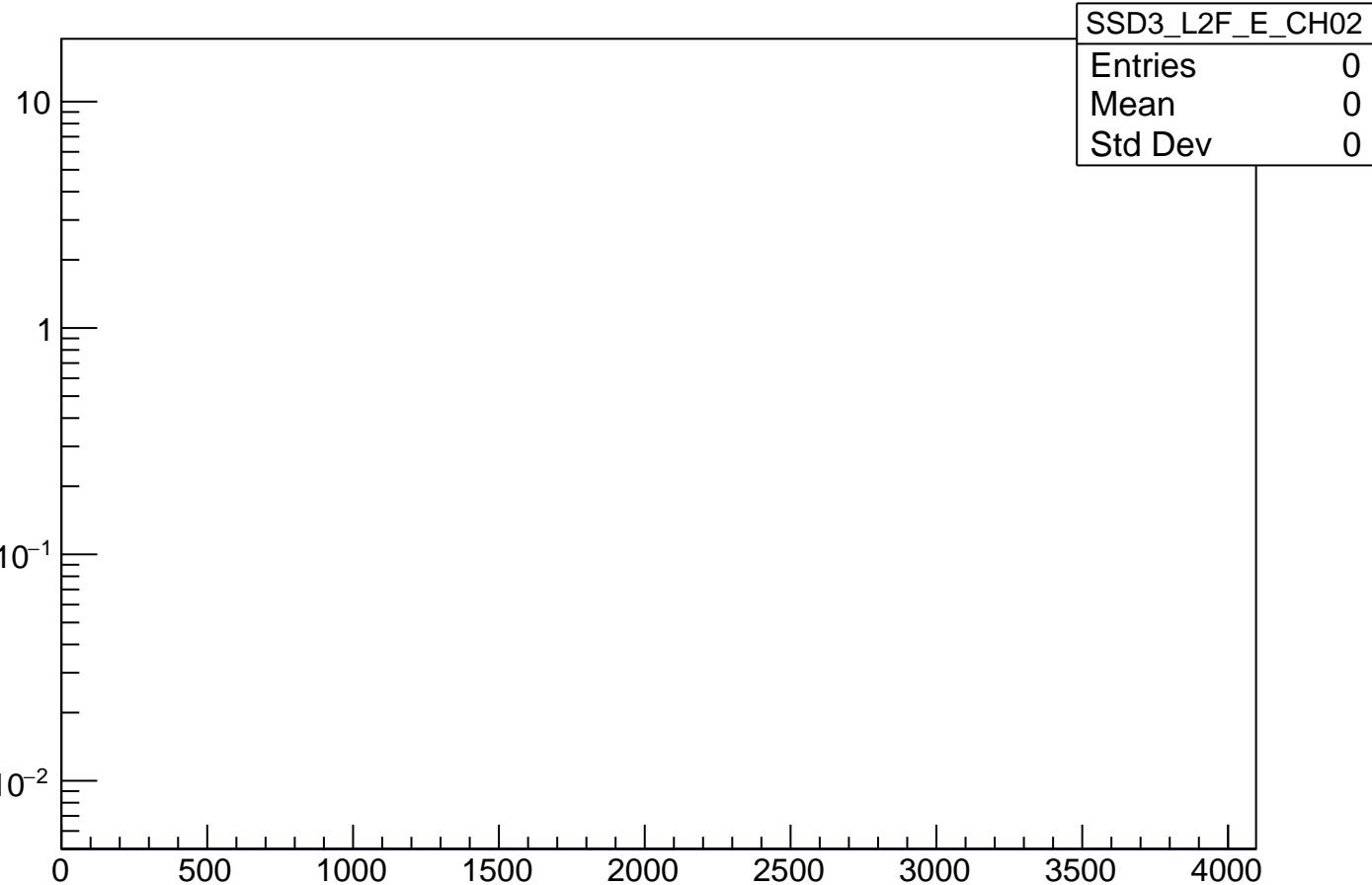
SSD3_L2F_E_CH00



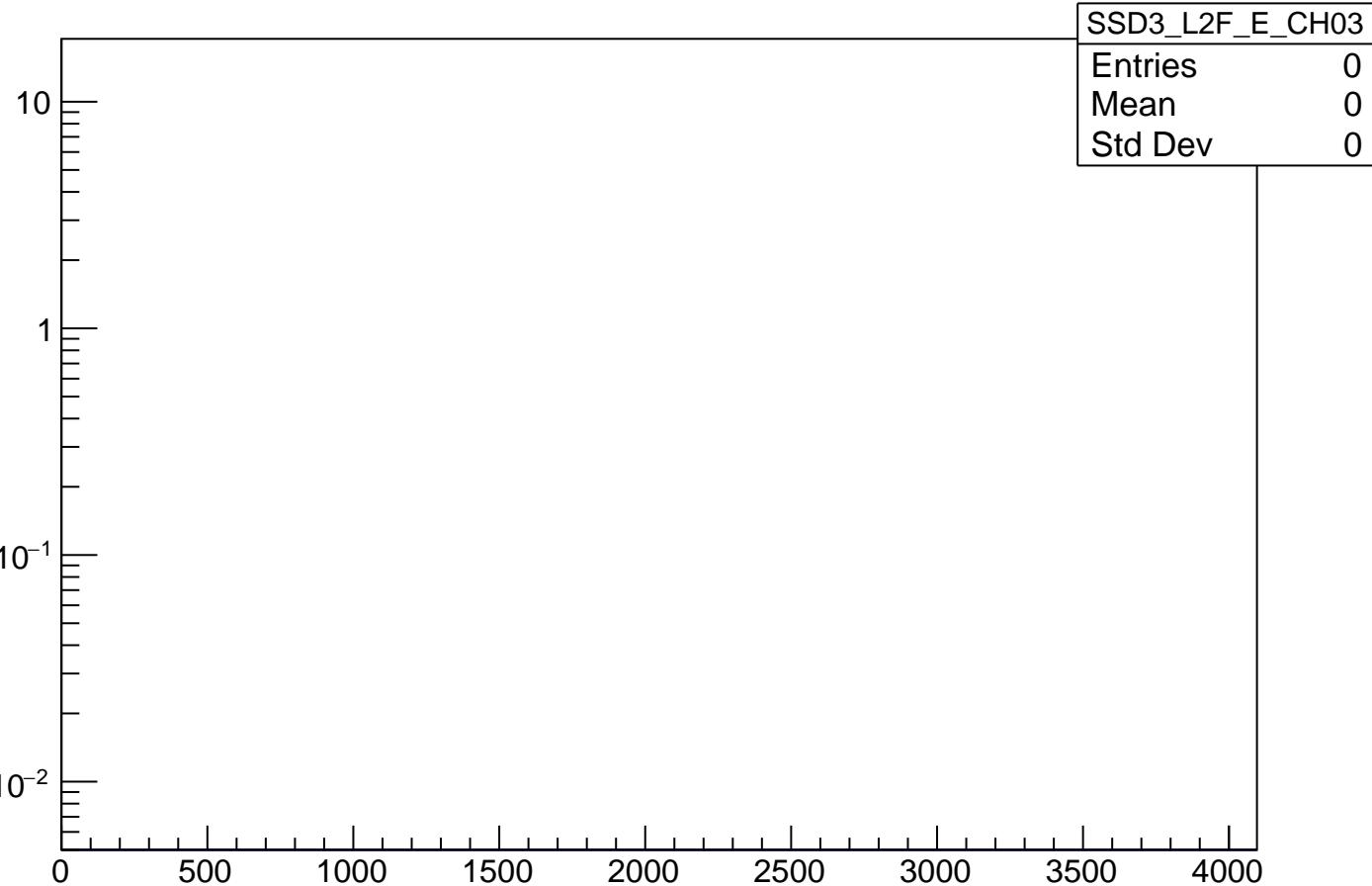
SSD3_L2F_E_CH01



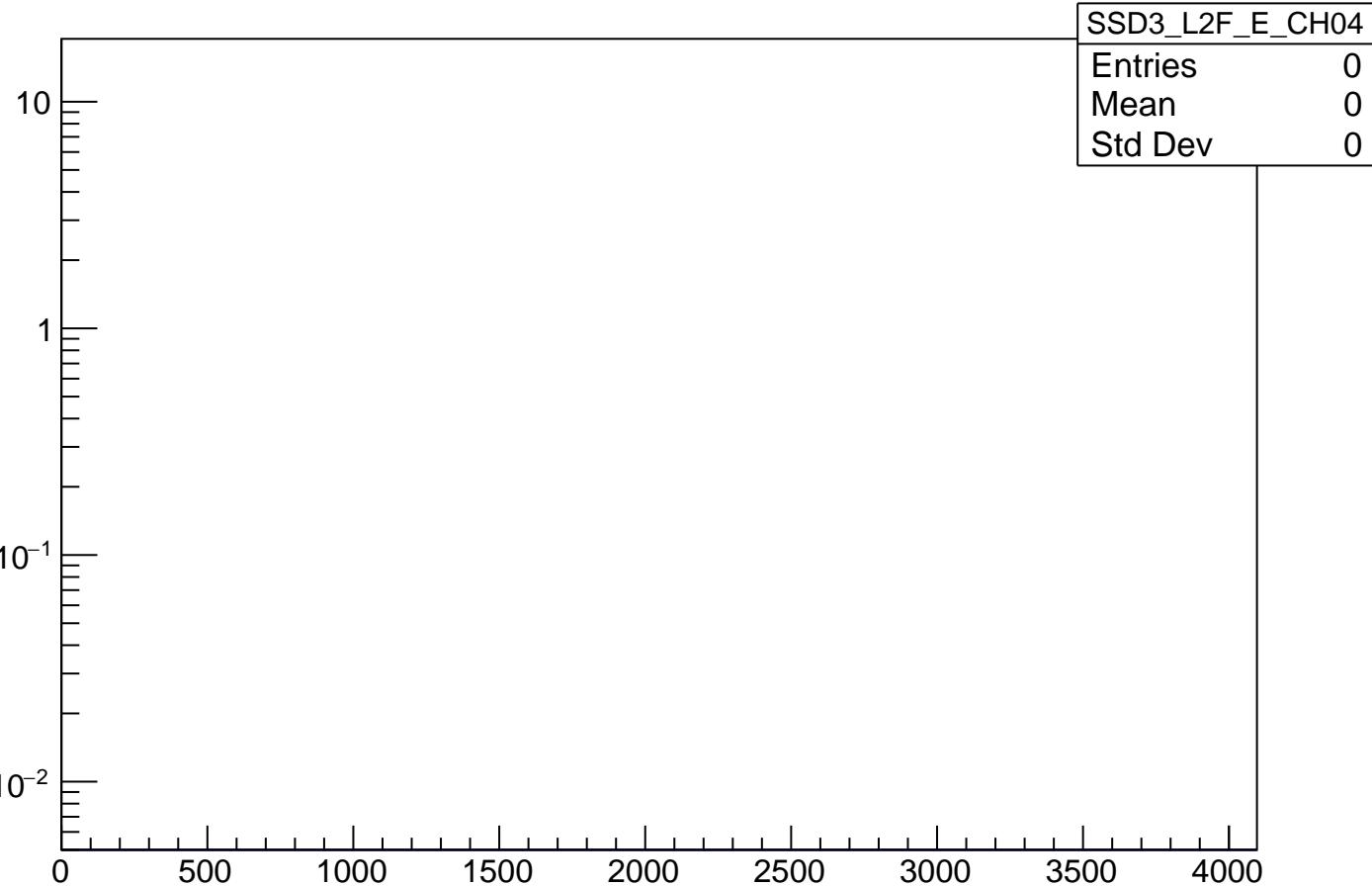
SSD3_L2F_E_CH02



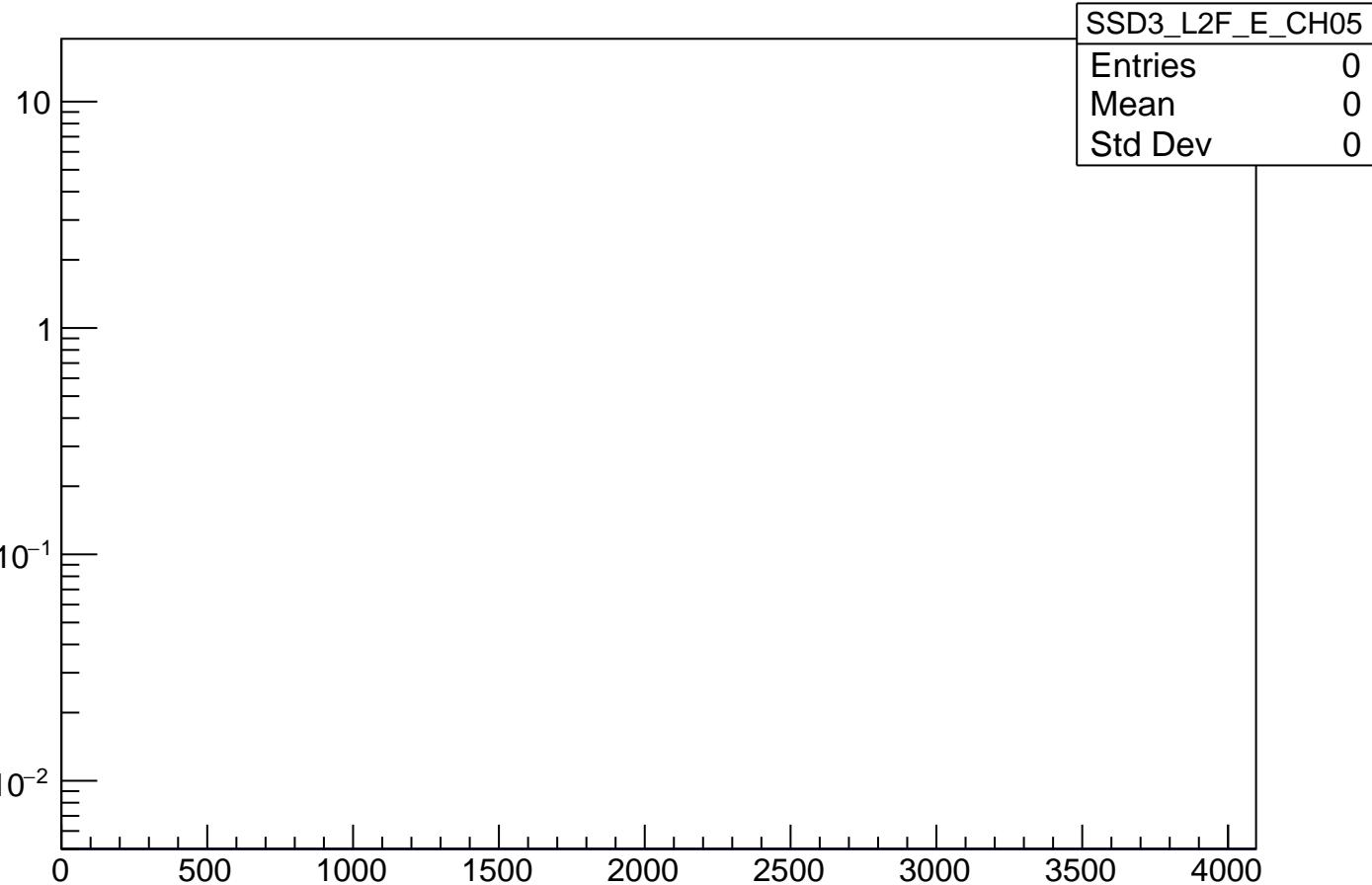
SSD3_L2F_E_CH03



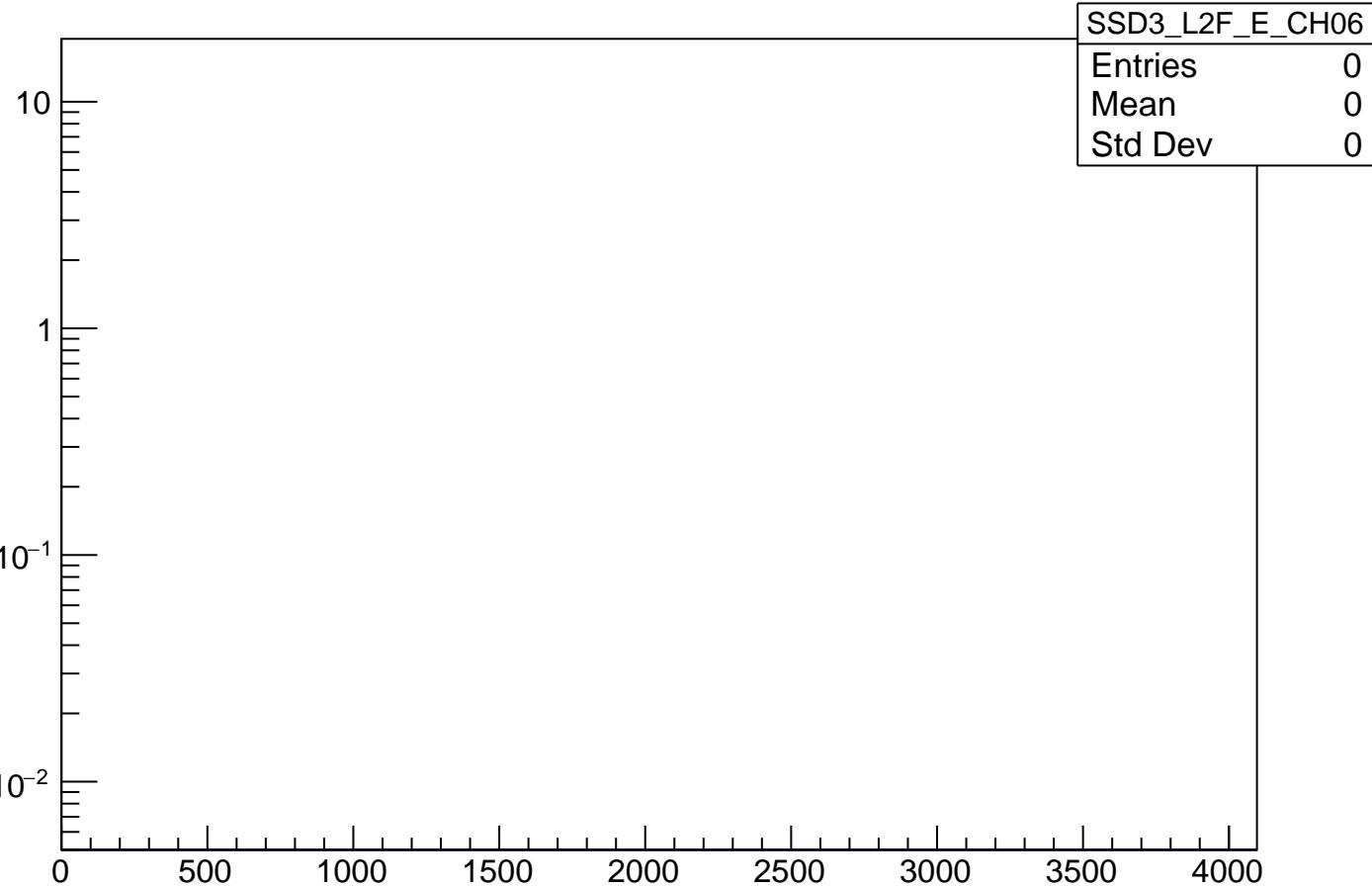
SSD3_L2F_E_CH04



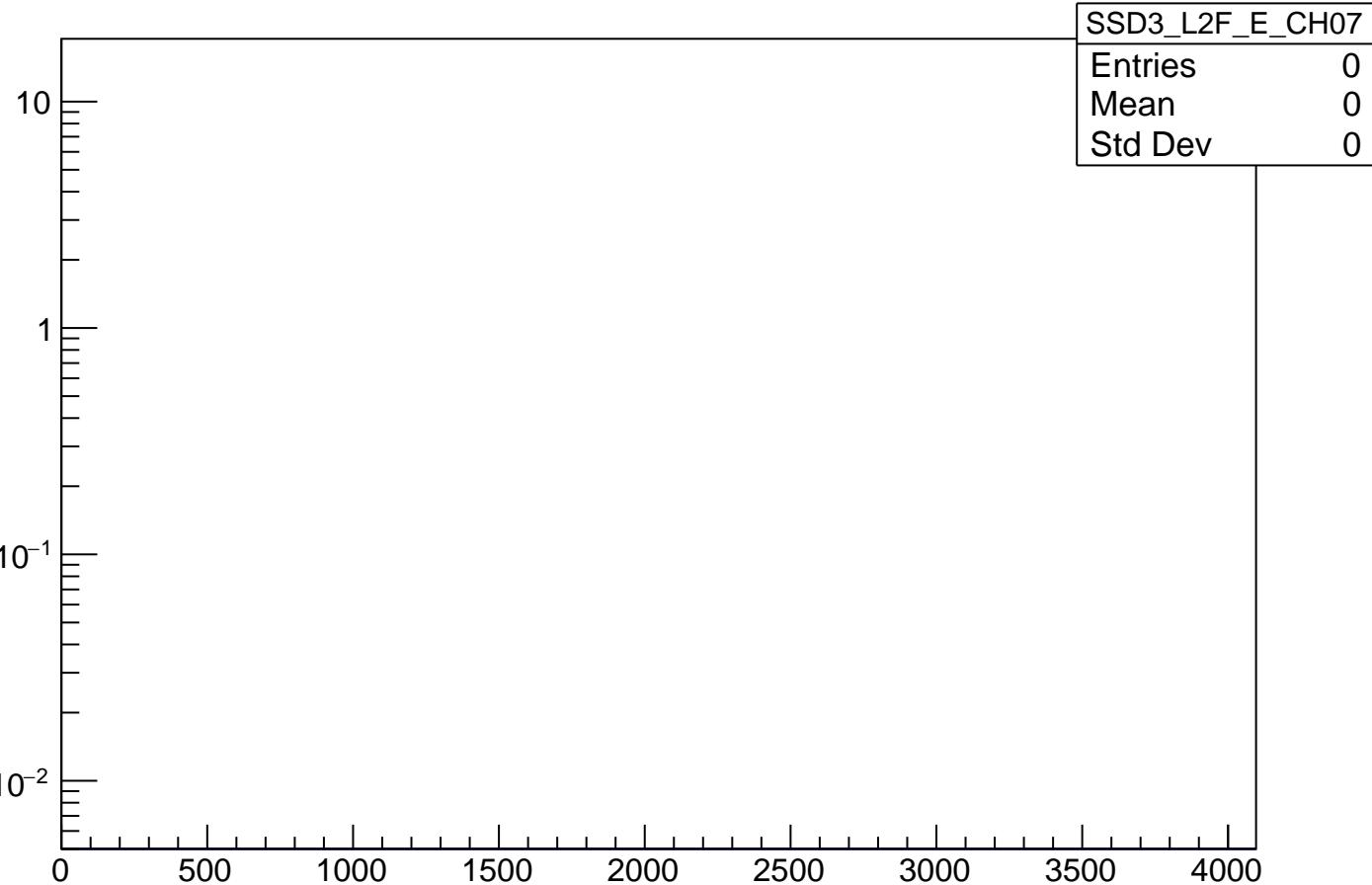
SSD3_L2F_E_CH05



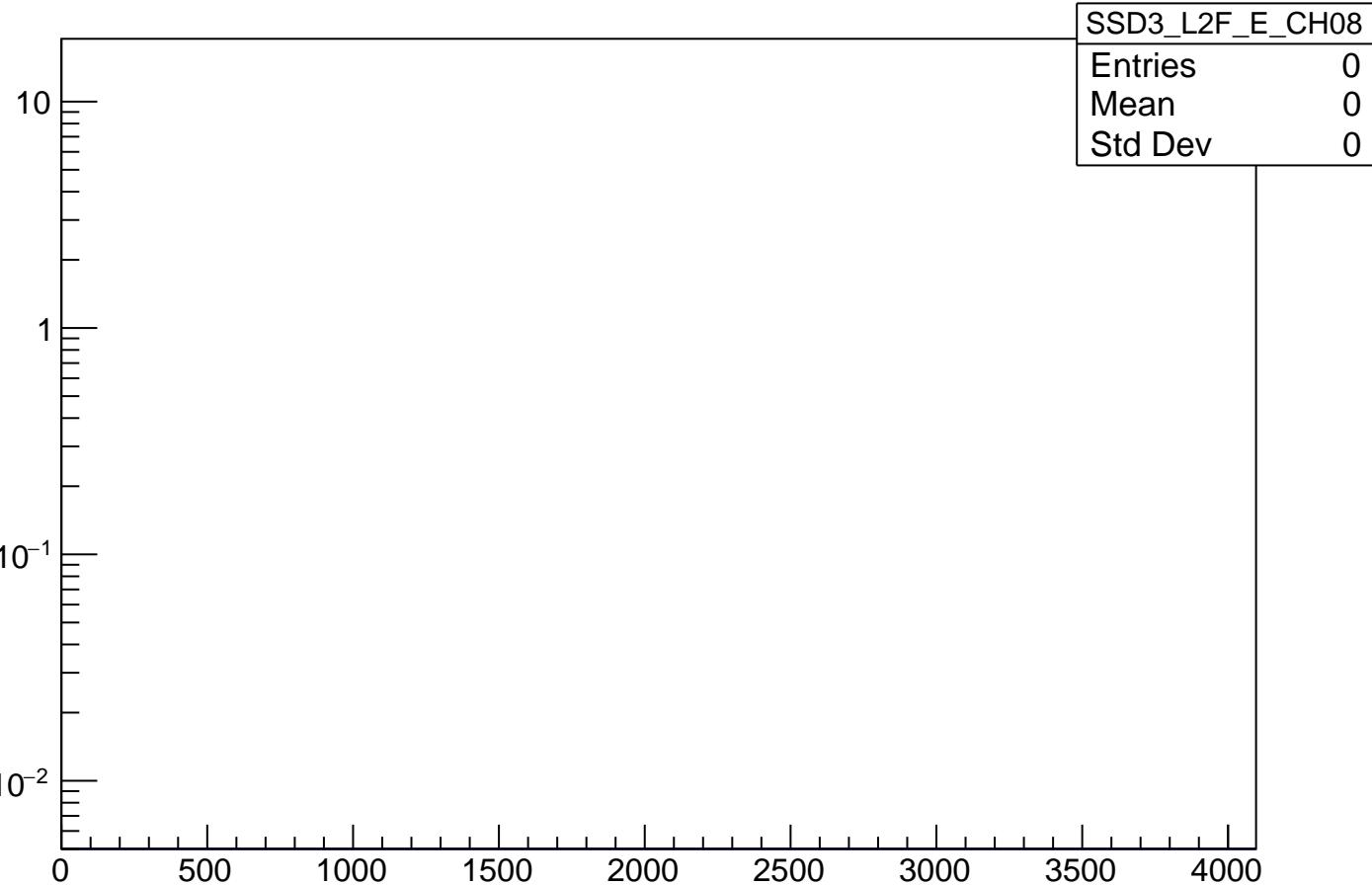
SSD3_L2F_E_CH06



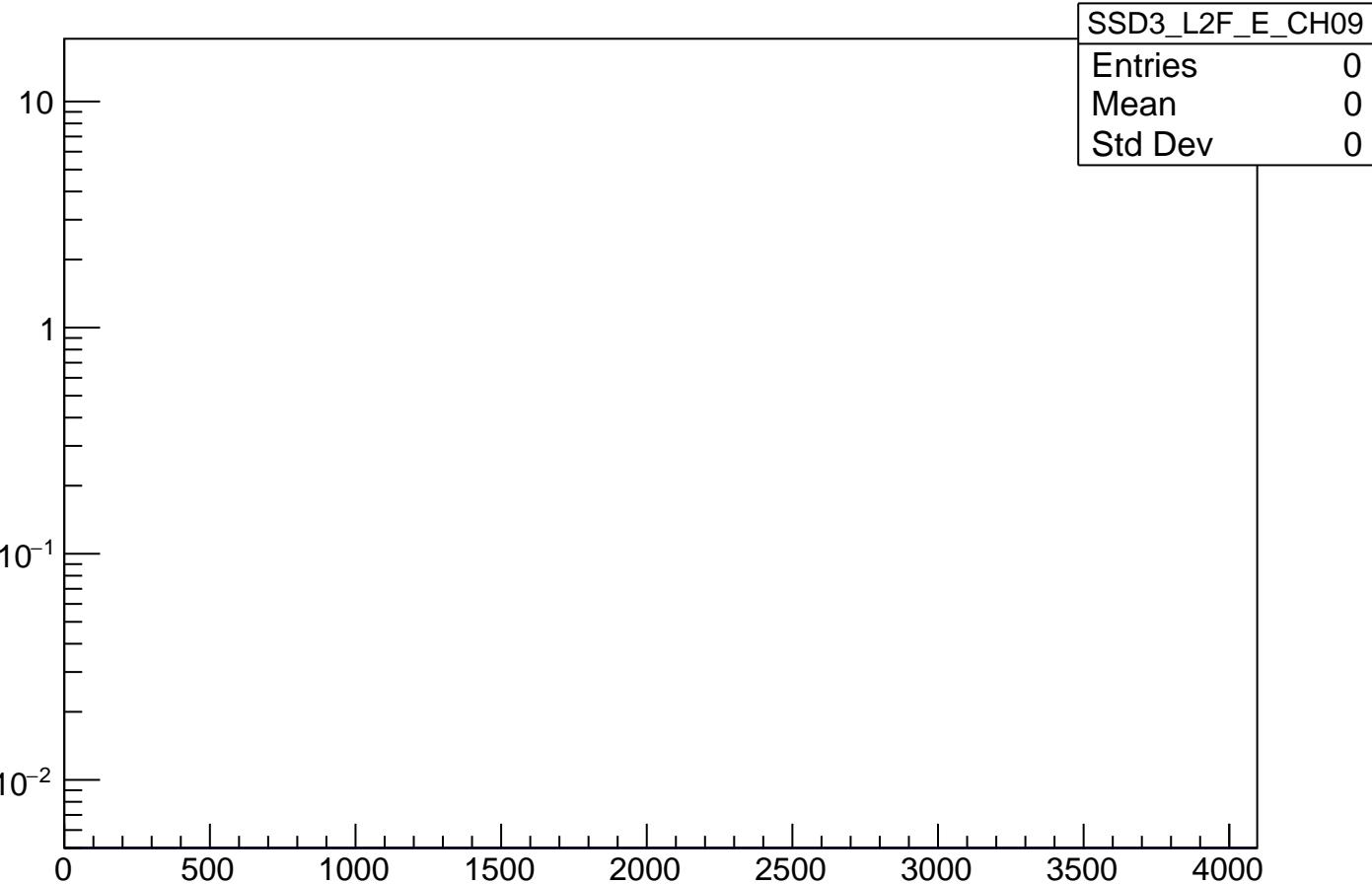
SSD3_L2F_E_CH07



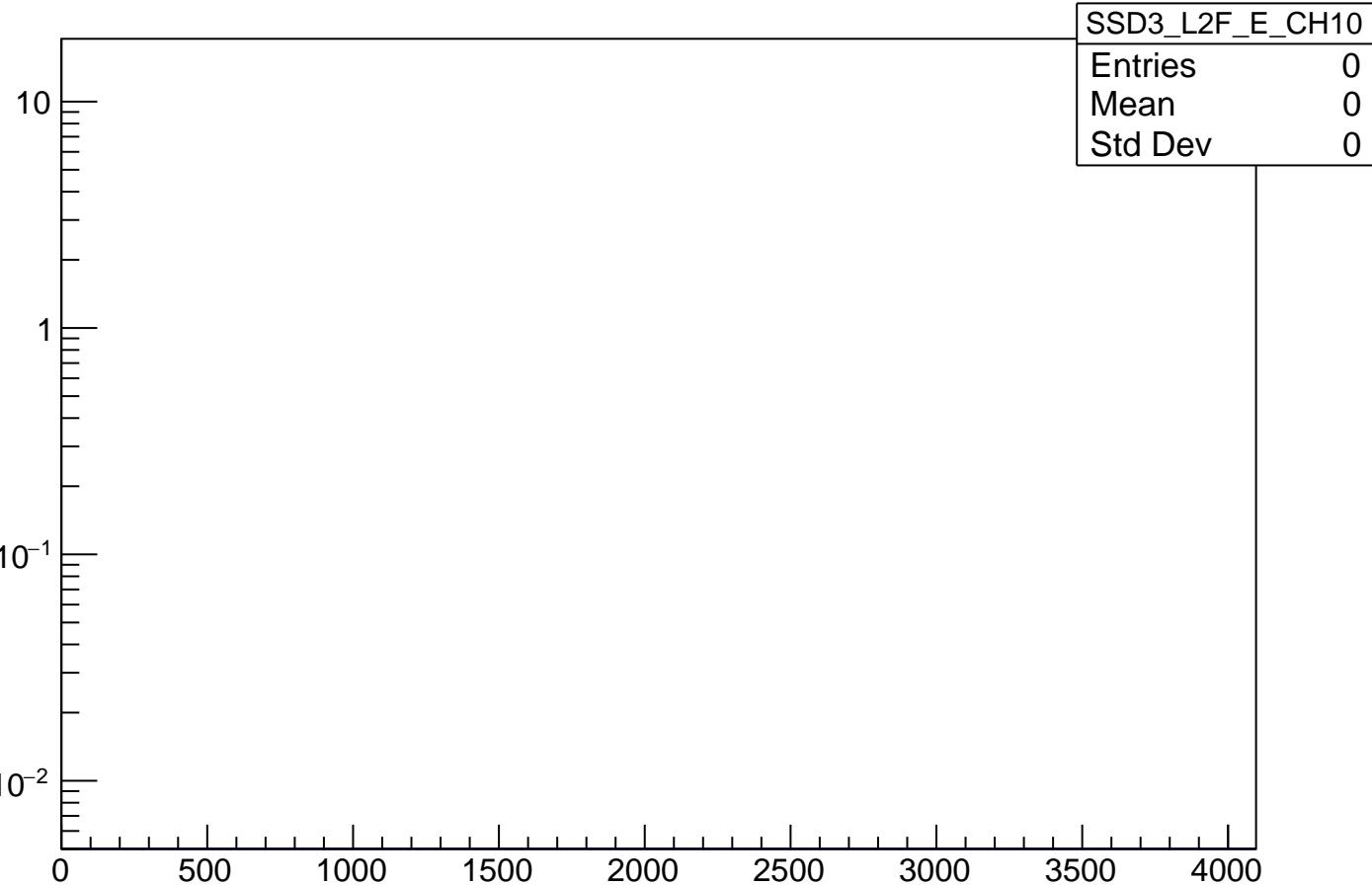
SSD3_L2F_E_CH08



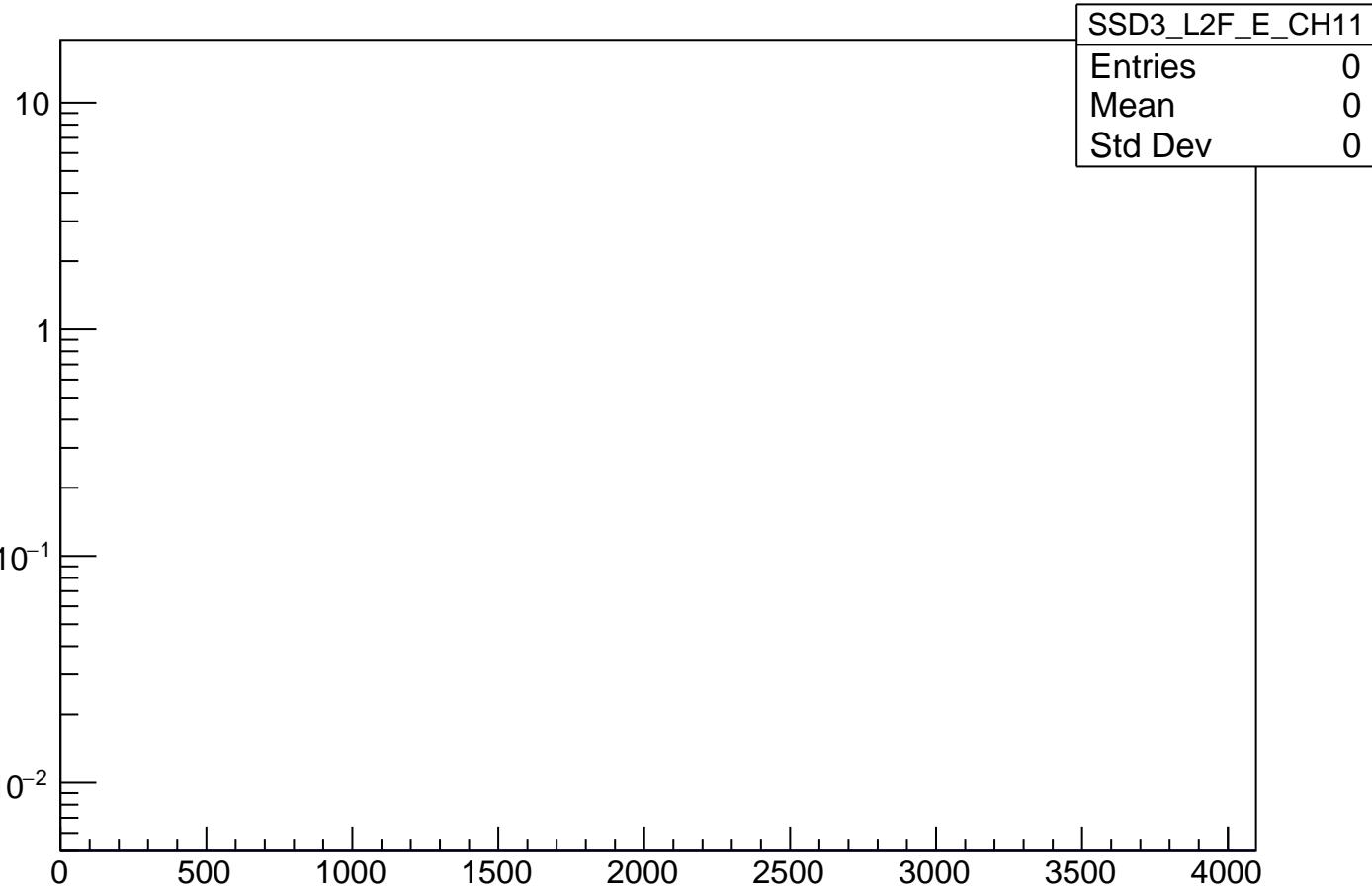
SSD3_L2F_E_CH09



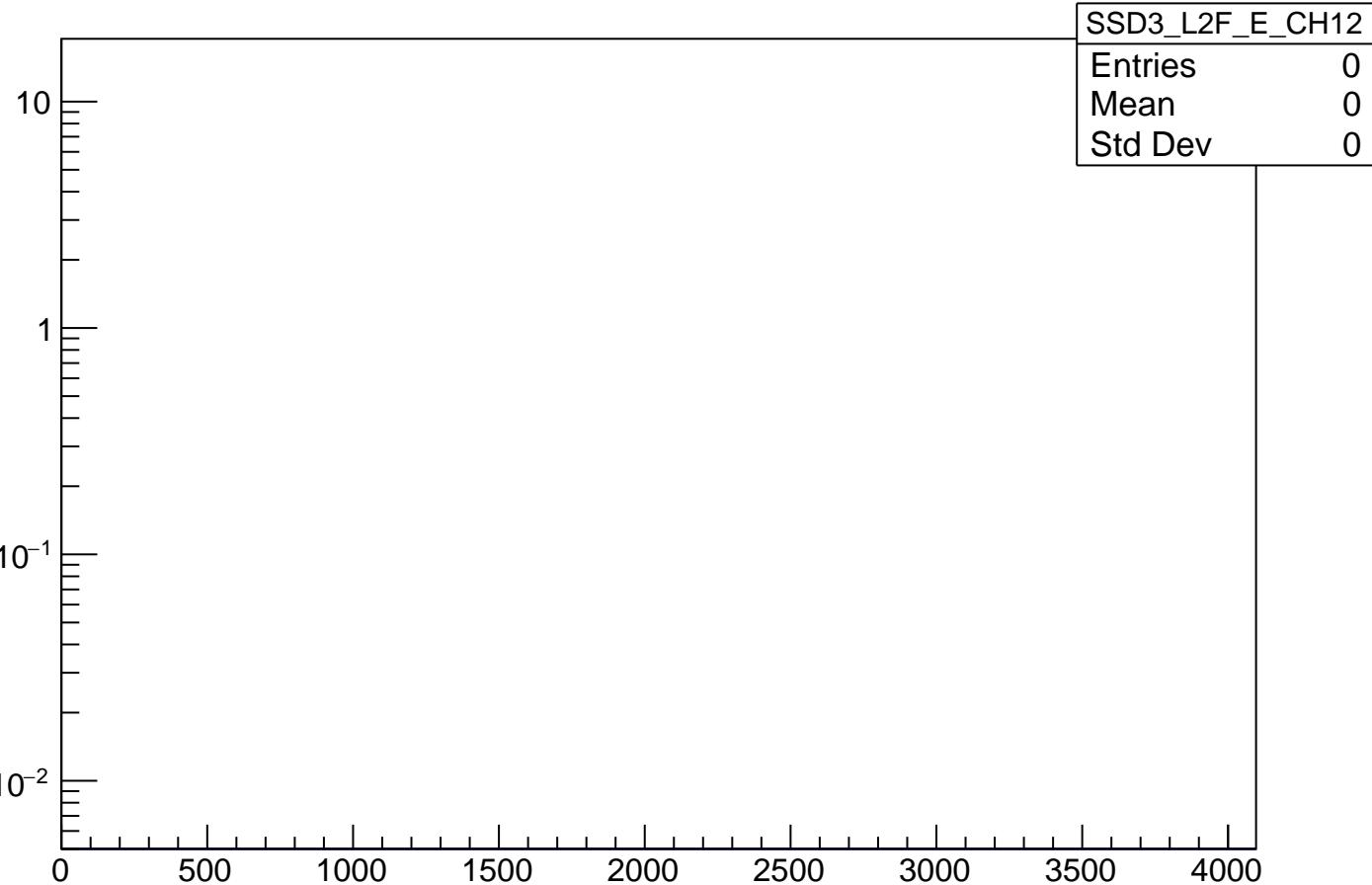
SSD3_L2F_E_CH10



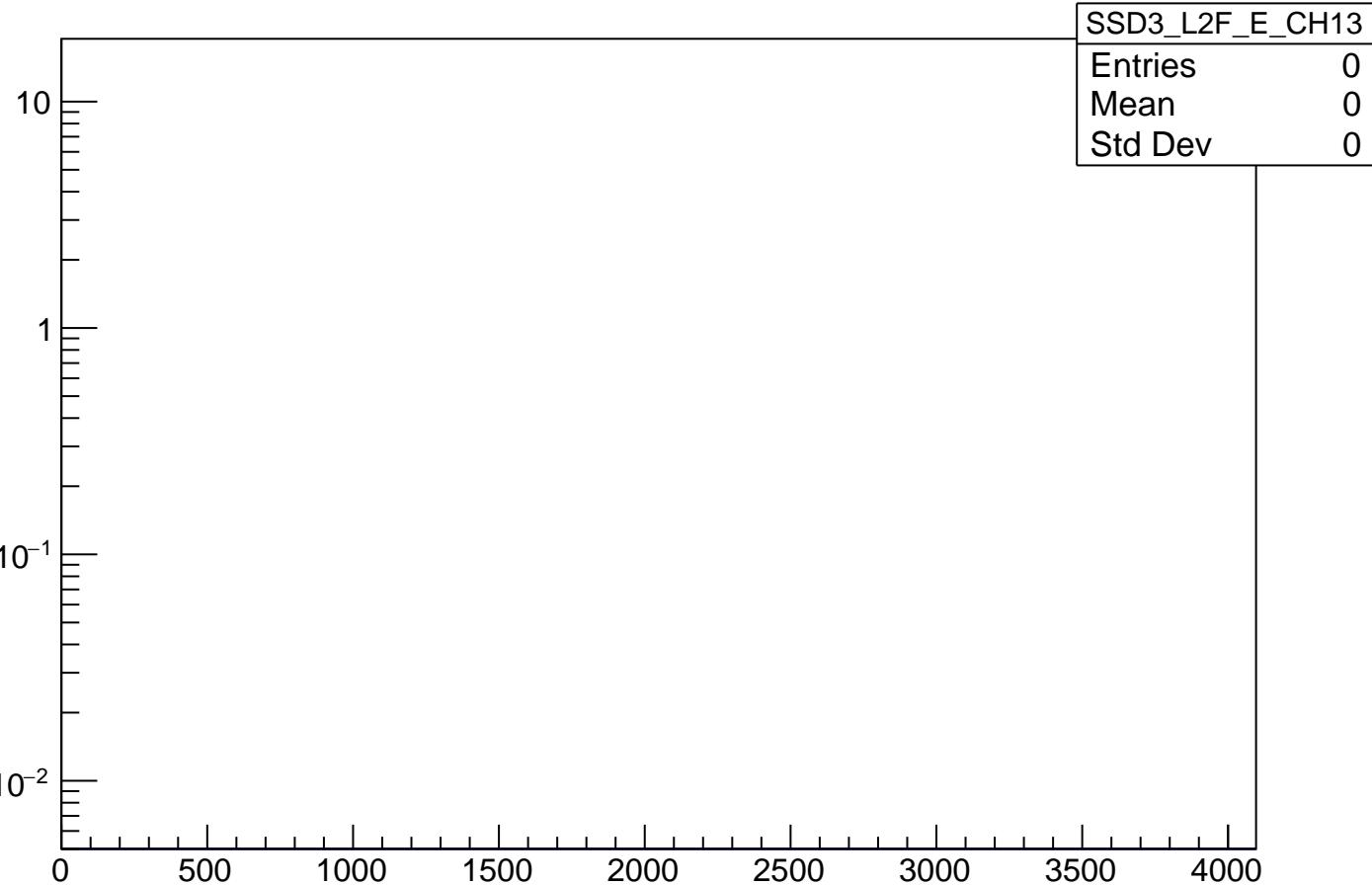
SSD3_L2F_E_CH11



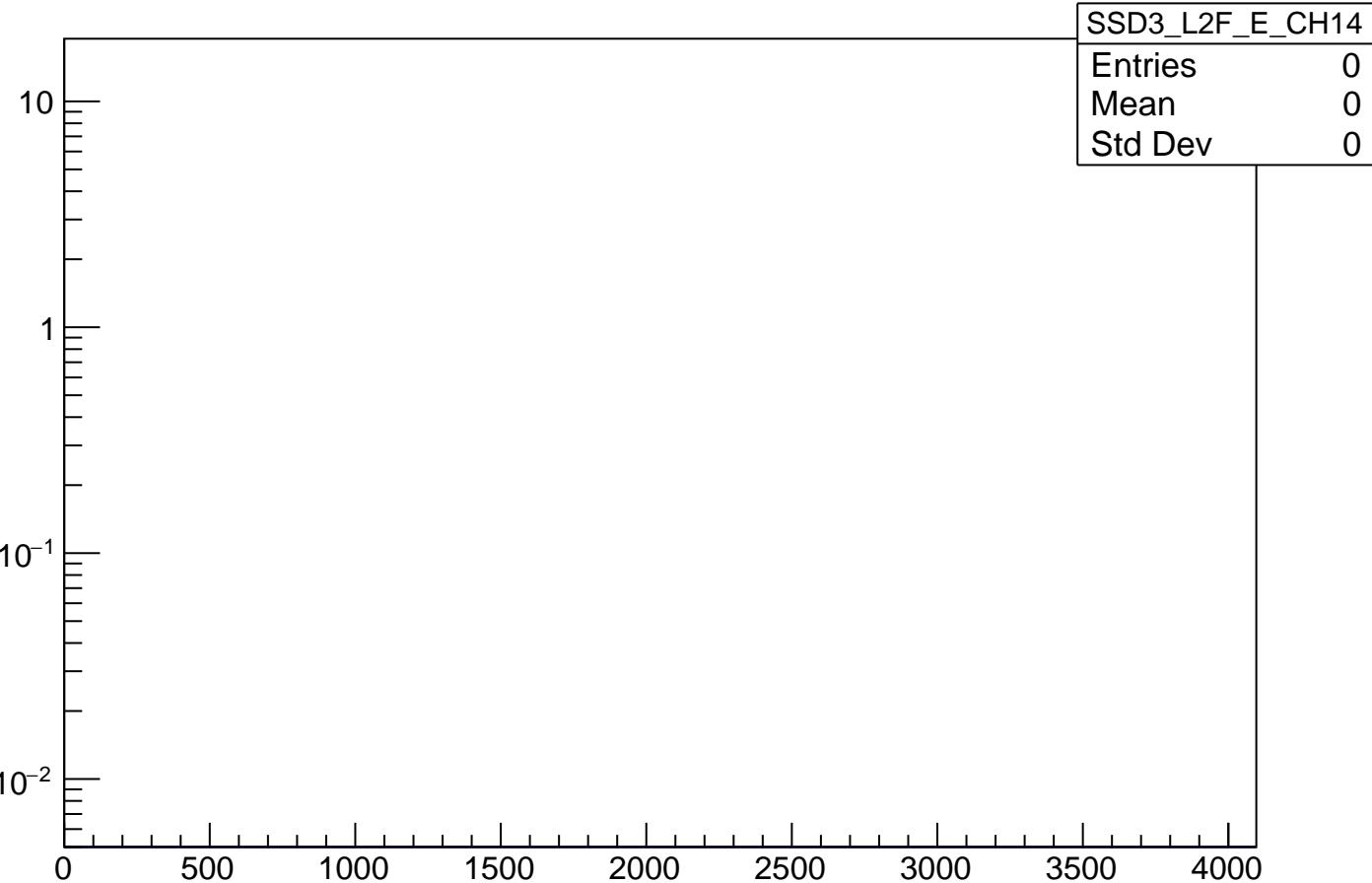
SSD3_L2F_E_CH12



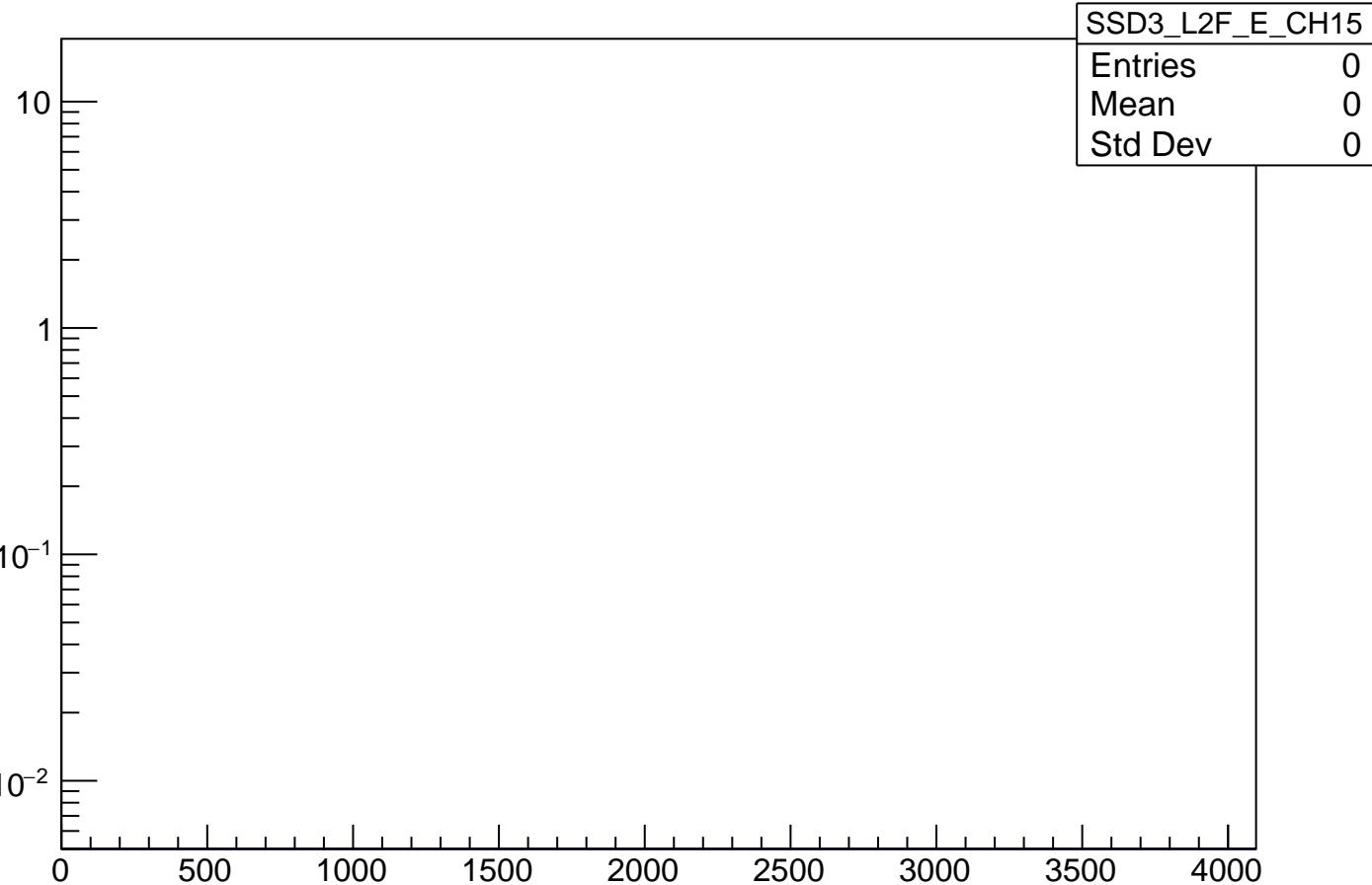
SSD3_L2F_E_CH13



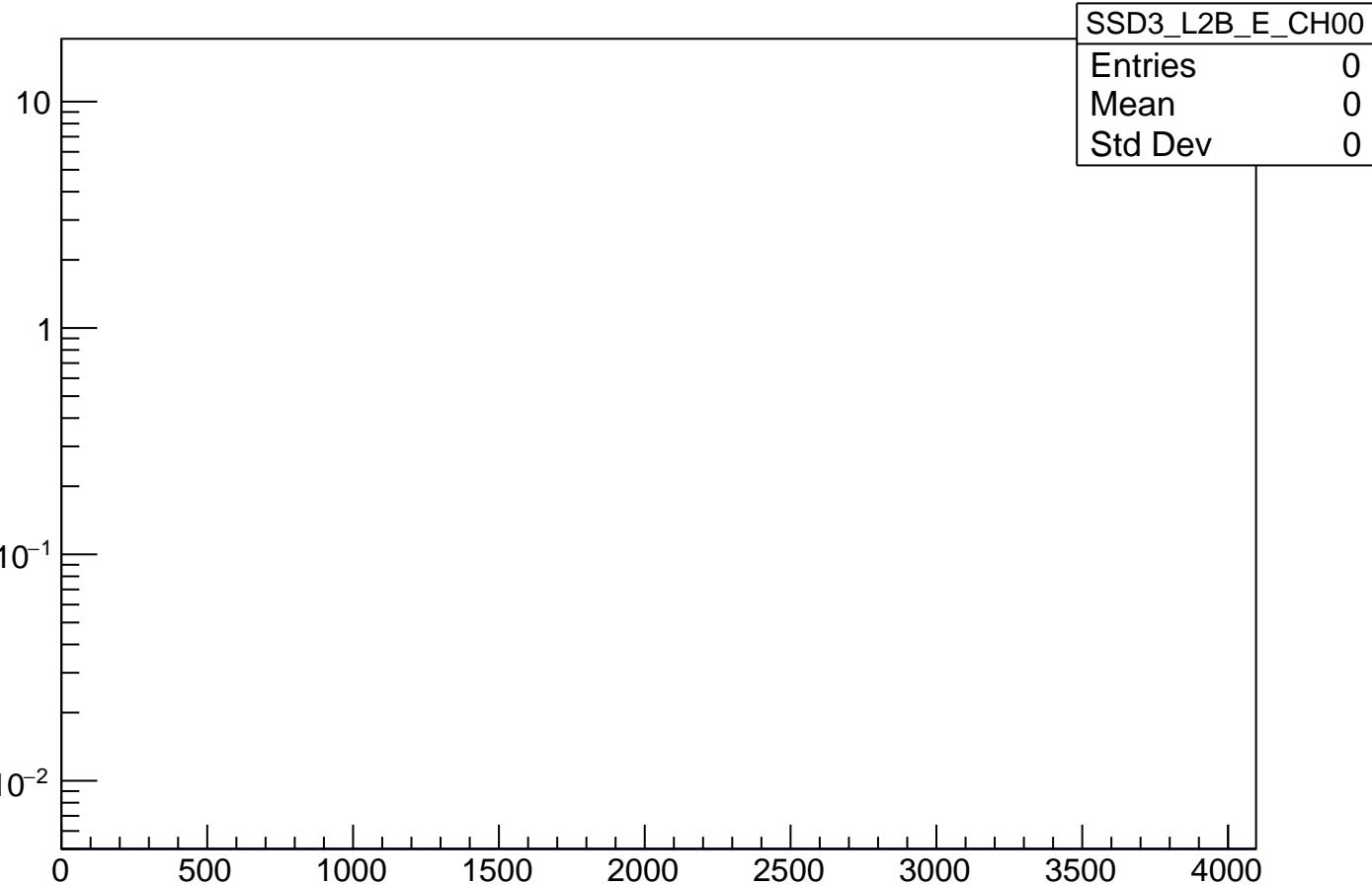
SSD3_L2F_E_CH14



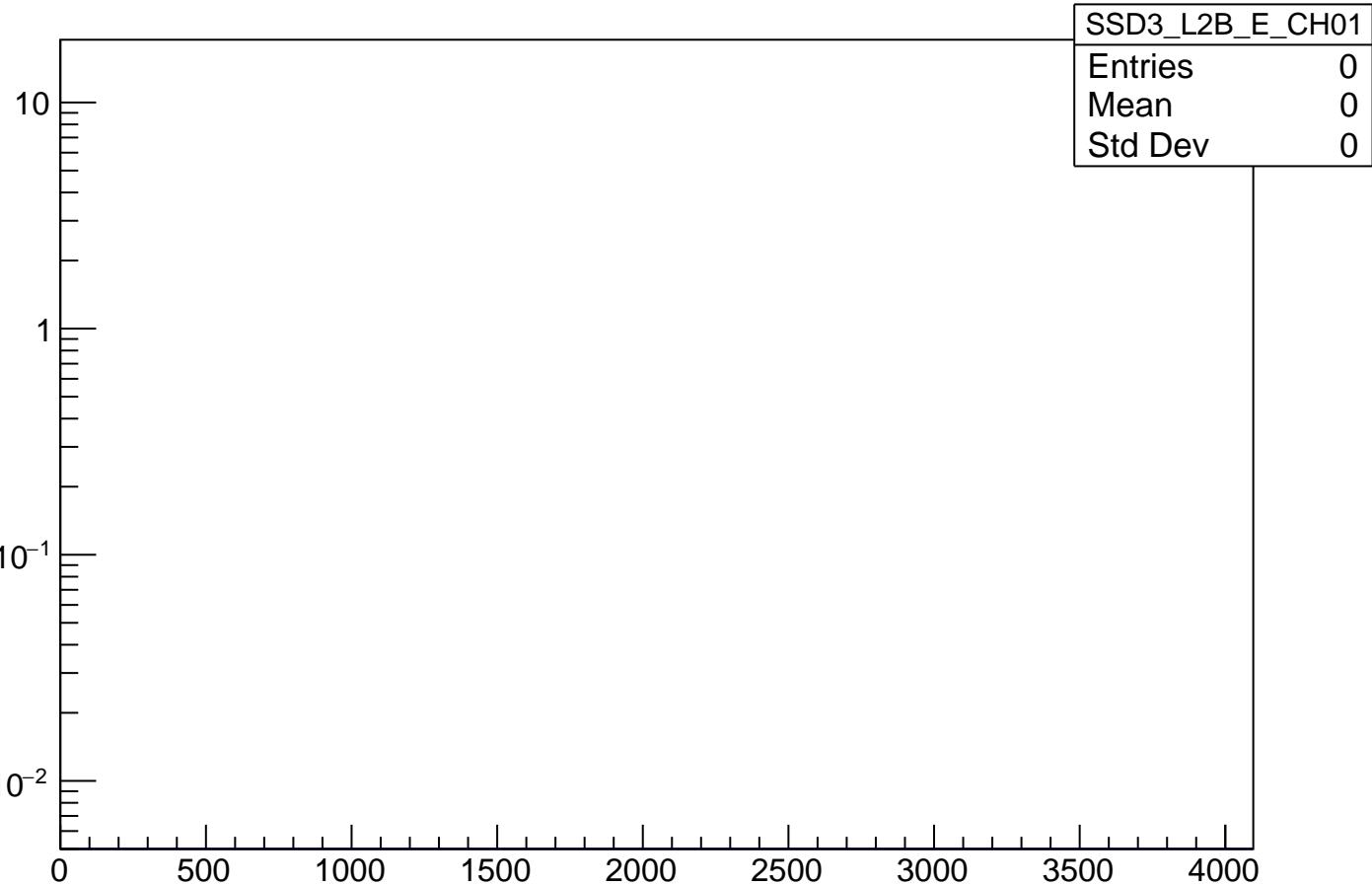
SSD3_L2F_E_CH15



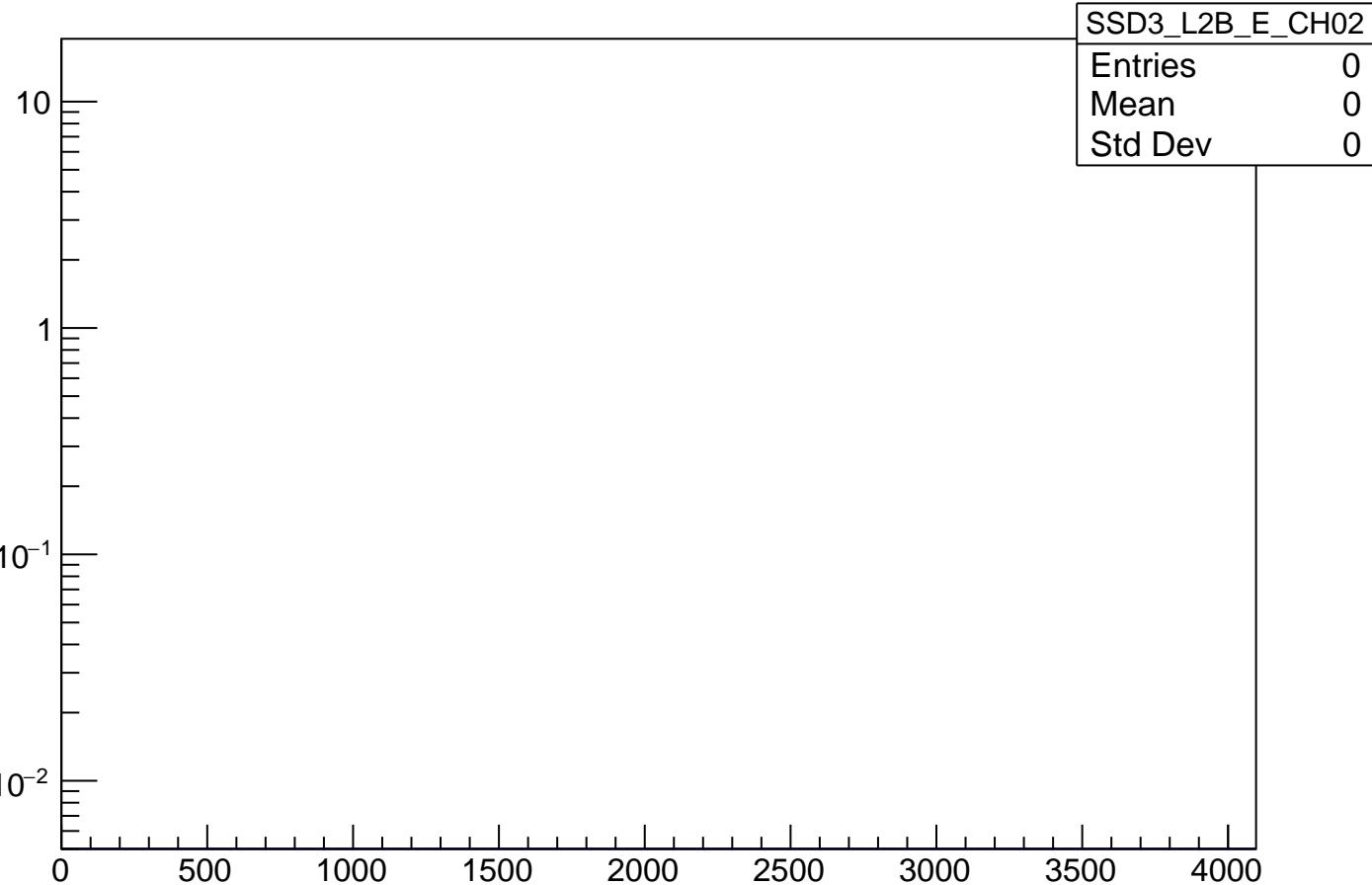
SSD3_L2B_E_CH00



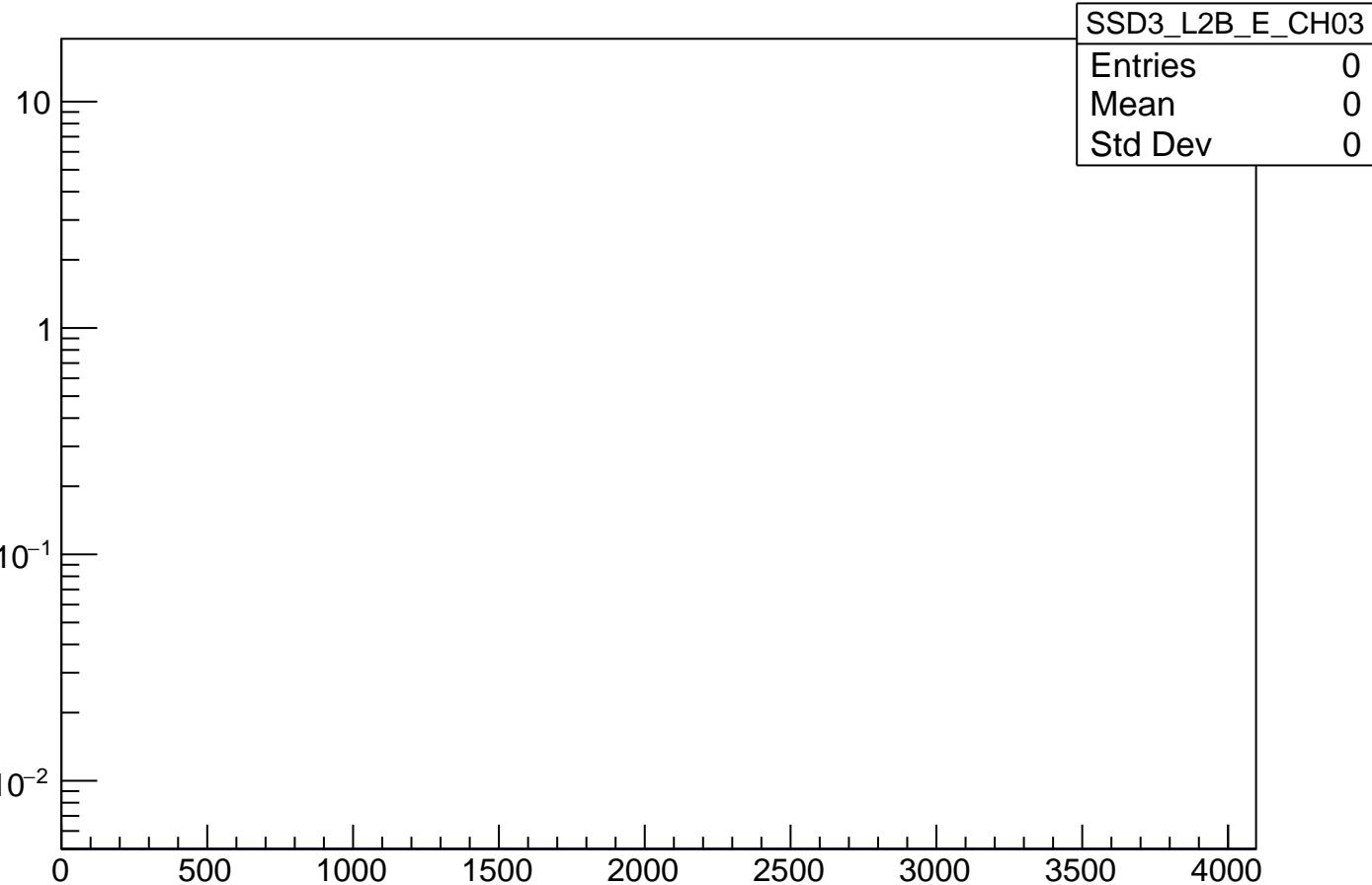
SSD3_L2B_E_CH01



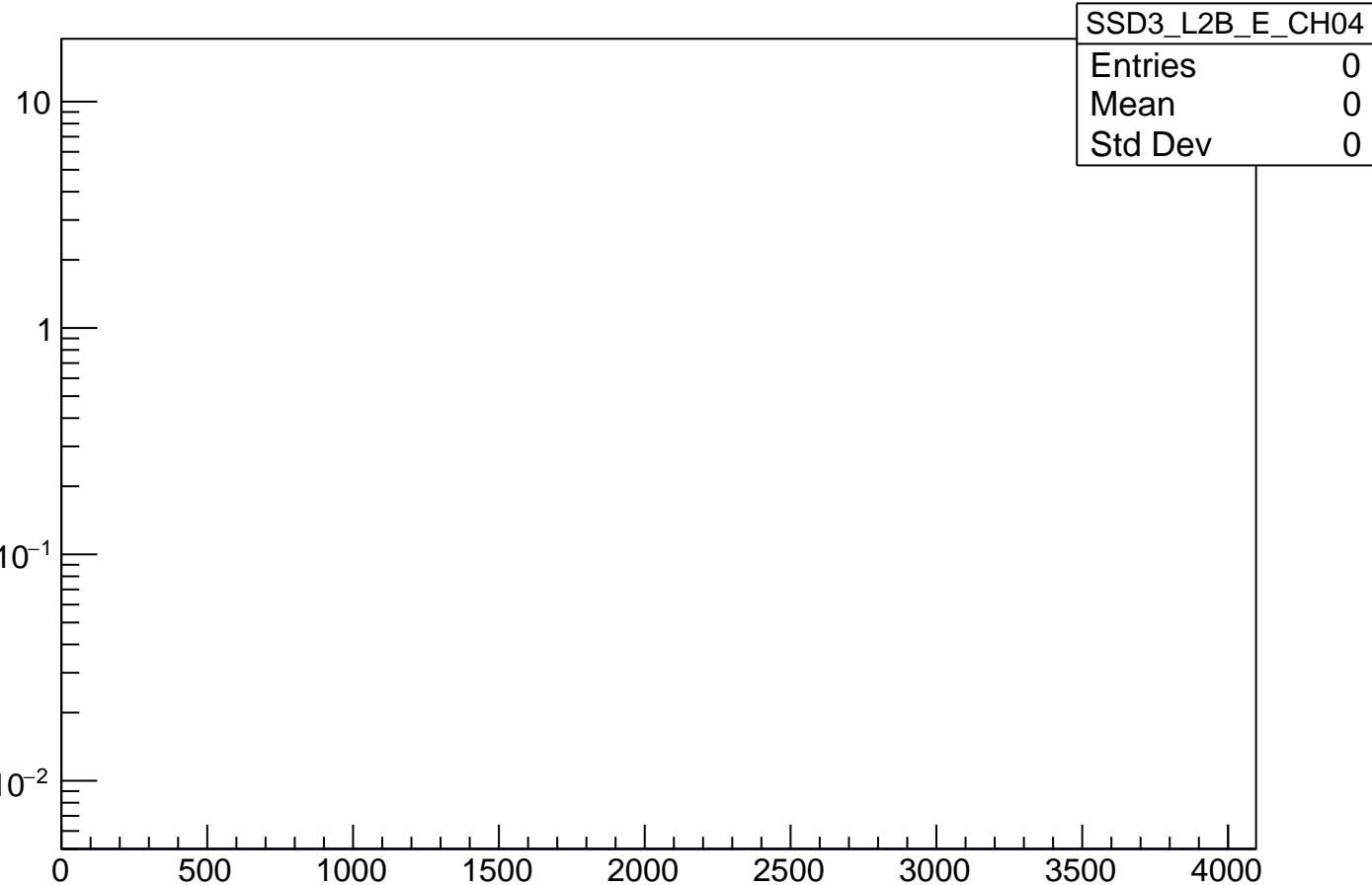
SSD3_L2B_E_CH02



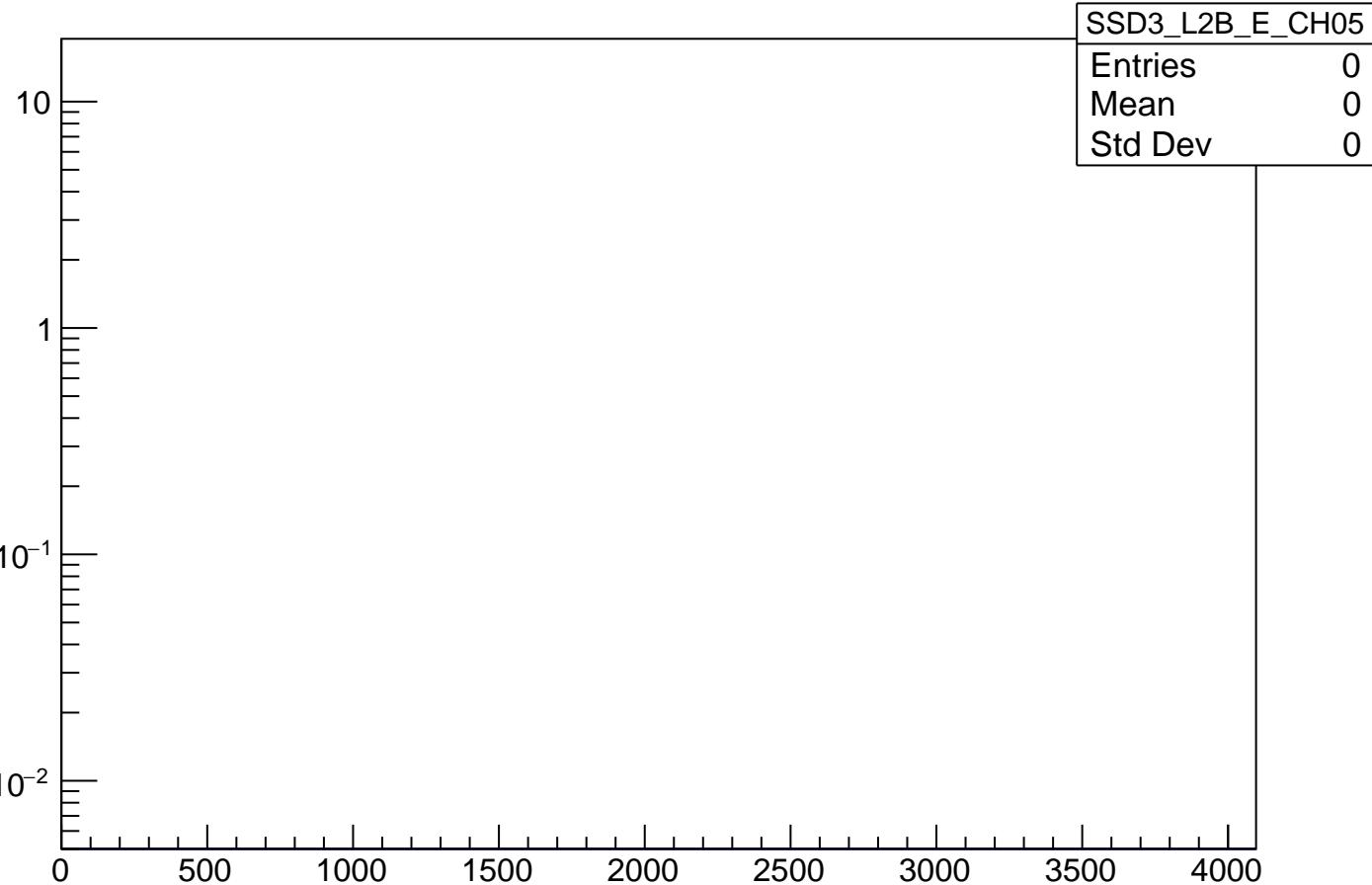
SSD3_L2B_E_CH03



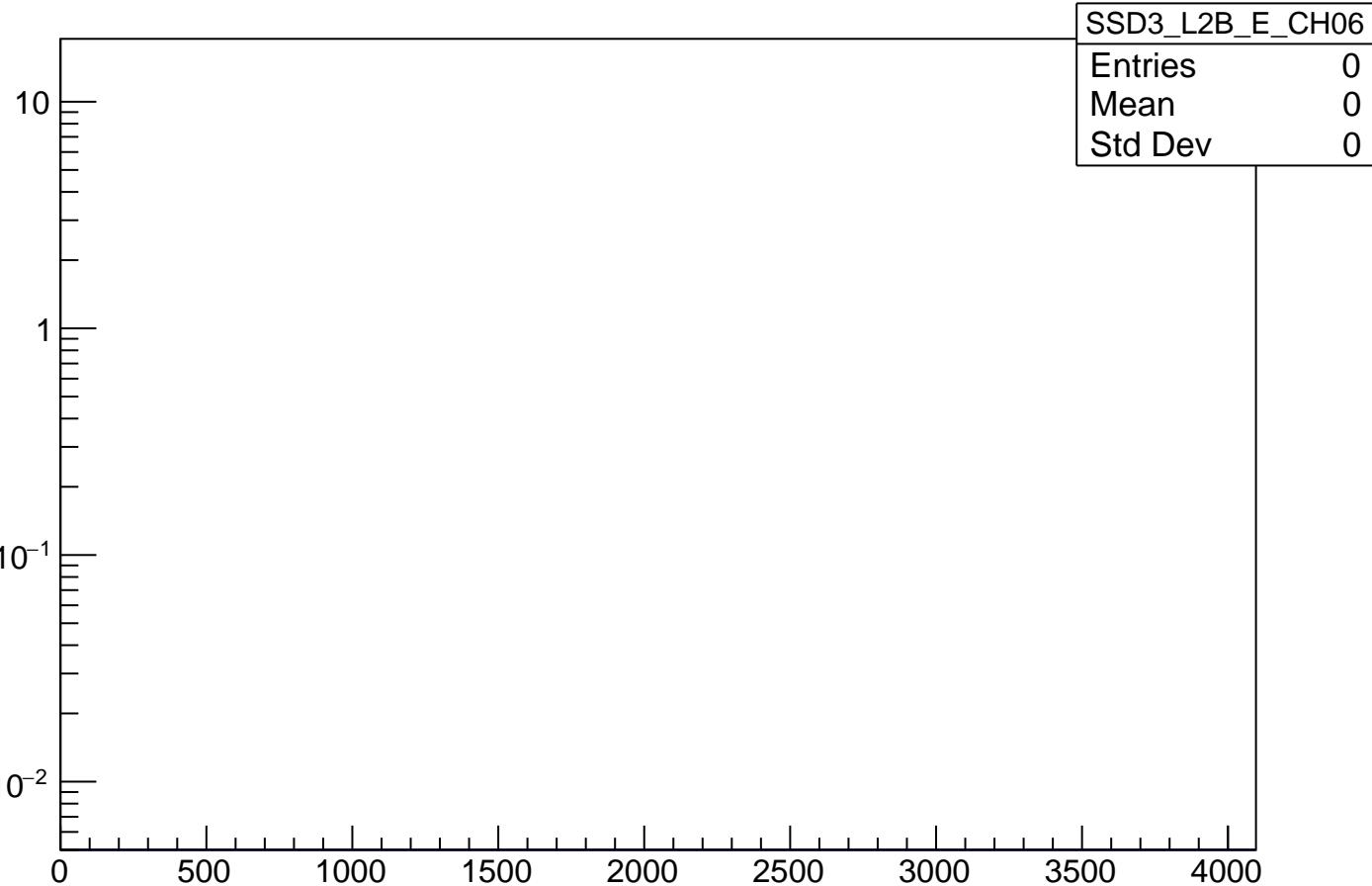
SSD3_L2B_E_CH04



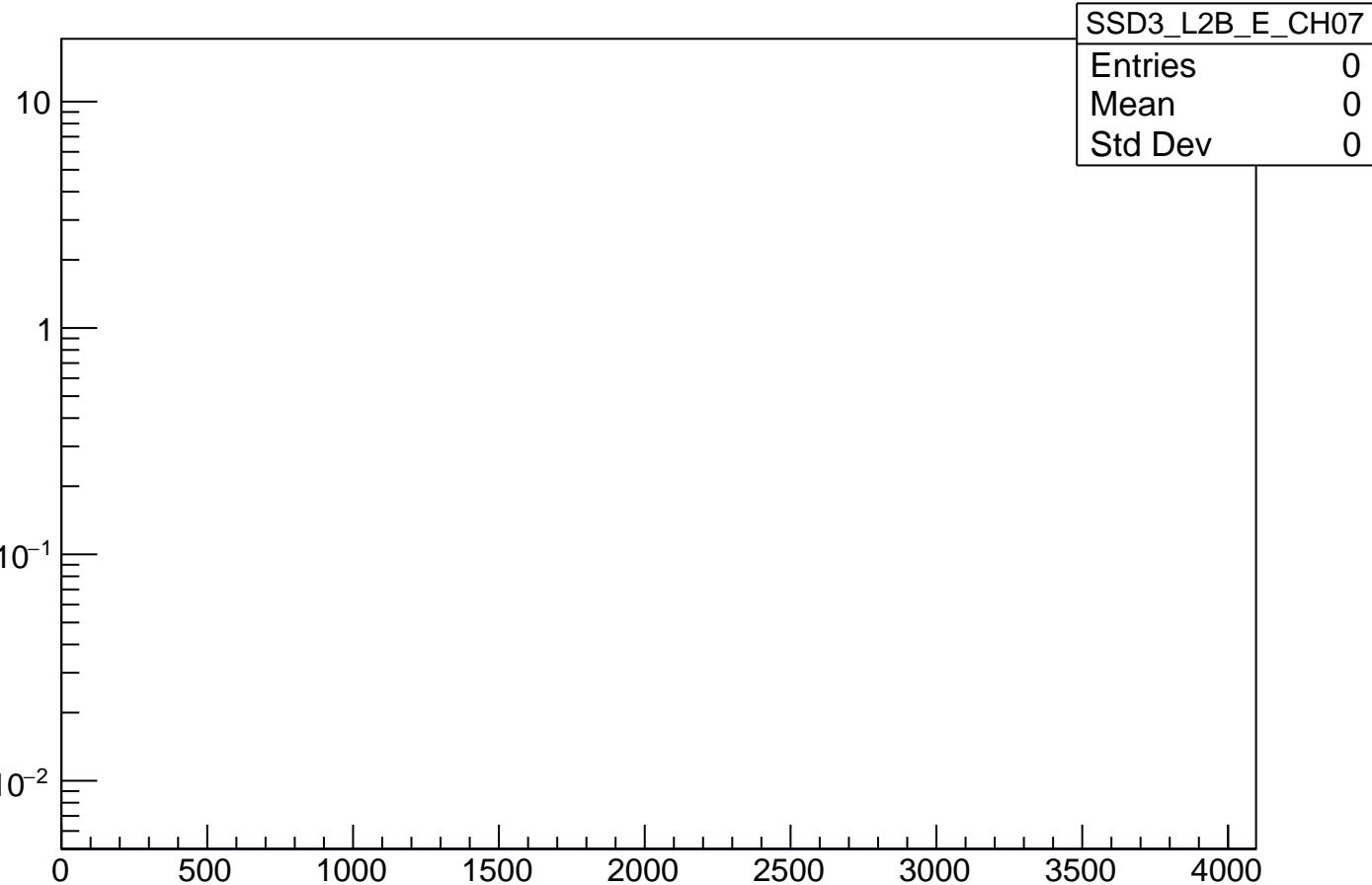
SSD3_L2B_E_CH05



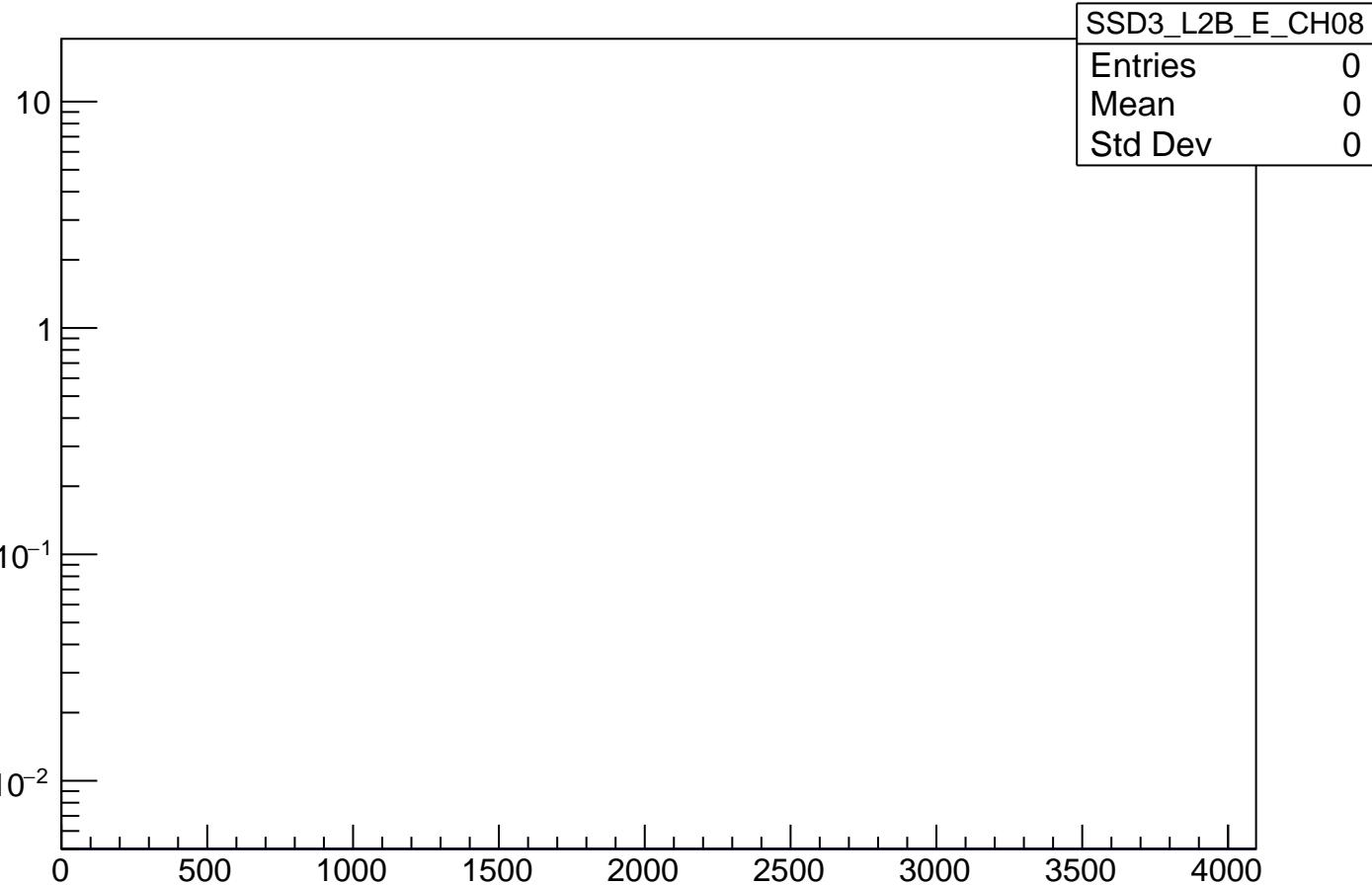
SSD3_L2B_E_CH06



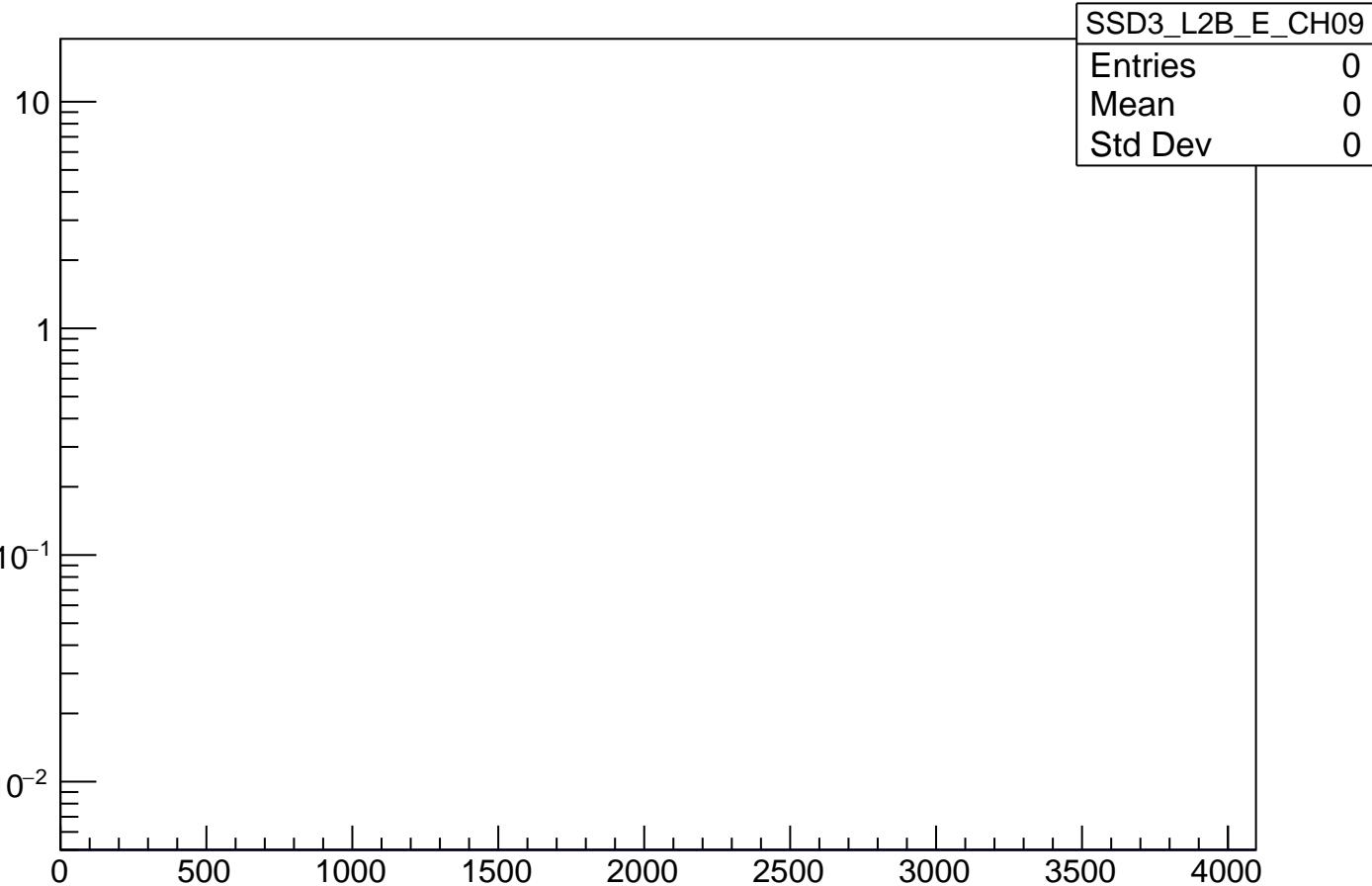
SSD3_L2B_E_CH07



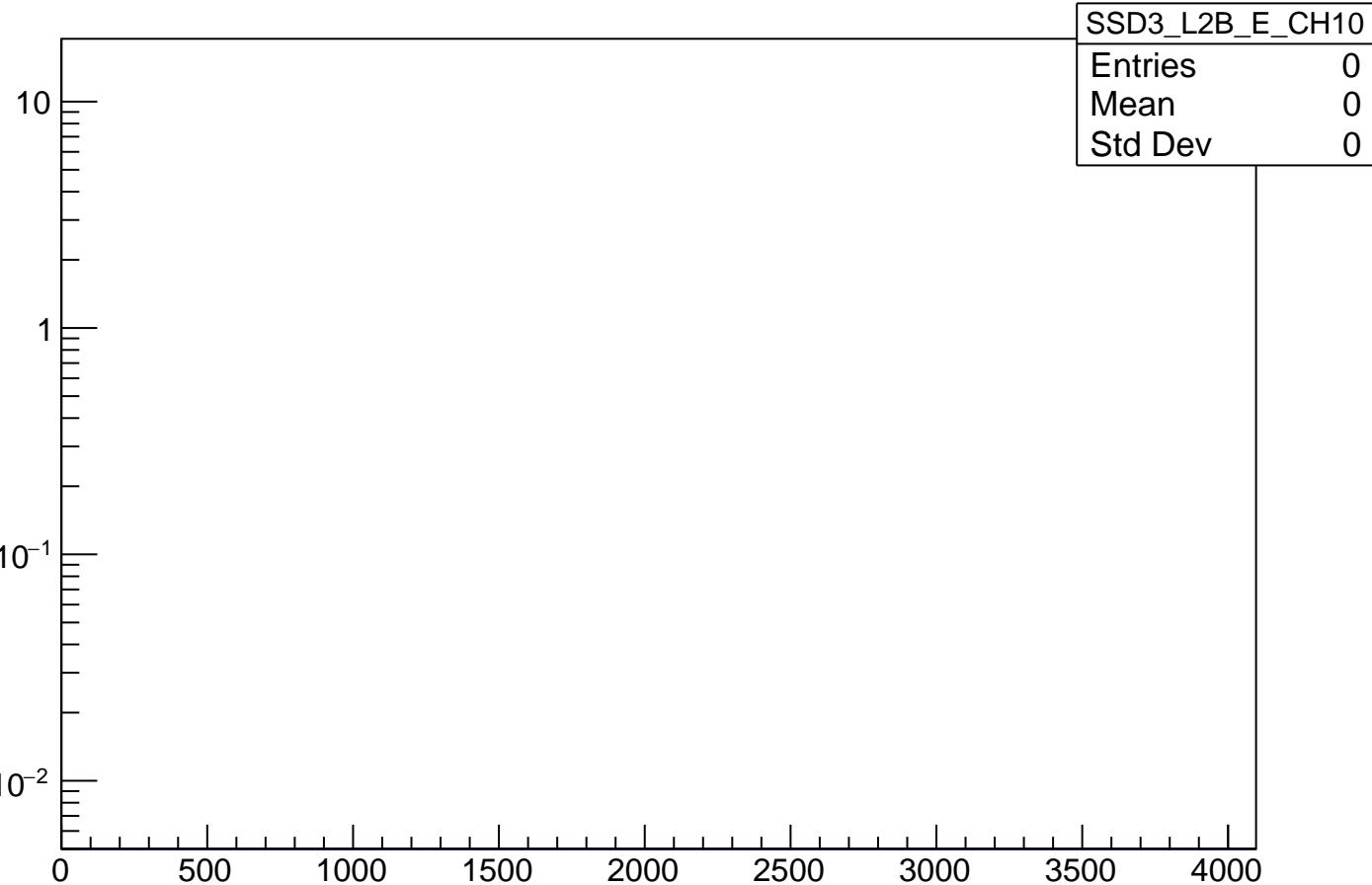
SSD3_L2B_E_CH08



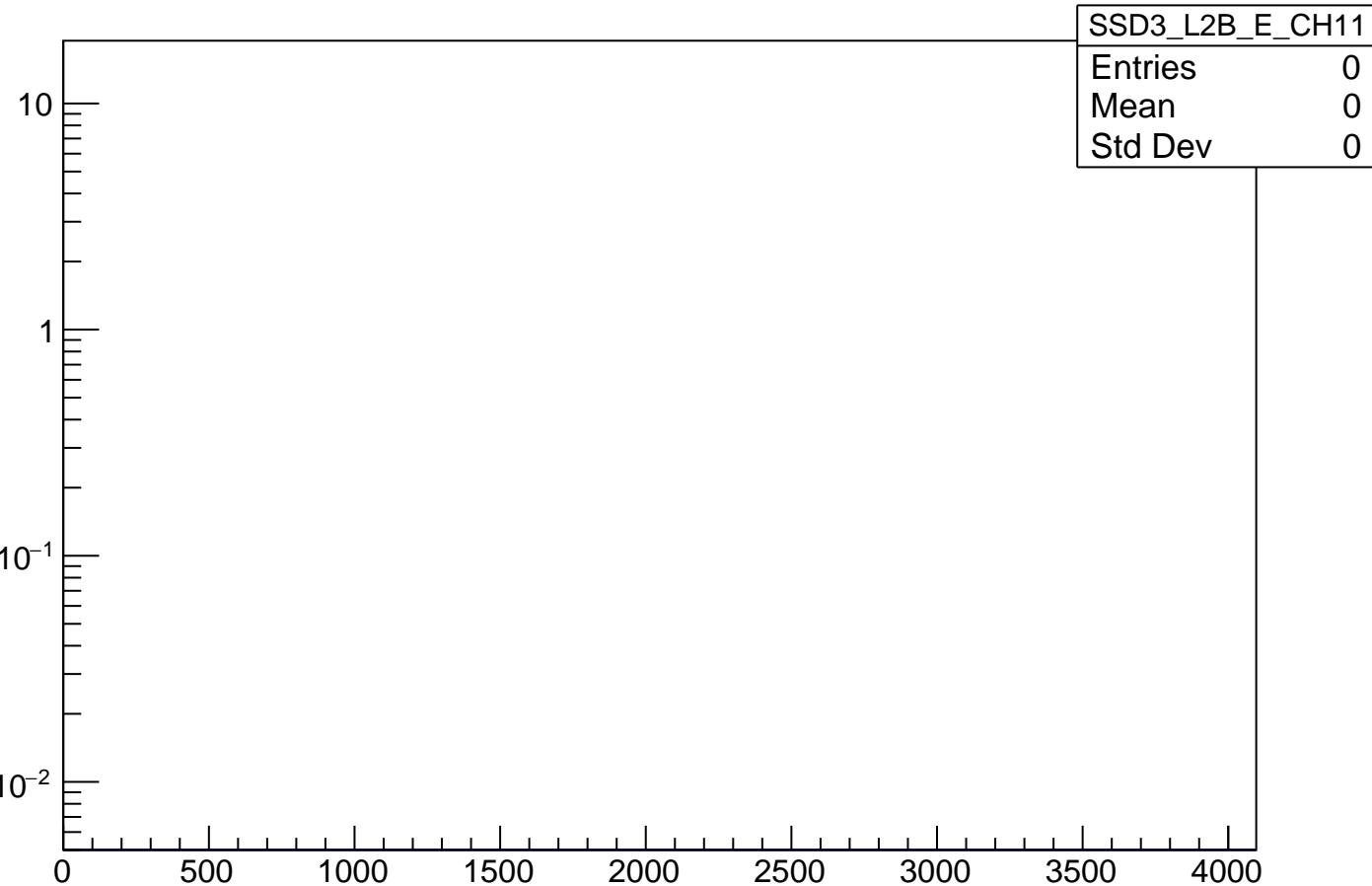
SSD3_L2B_E_CH09



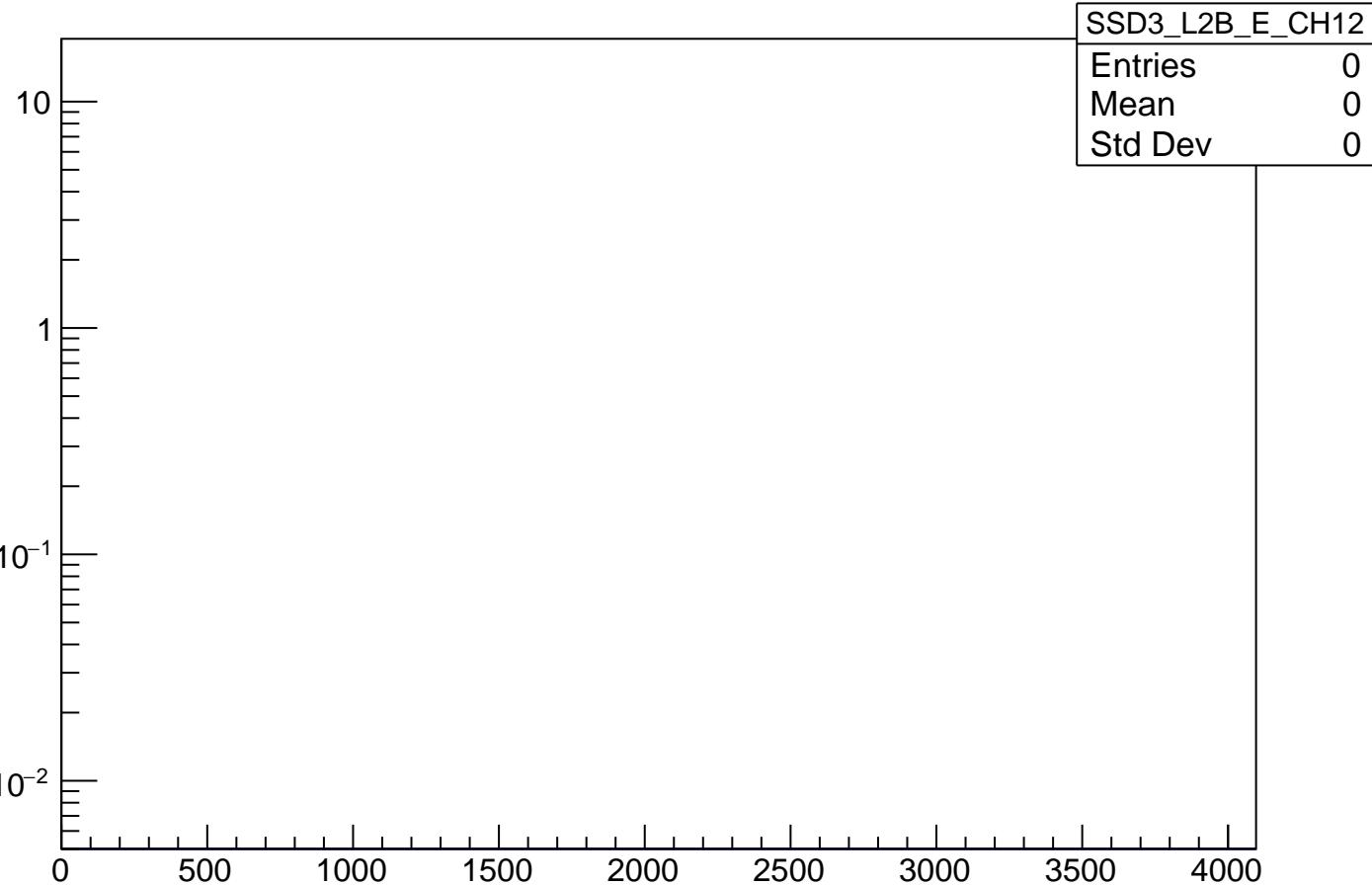
SSD3_L2B_E_CH10



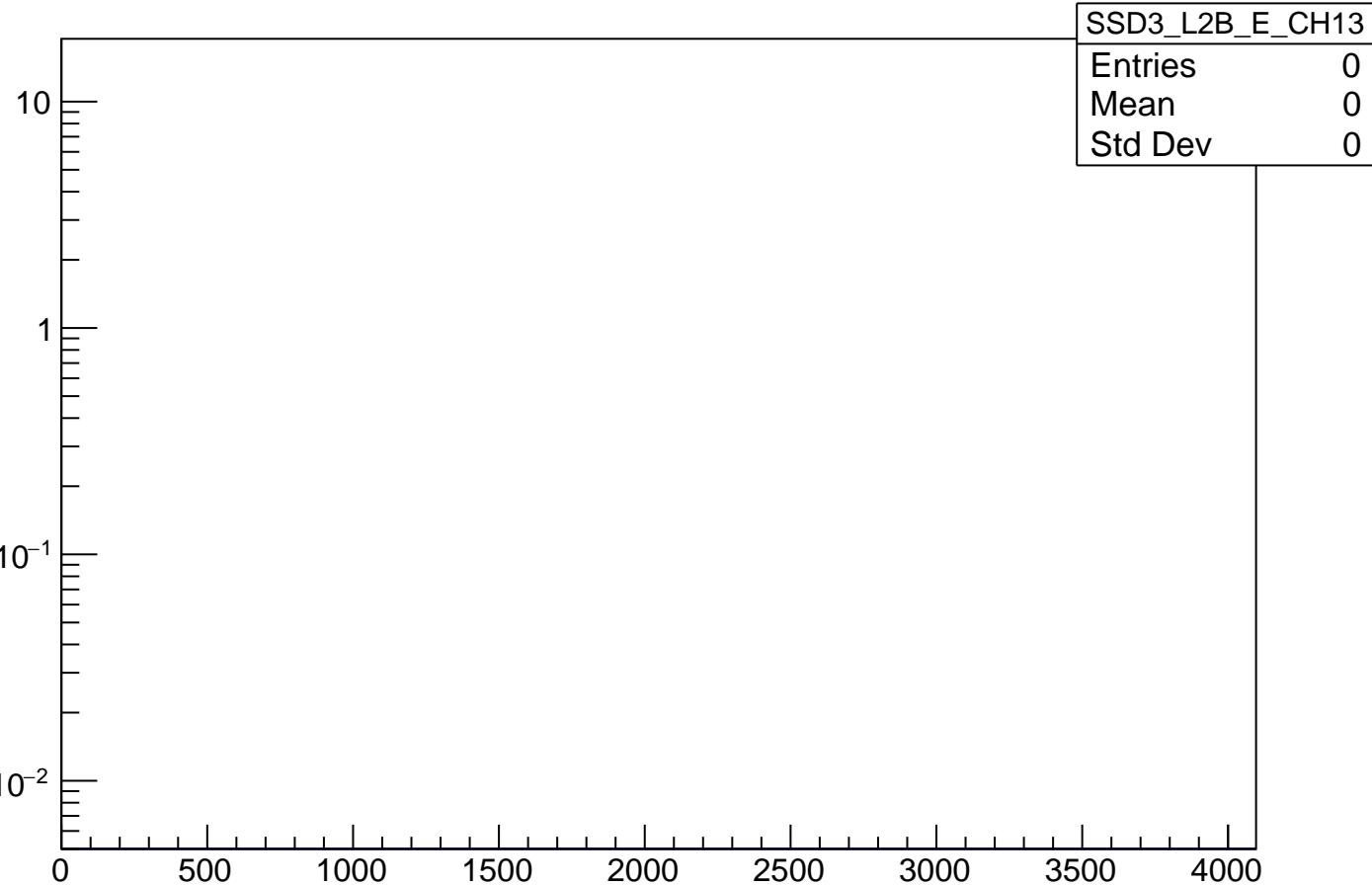
SSD3_L2B_E_CH11



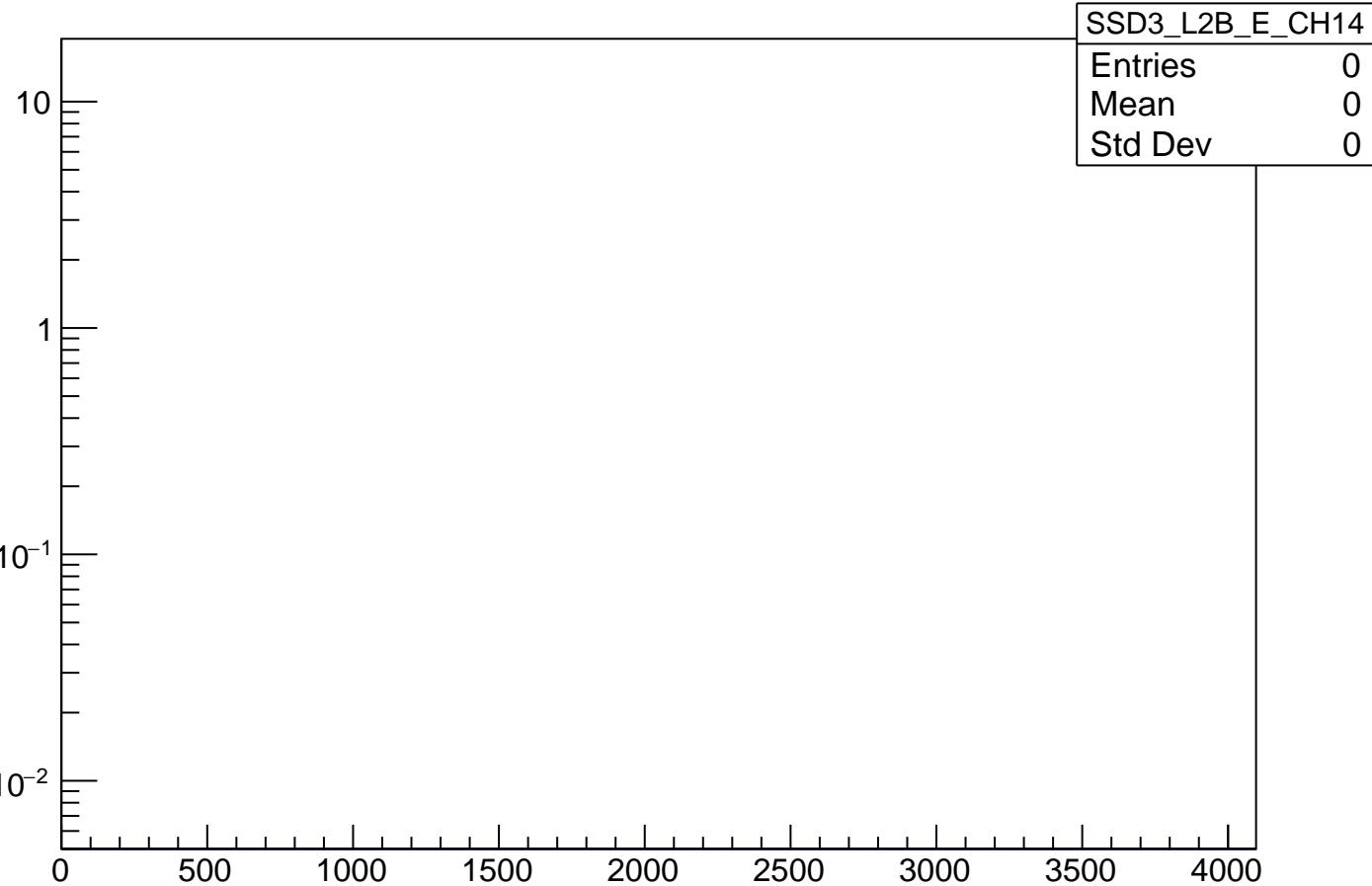
SSD3_L2B_E_CH12



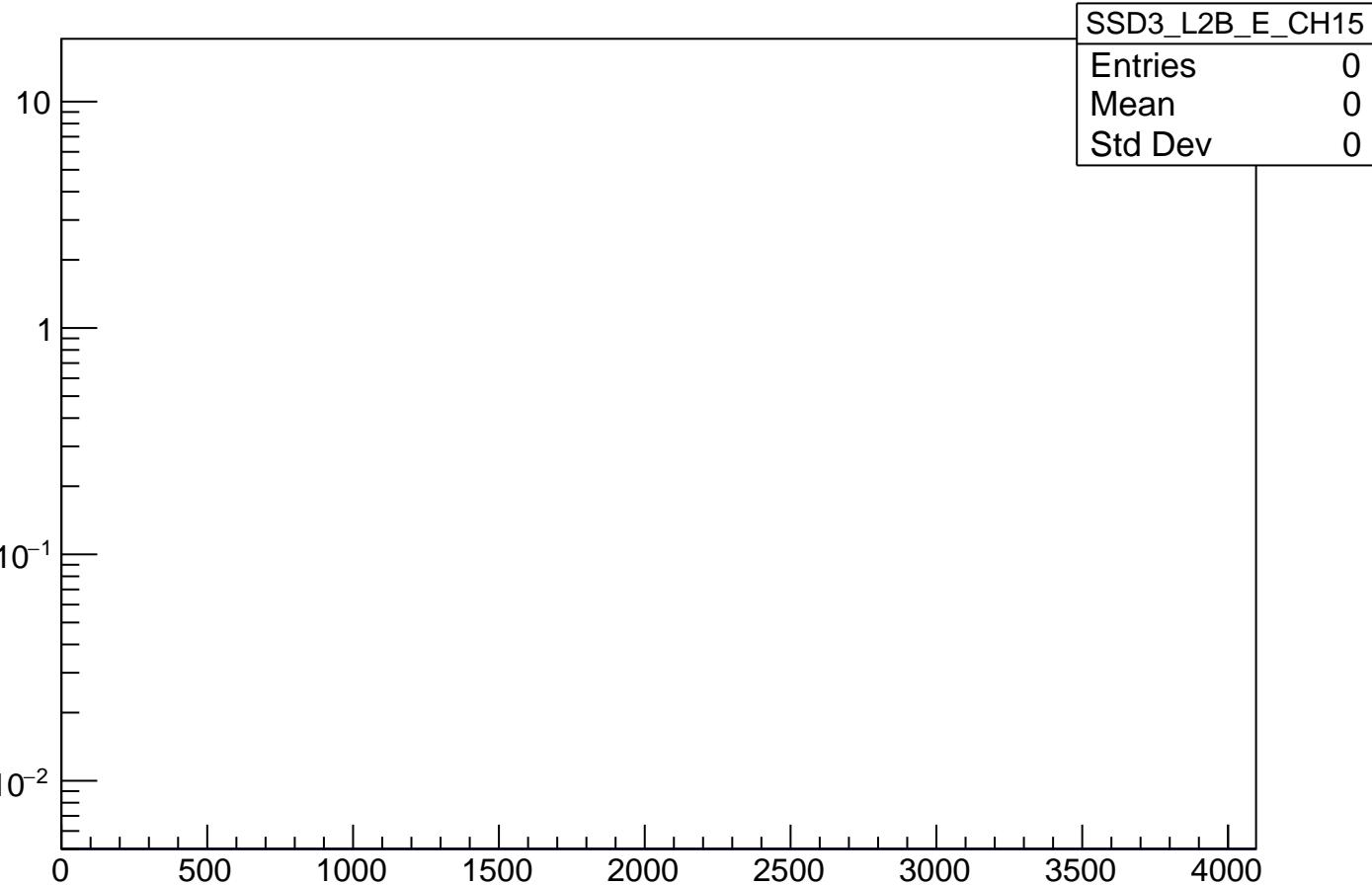
SSD3_L2B_E_CH13



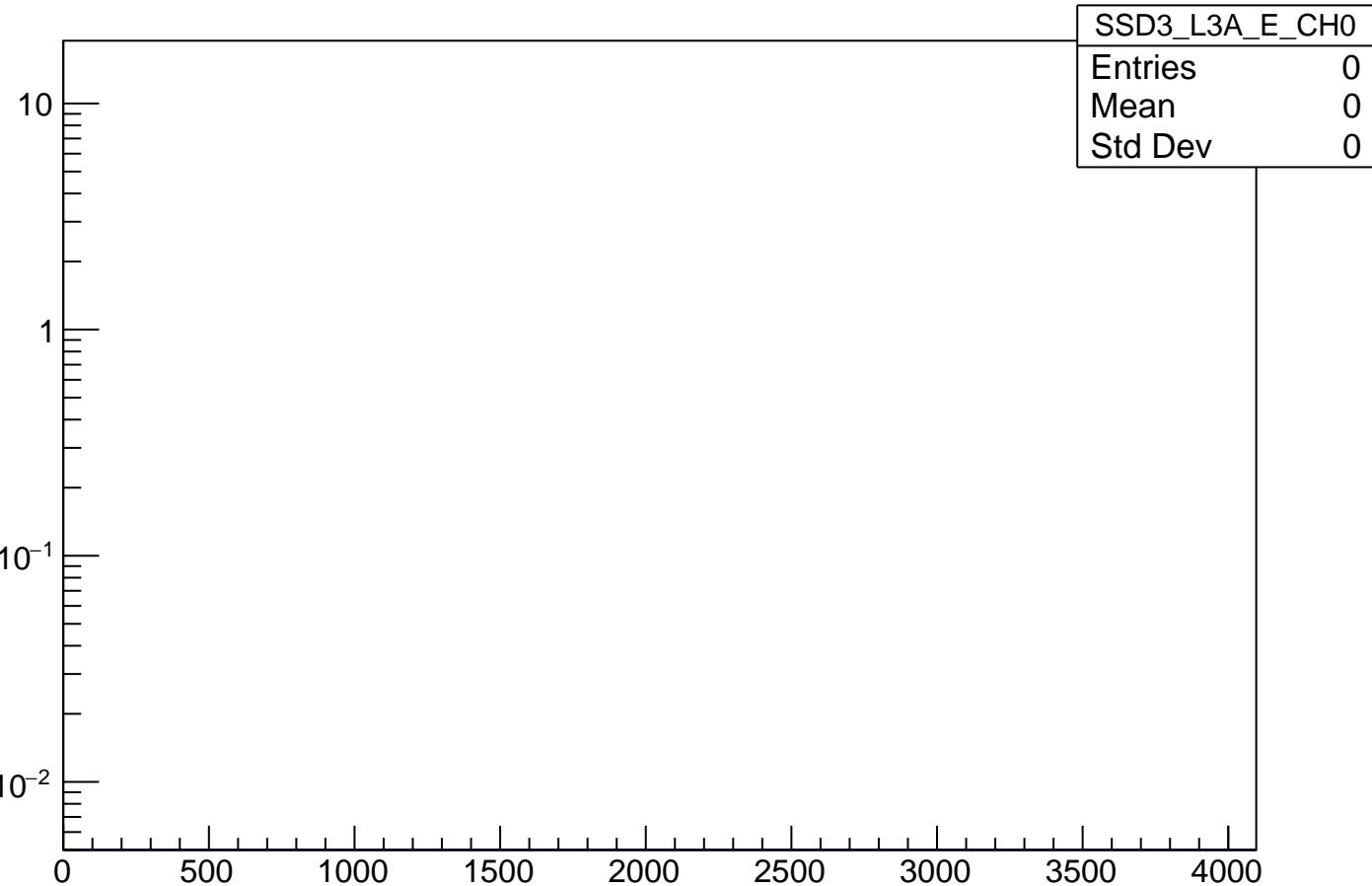
SSD3_L2B_E_CH14



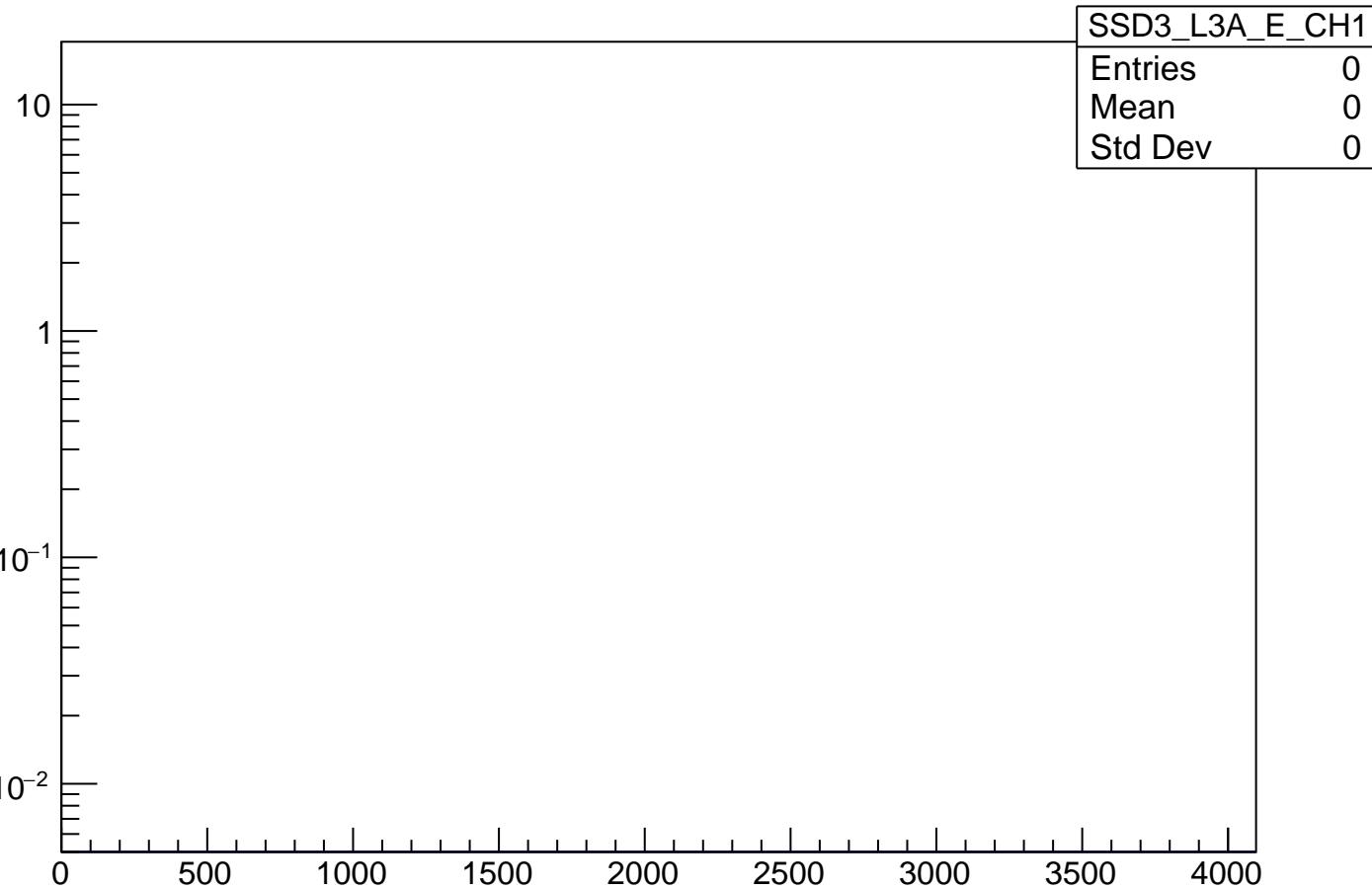
SSD3_L2B_E_CH15



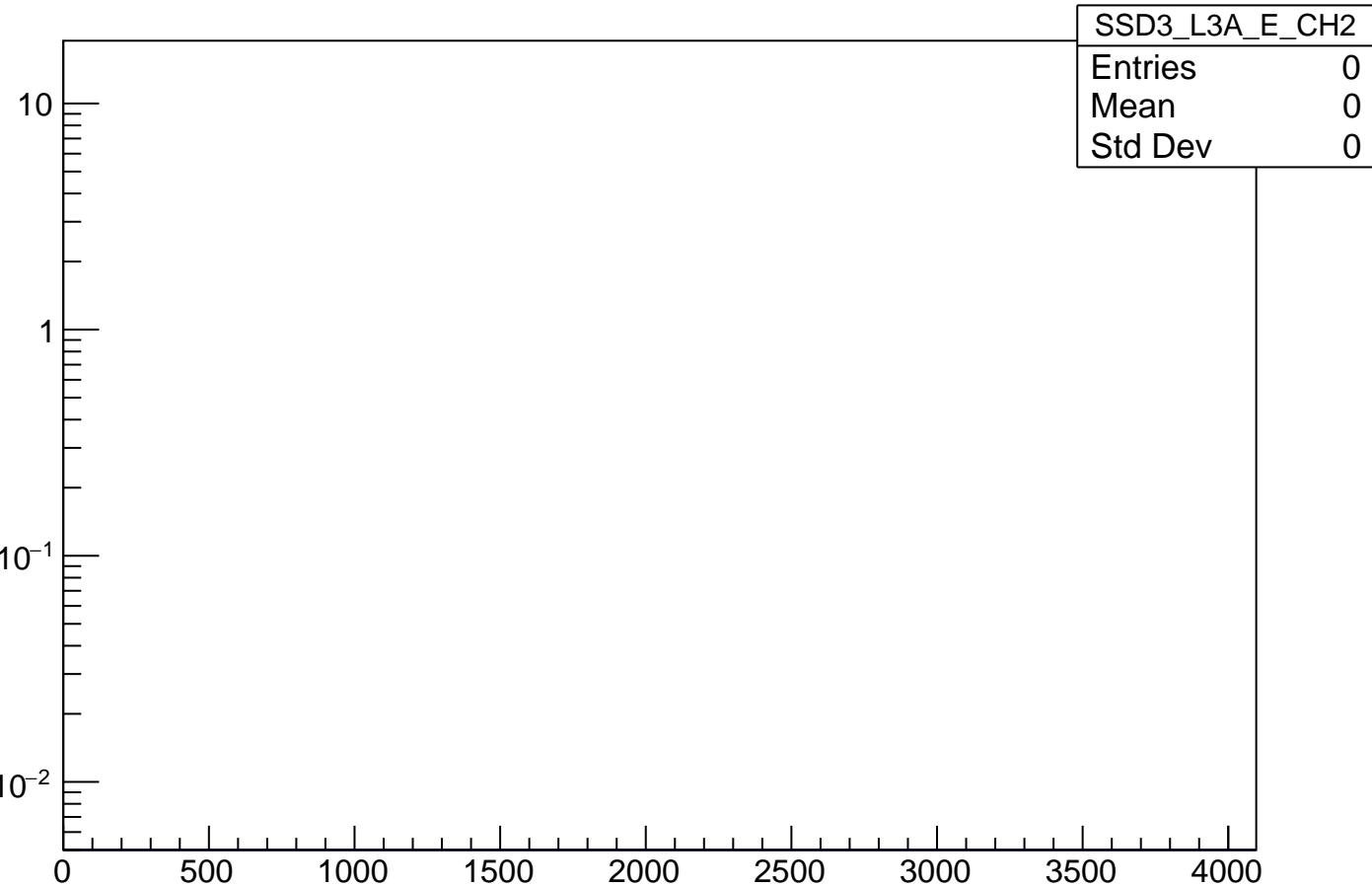
SSD3_L3A_E_CH0



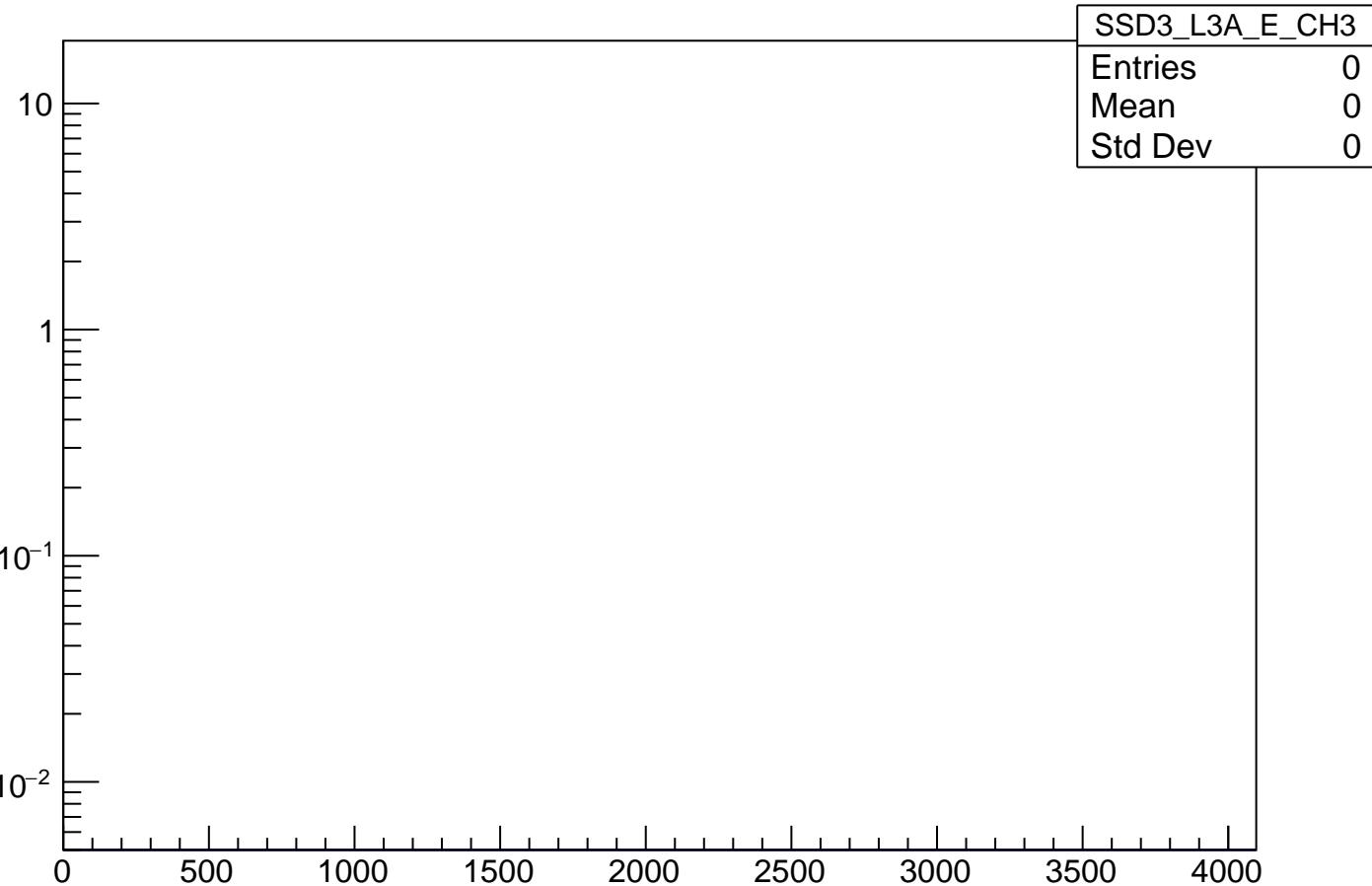
SSD3_L3A_E_CH1



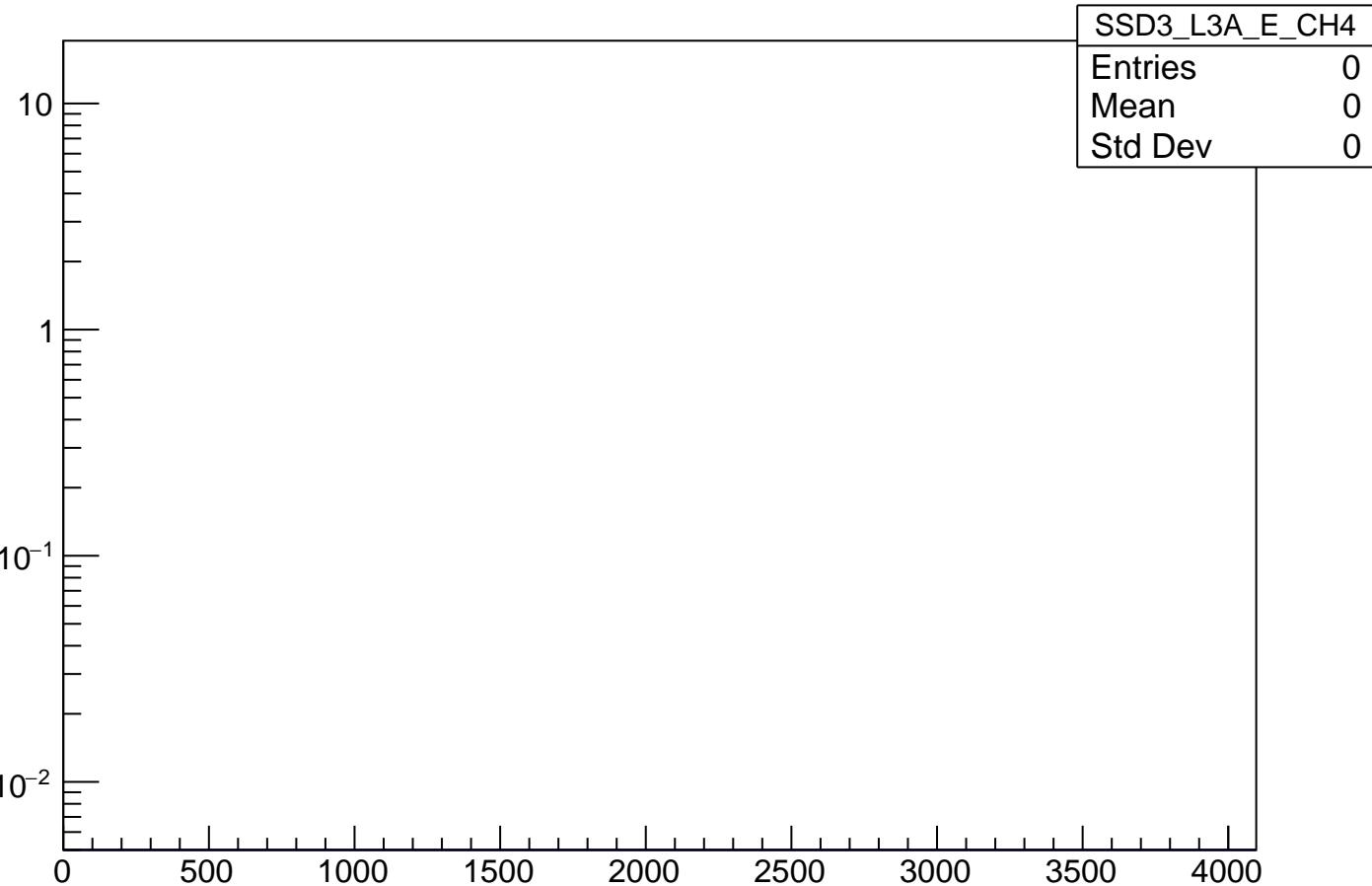
SSD3_L3A_E_CH2



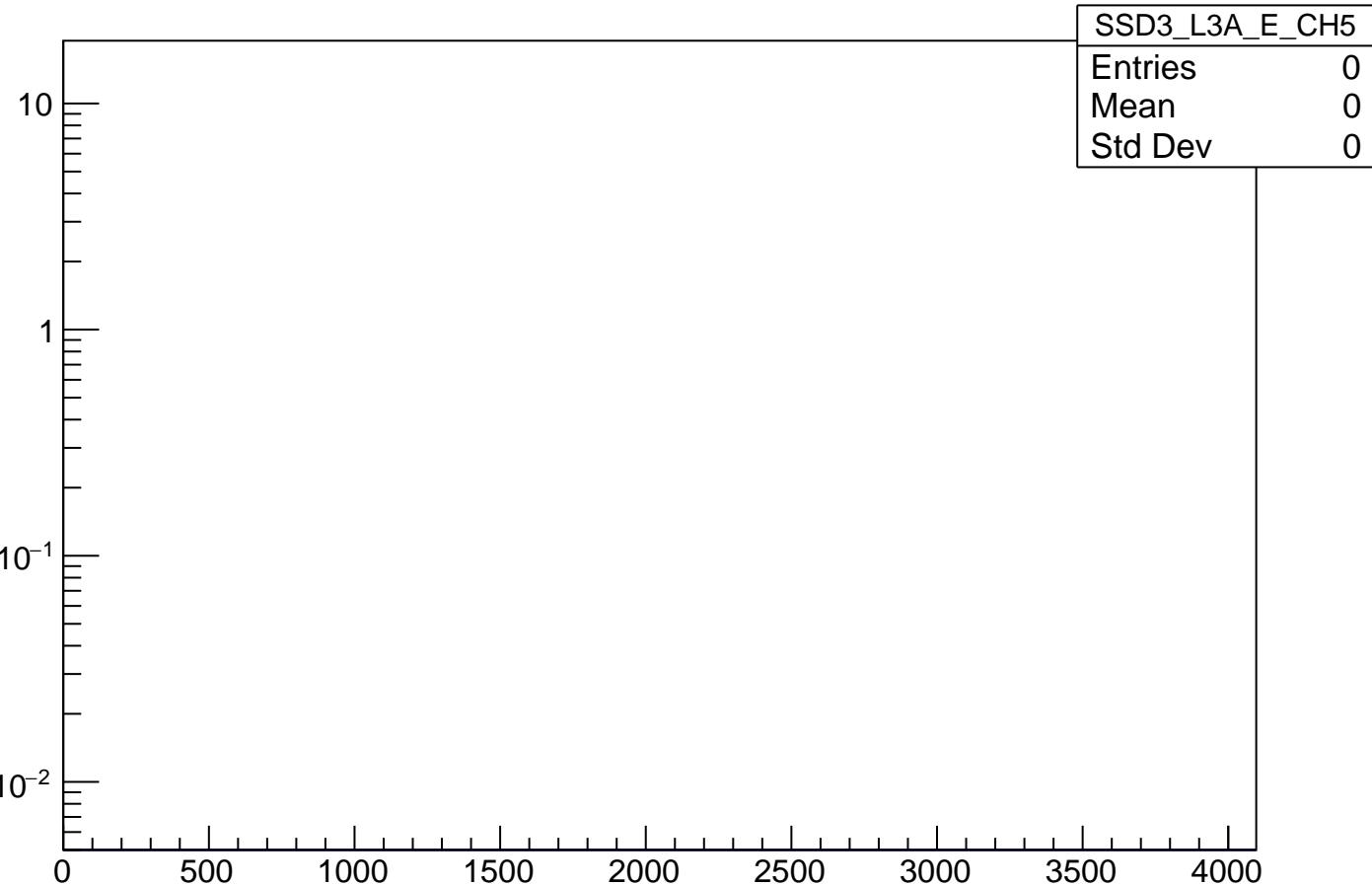
SSD3_L3A_E_CH3



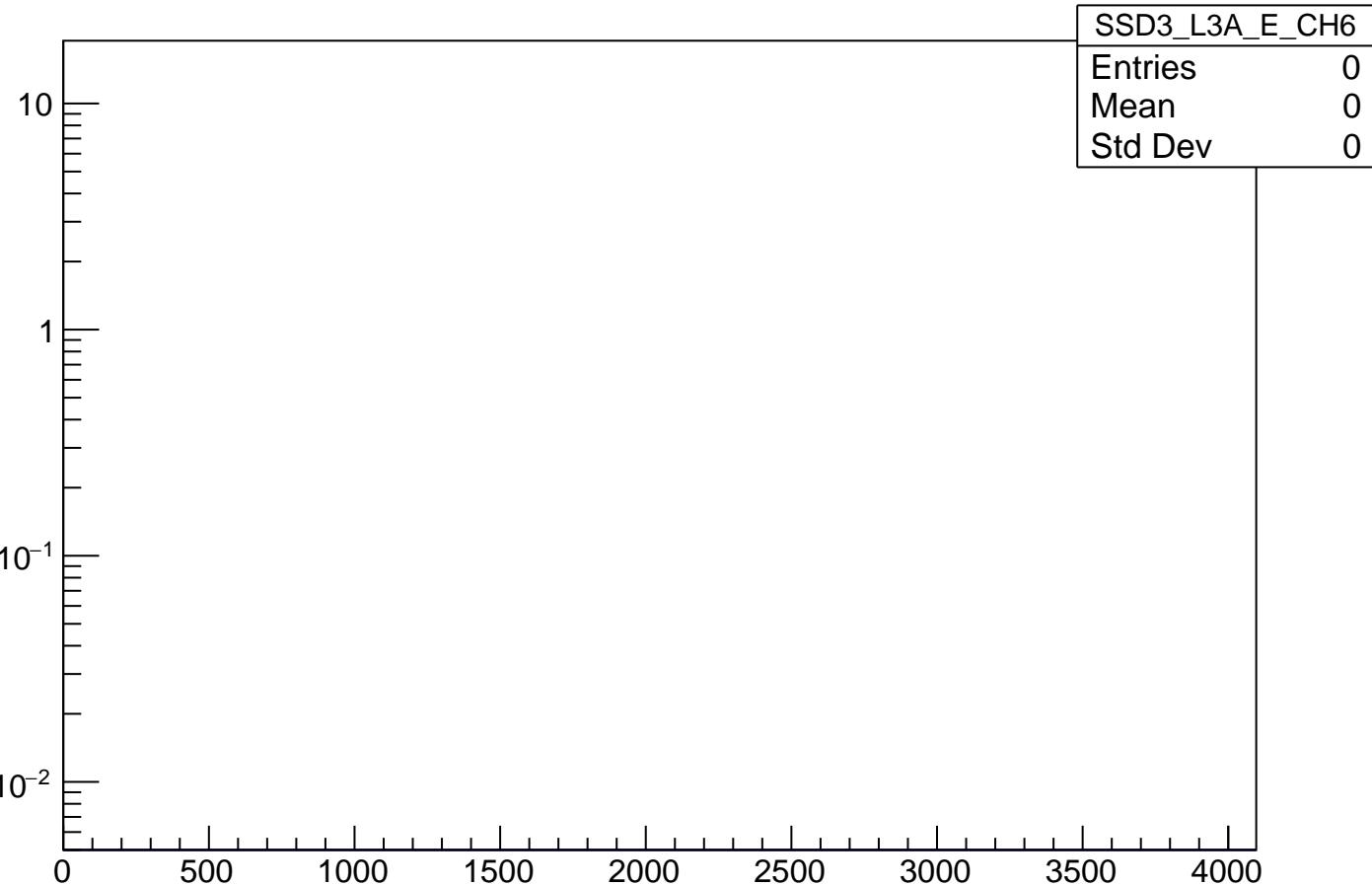
SSD3_L3A_E_CH4



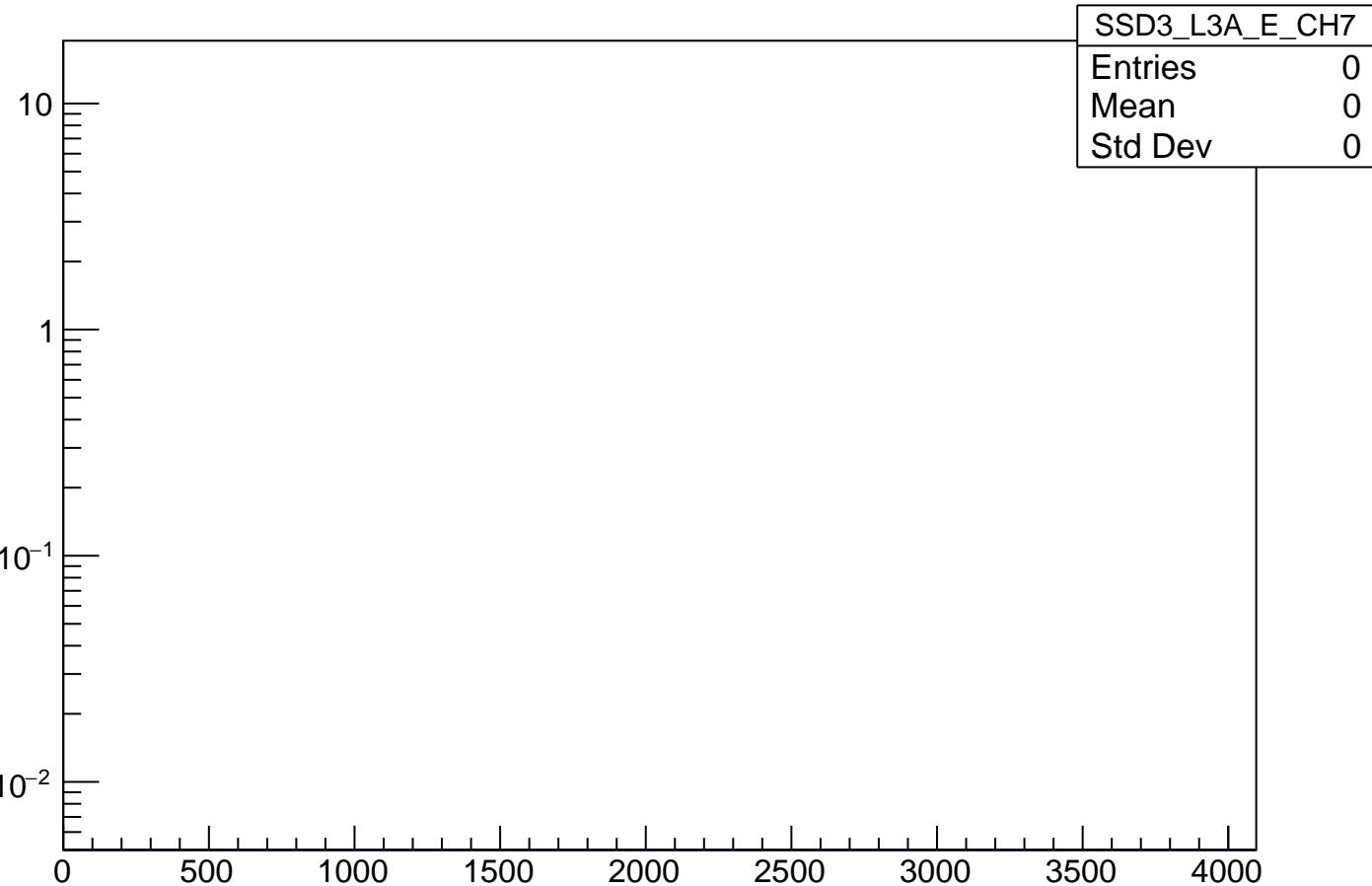
SSD3_L3A_E_CH5



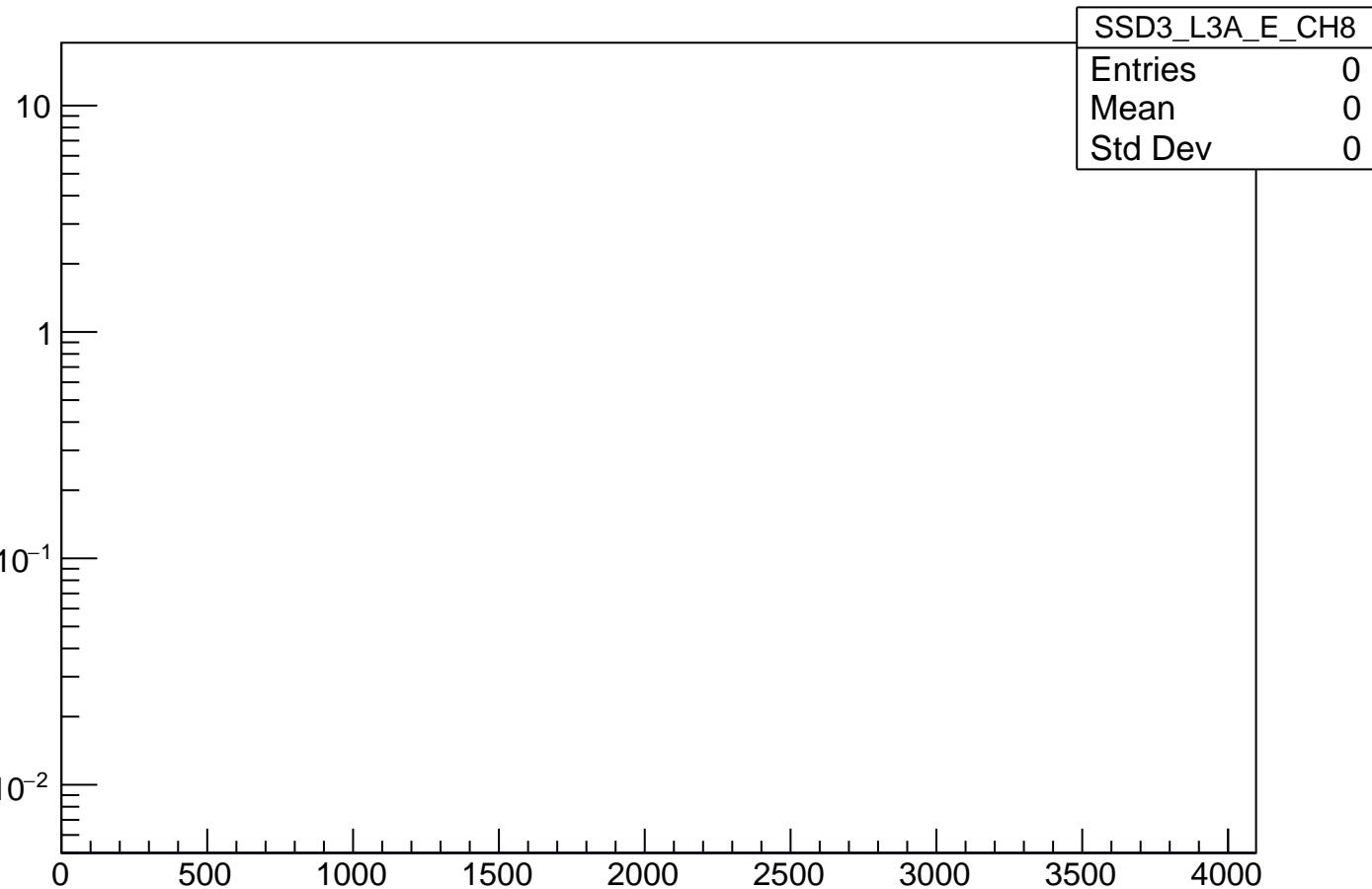
SSD3_L3A_E_CH6



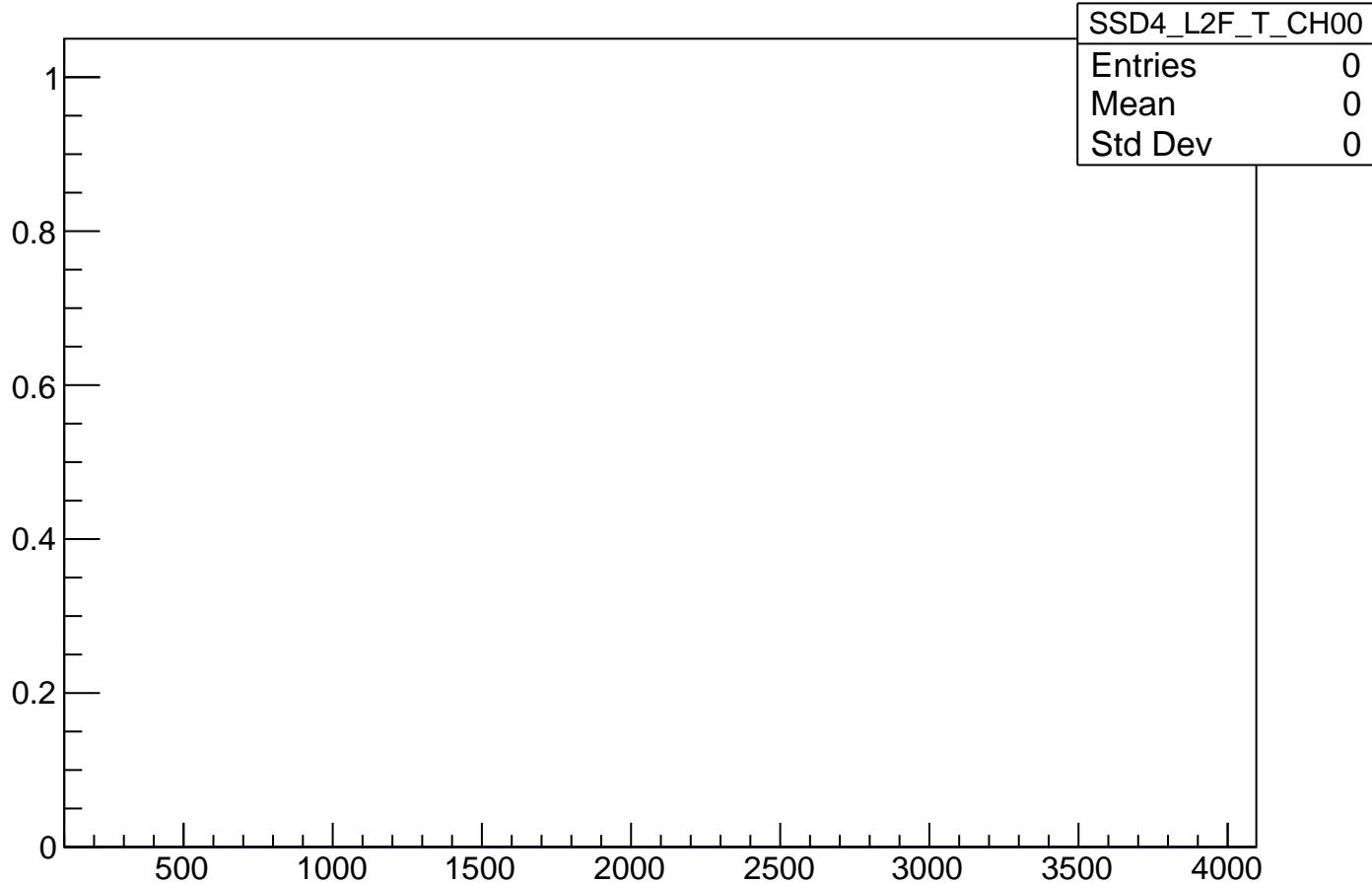
SSD3_L3A_E_CH7



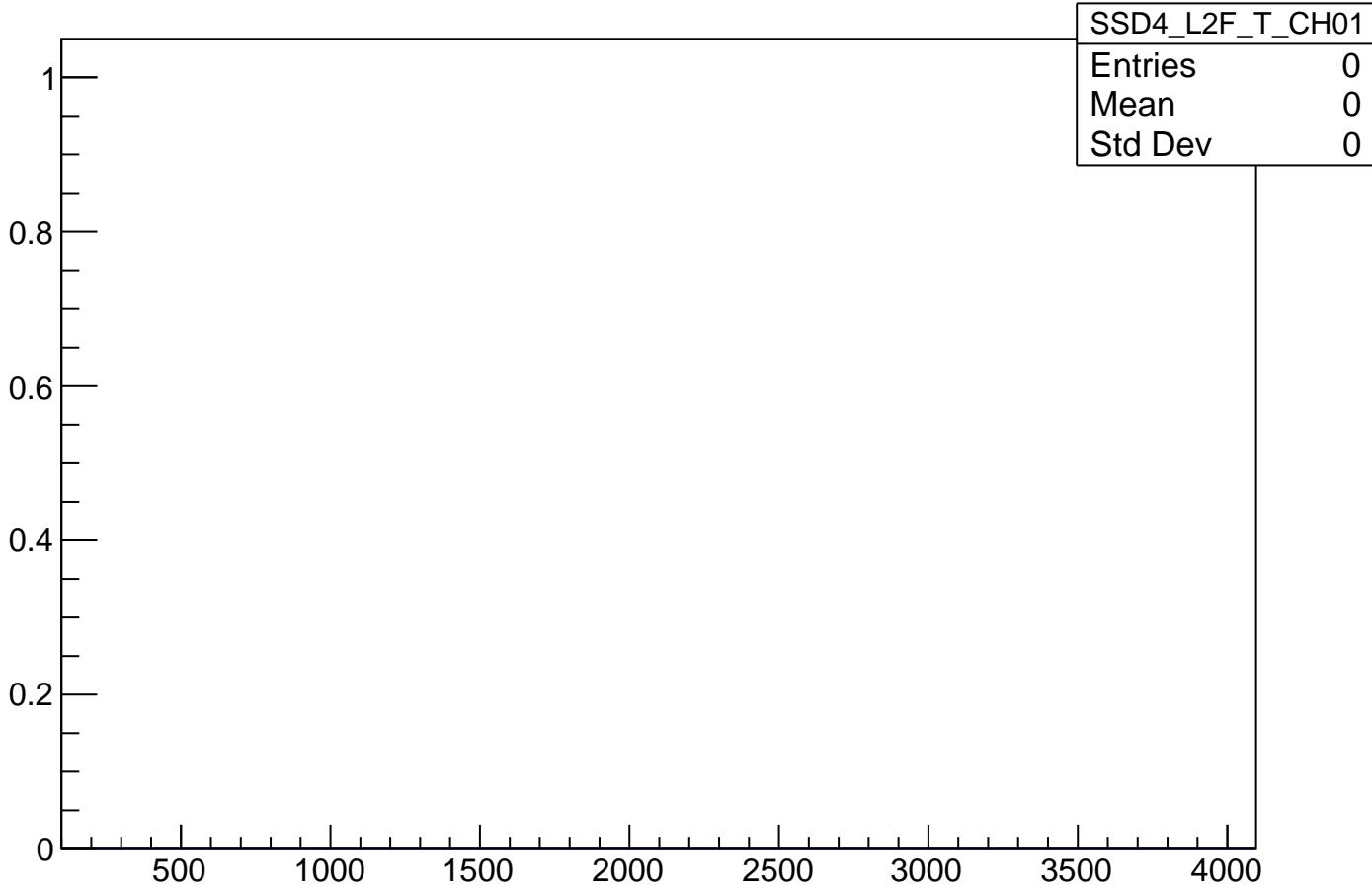
SSD3_L3A_E_CH8



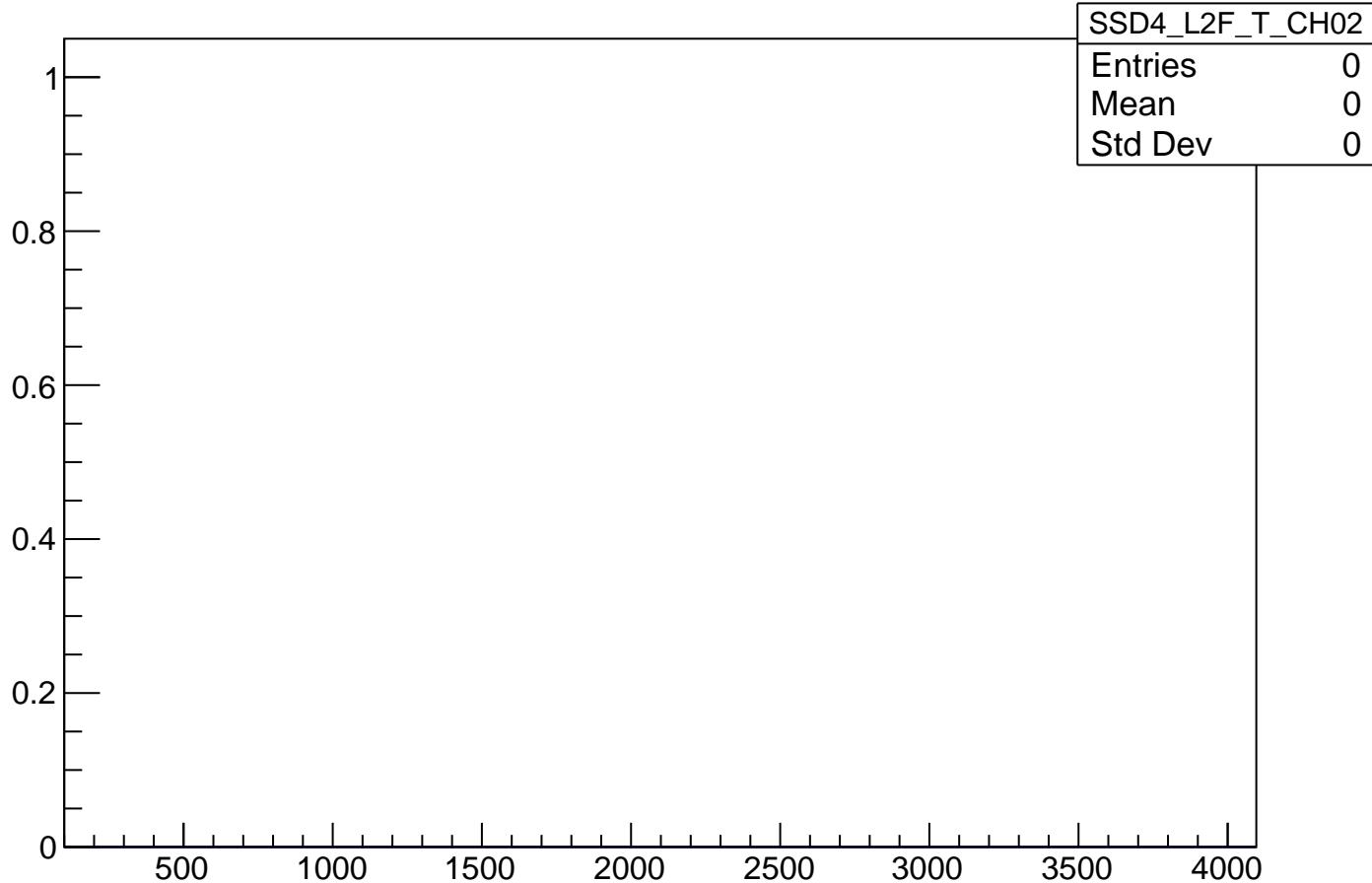
SSD4_L2F_T_CH00



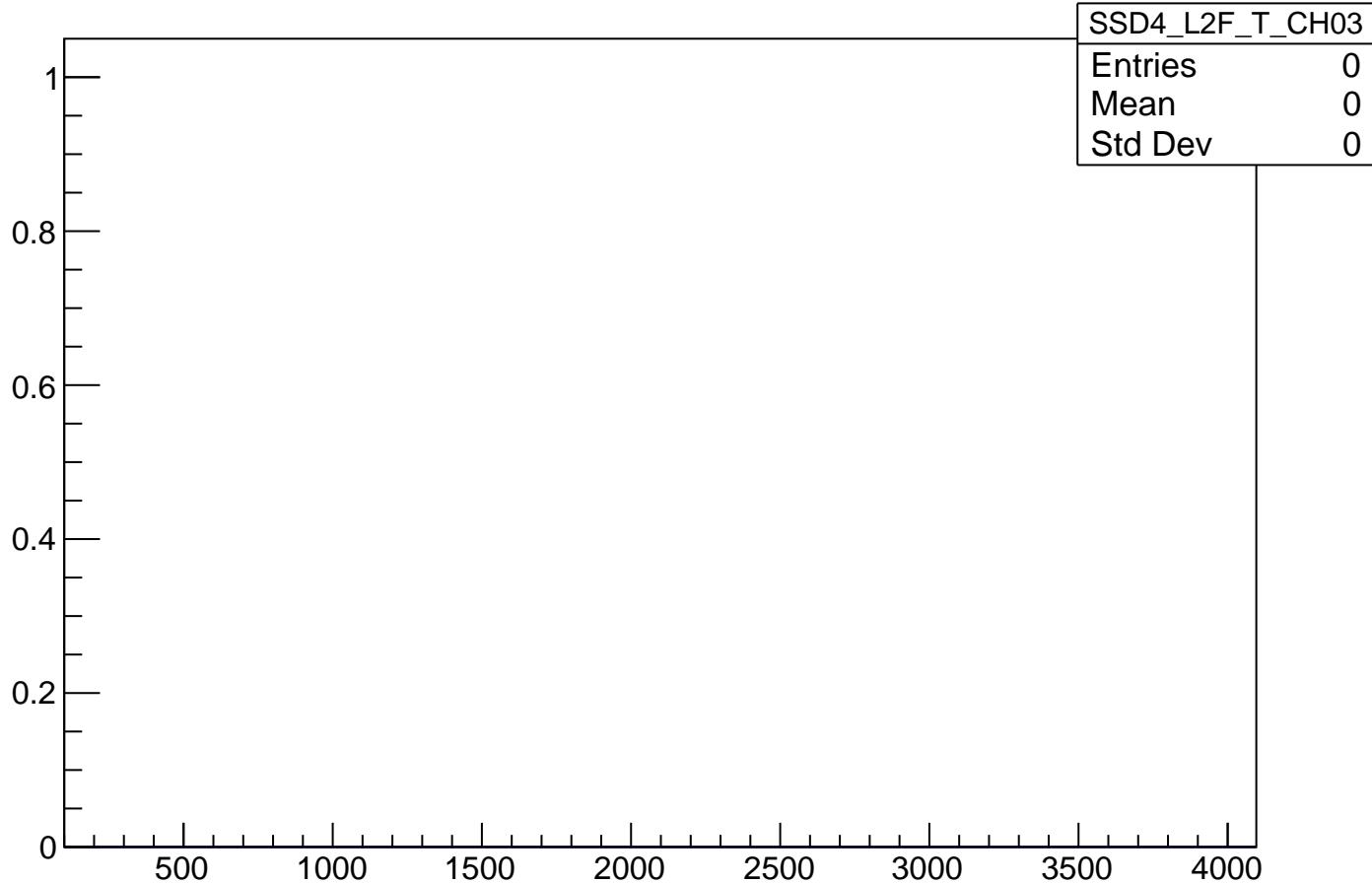
SSD4_L2F_T_CH01



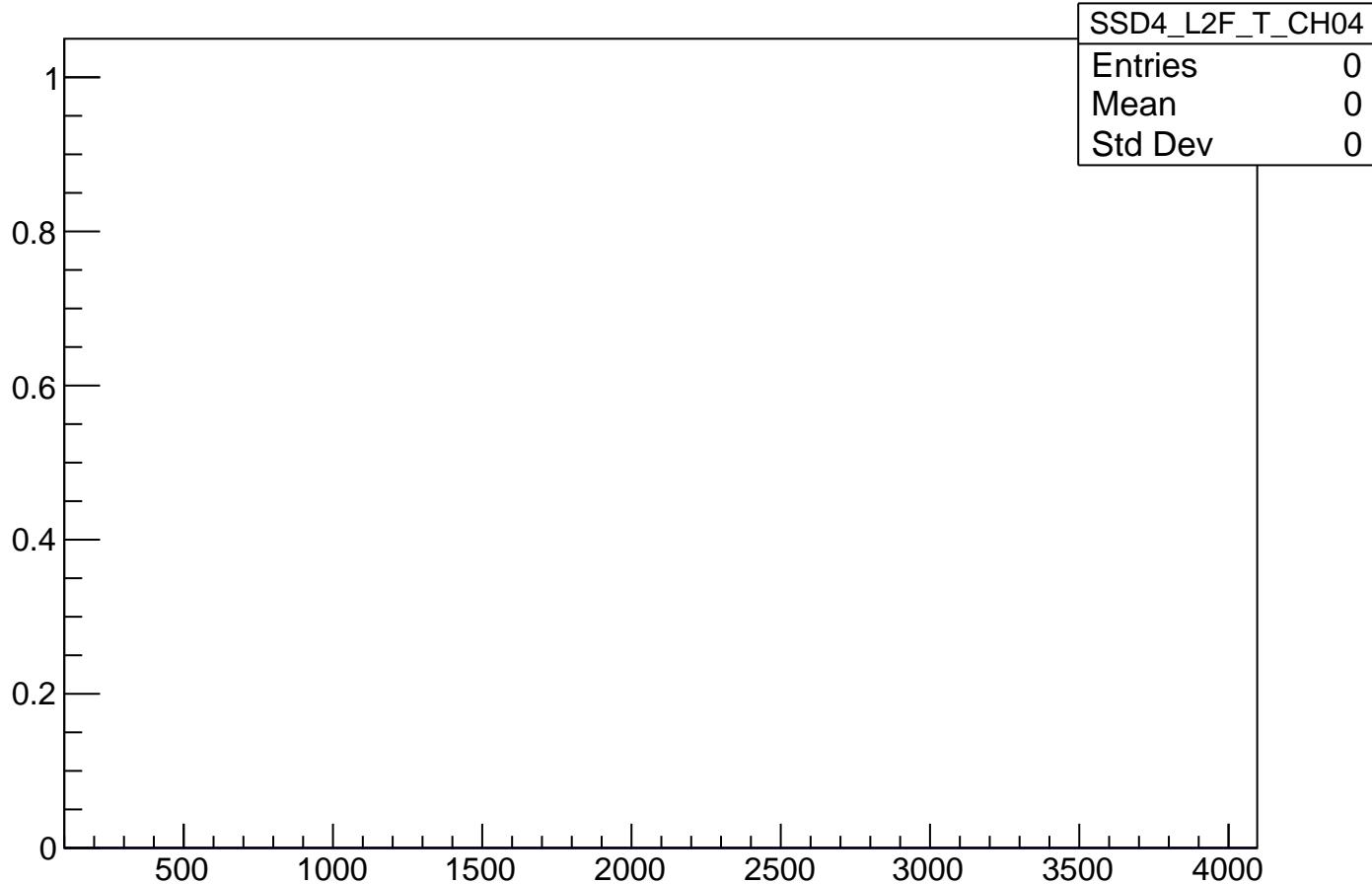
SSD4_L2F_T_CH02



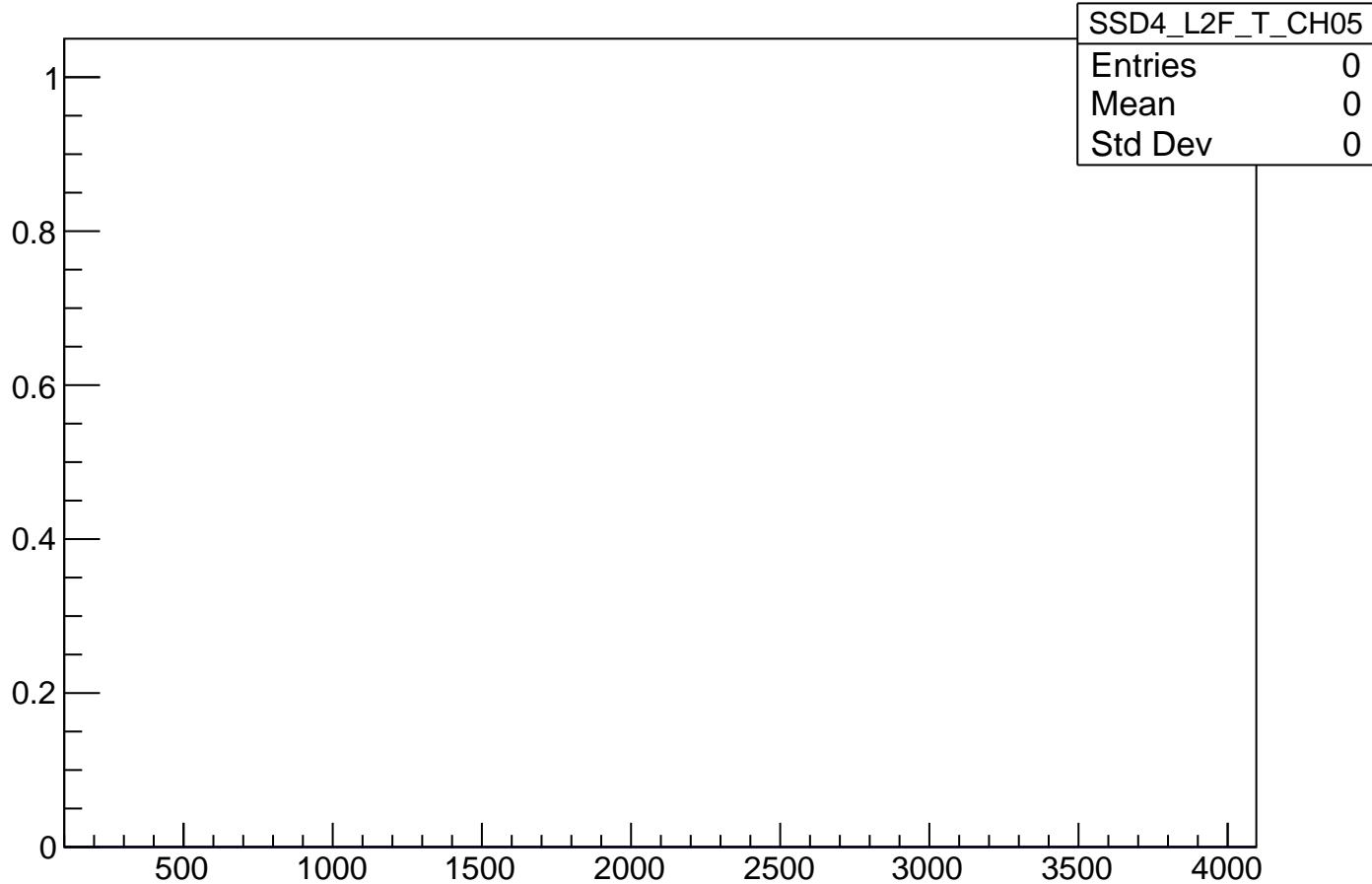
SSD4_L2F_T_CH03



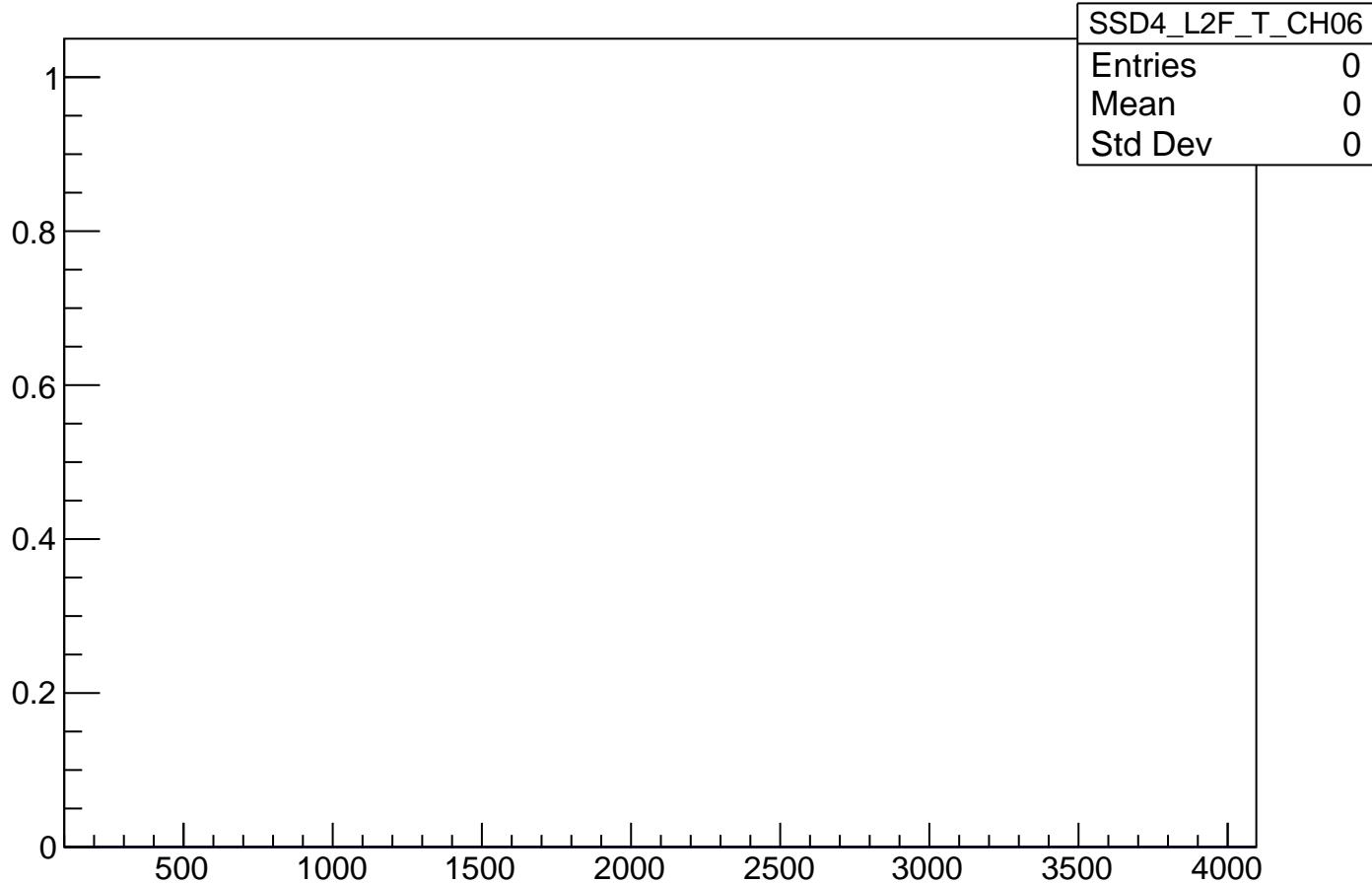
SSD4_L2F_T_CH04



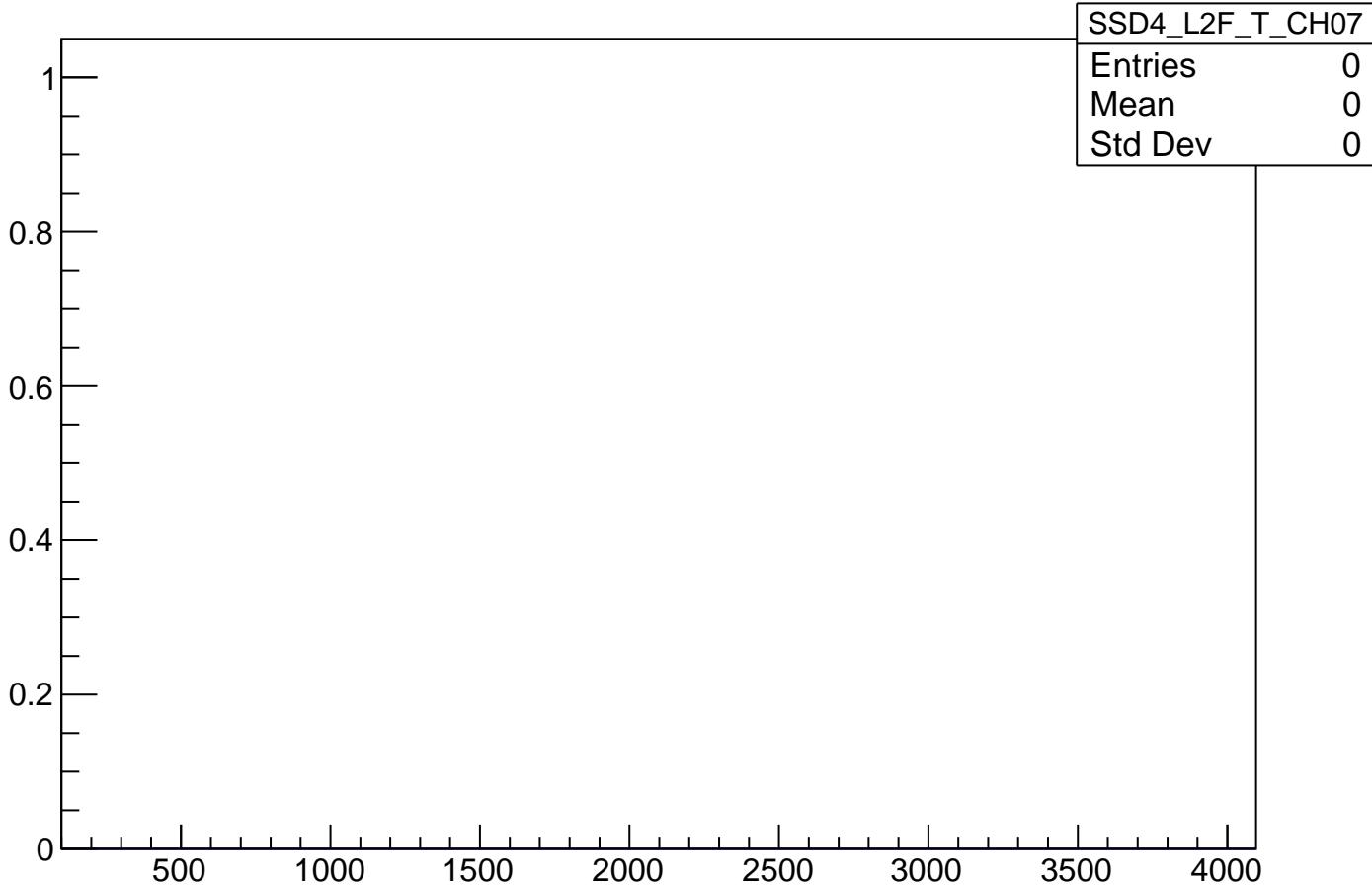
SSD4_L2F_T_CH05



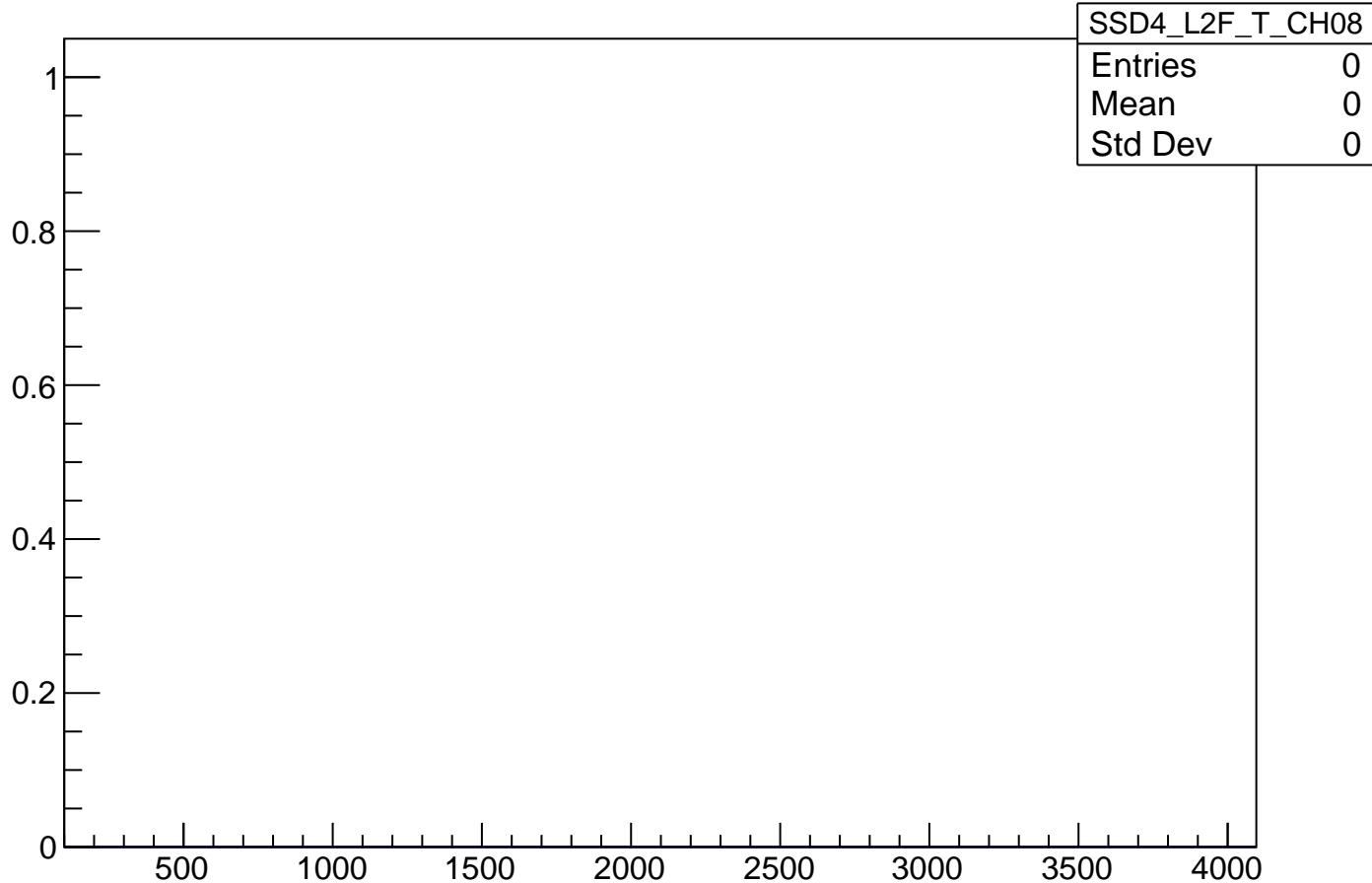
SSD4_L2F_T_CH06



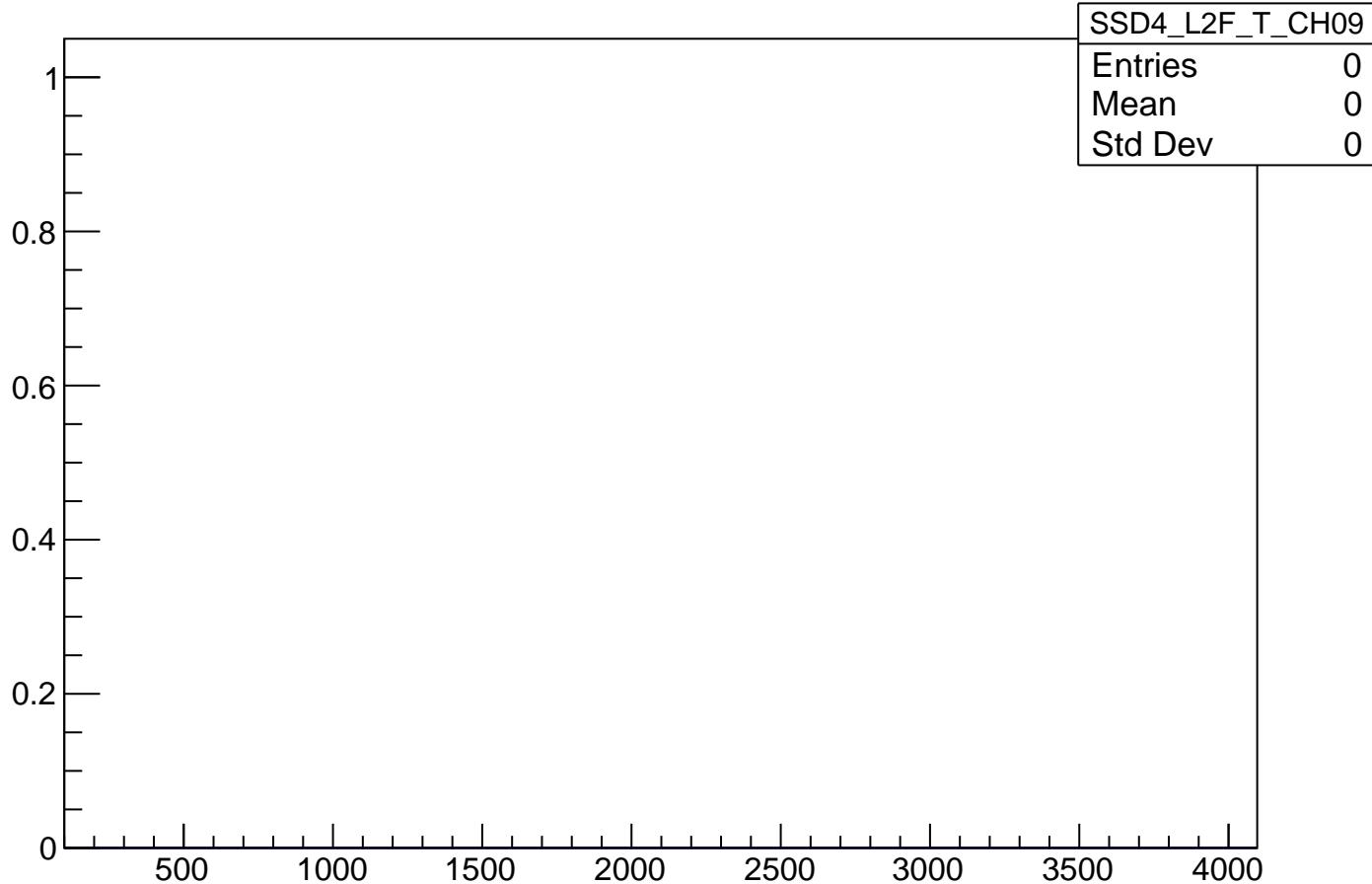
SSD4_L2F_T_CH07



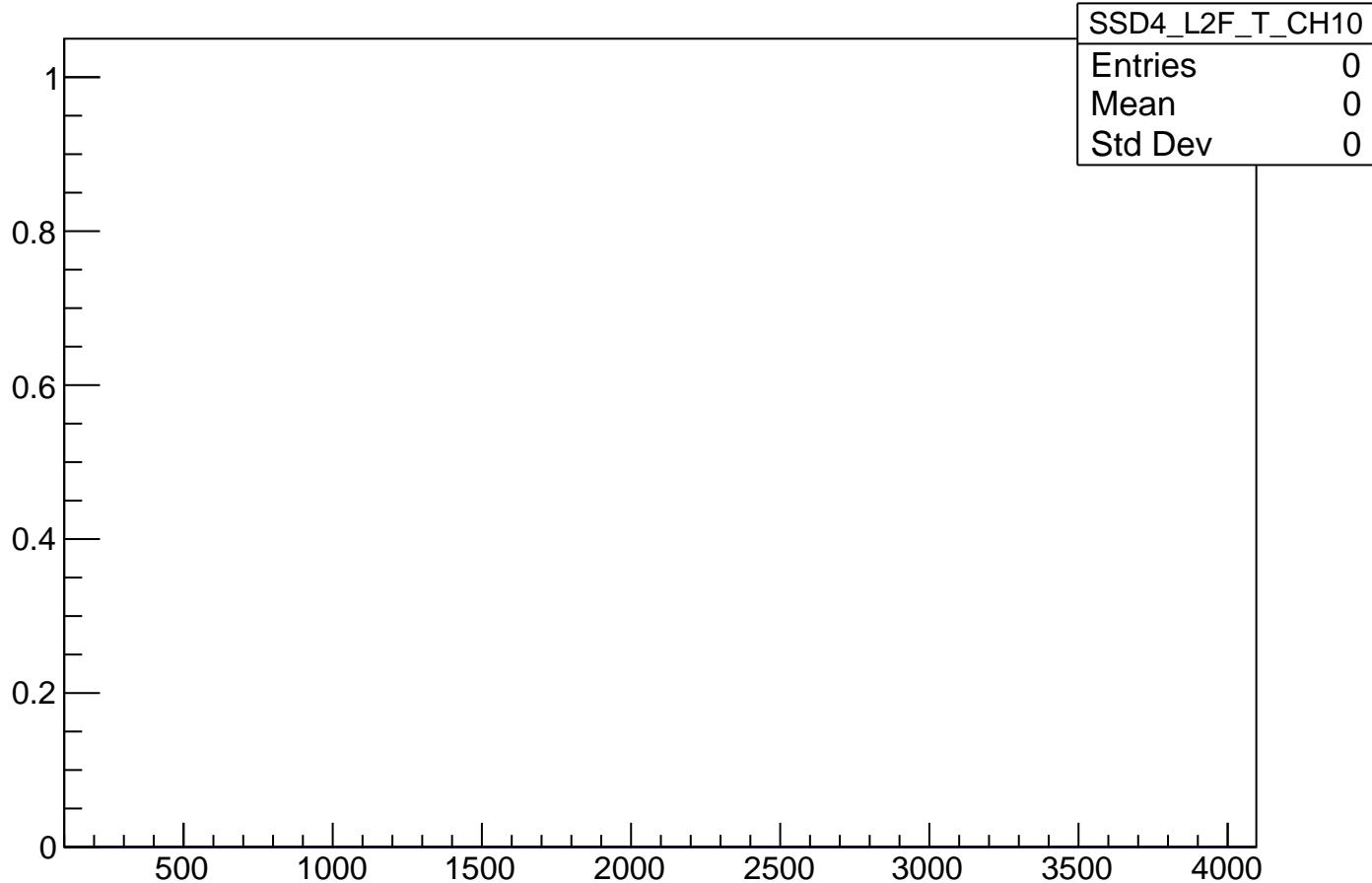
SSD4_L2F_T_CH08



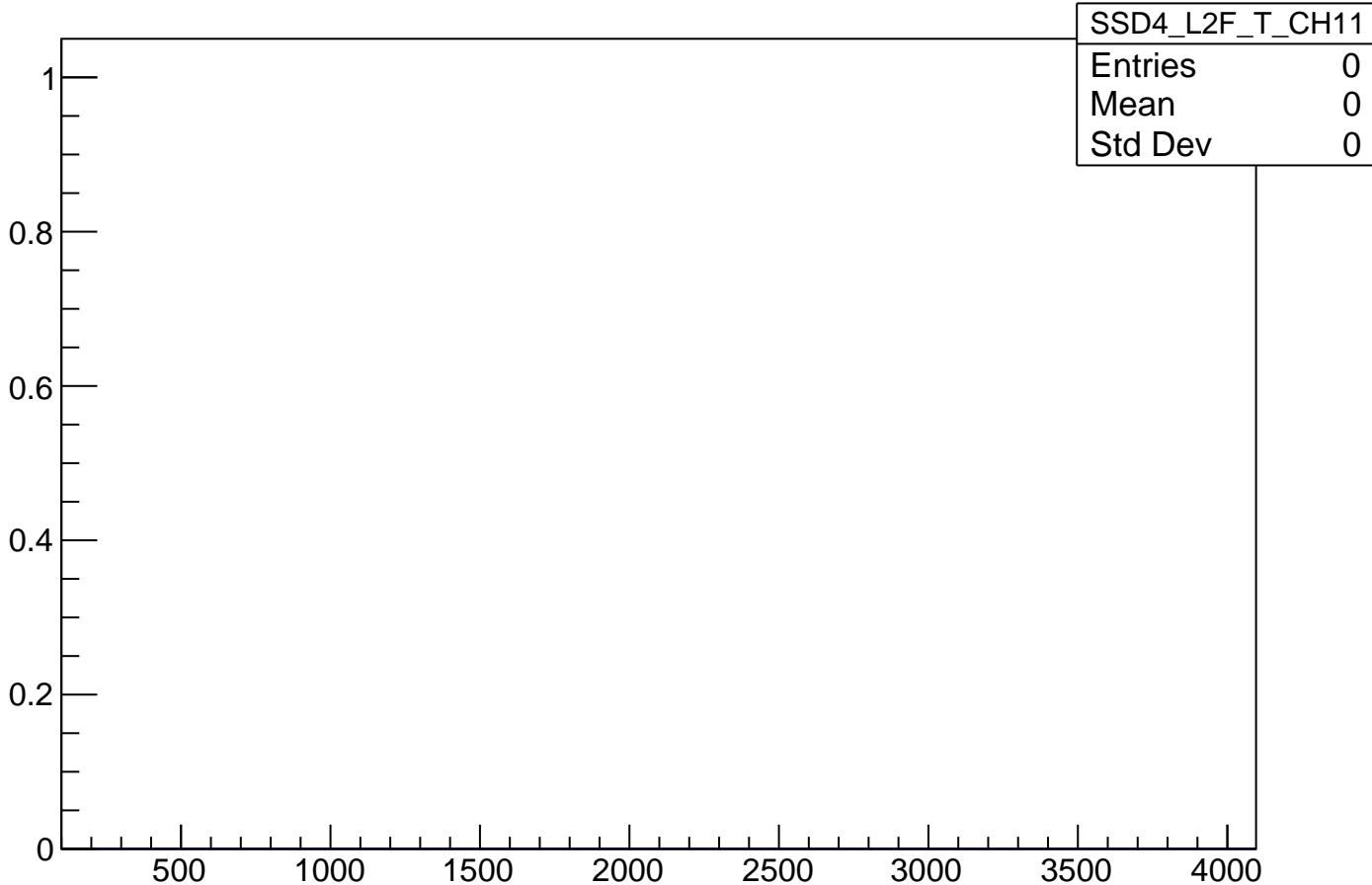
SSD4_L2F_T_CH09



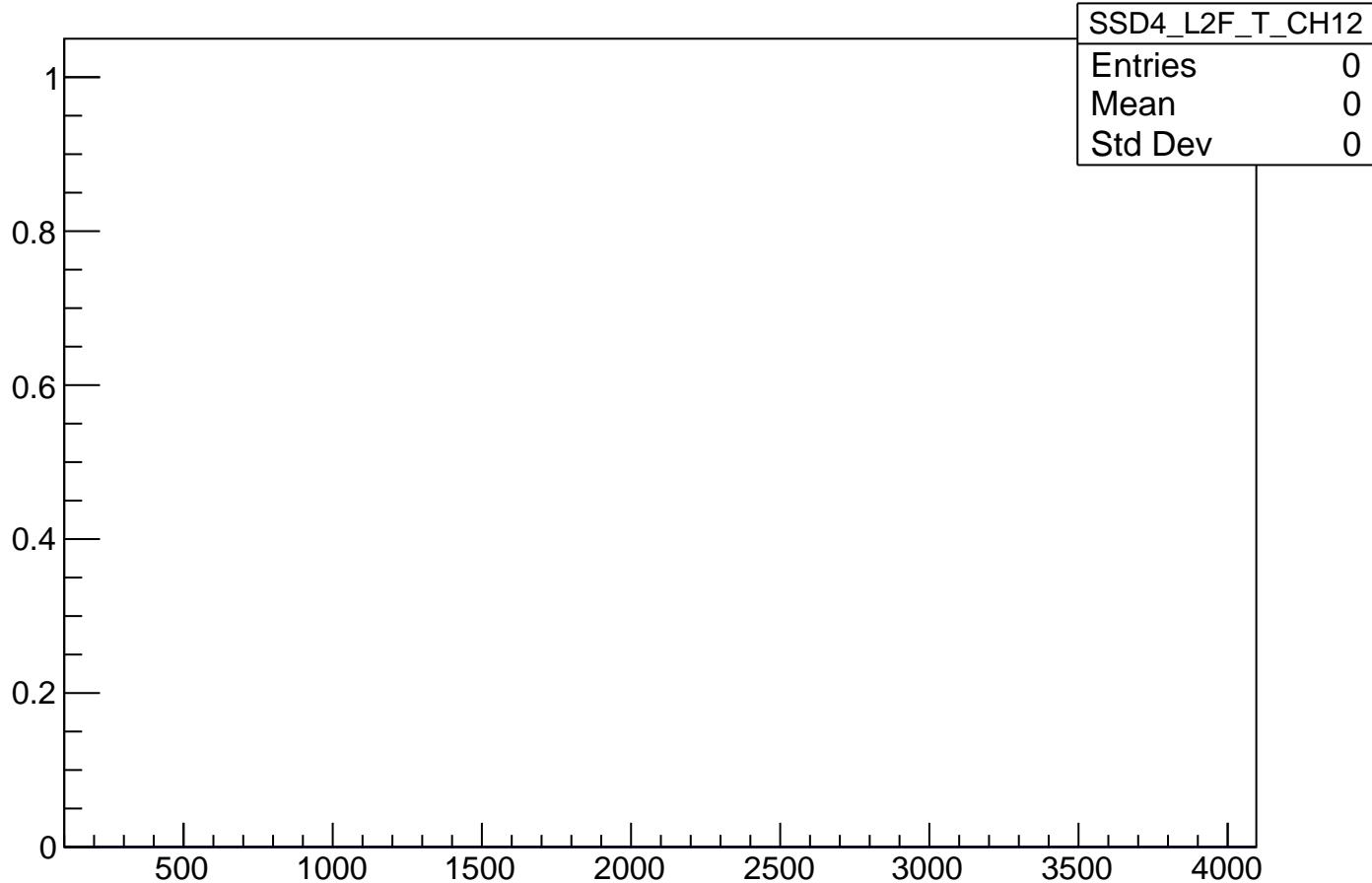
SSD4_L2F_T_CH10



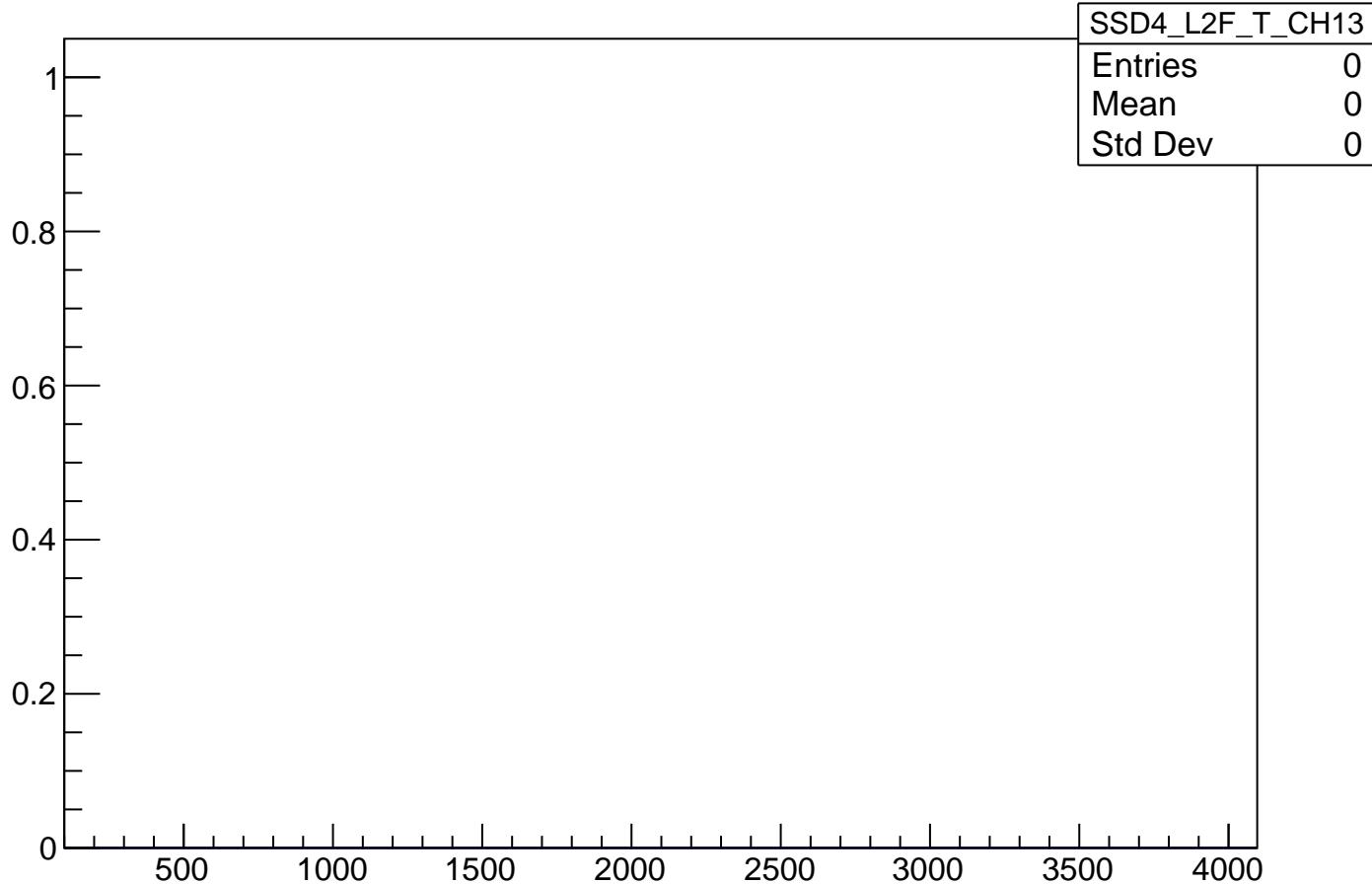
SSD4_L2F_T_CH11



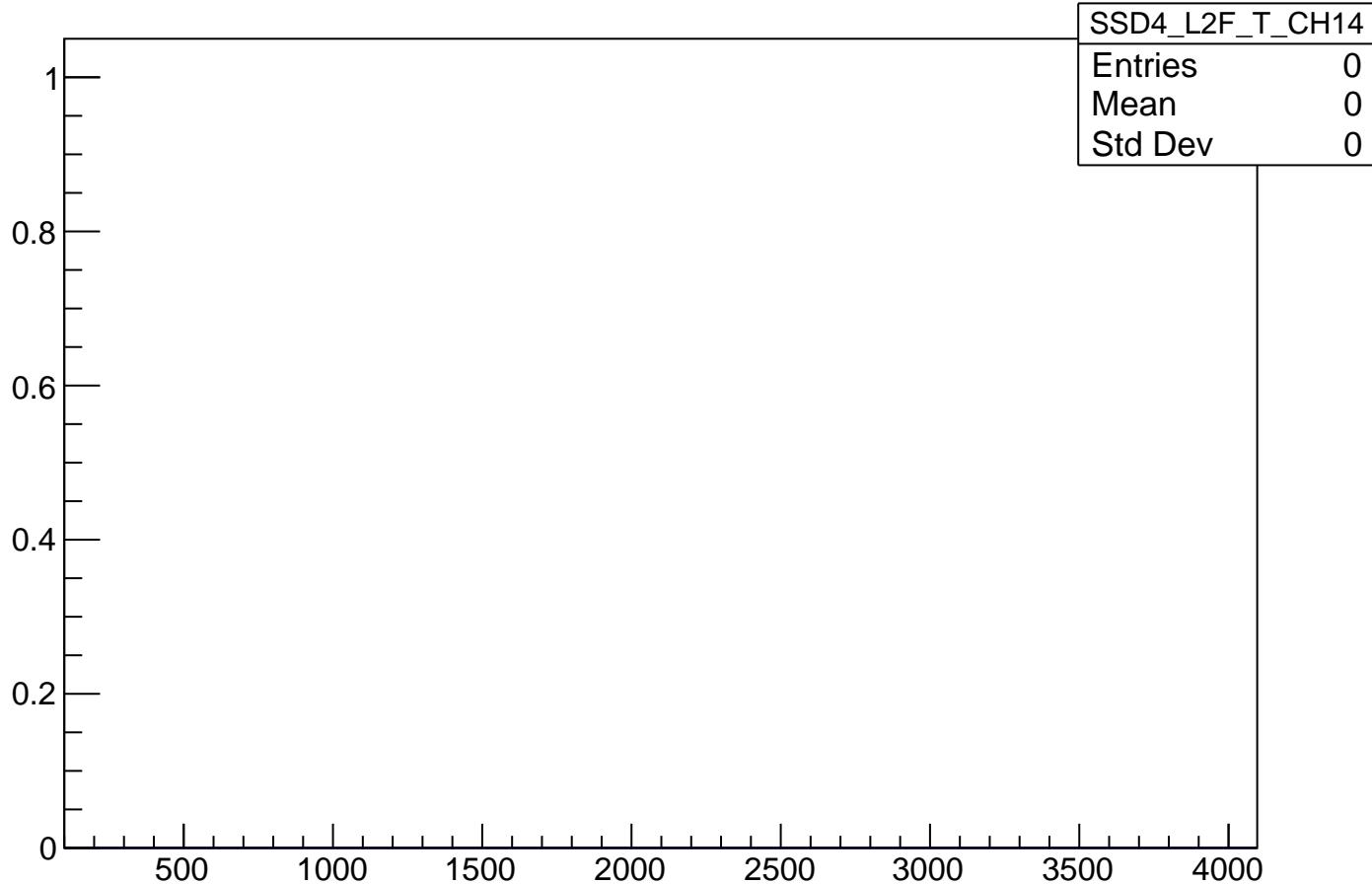
SSD4_L2F_T_CH12



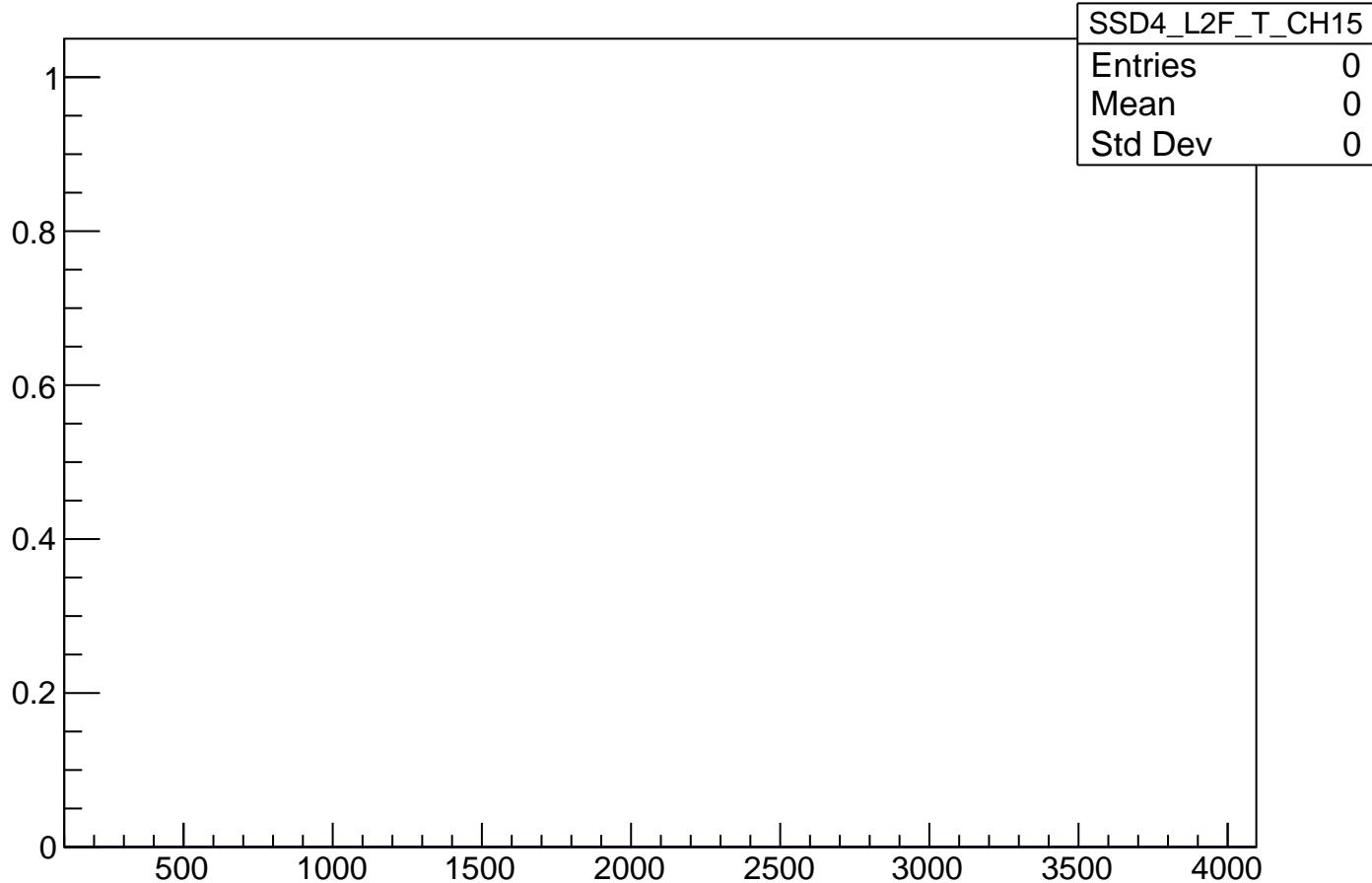
SSD4_L2F_T_CH13



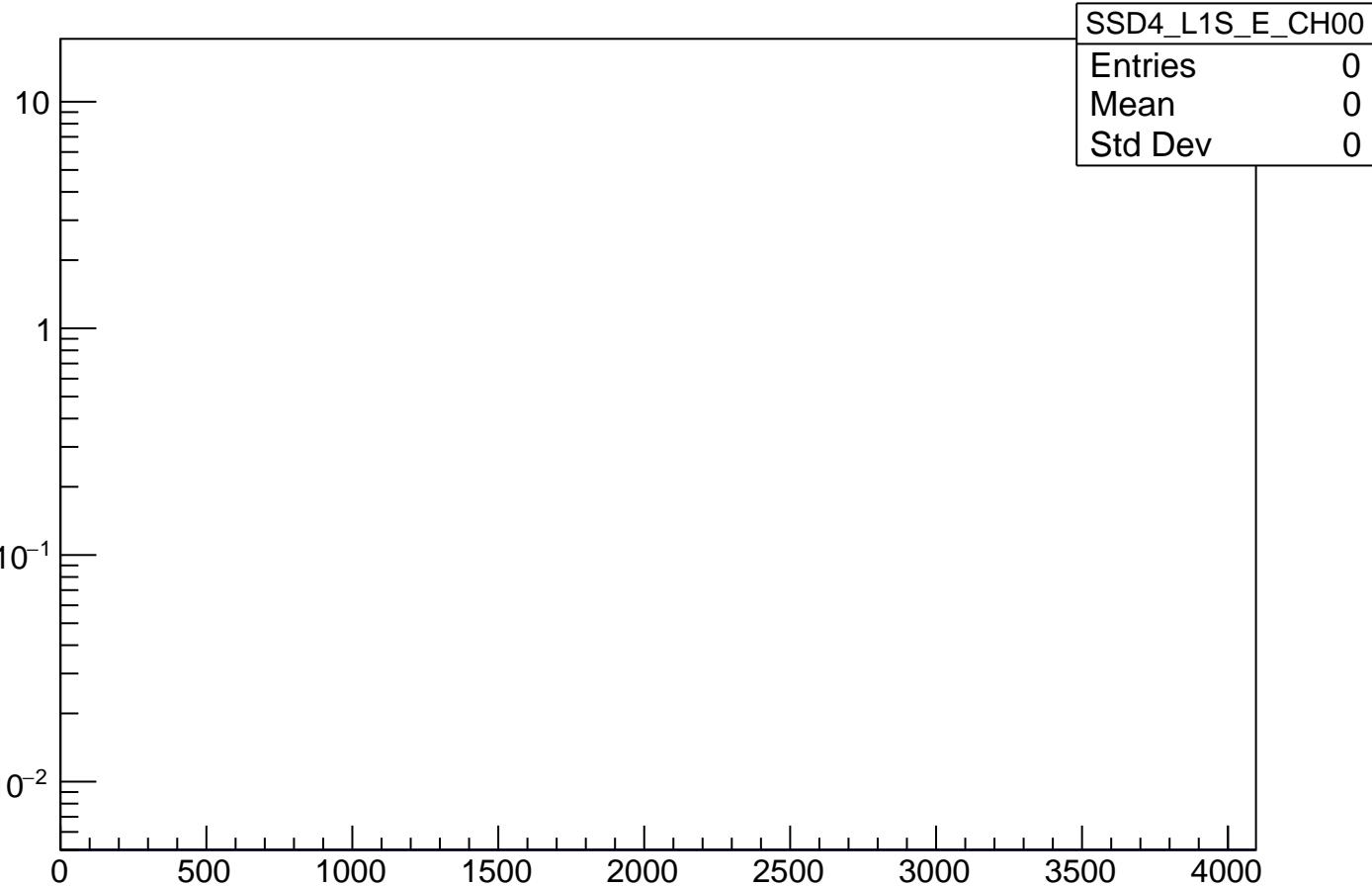
SSD4_L2F_T_CH14



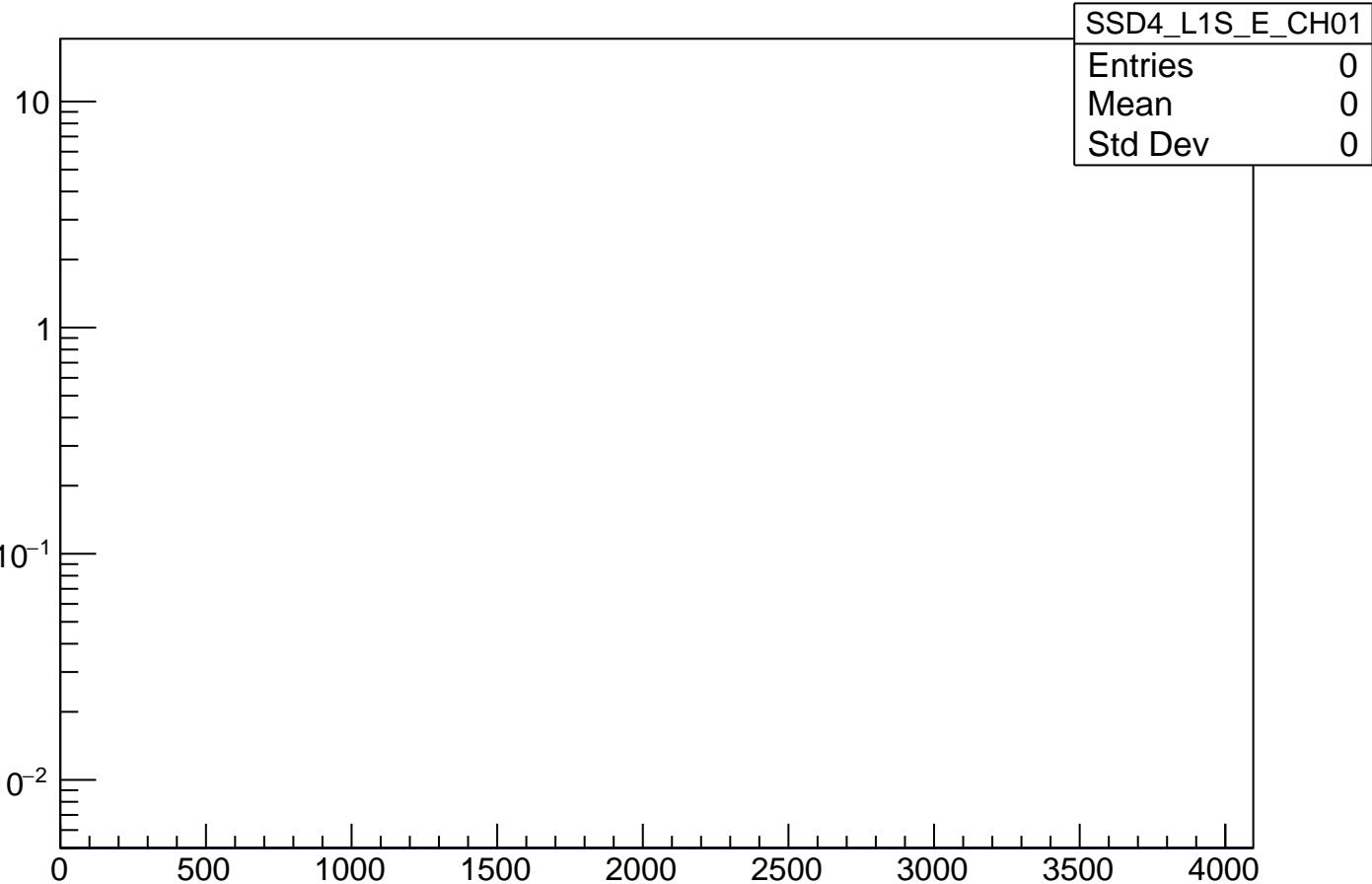
SSD4_L2F_T_CH15



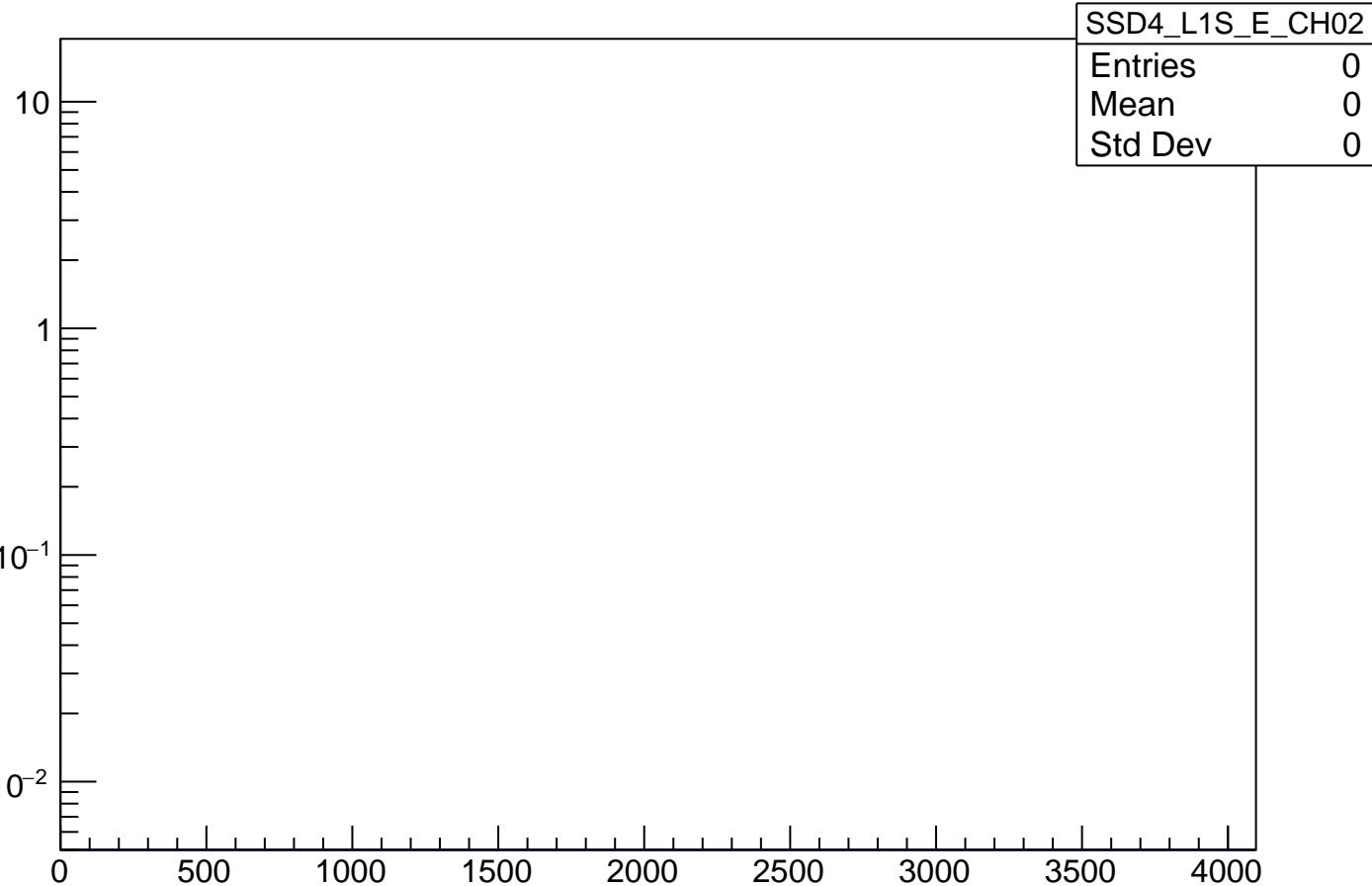
SSD4_L1S_E_CH00



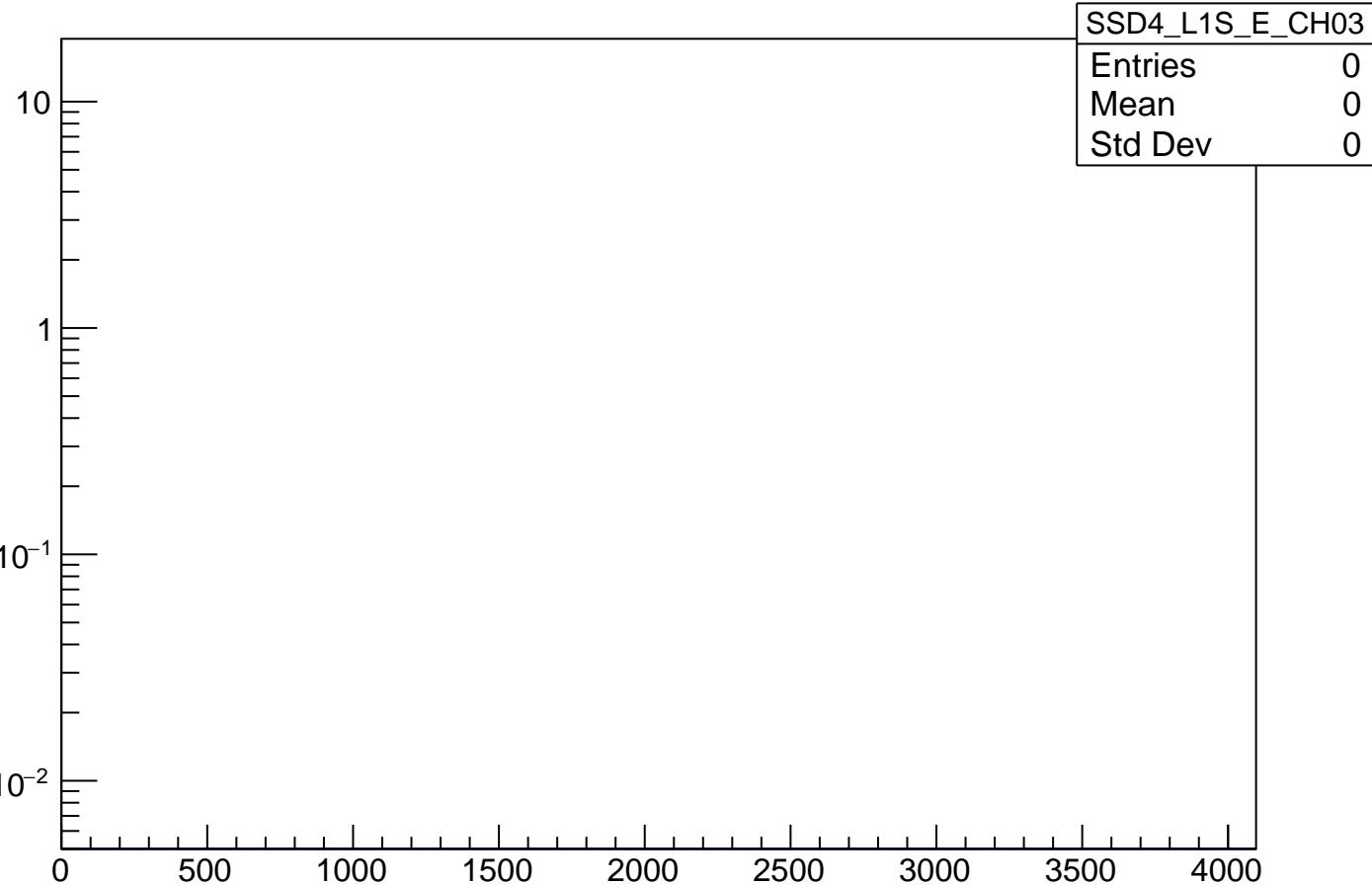
SSD4_L1S_E_CH01



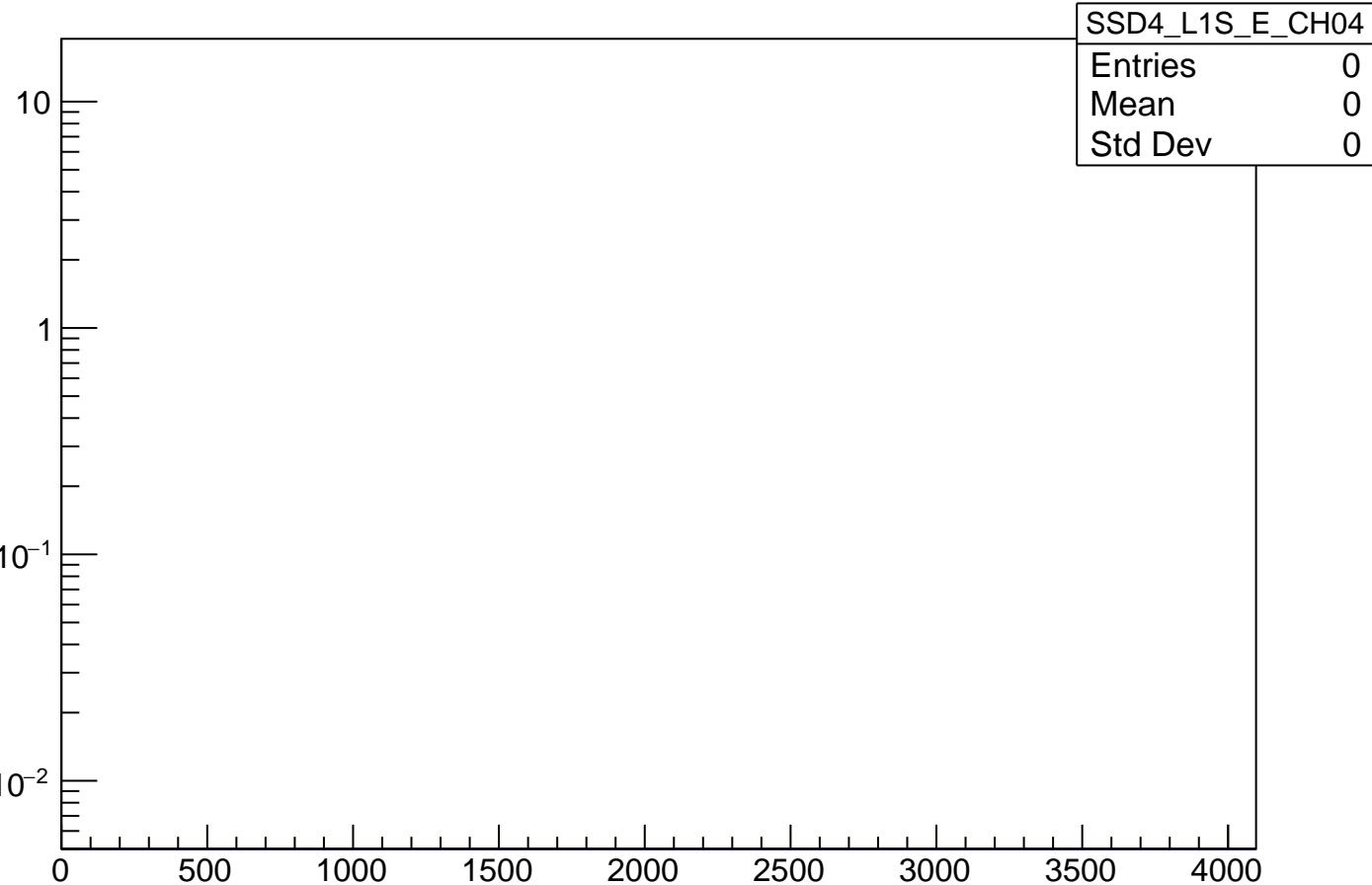
SSD4_L1S_E_CH02



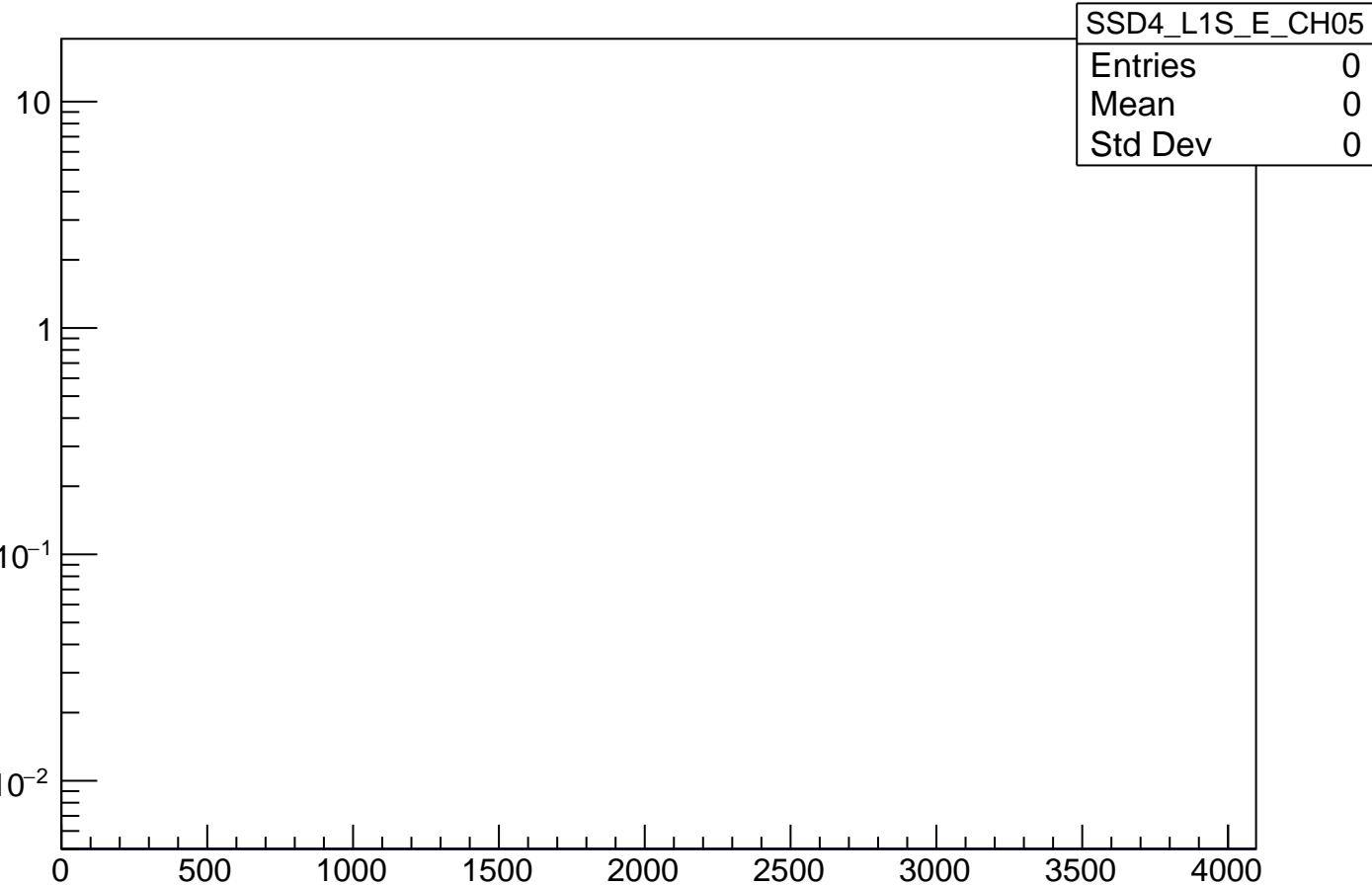
SSD4_L1S_E_CH03



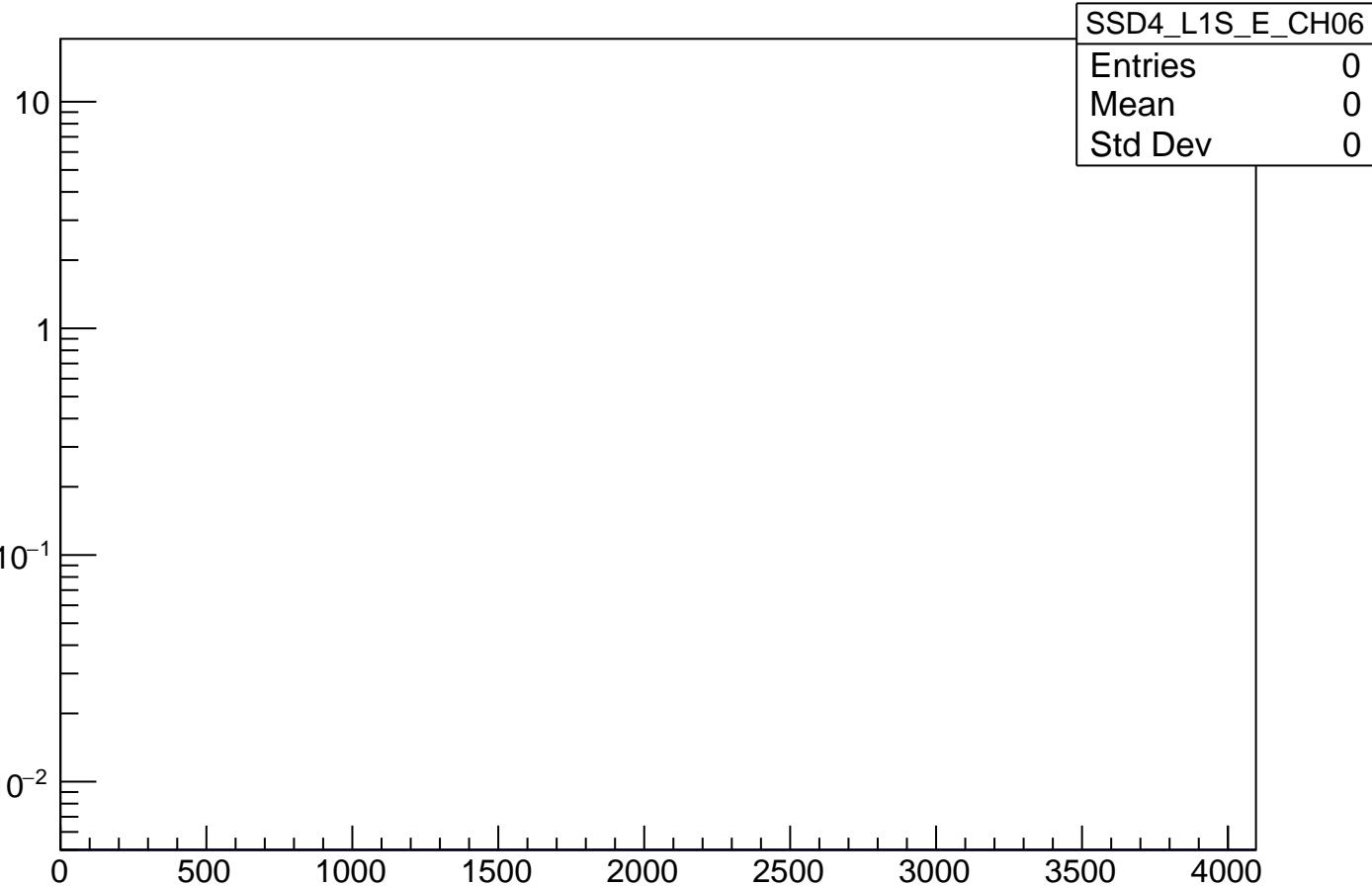
SSD4_L1S_E_CH04



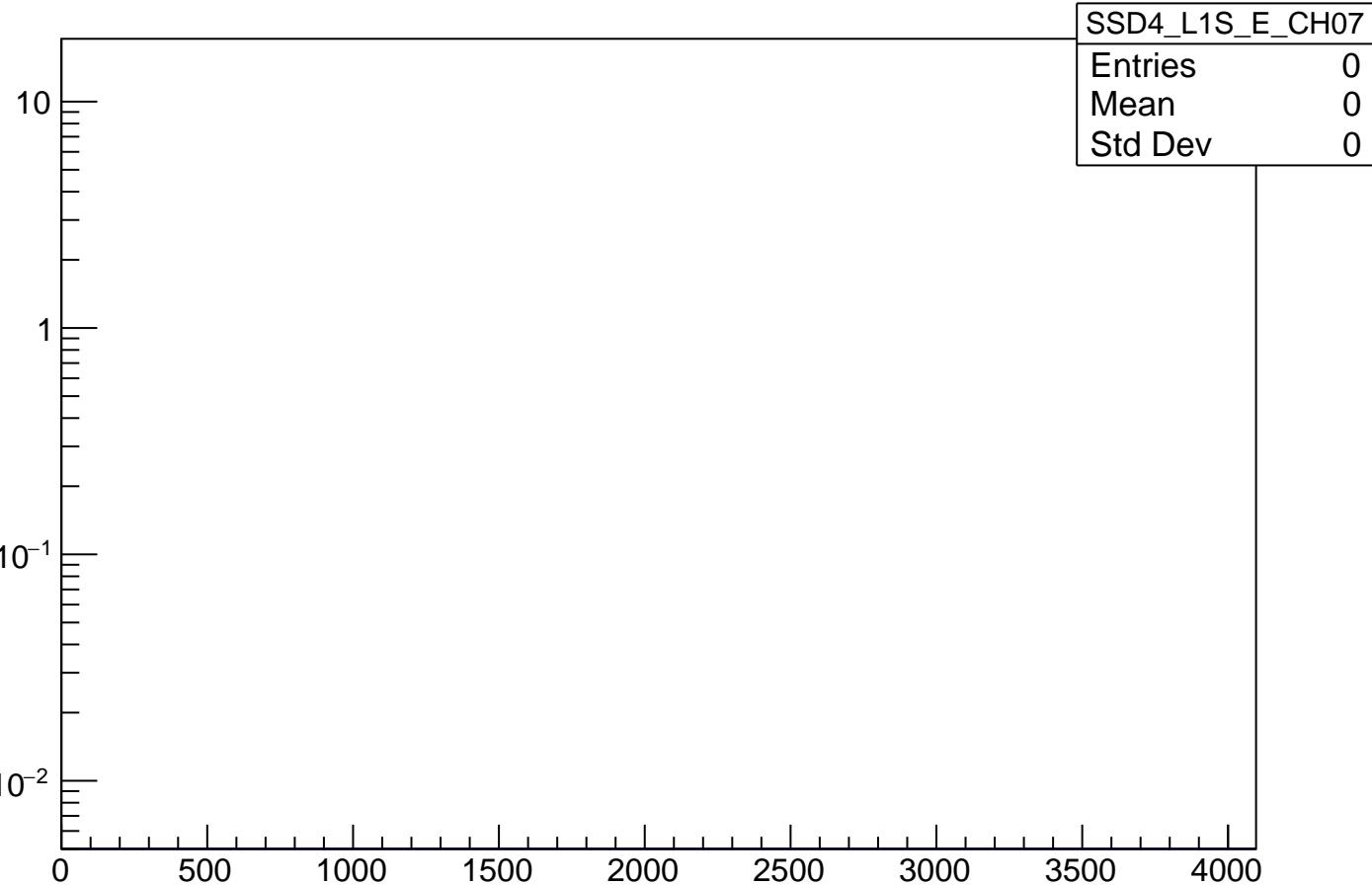
SSD4_L1S_E_CH05



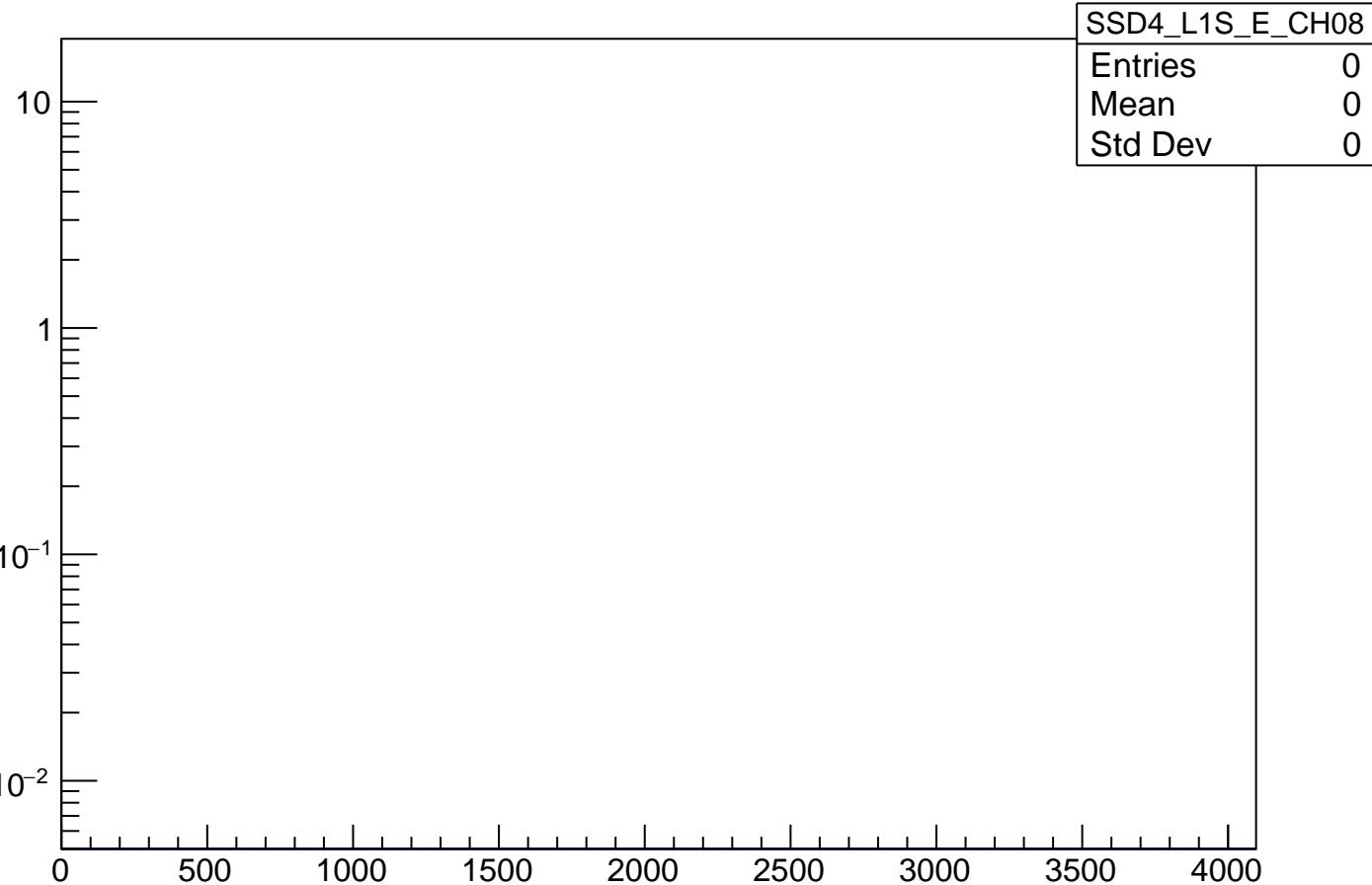
SSD4_L1S_E_CH06



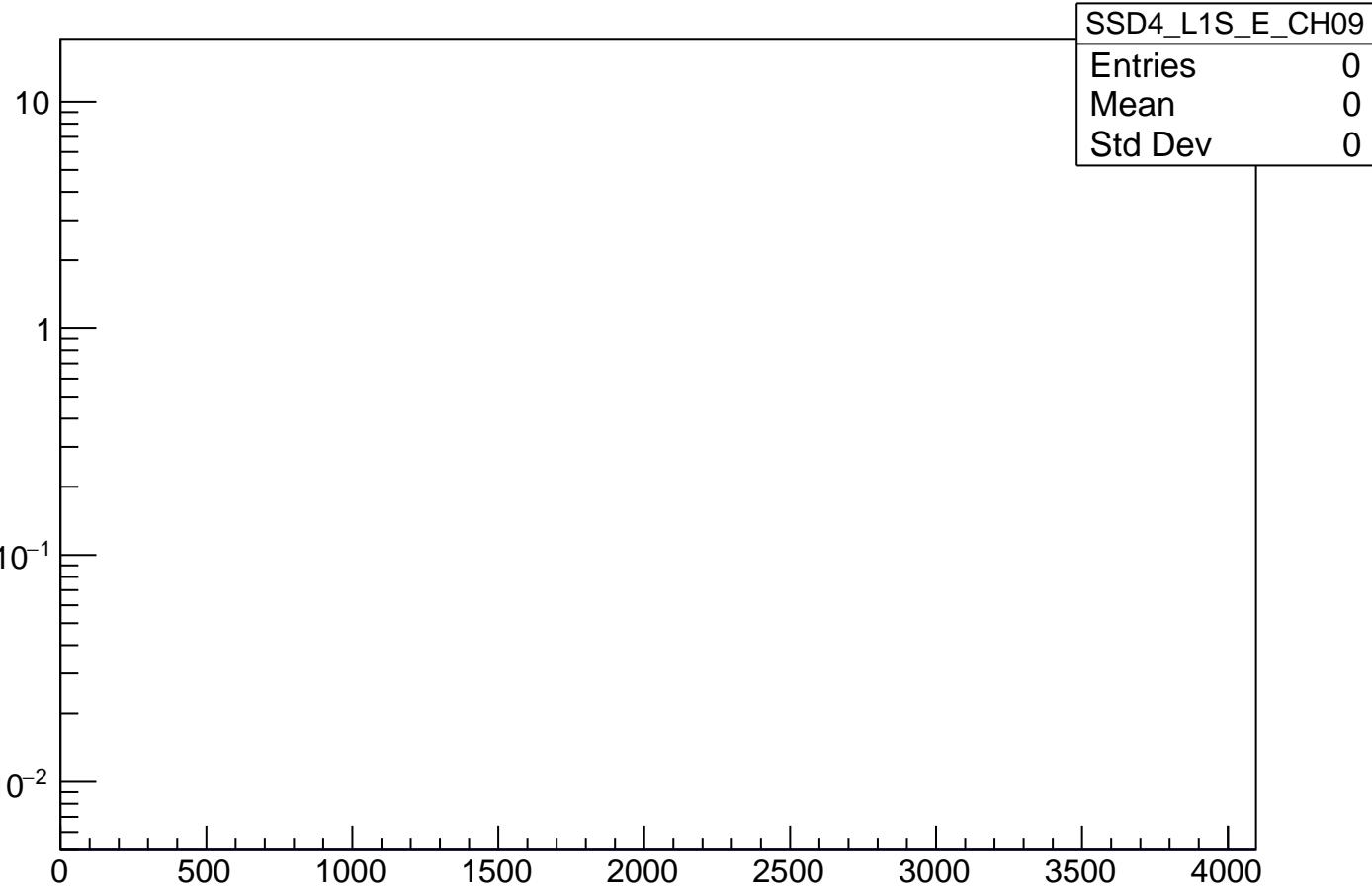
SSD4_L1S_E_CH07



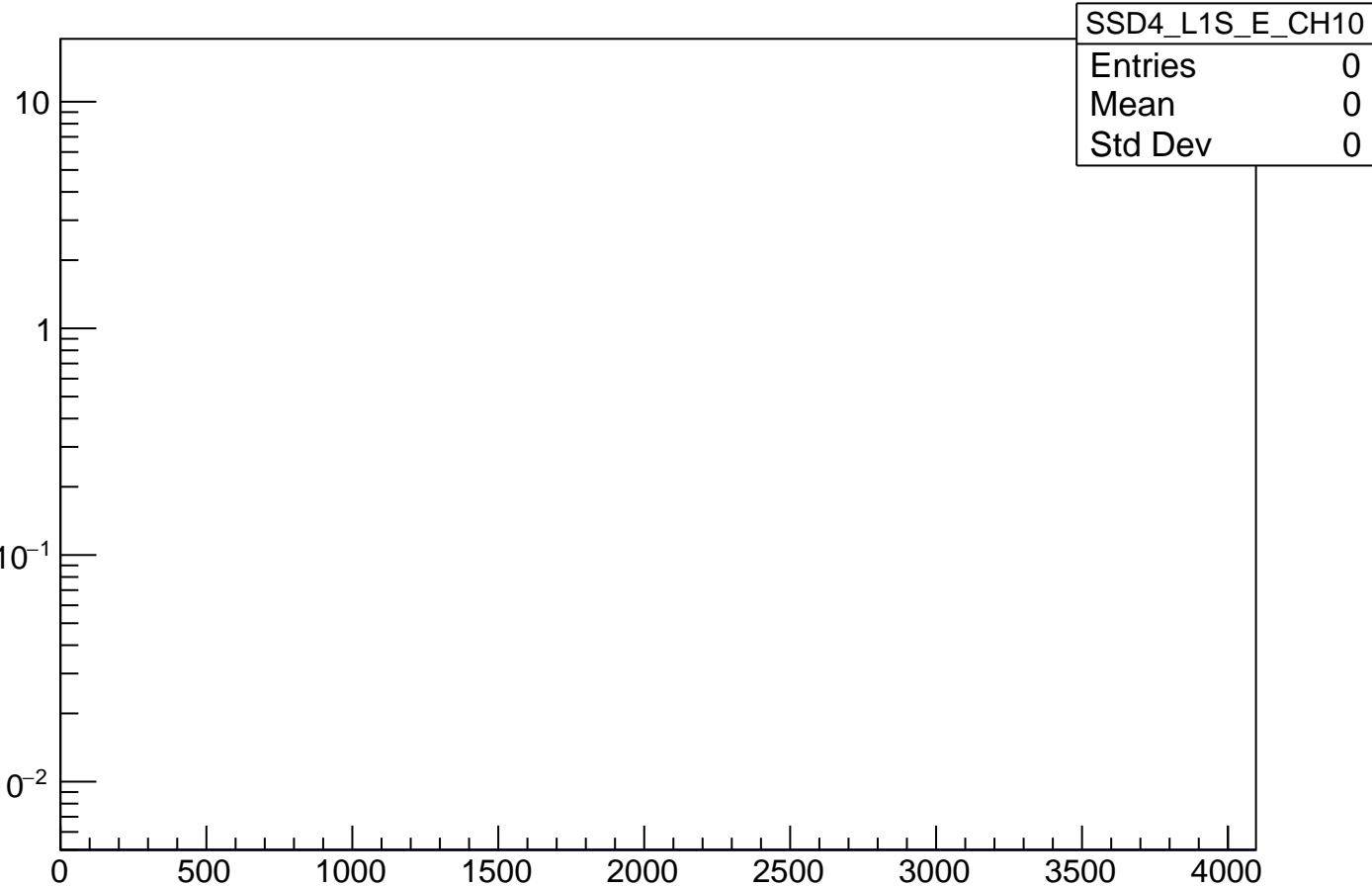
SSD4_L1S_E_CH08



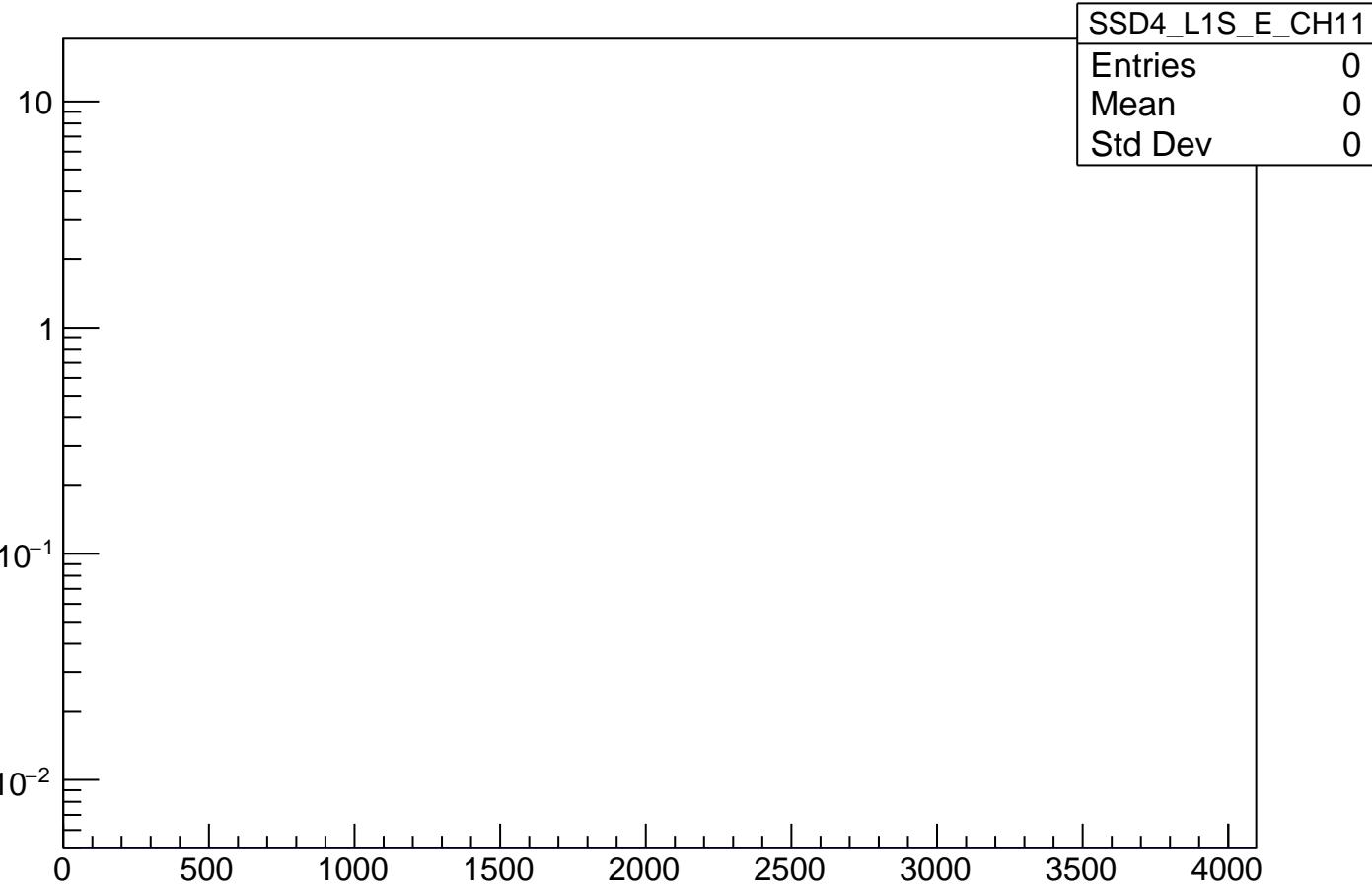
SSD4_L1S_E_CH09



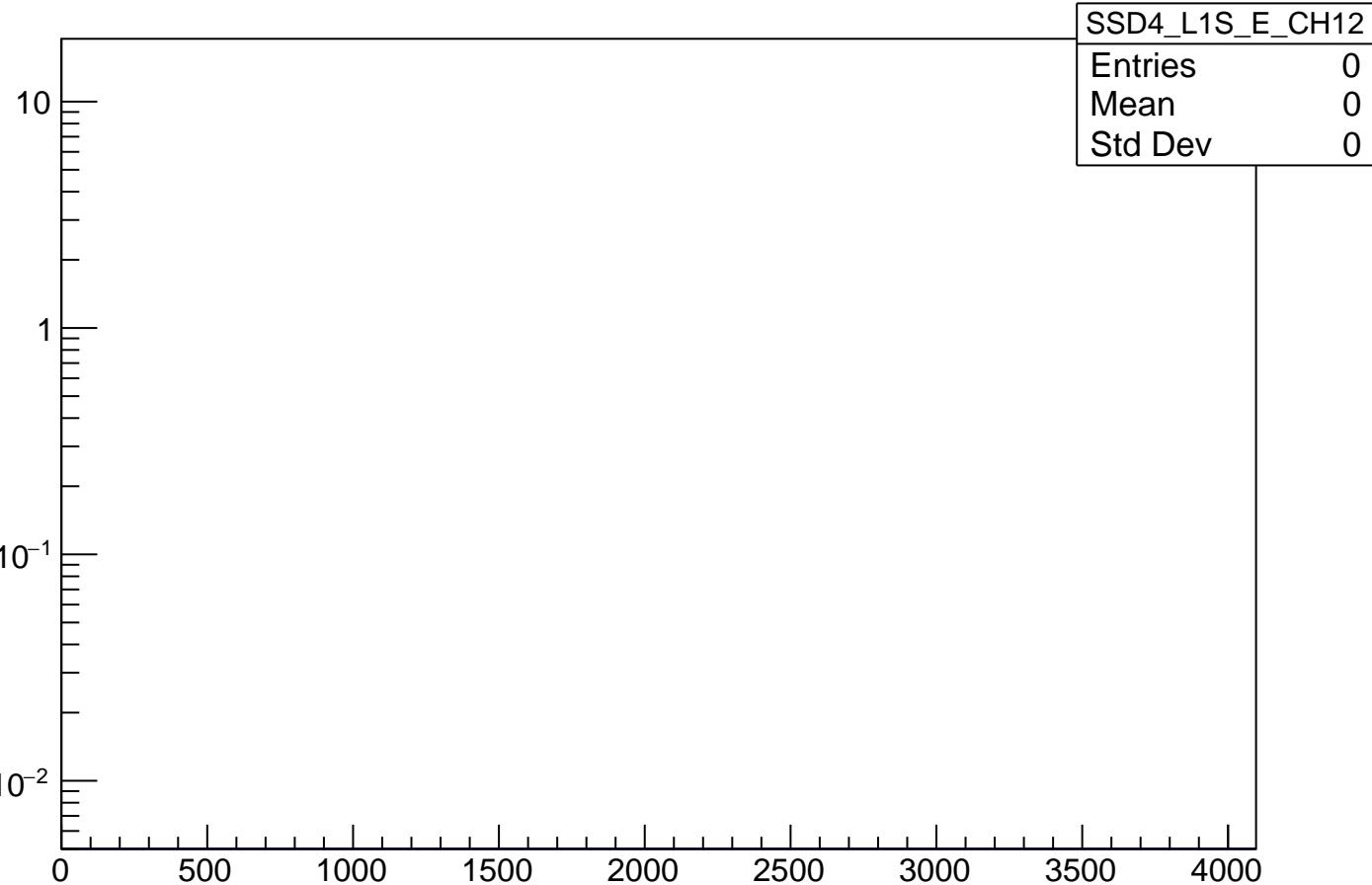
SSD4_L1S_E_CH10



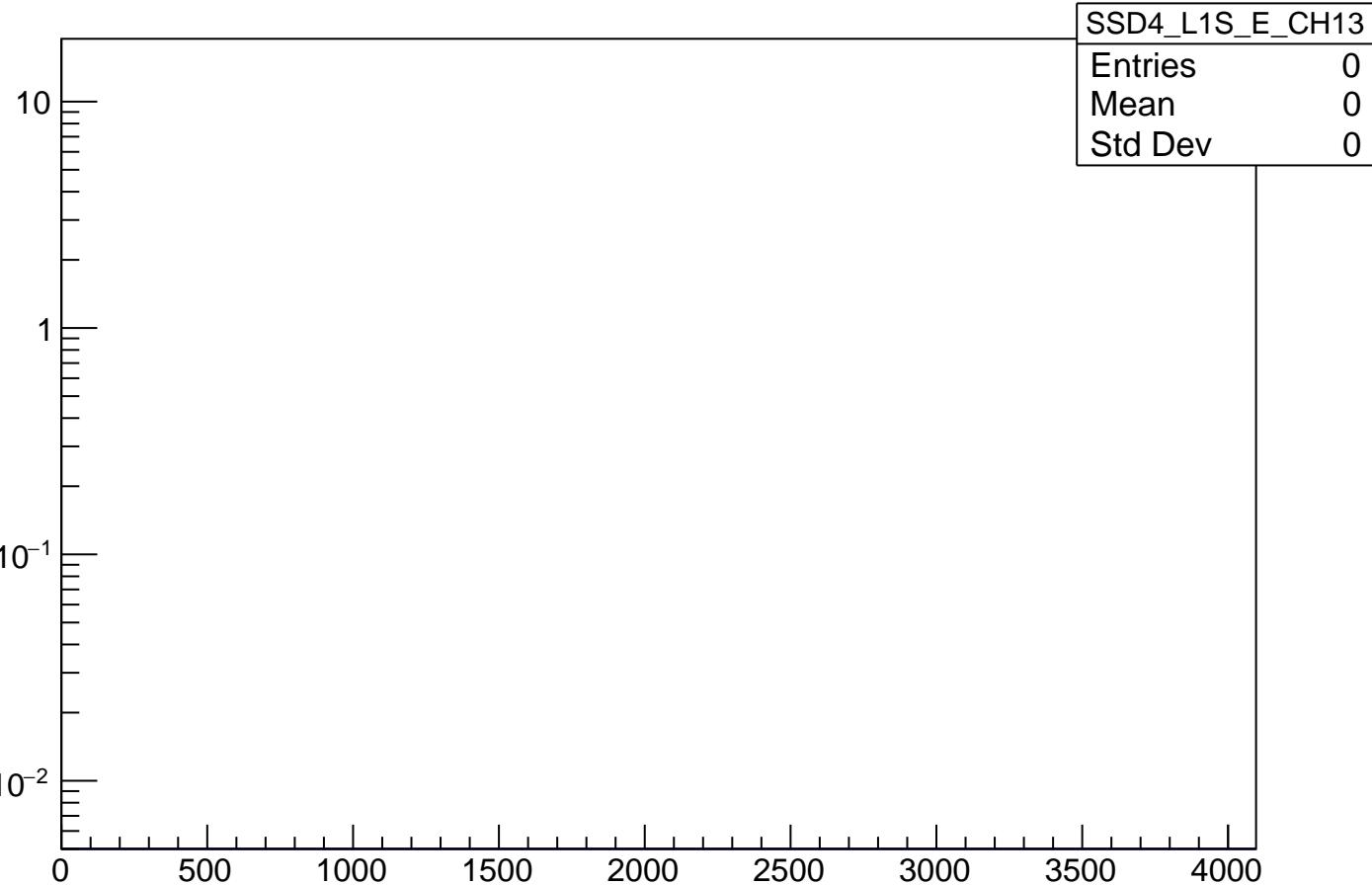
SSD4_L1S_E_CH11



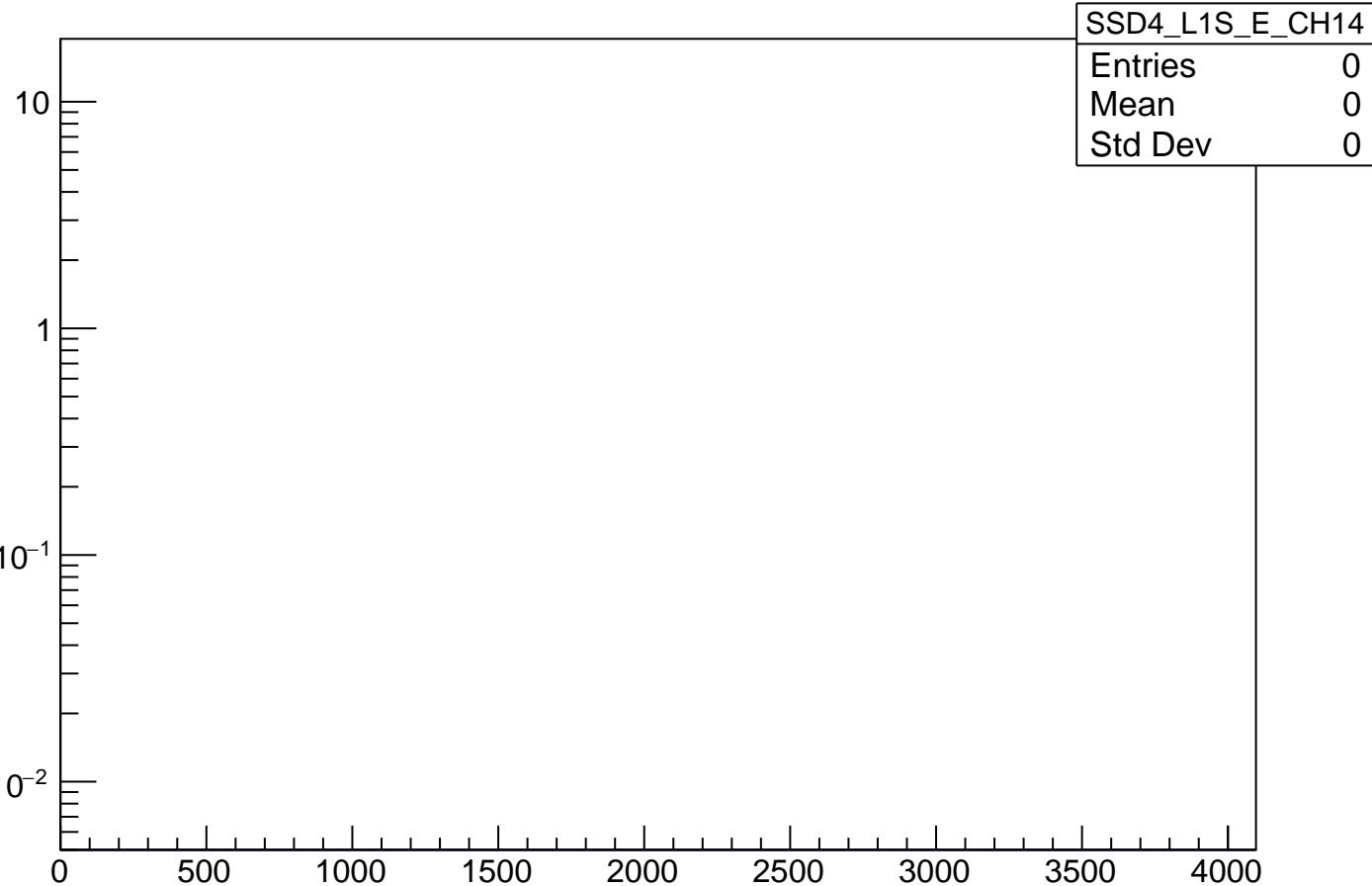
SSD4_L1S_E_CH12



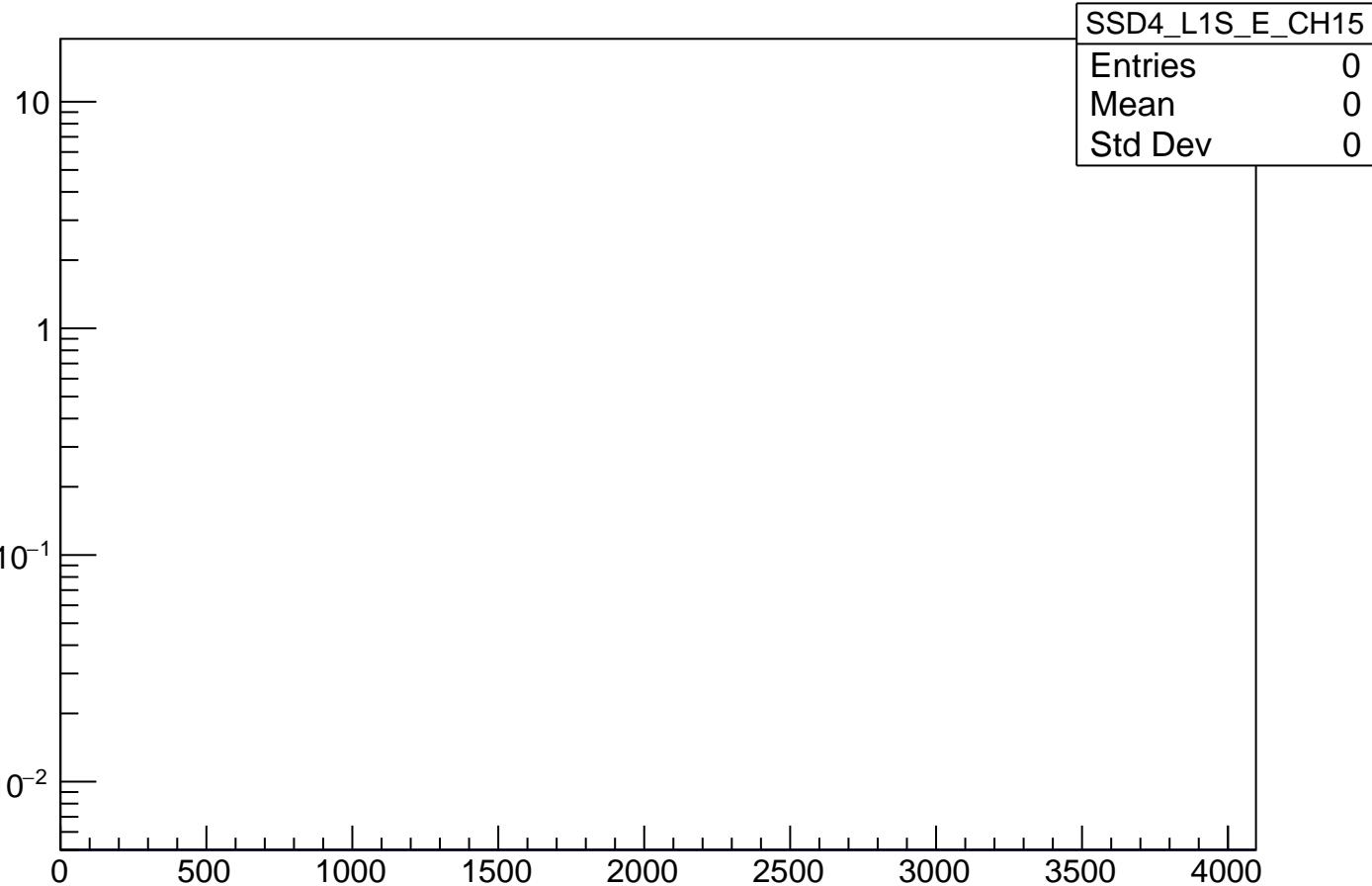
SSD4_L1S_E_CH13



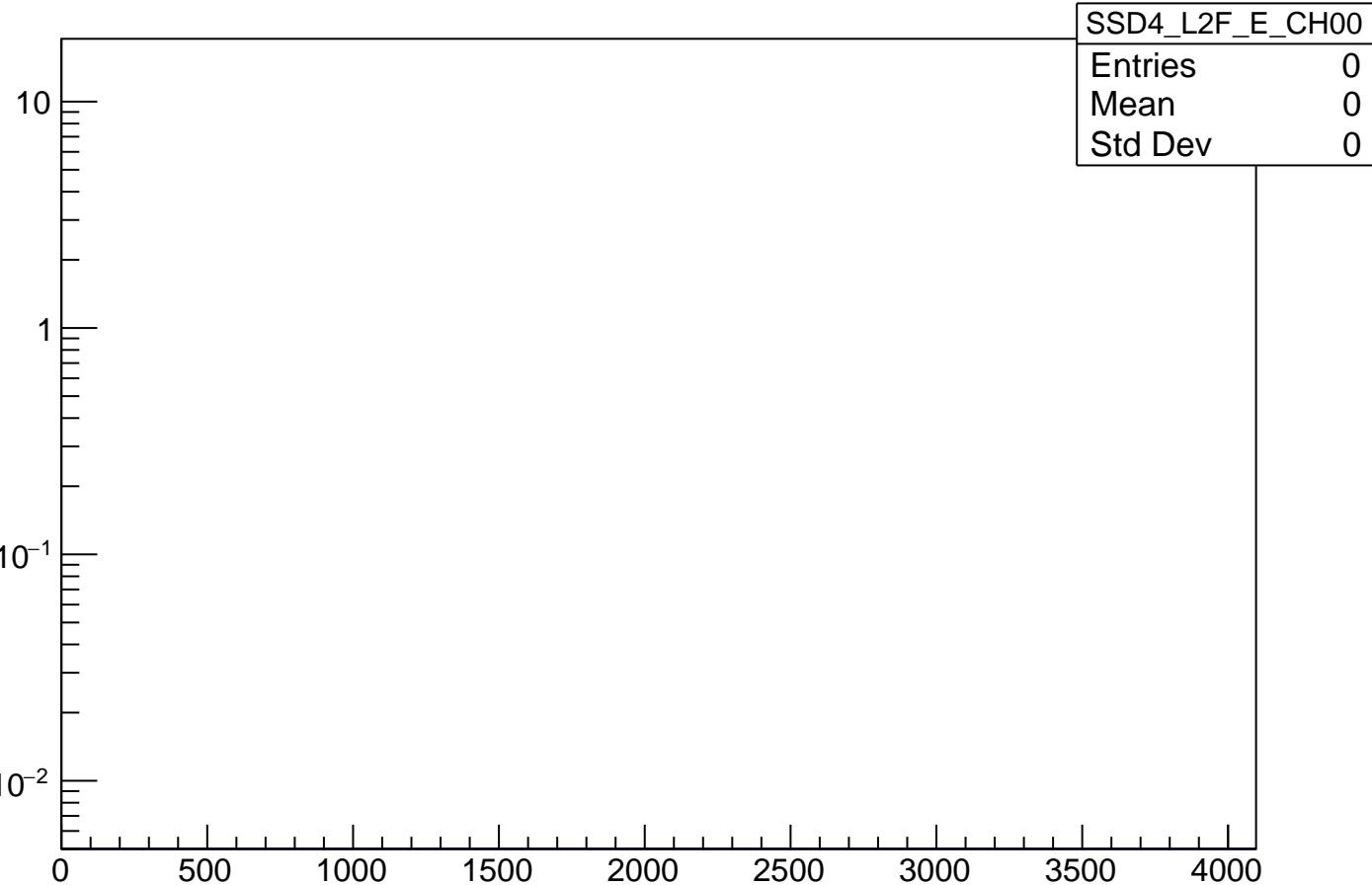
SSD4_L1S_E_CH14



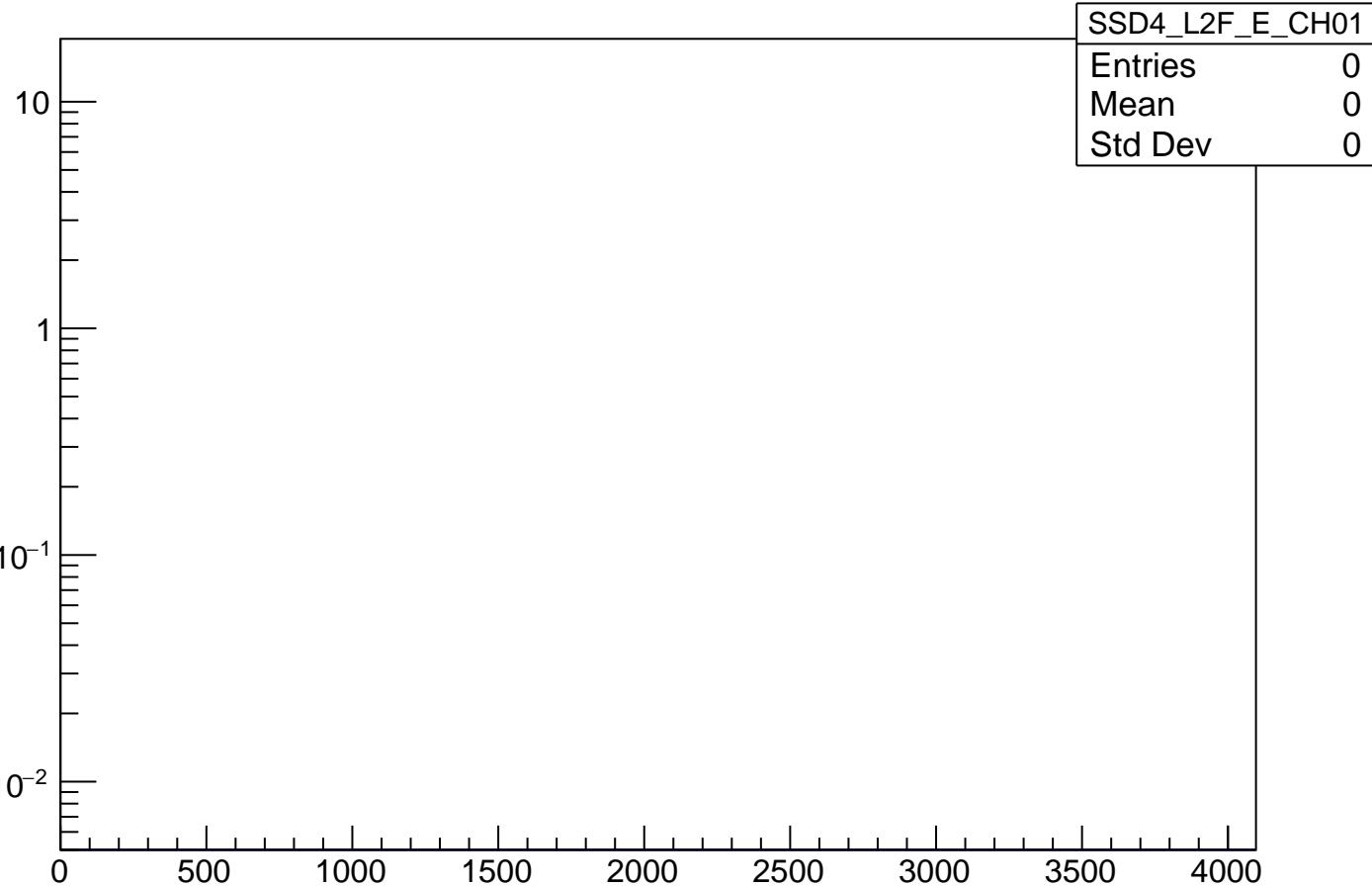
SSD4_L1S_E_CH15



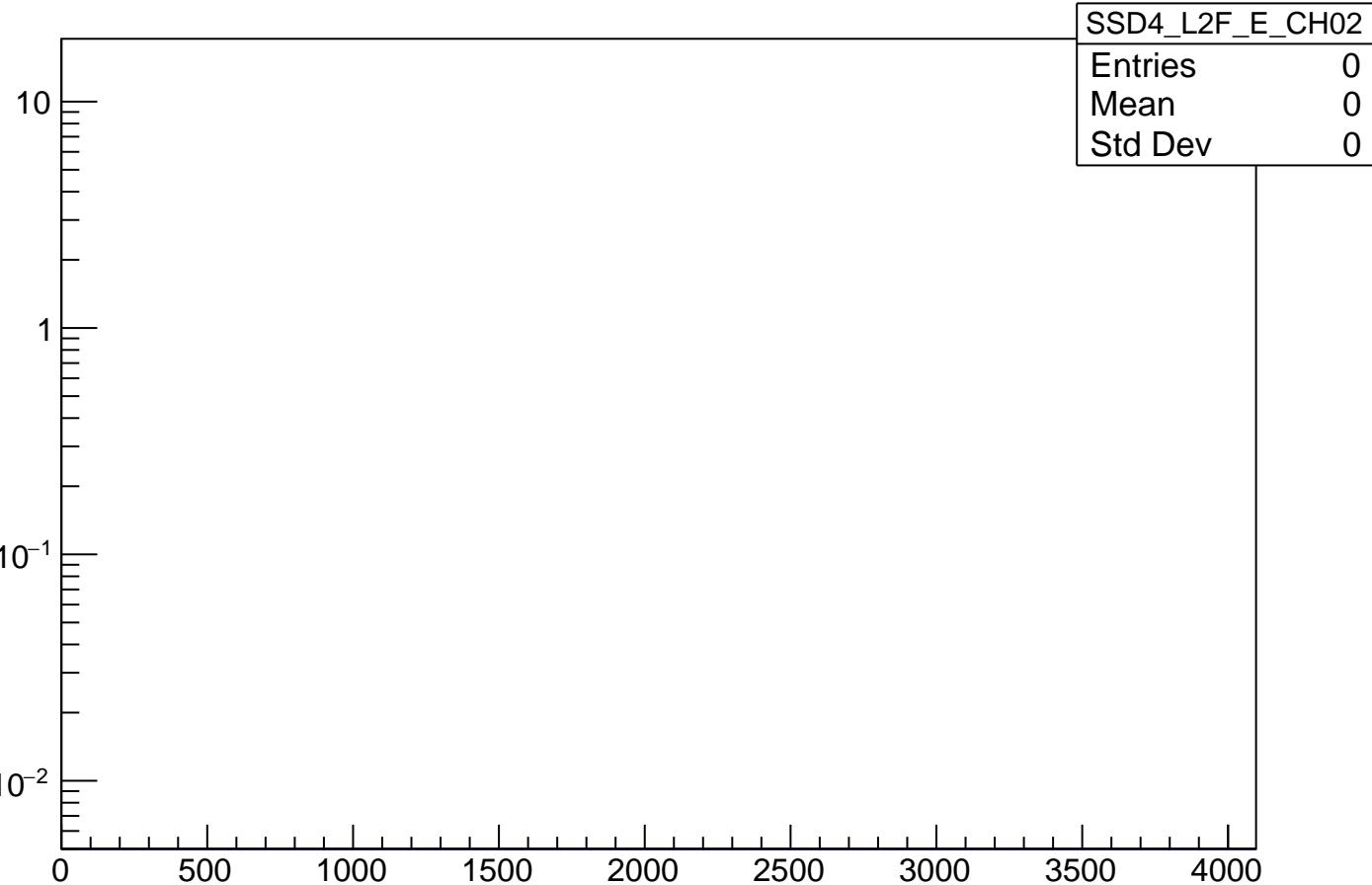
SSD4_L2F_E_CH00



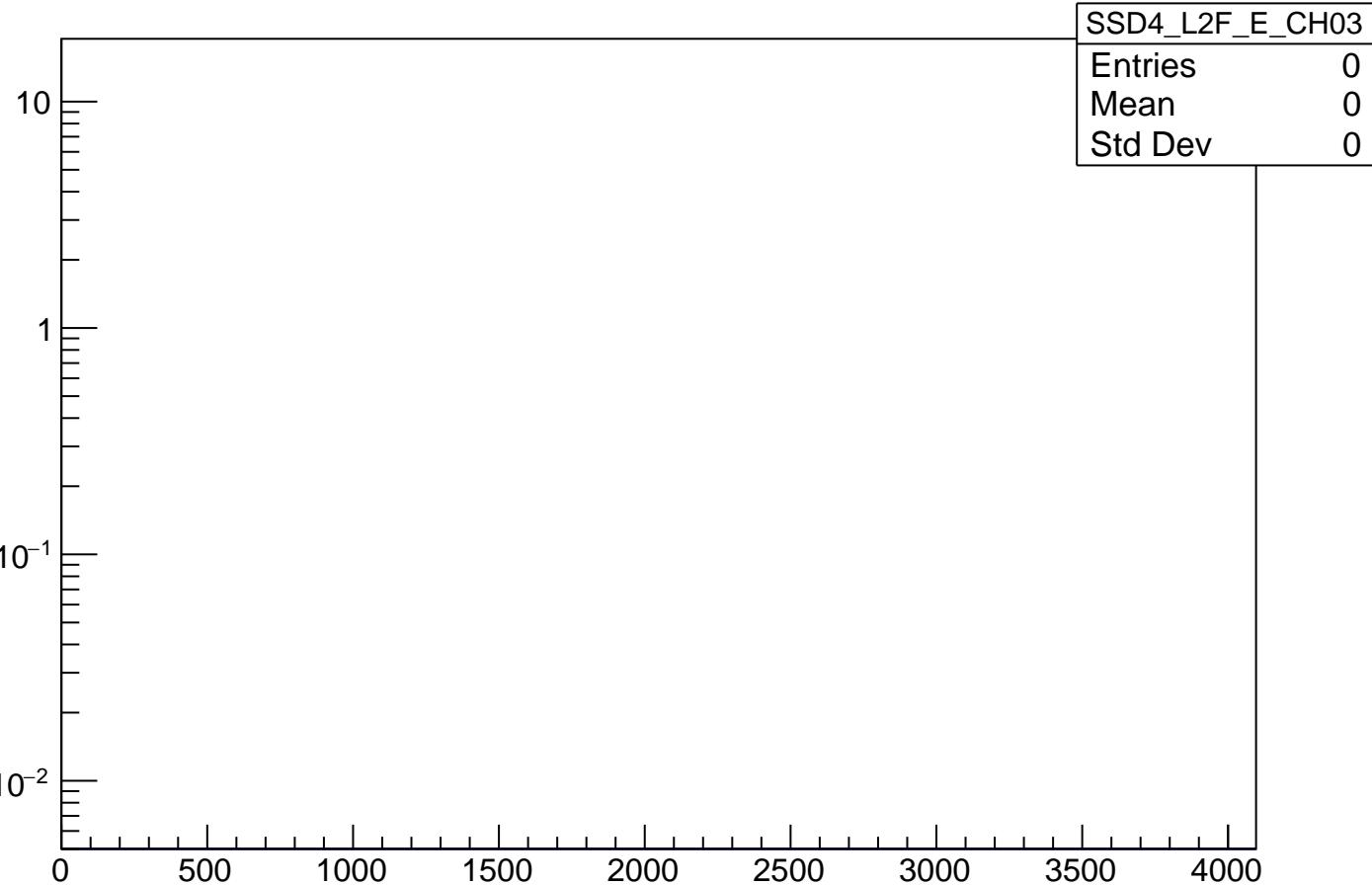
SSD4_L2F_E_CH01



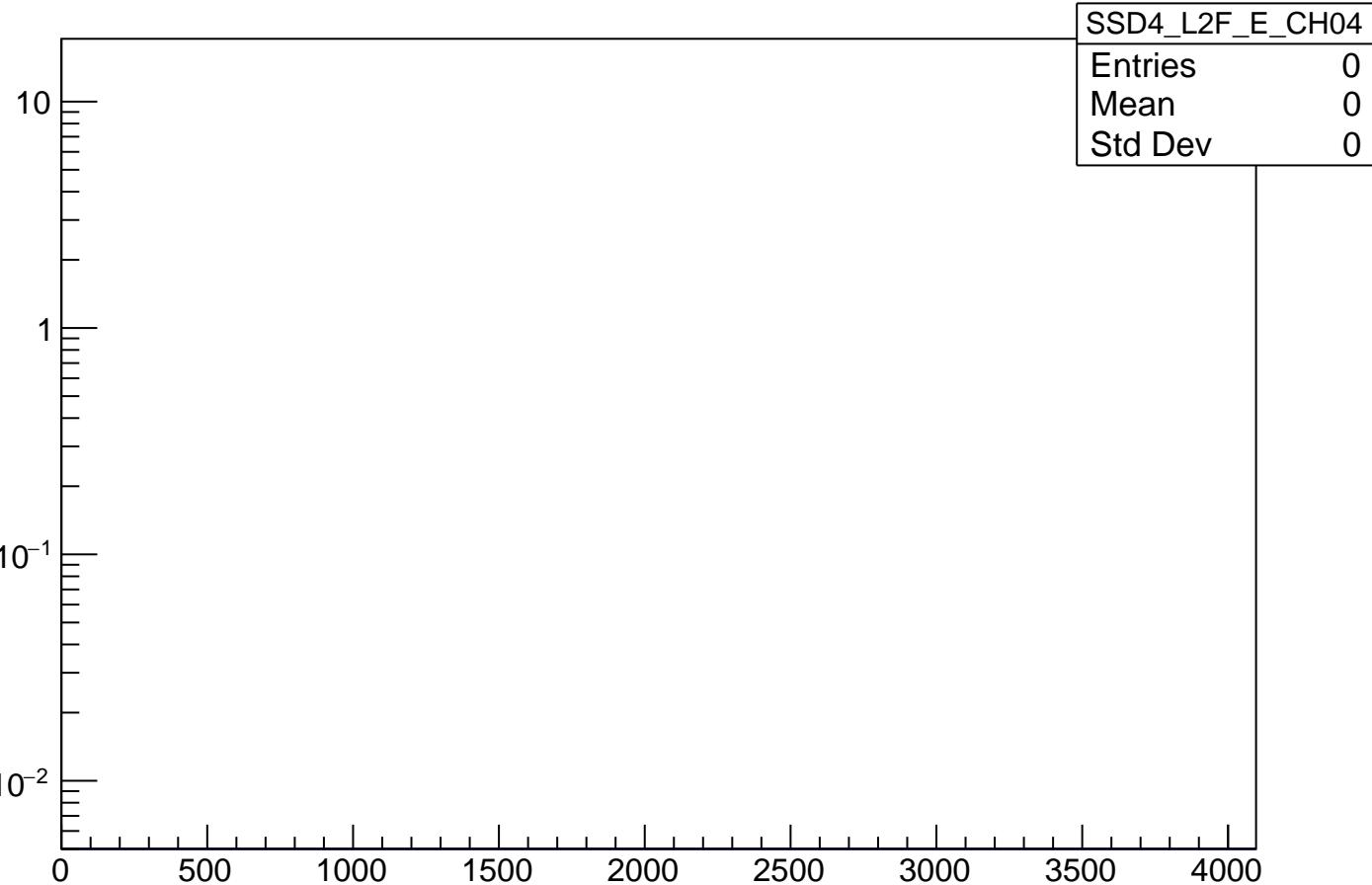
SSD4_L2F_E_CH02



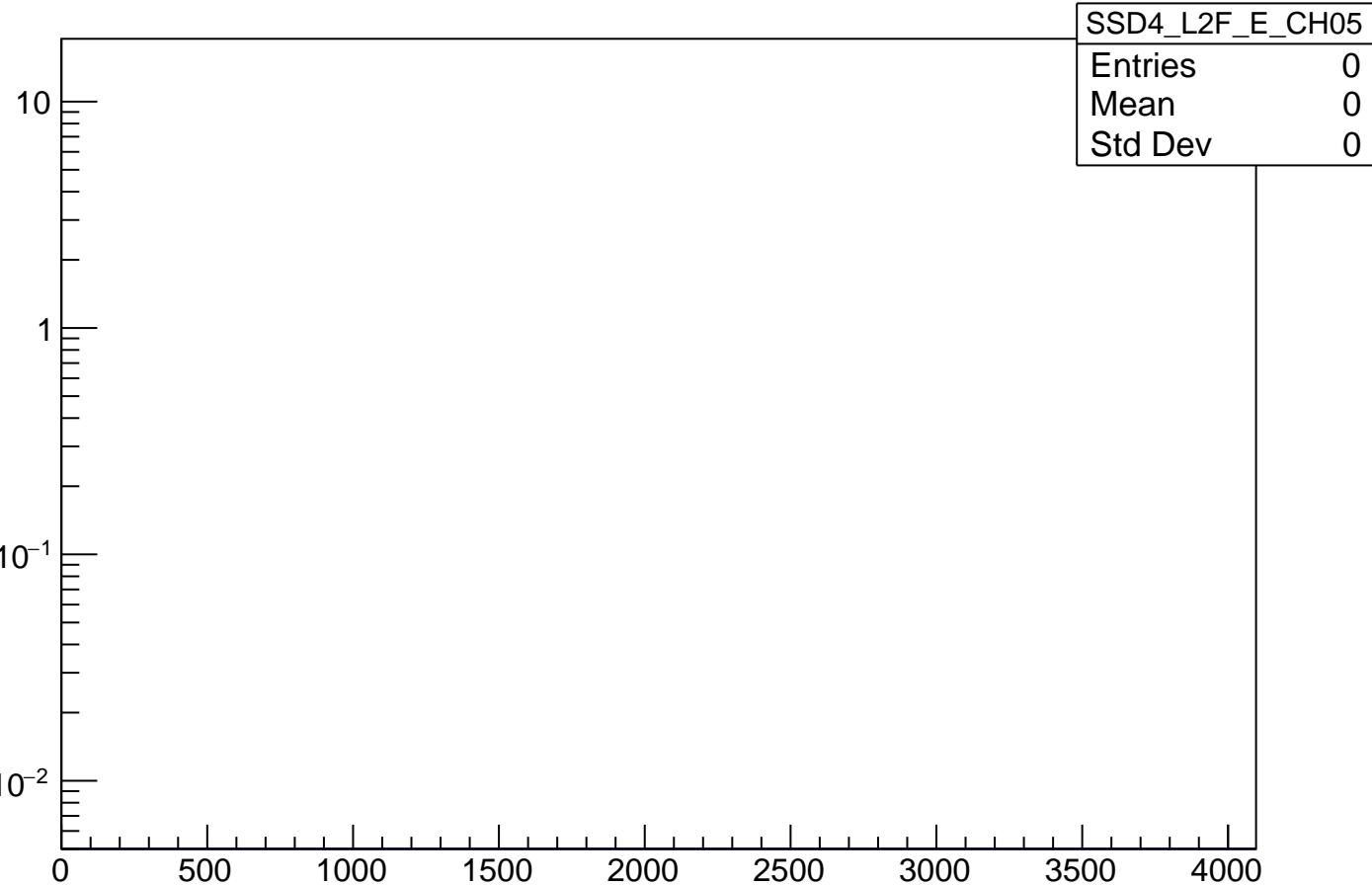
SSD4_L2F_E_CH03



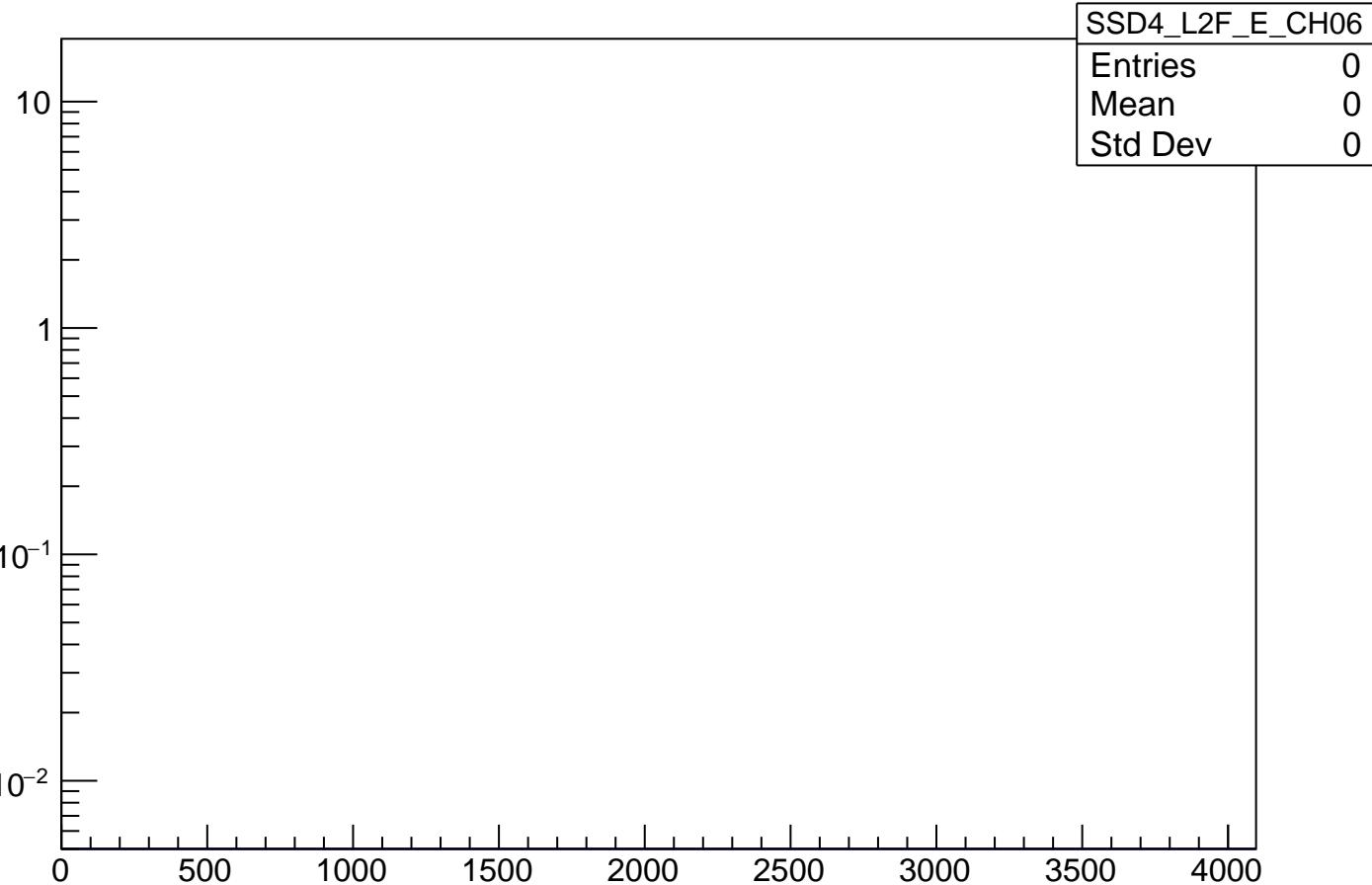
SSD4_L2F_E_CH04



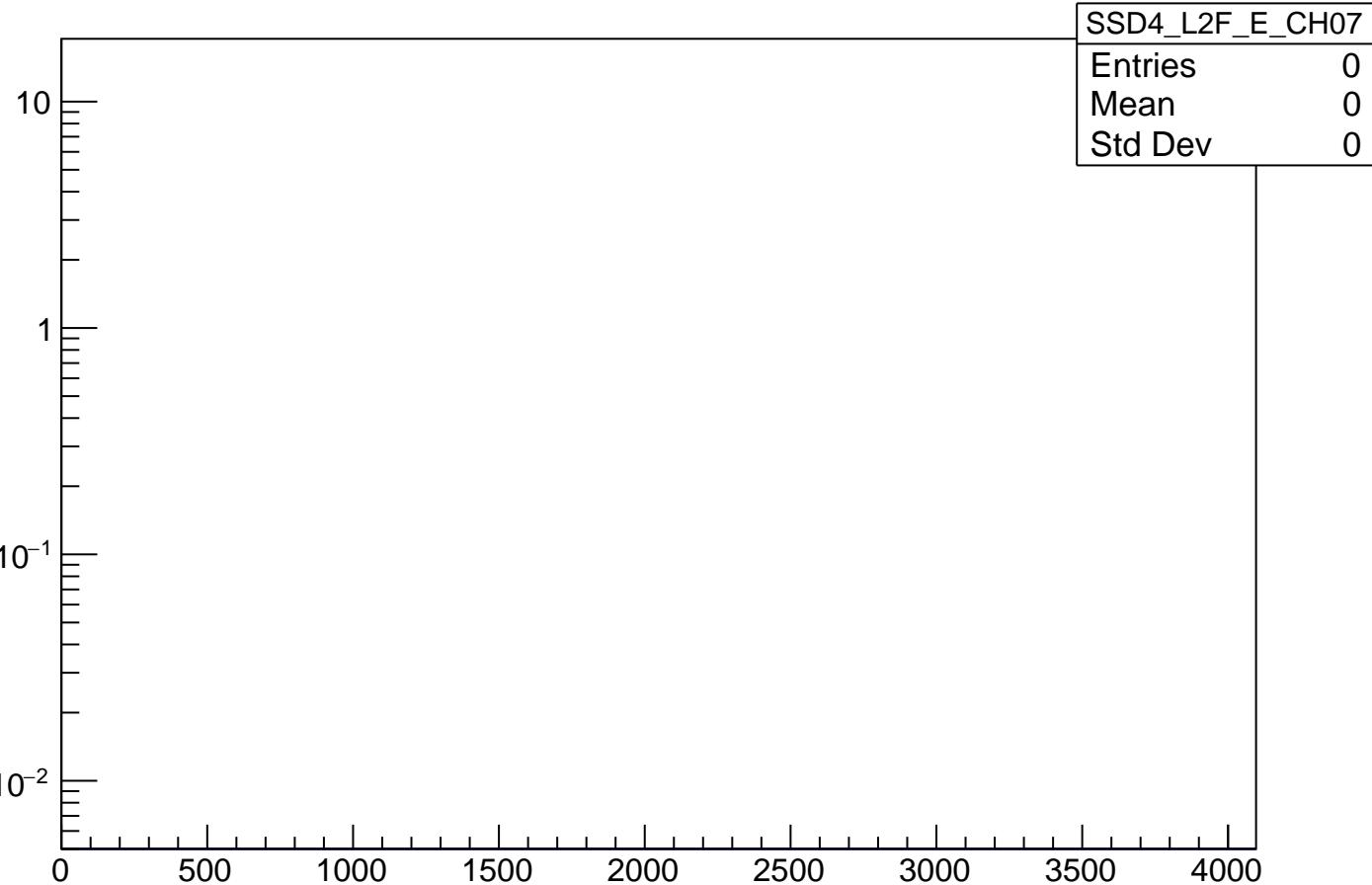
SSD4_L2F_E_CH05



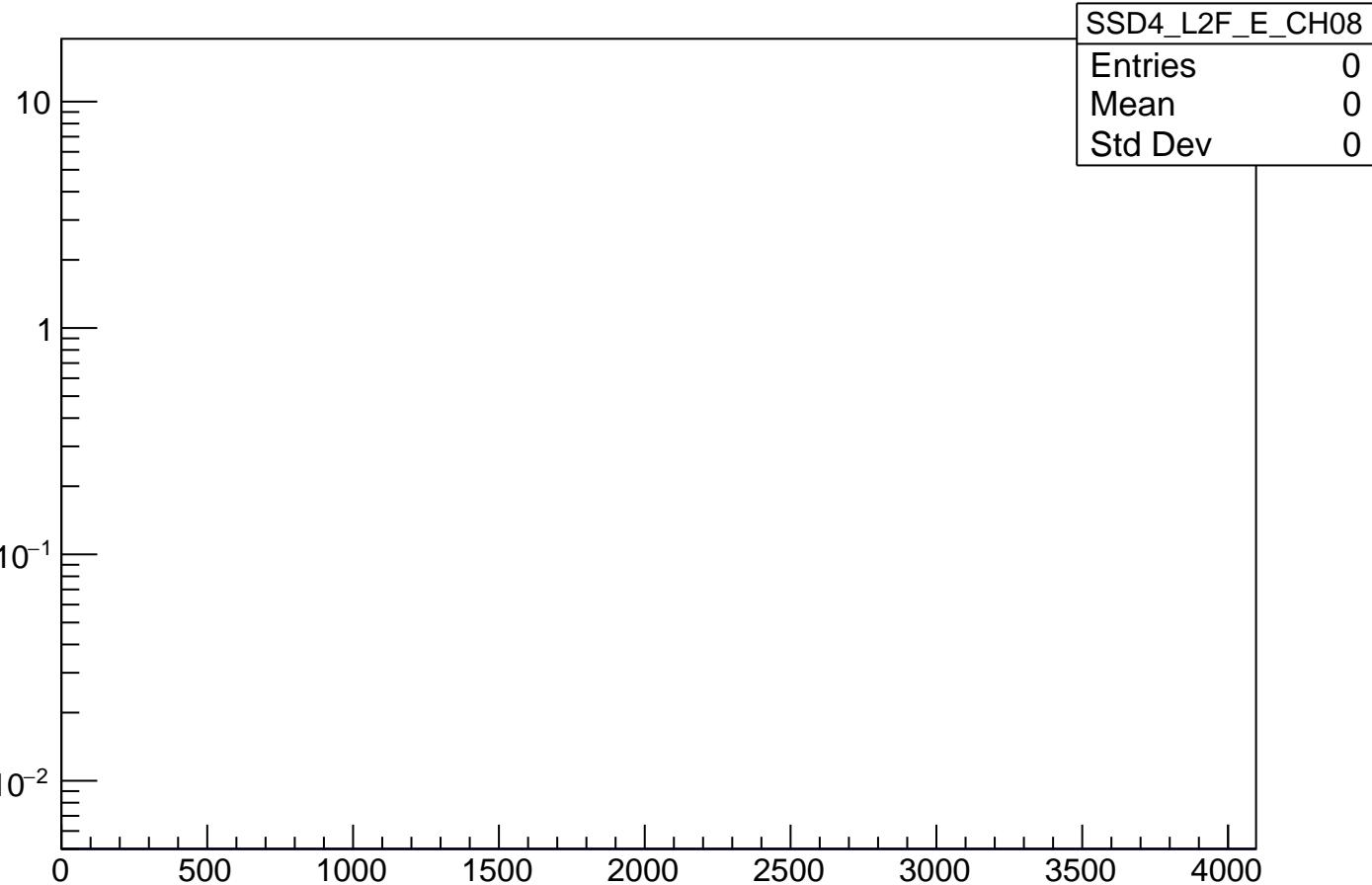
SSD4_L2F_E_CH06



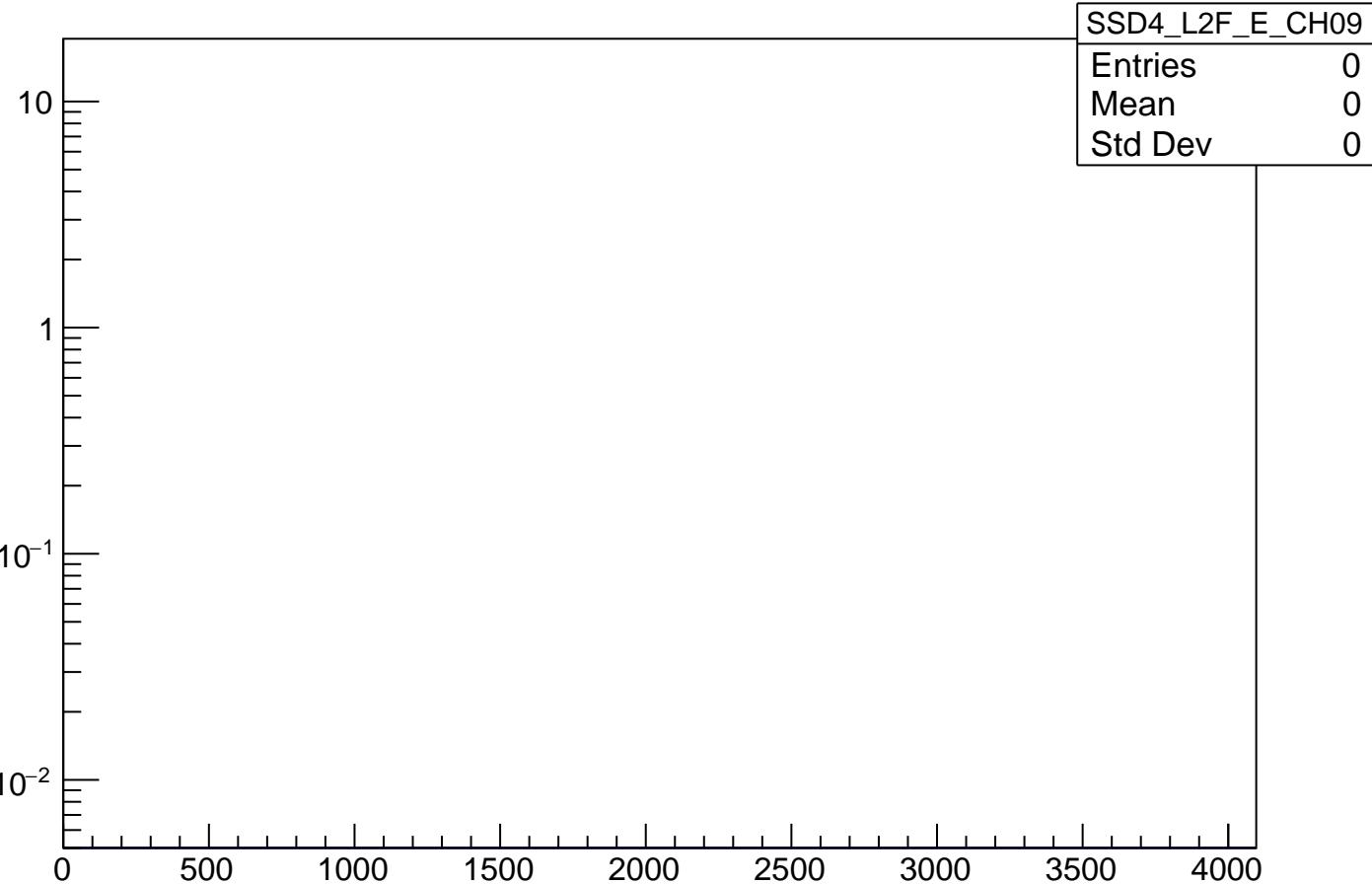
SSD4_L2F_E_CH07



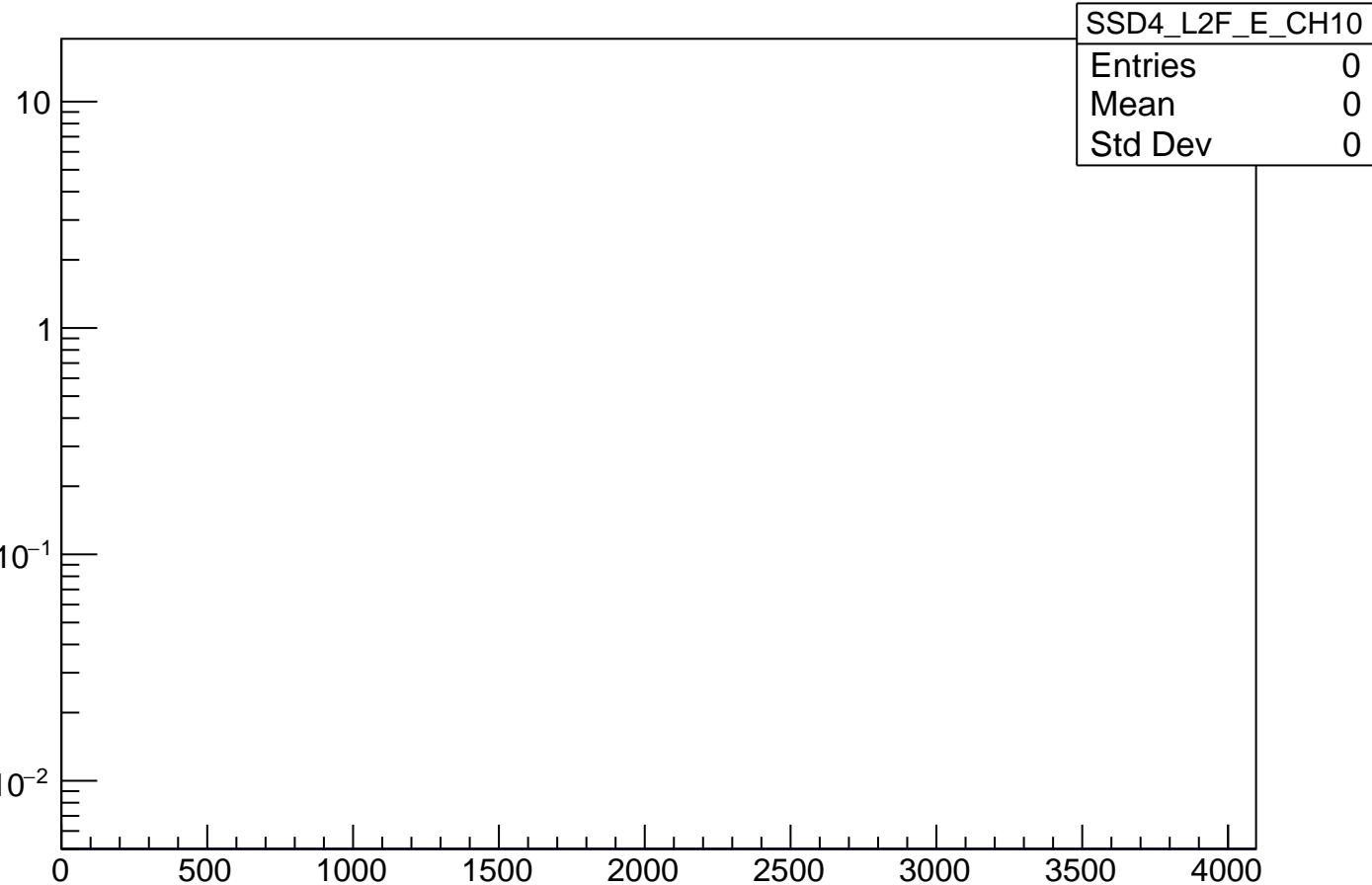
SSD4_L2F_E_CH08



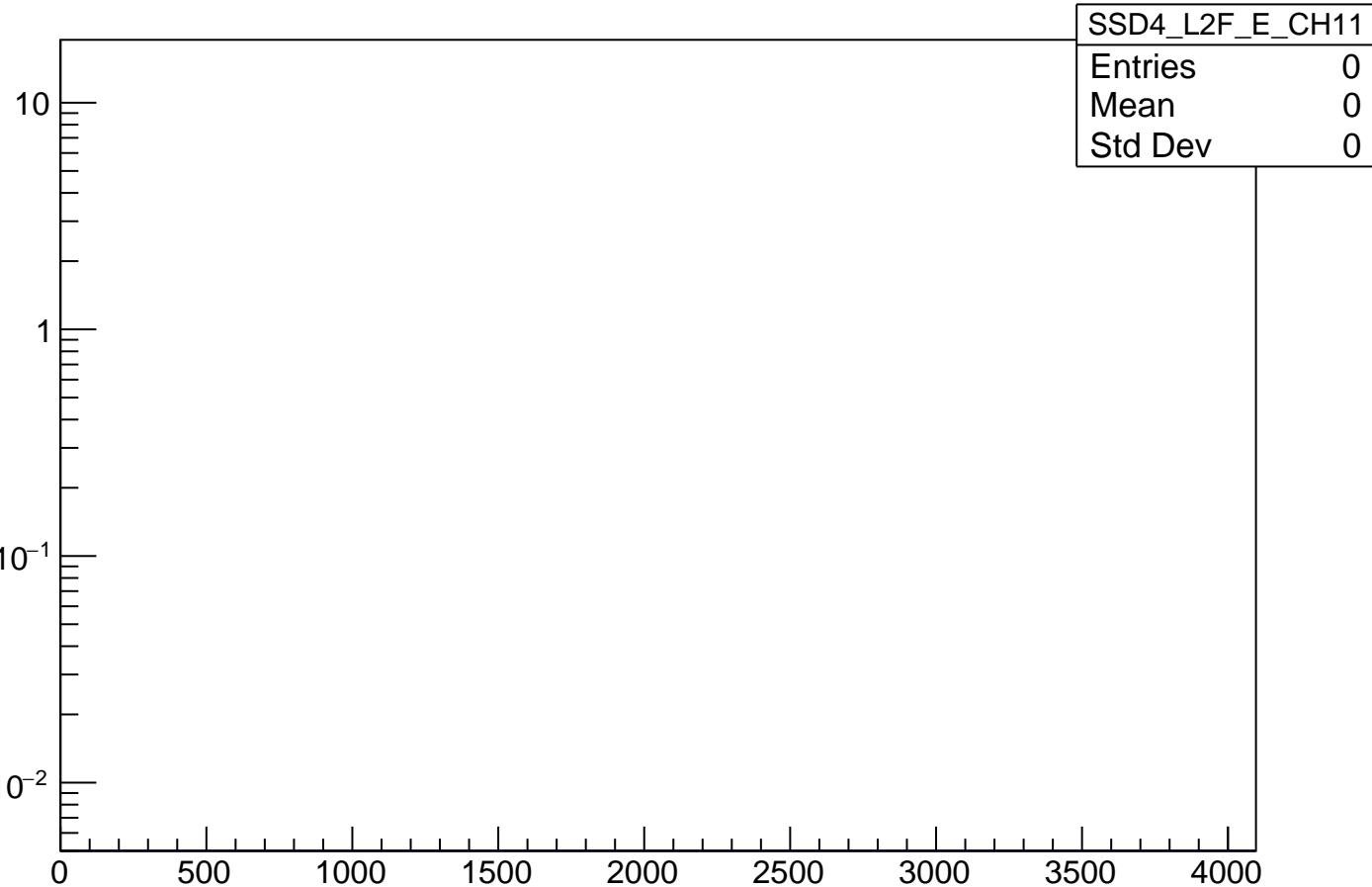
SSD4_L2F_E_CH09



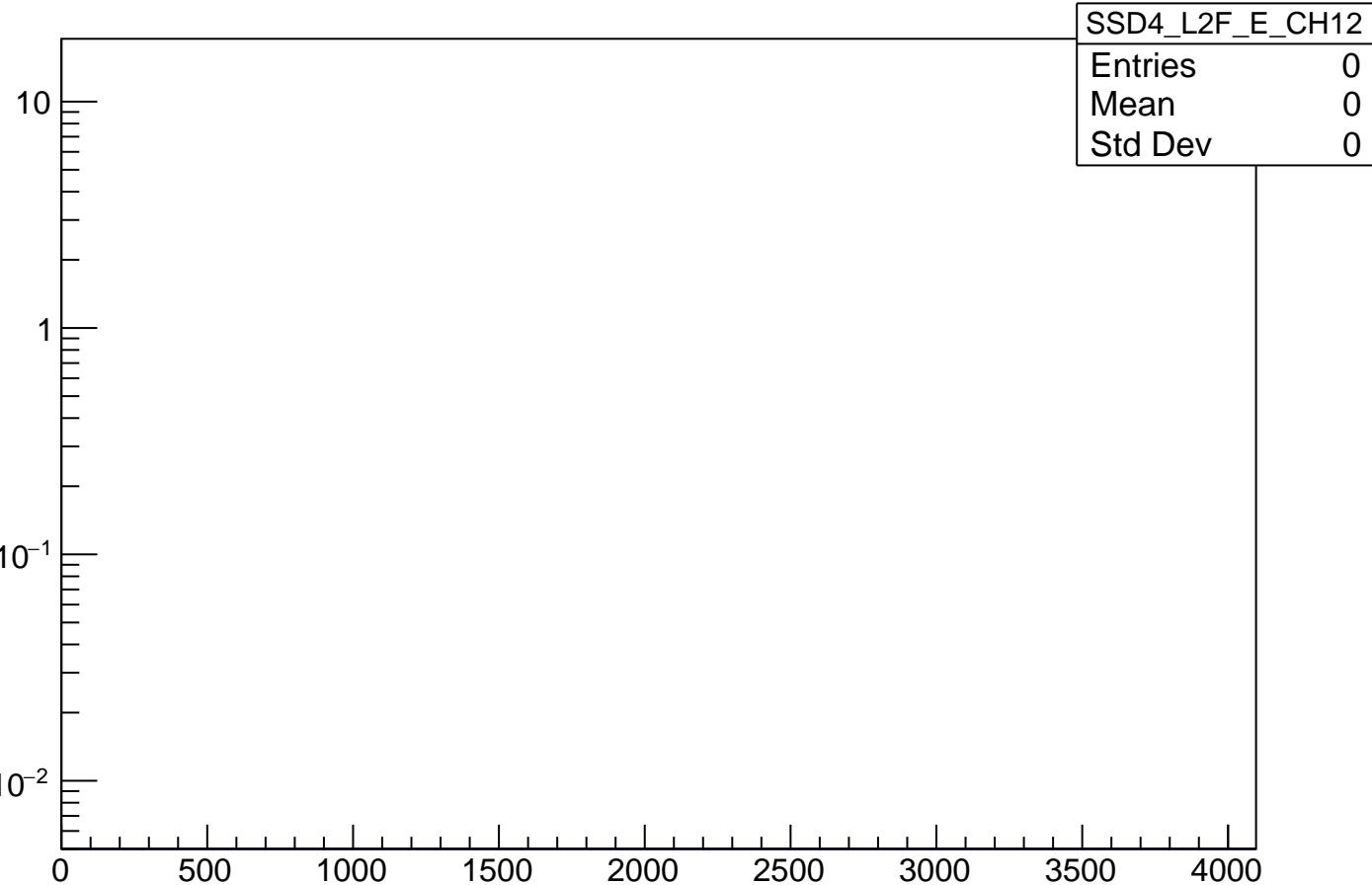
SSD4_L2F_E_CH10



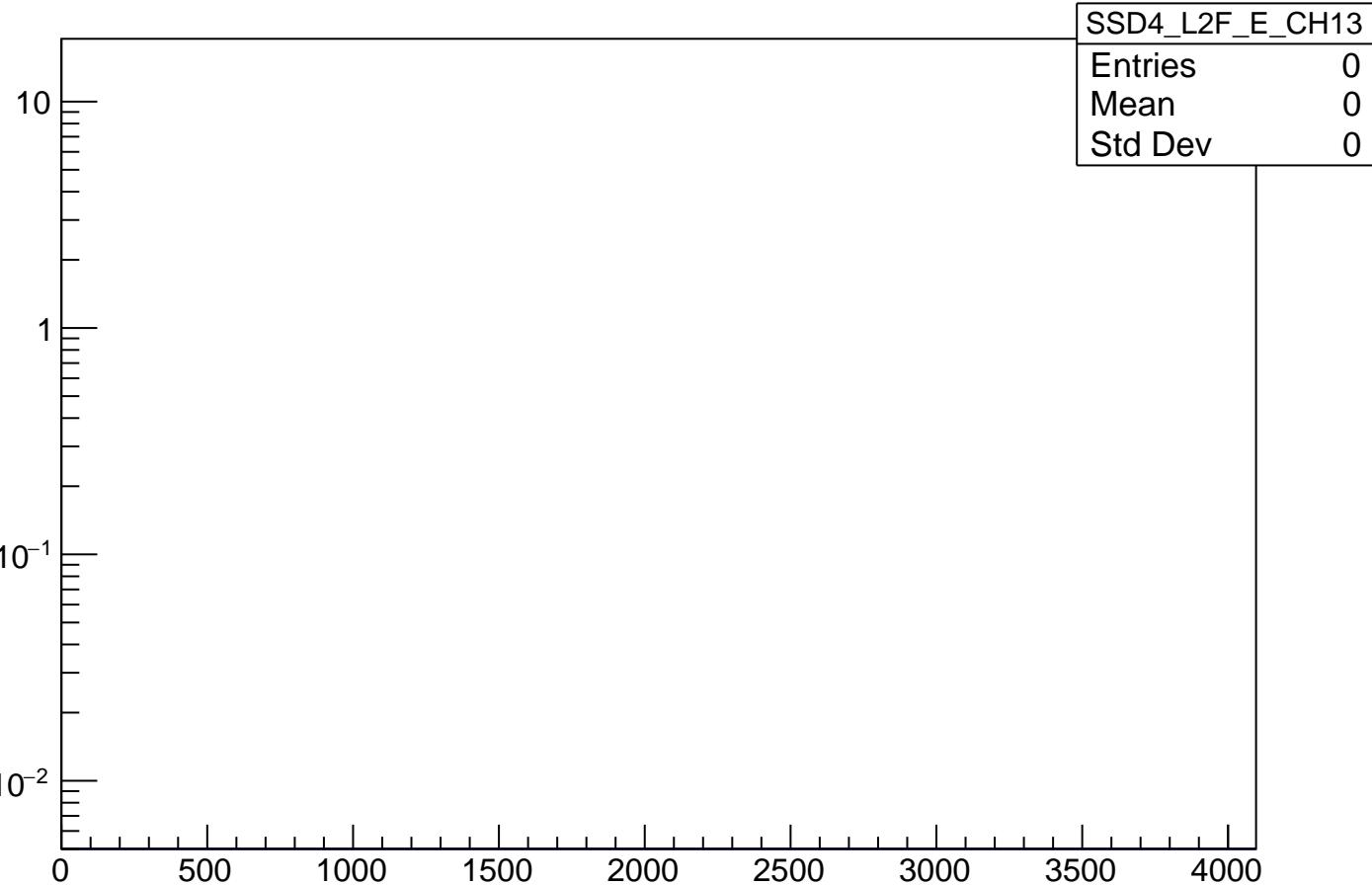
SSD4_L2F_E_CH11



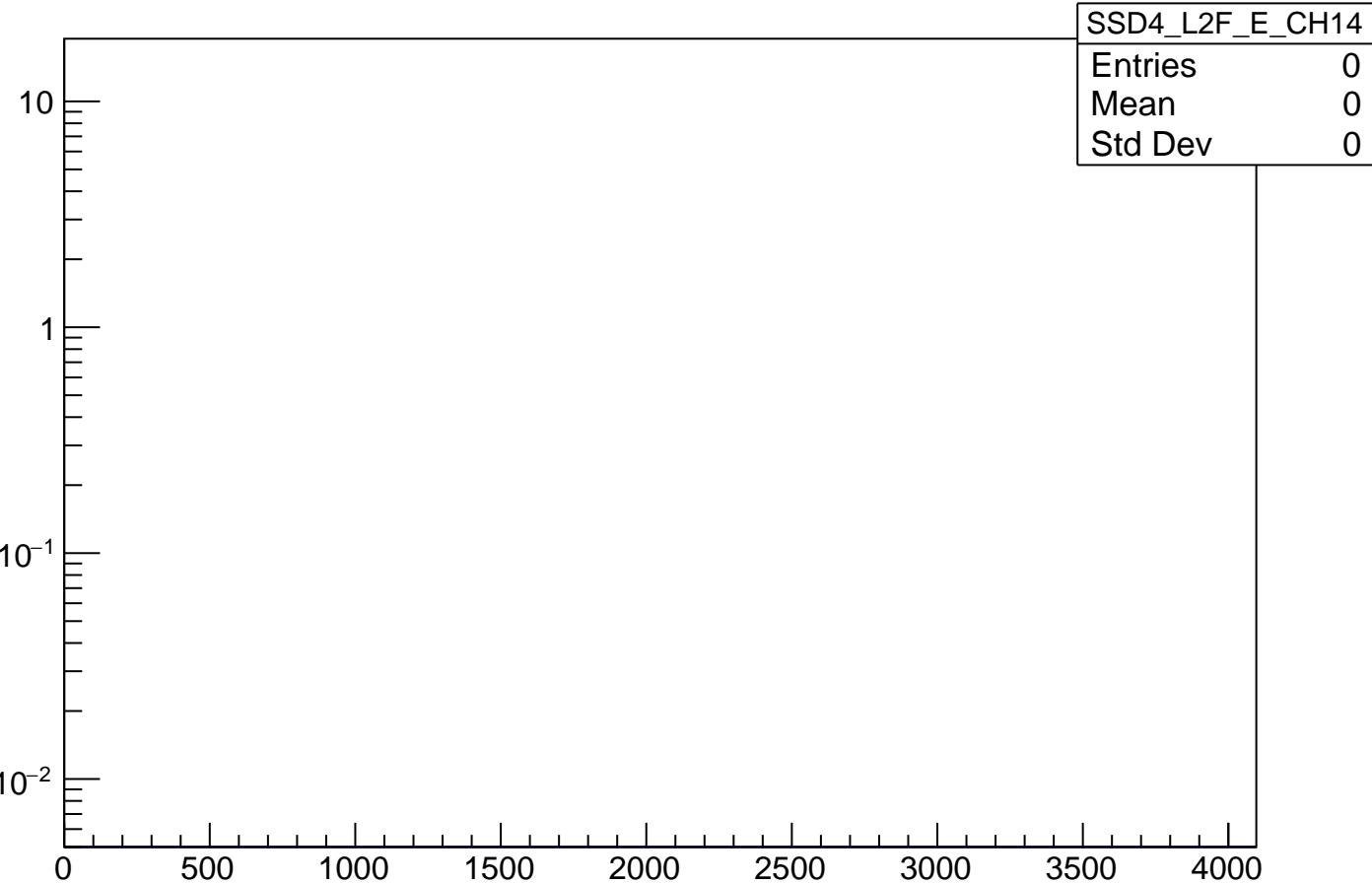
SSD4_L2F_E_CH12



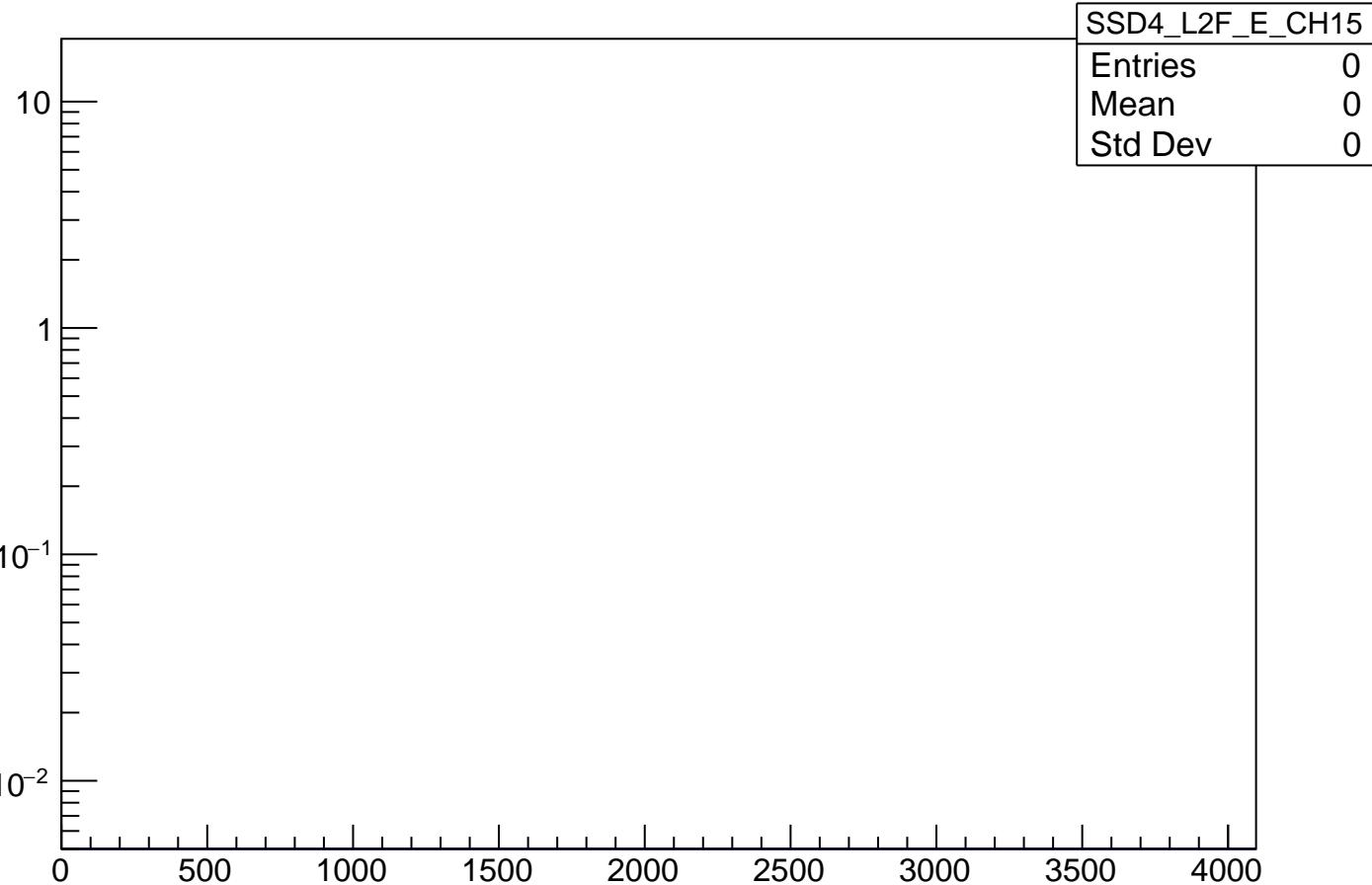
SSD4_L2F_E_CH13



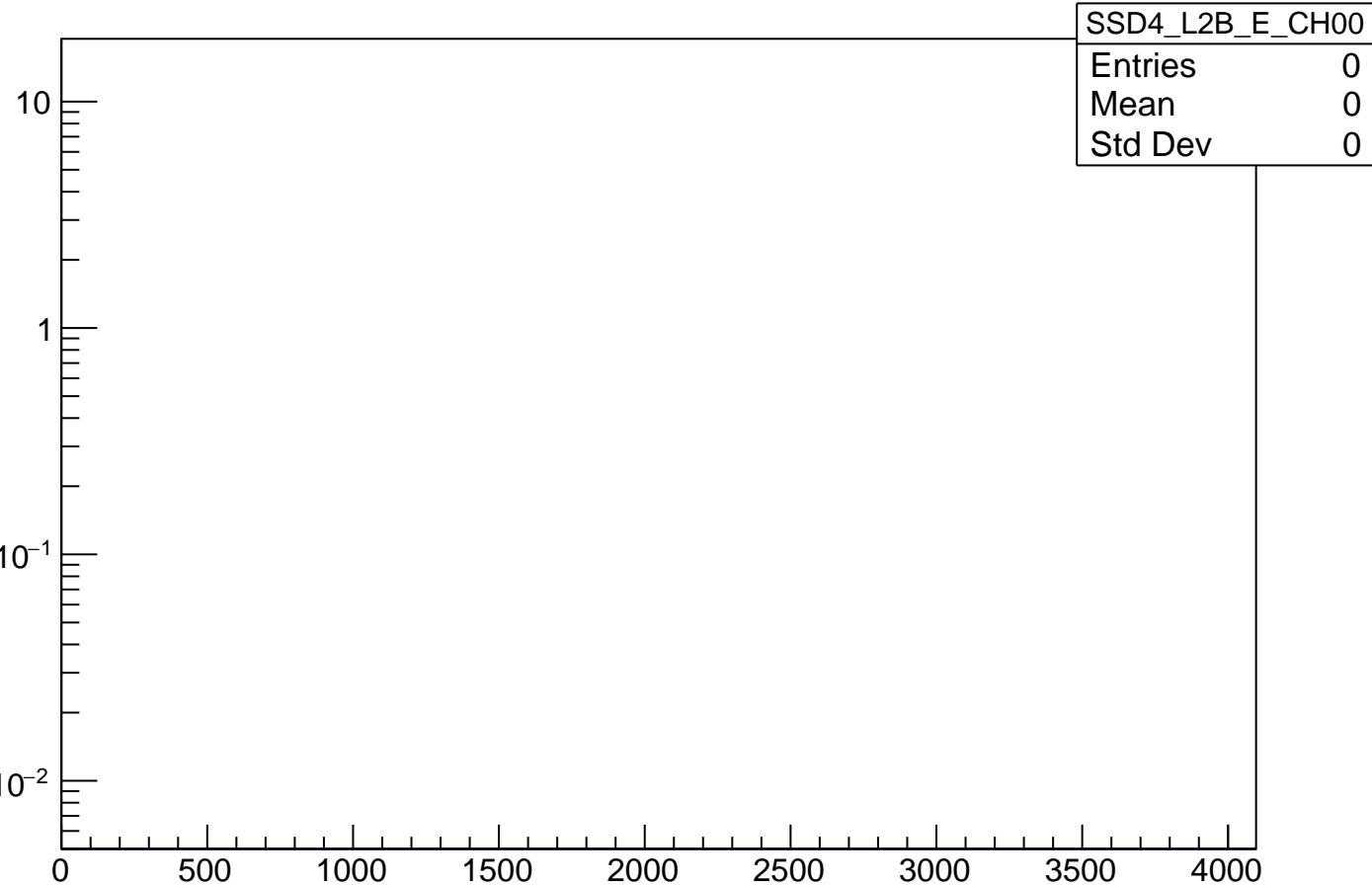
SSD4_L2F_E_CH14



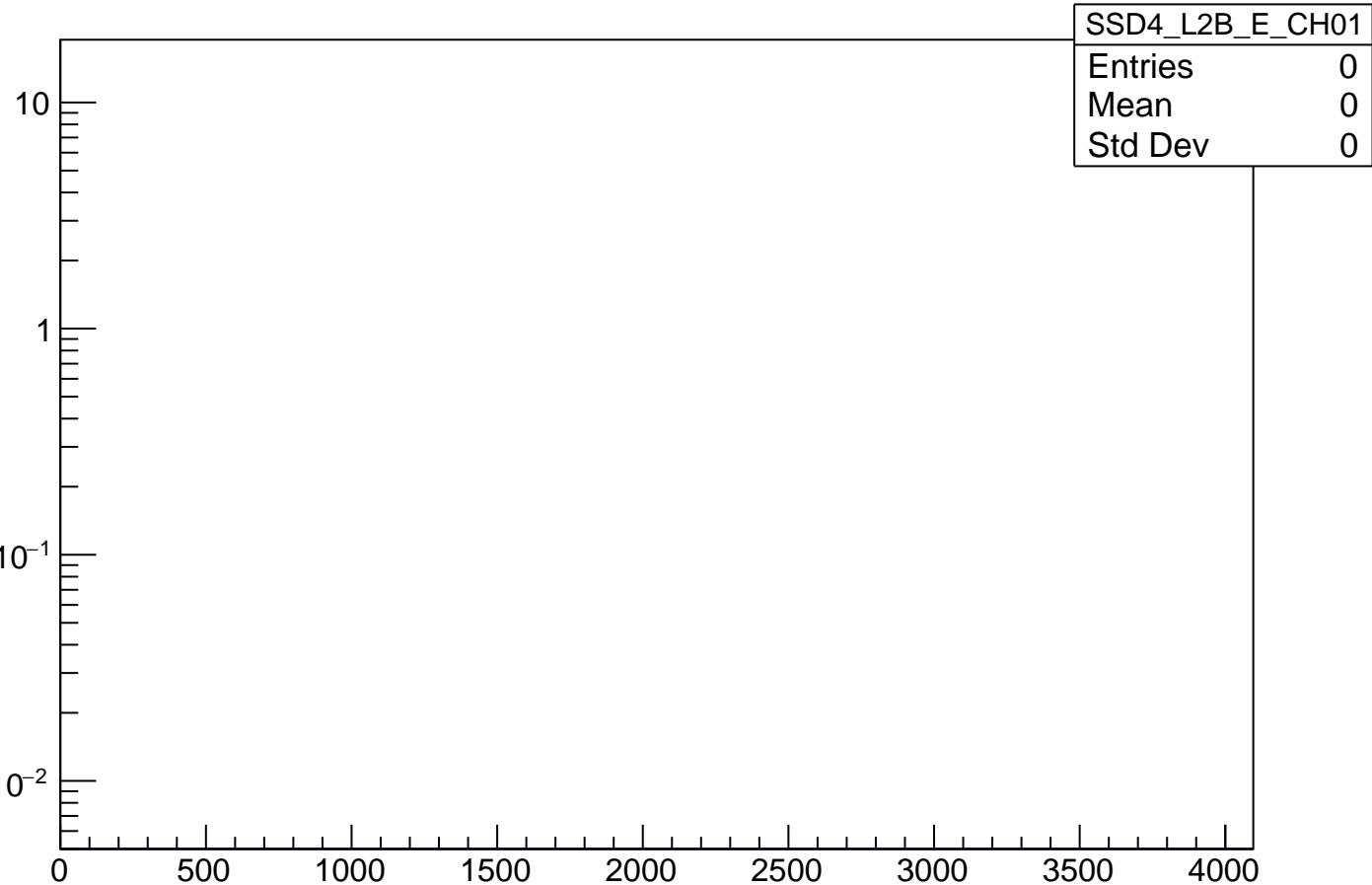
SSD4_L2F_E_CH15



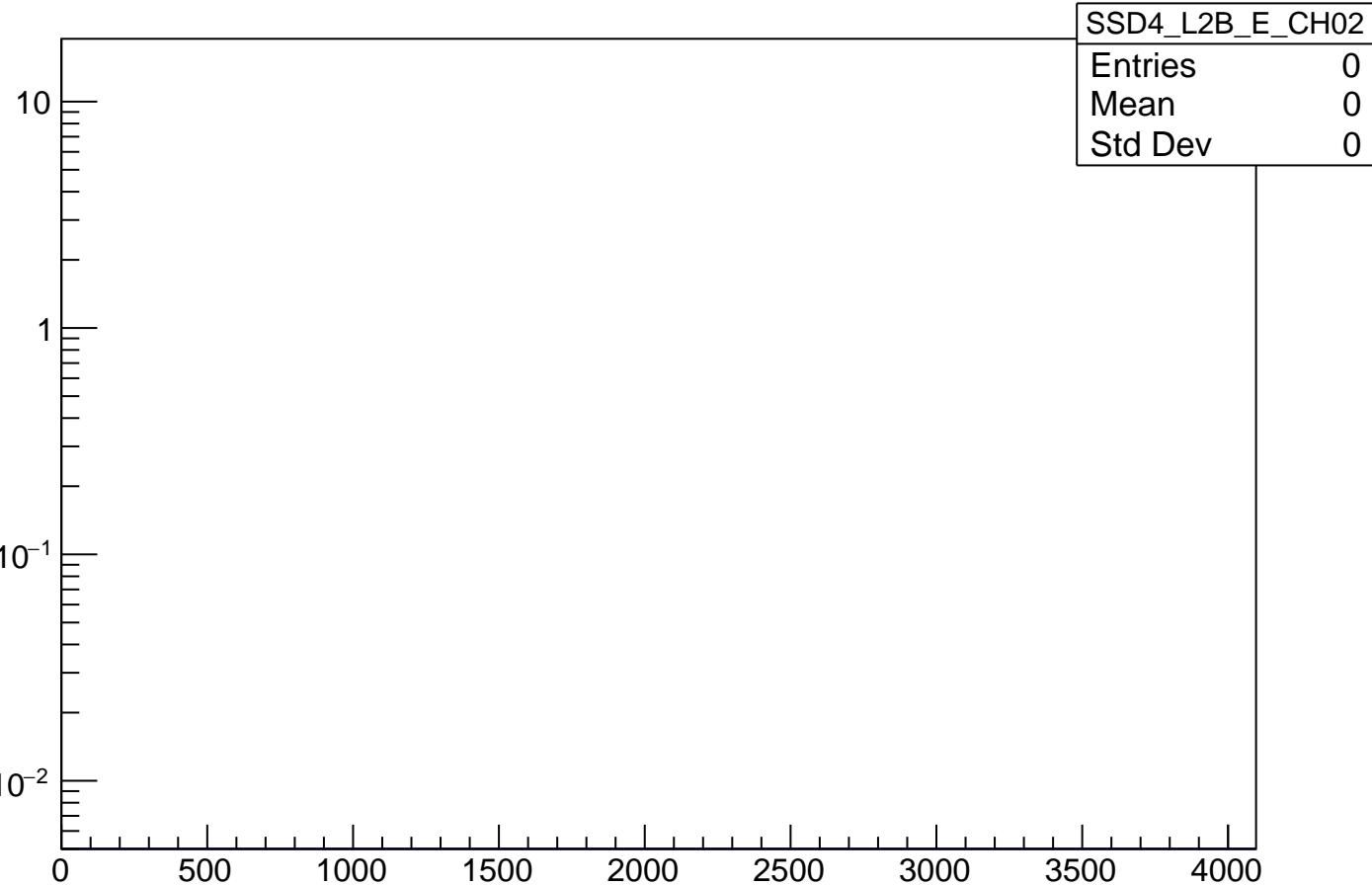
SSD4_L2B_E_CH00



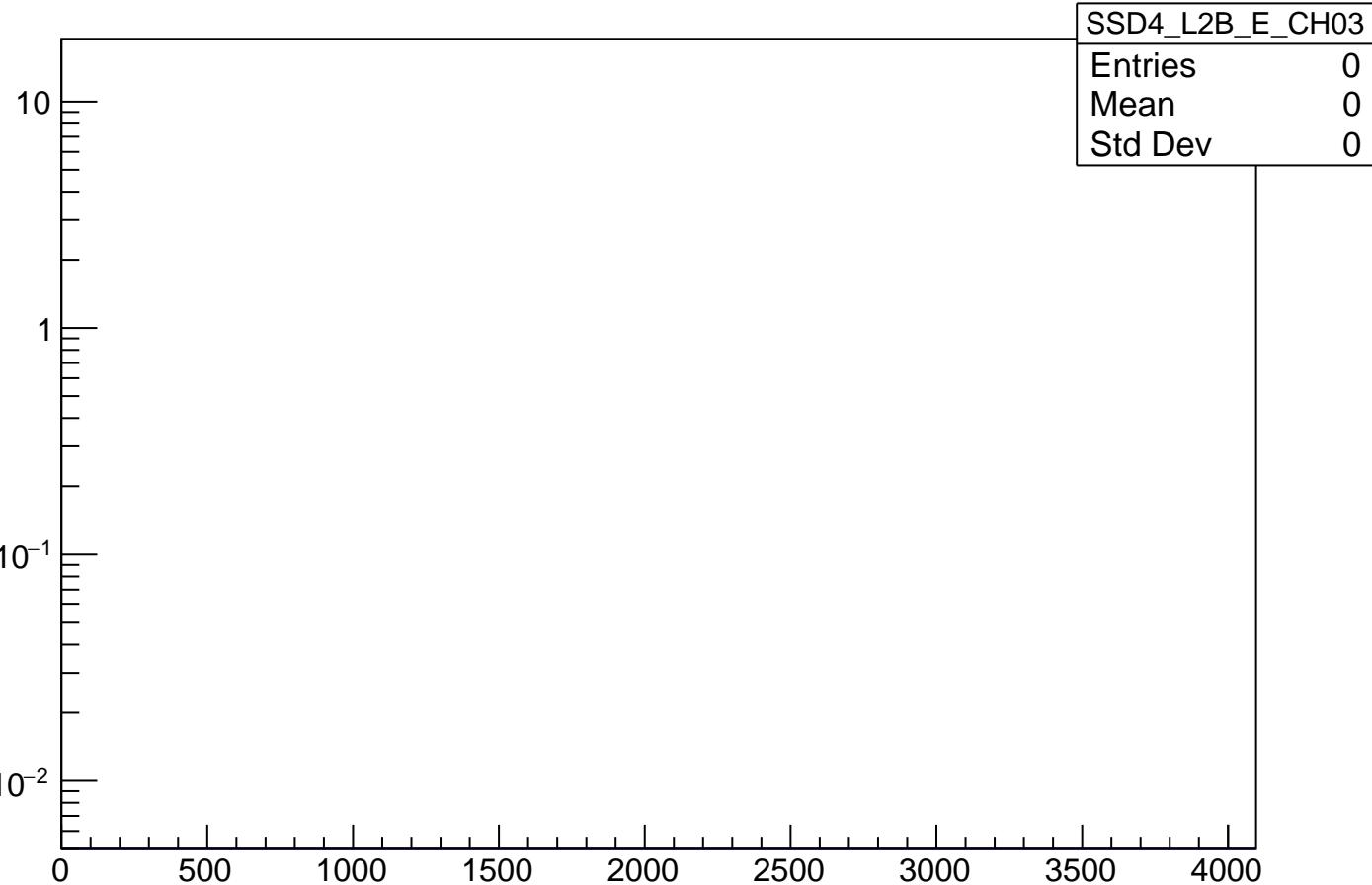
SSD4_L2B_E_CH01



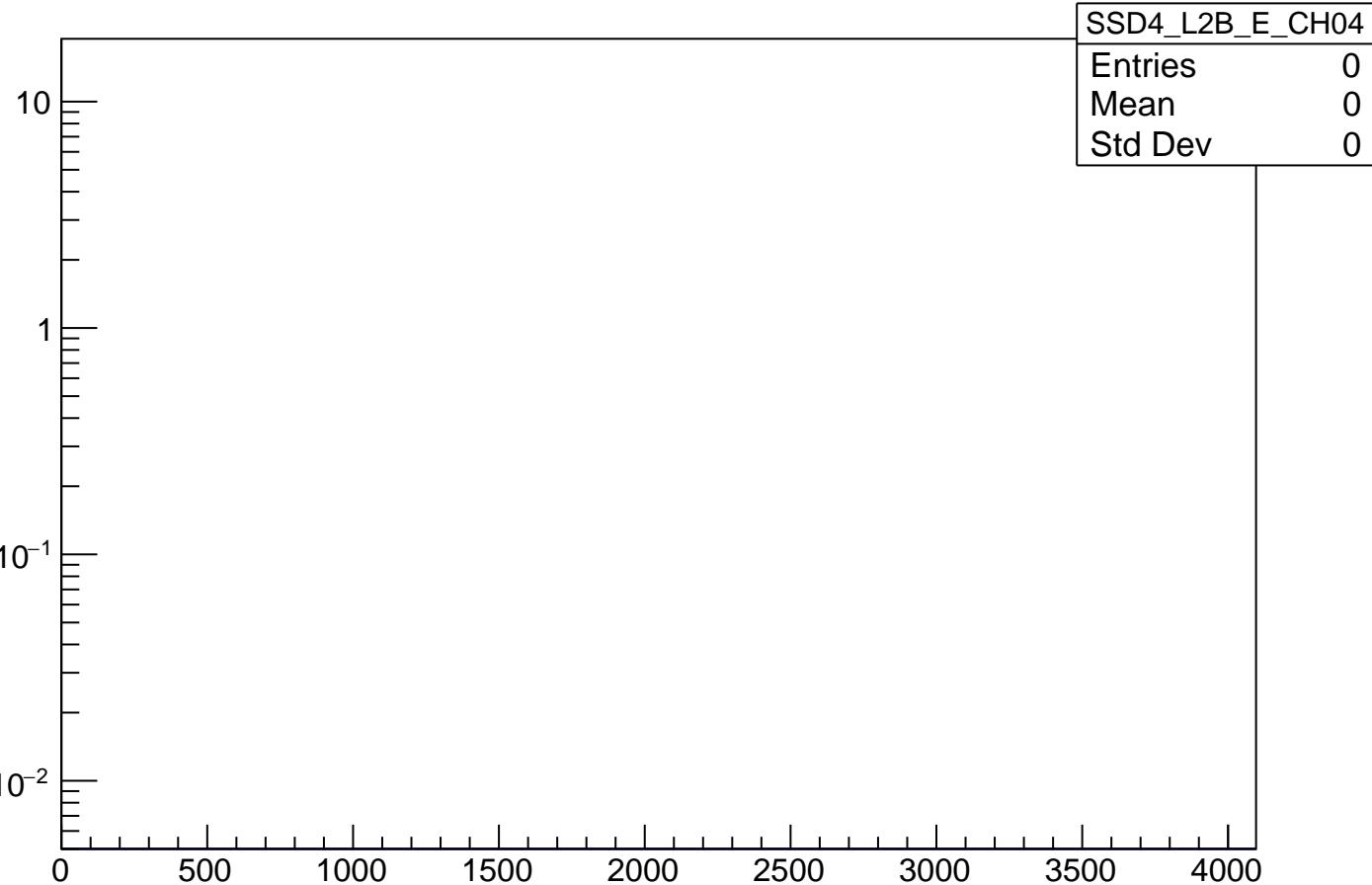
SSD4_L2B_E_CH02



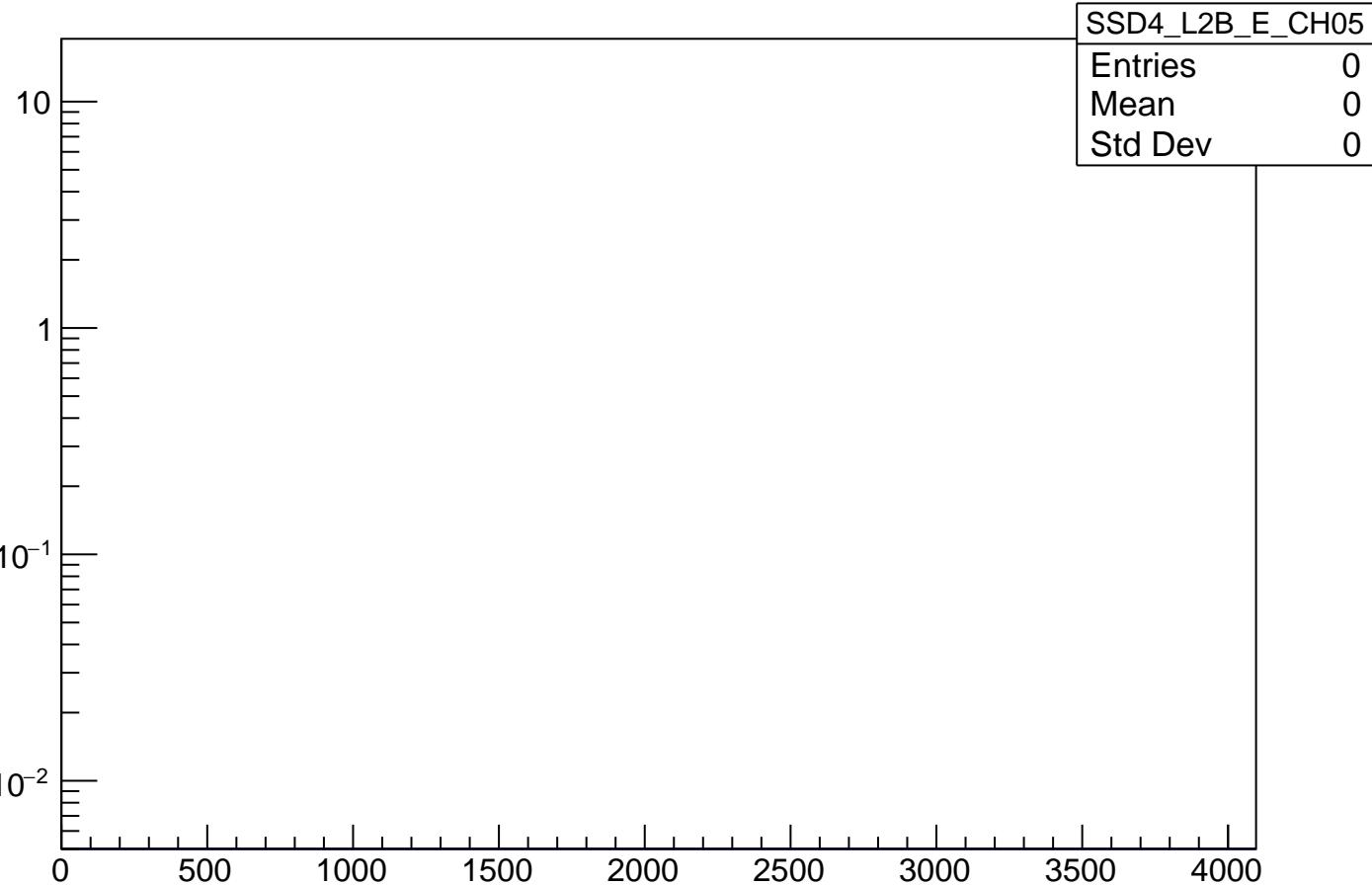
SSD4_L2B_E_CH03



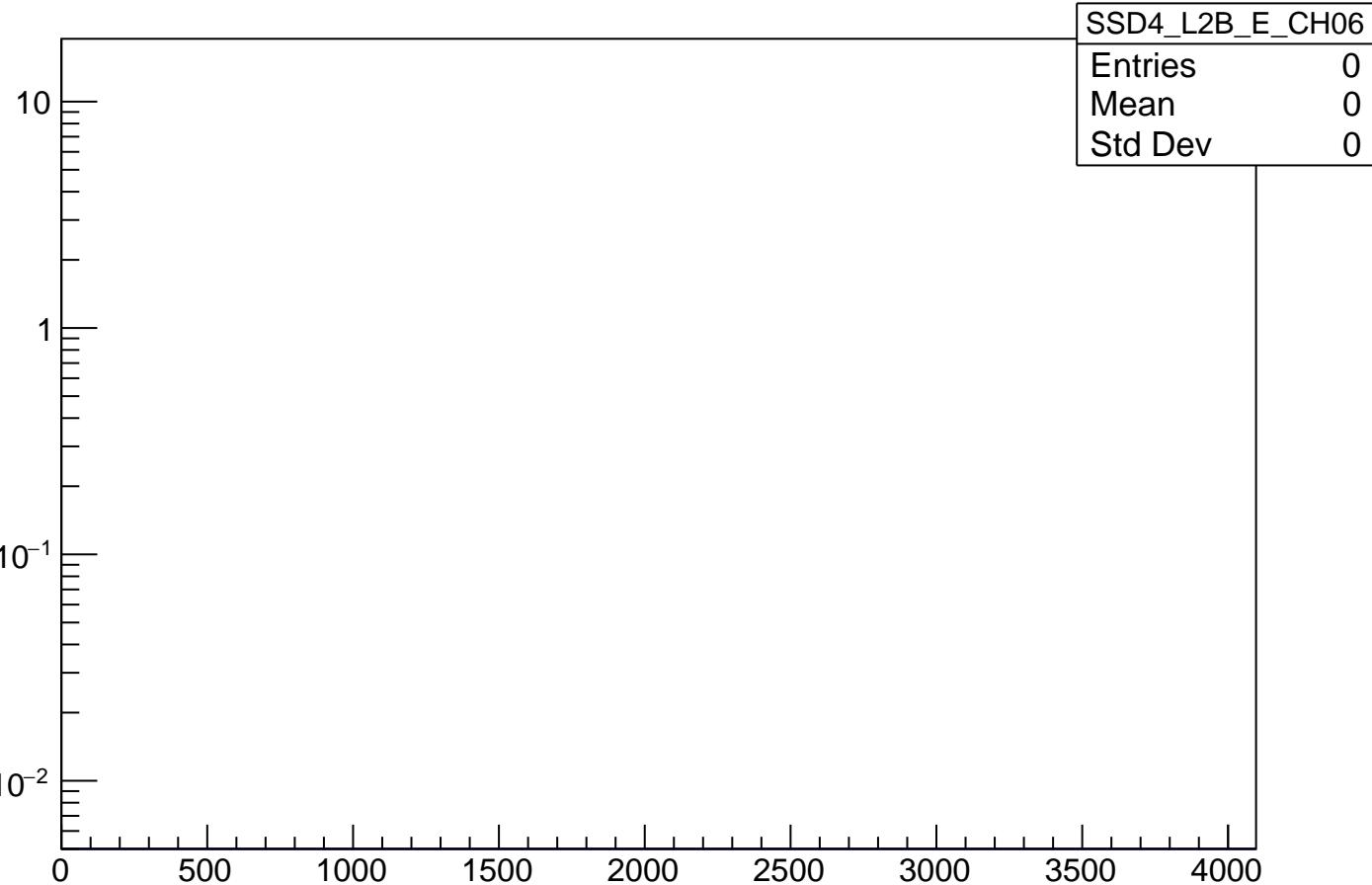
SSD4_L2B_E_CH04



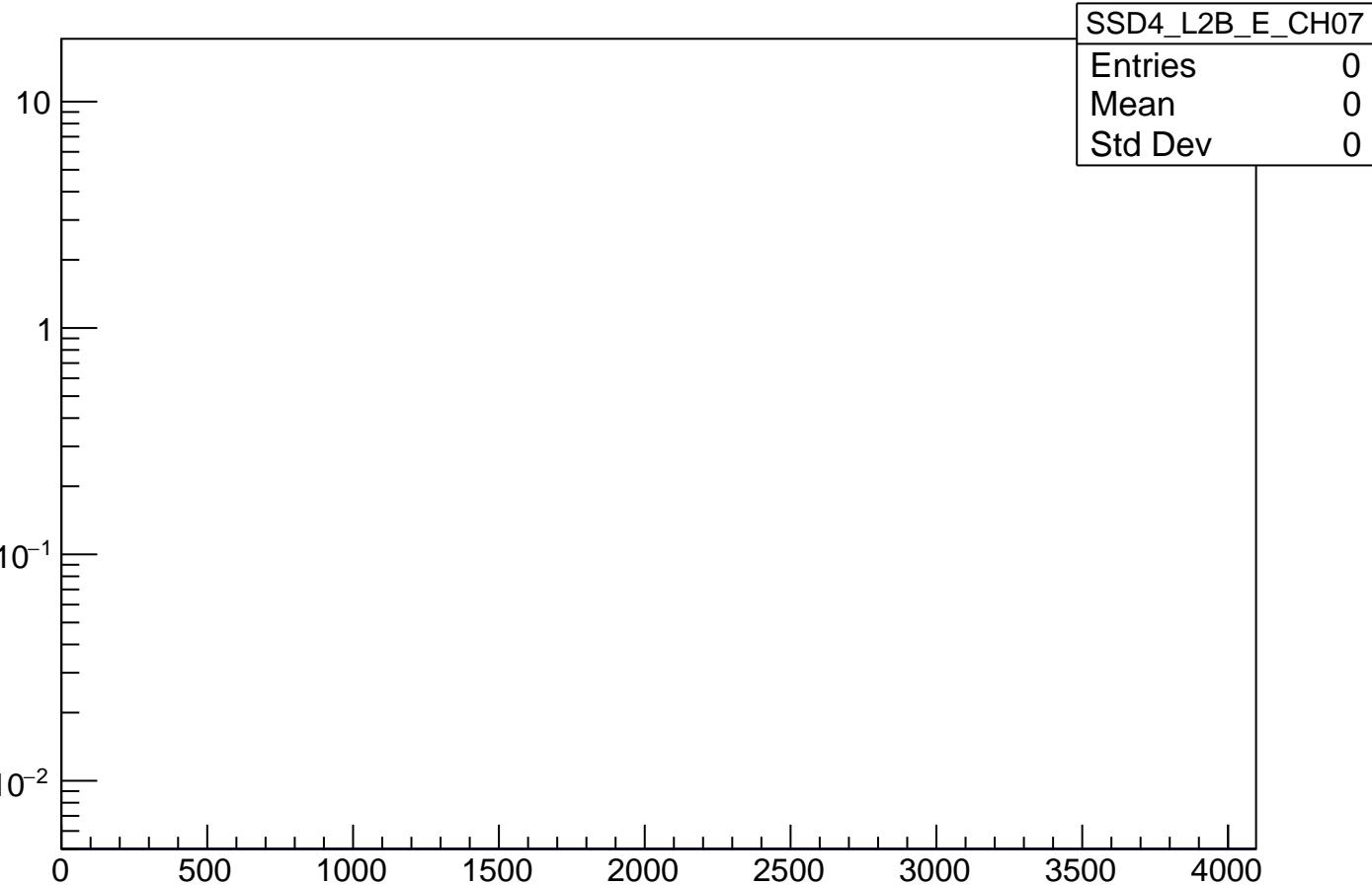
SSD4_L2B_E_CH05



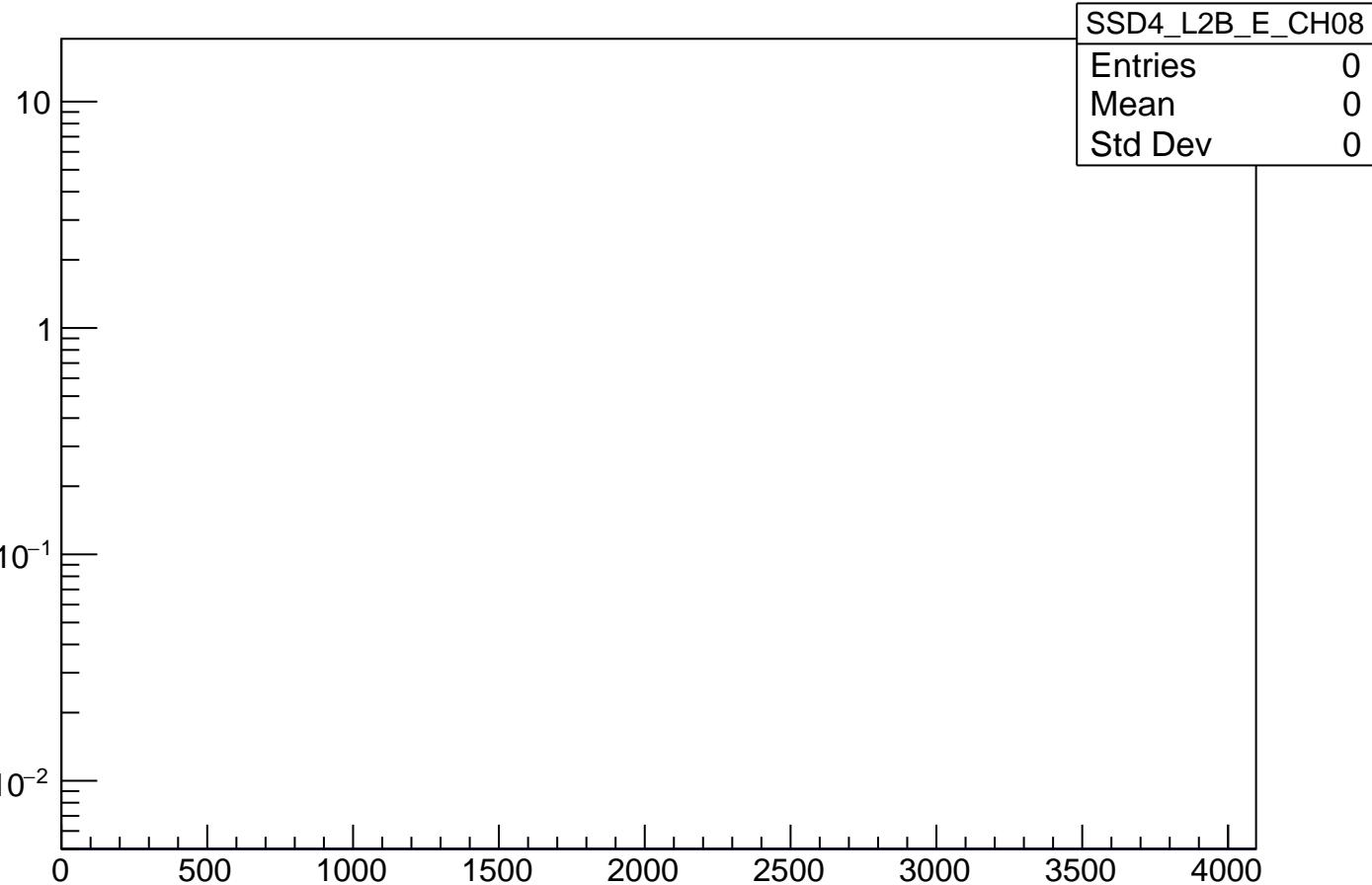
SSD4_L2B_E_CH06



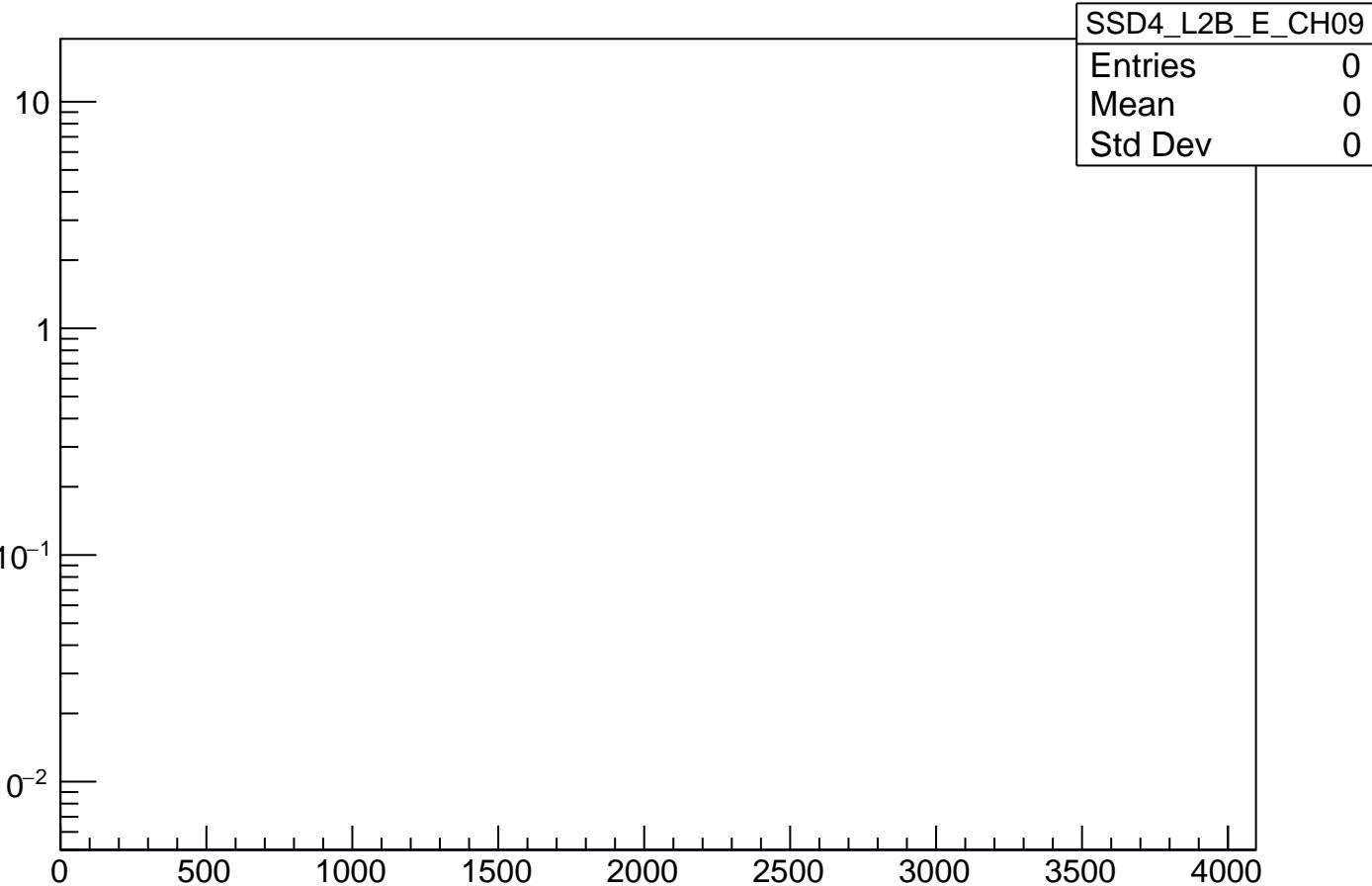
SSD4_L2B_E_CH07



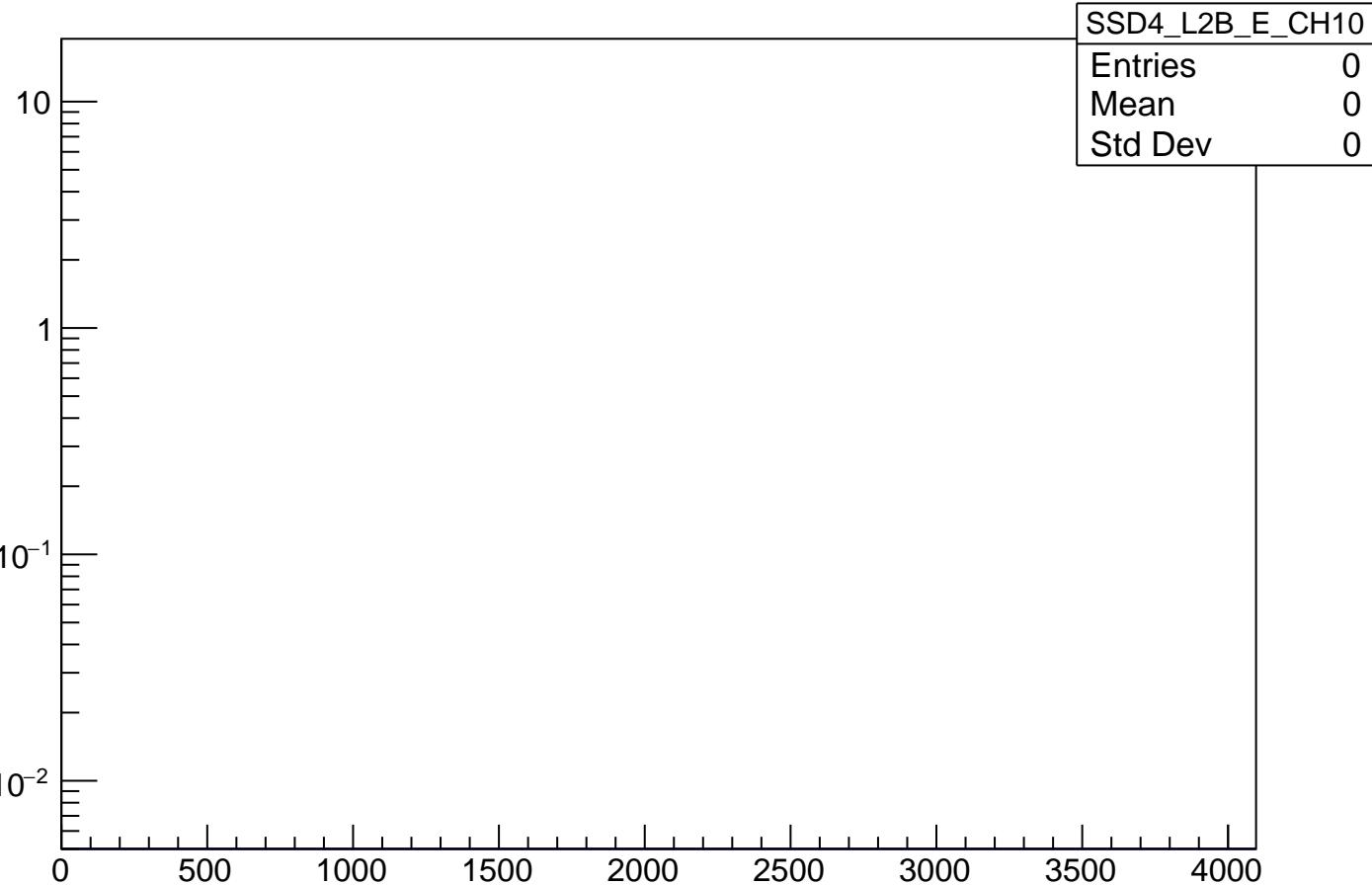
SSD4_L2B_E_CH08



SSD4_L2B_E_CH09

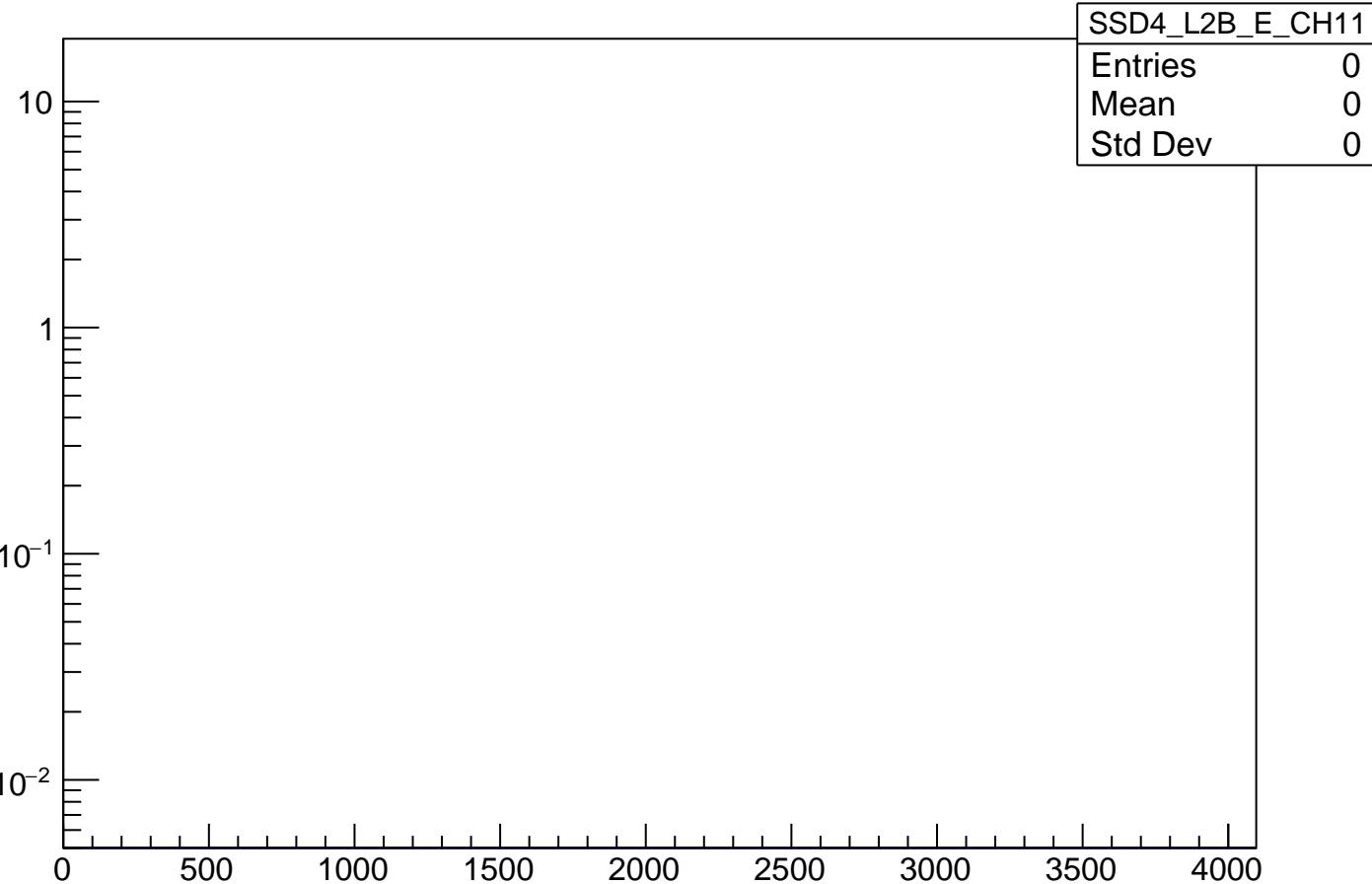


SSD4_L2B_E_CH10

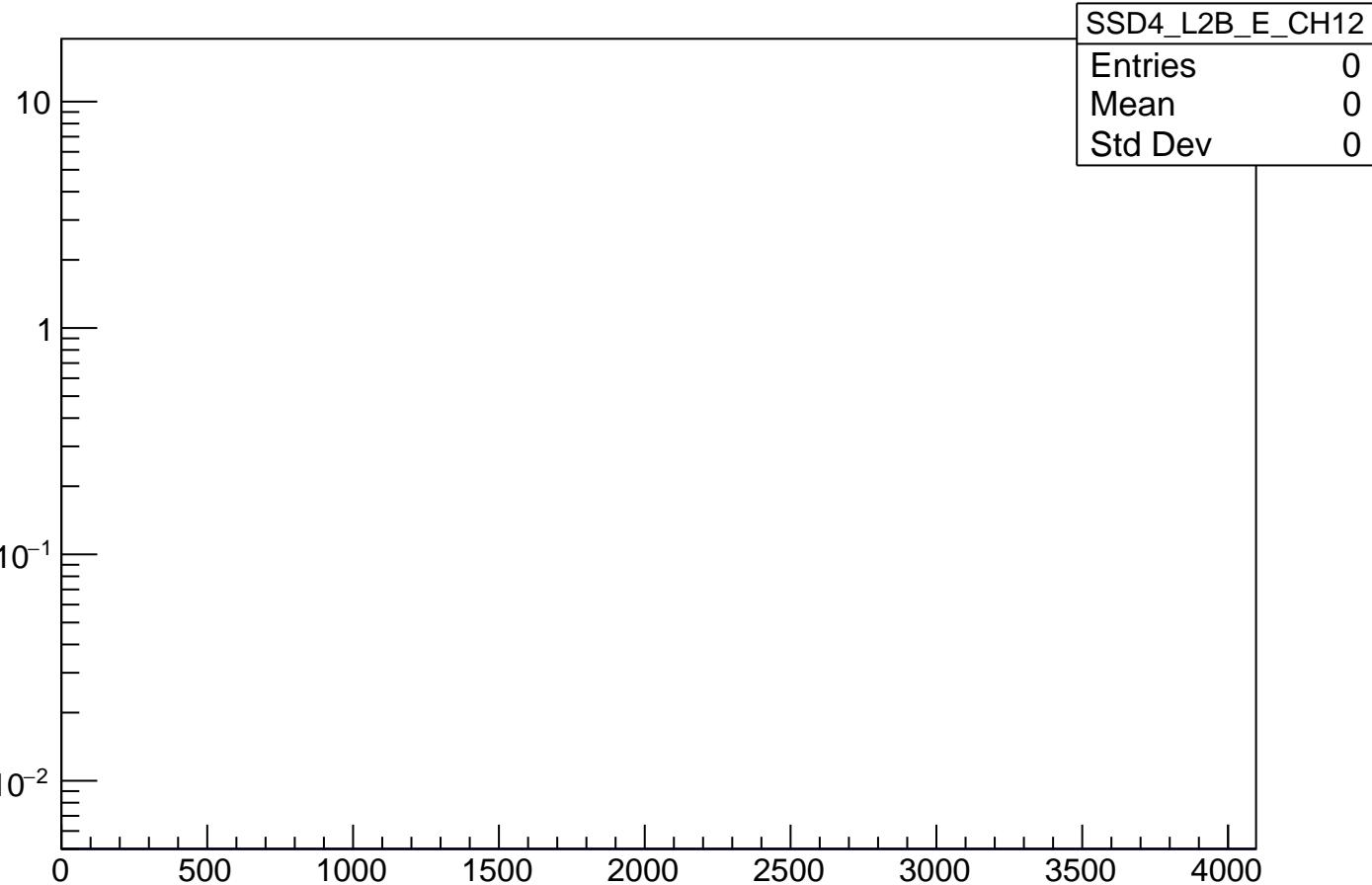


SSD4_L2B_E_CH10	
Entries	0
Mean	0
Std Dev	0

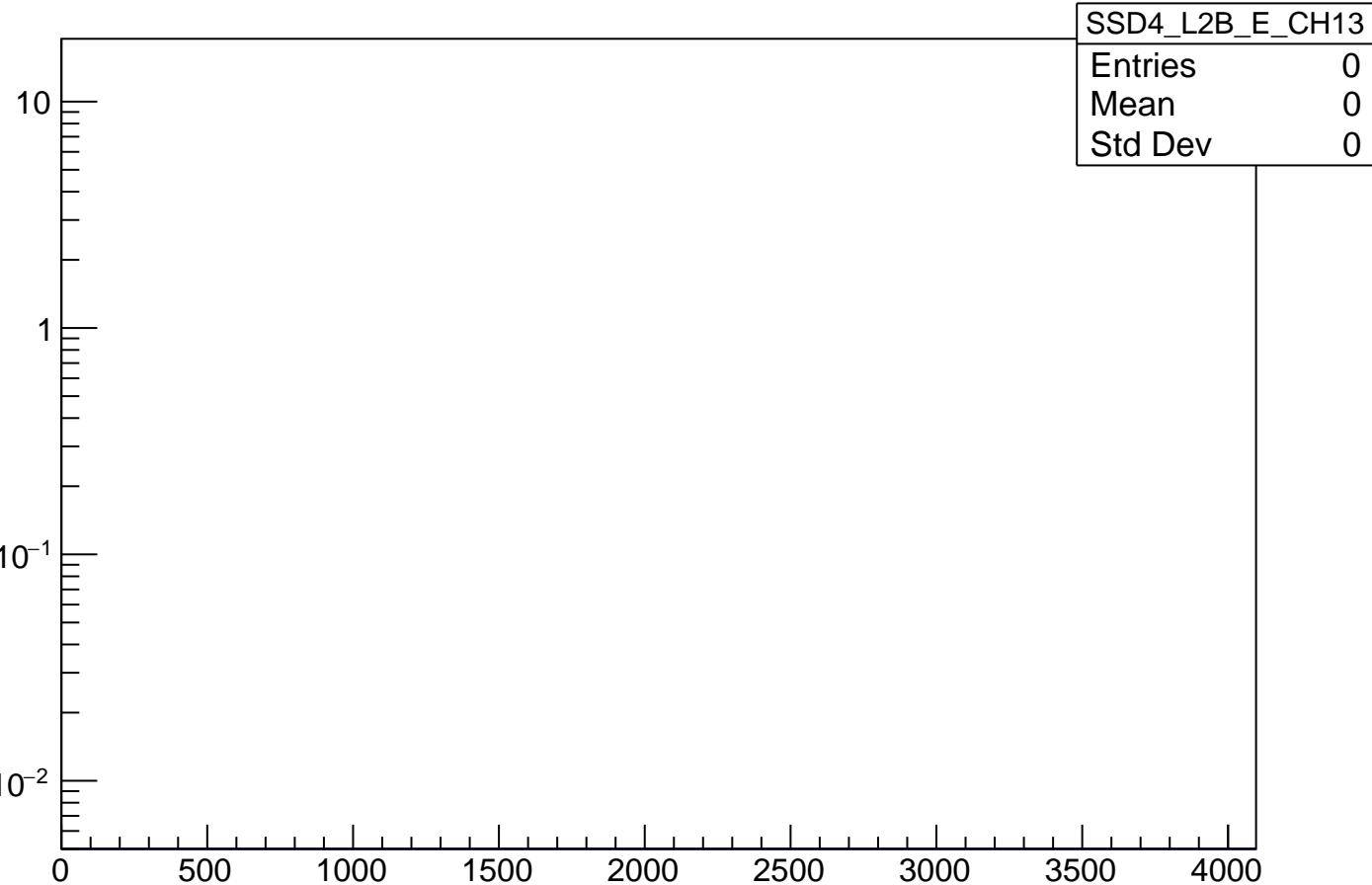
SSD4_L2B_E_CH11



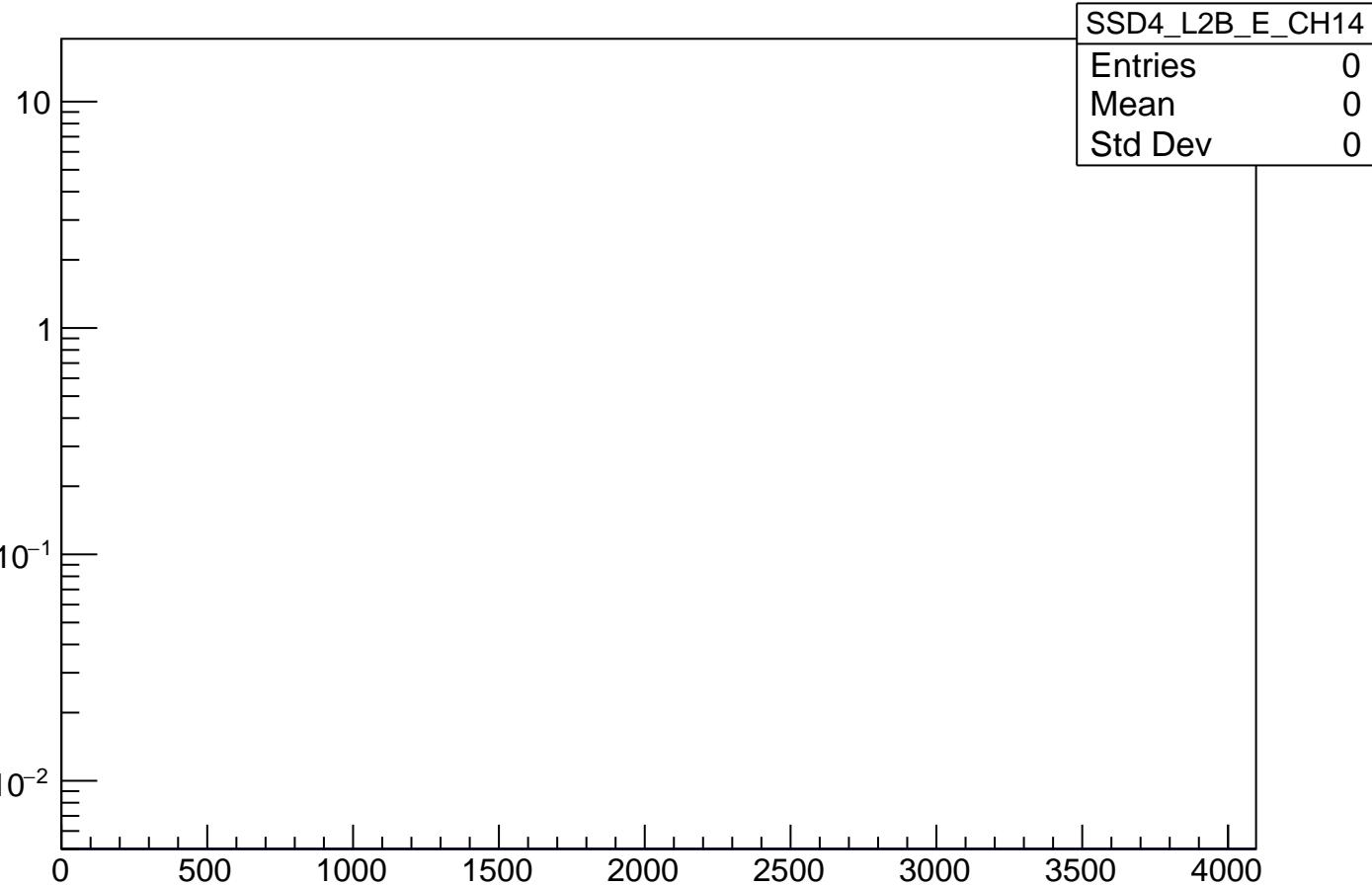
SSD4_L2B_E_CH12



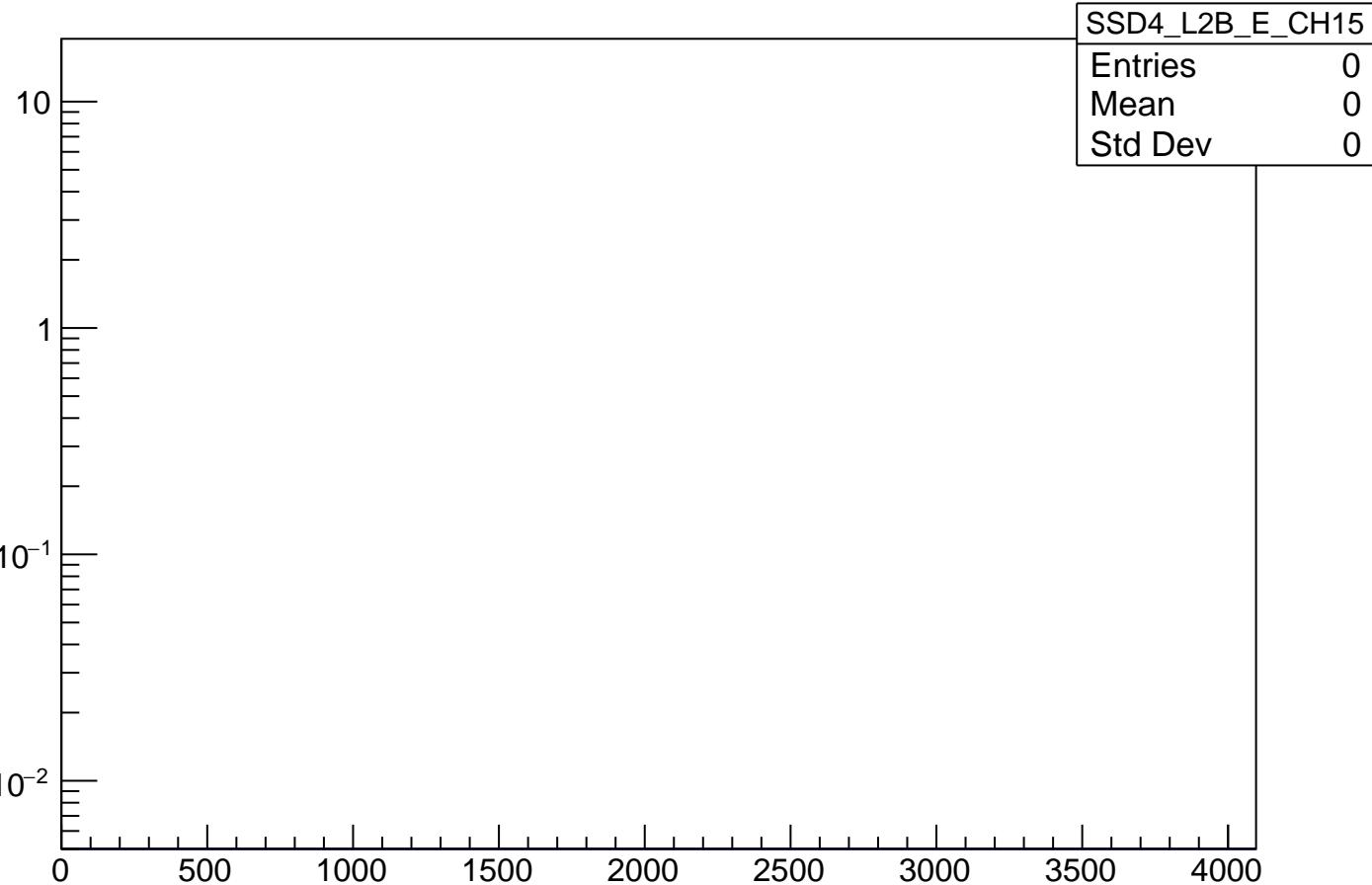
SSD4_L2B_E_CH13



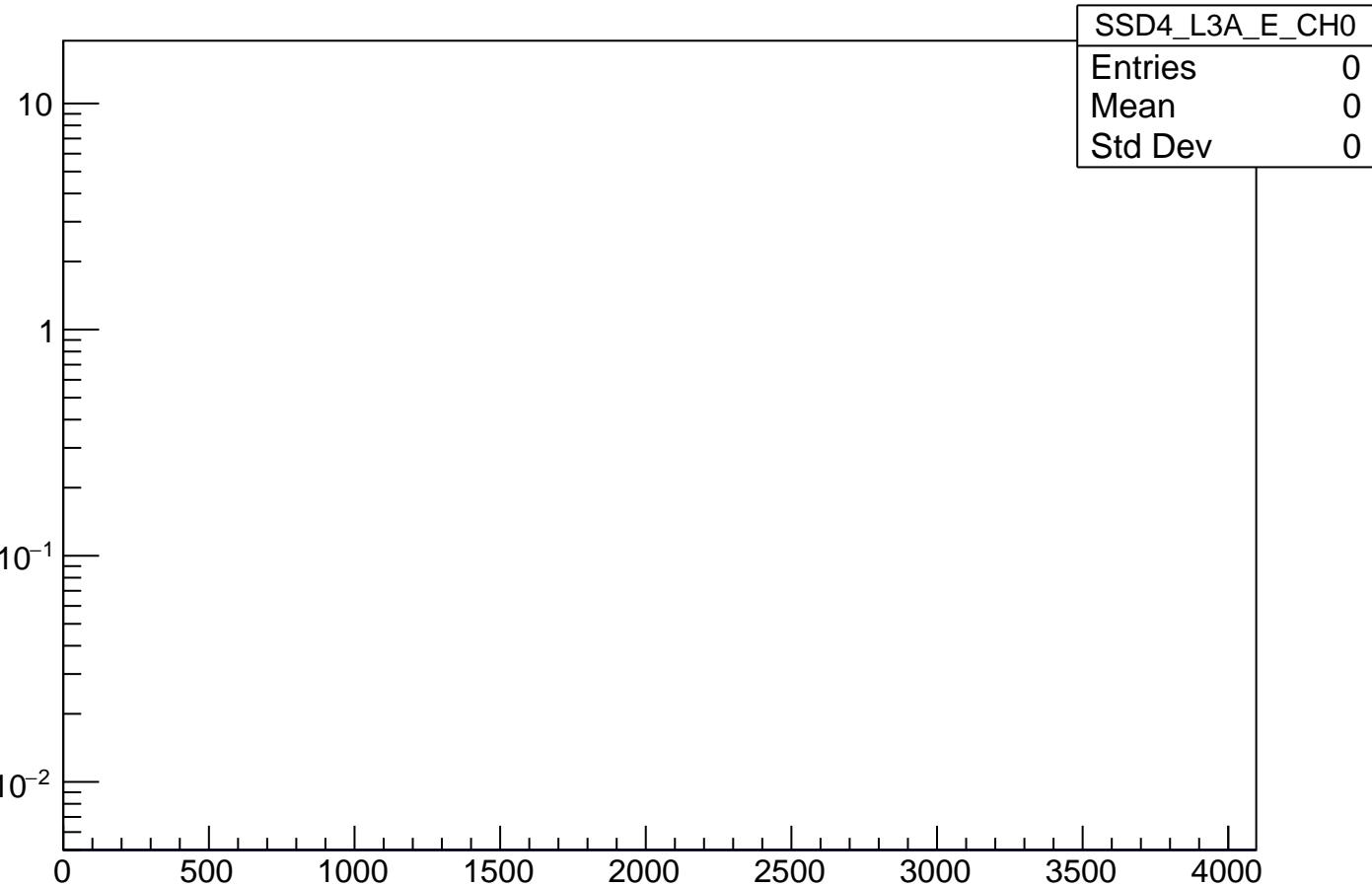
SSD4_L2B_E_CH14



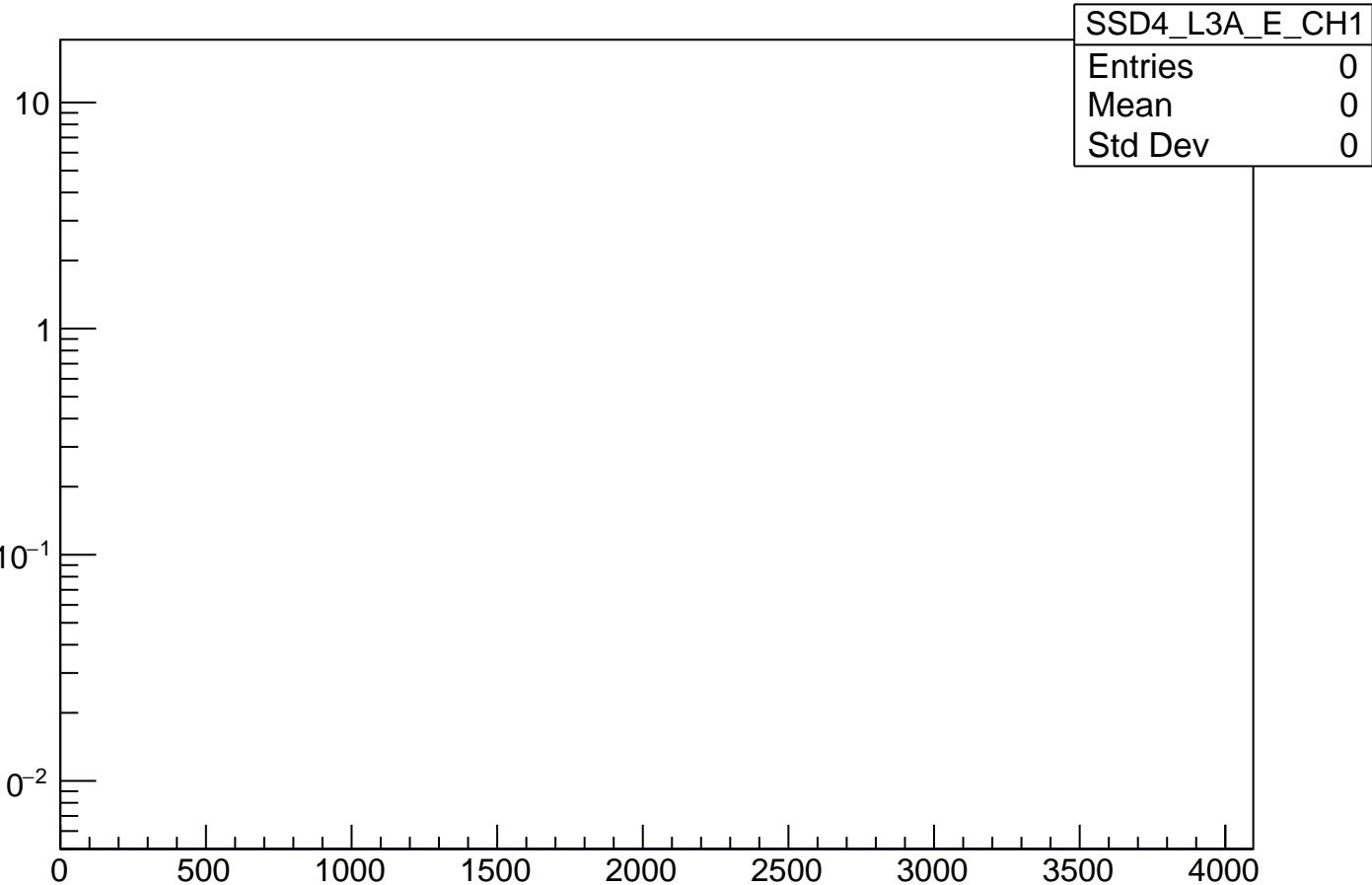
SSD4_L2B_E_CH15



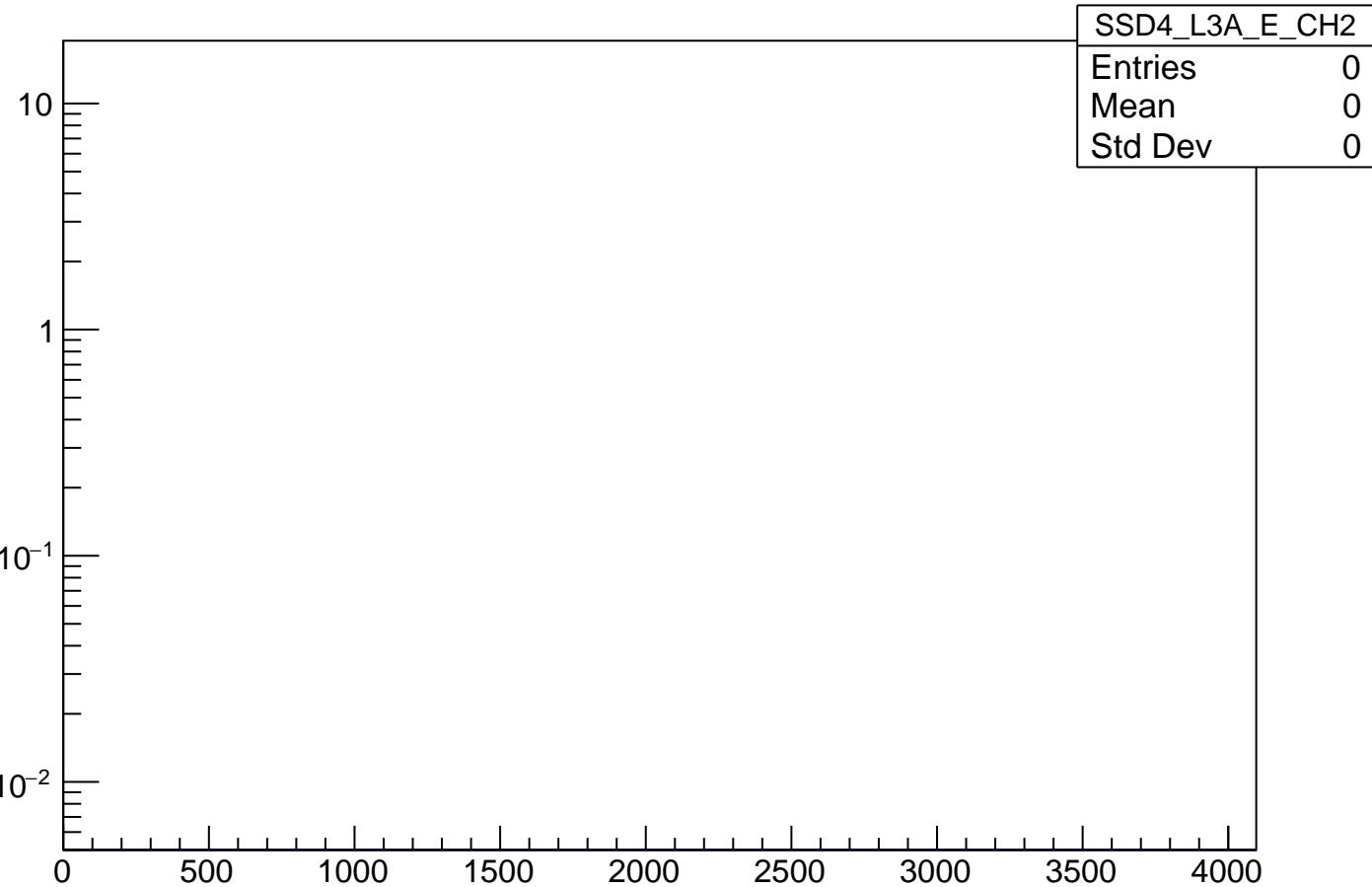
SSD4_L3A_E_CH0



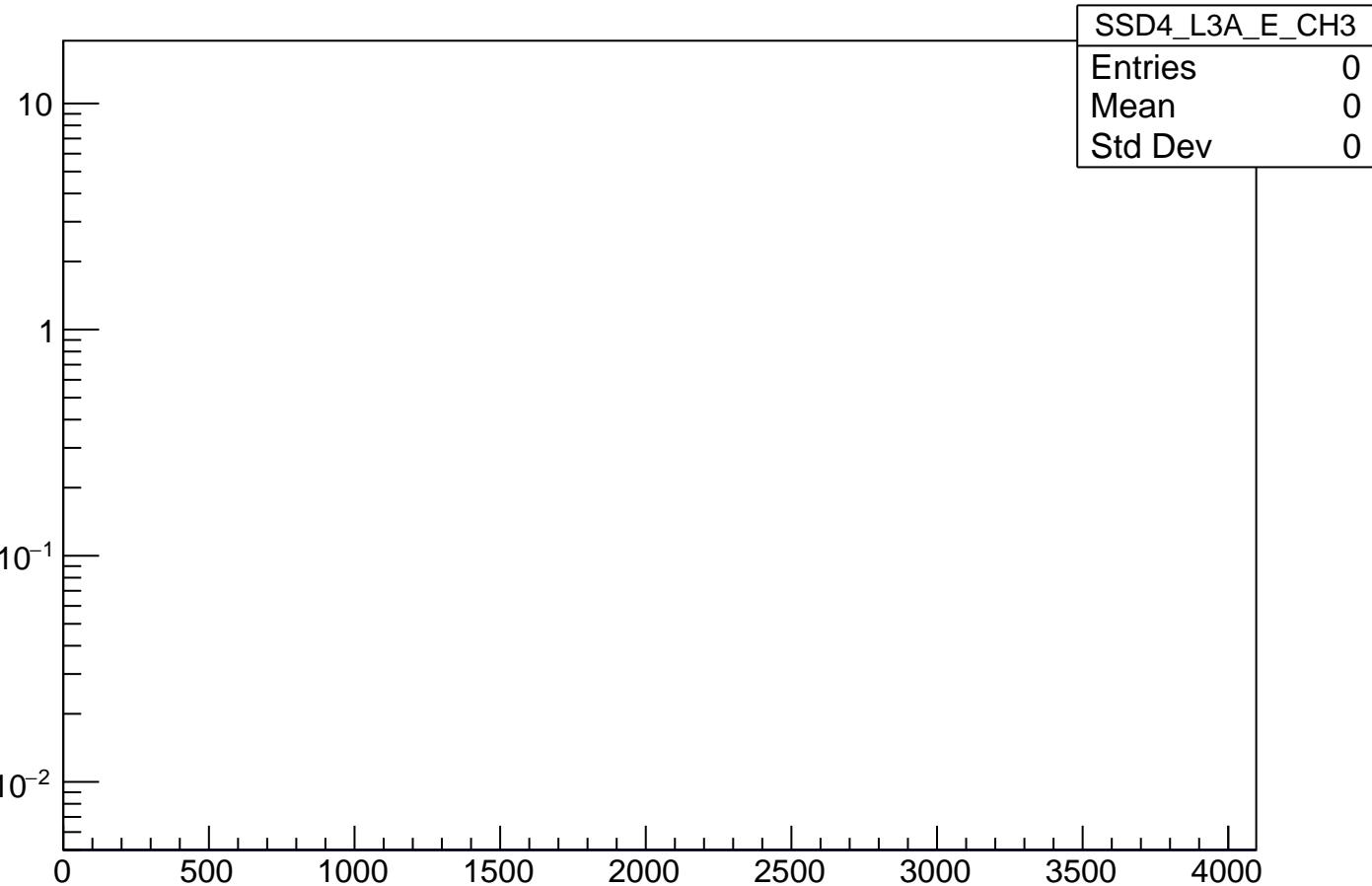
SSD4_L3A_E_CH1



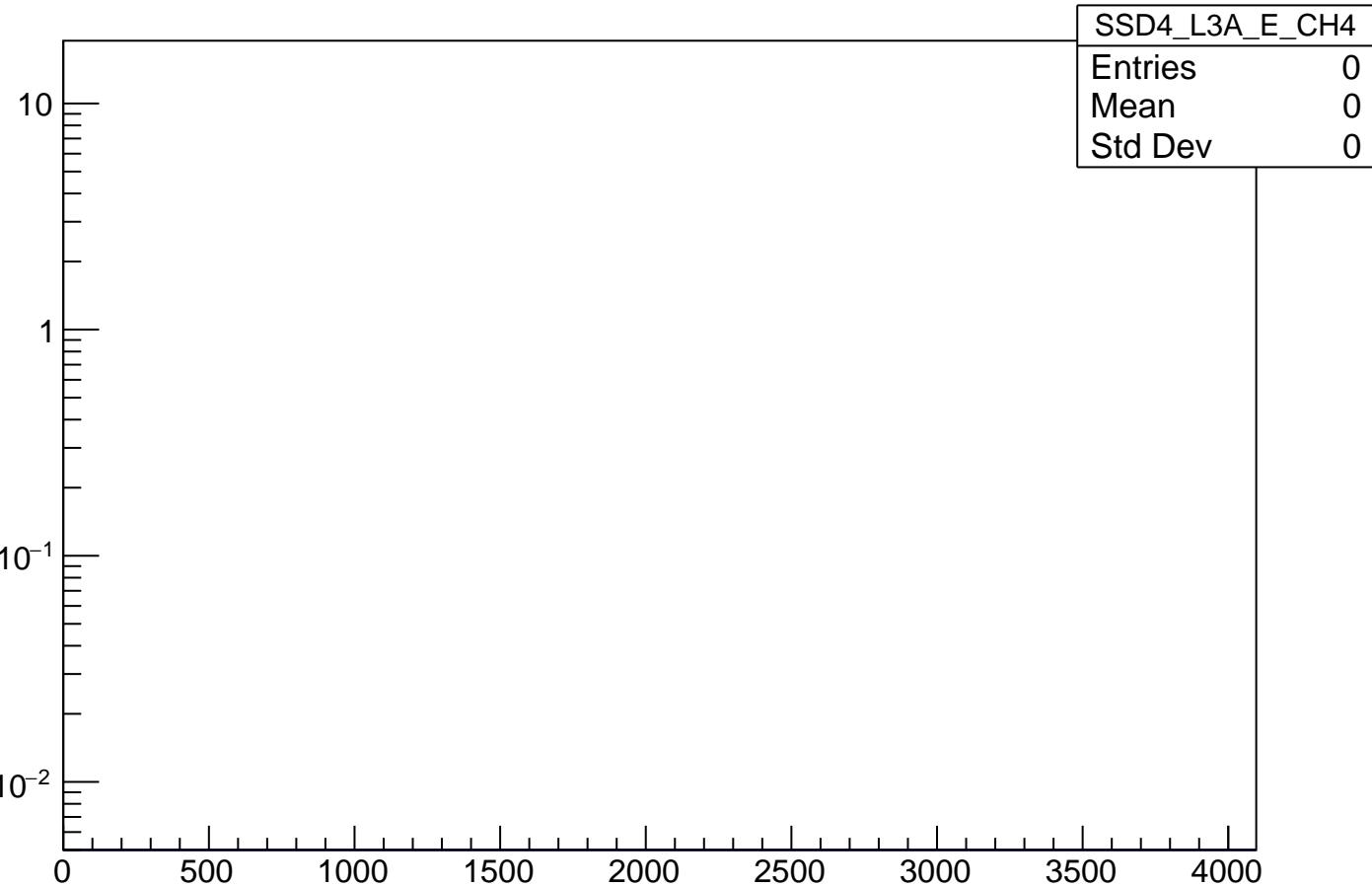
SSD4_L3A_E_CH2



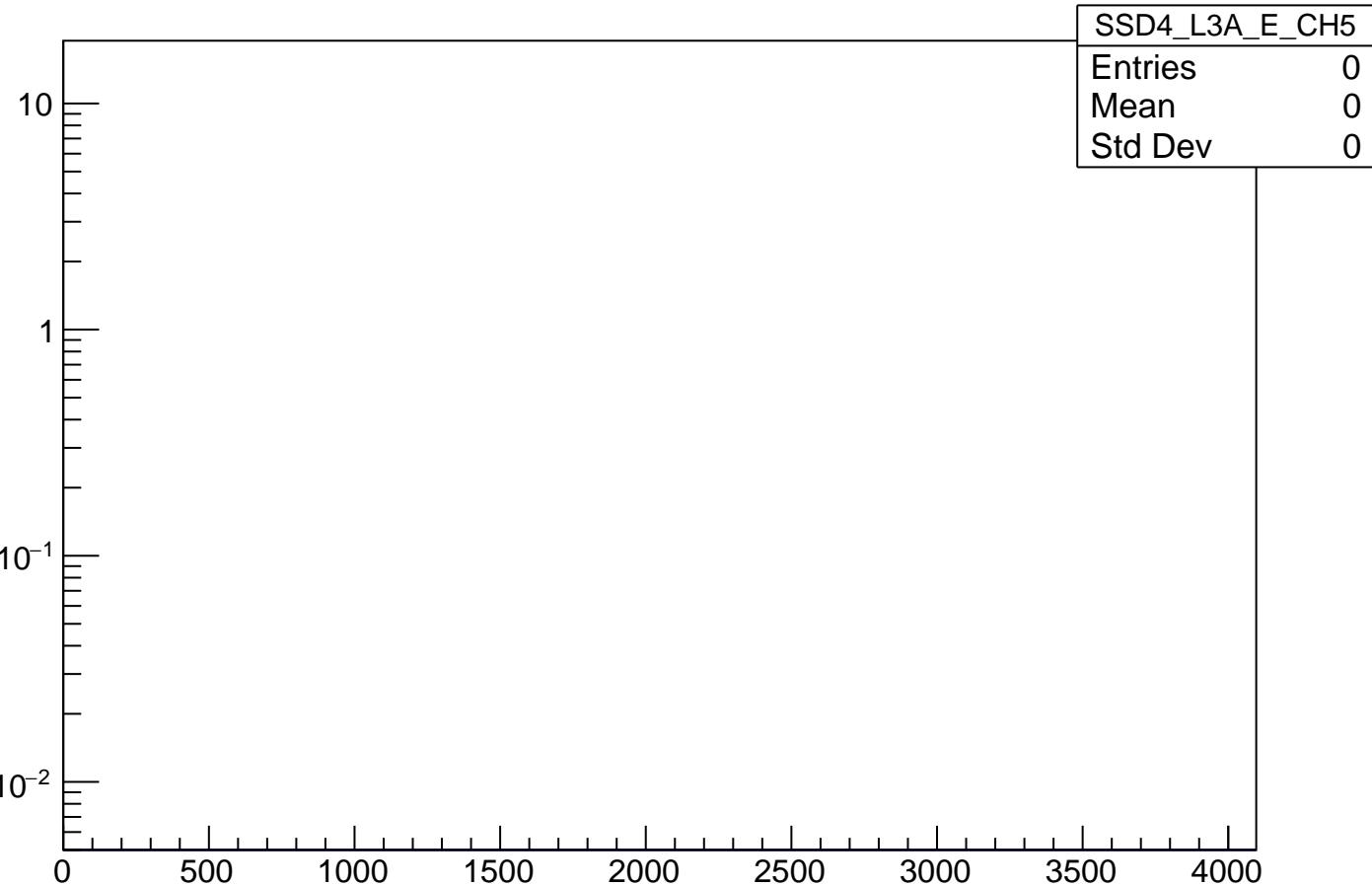
SSD4_L3A_E_CH3



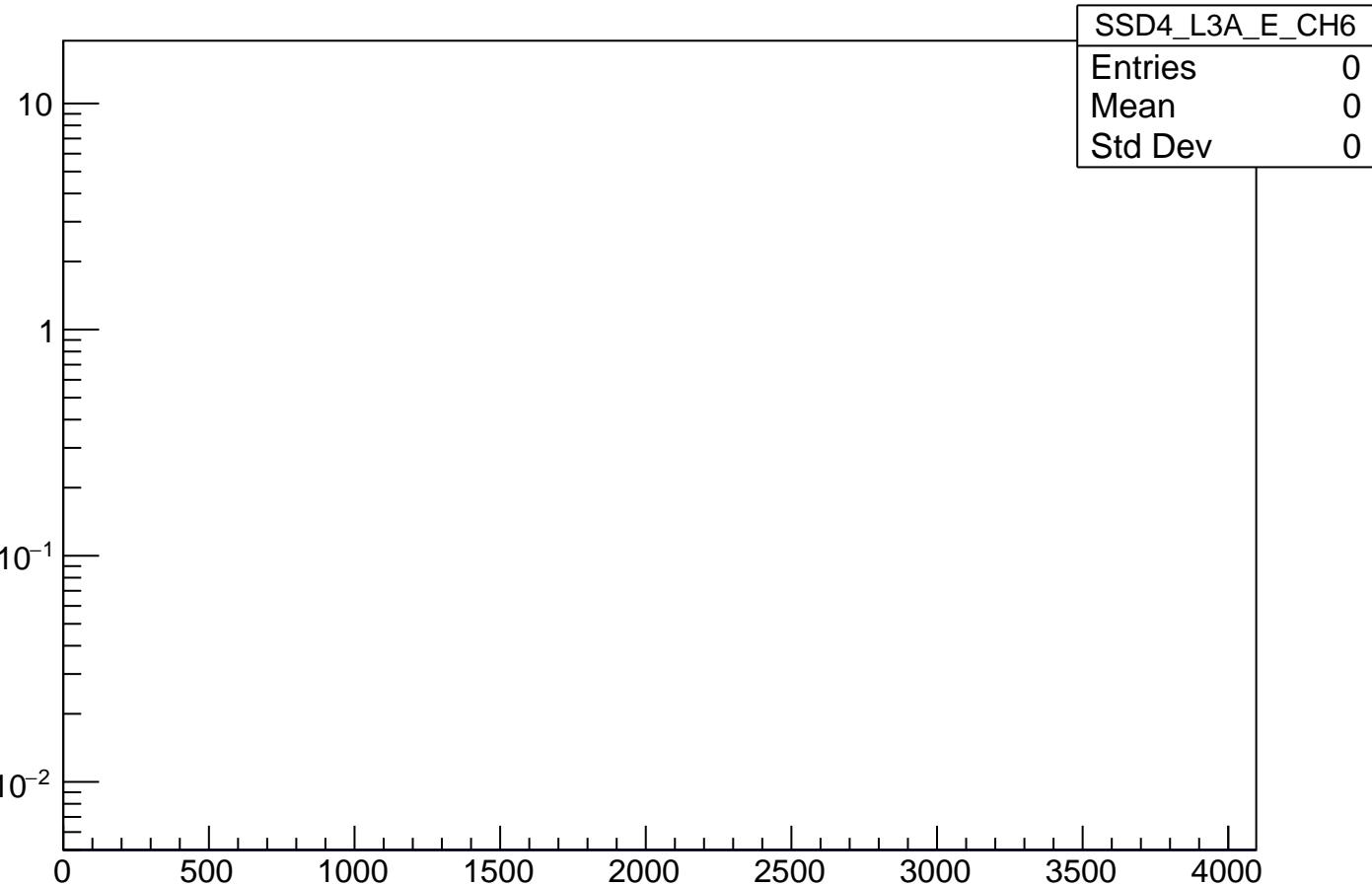
SSD4_L3A_E_CH4



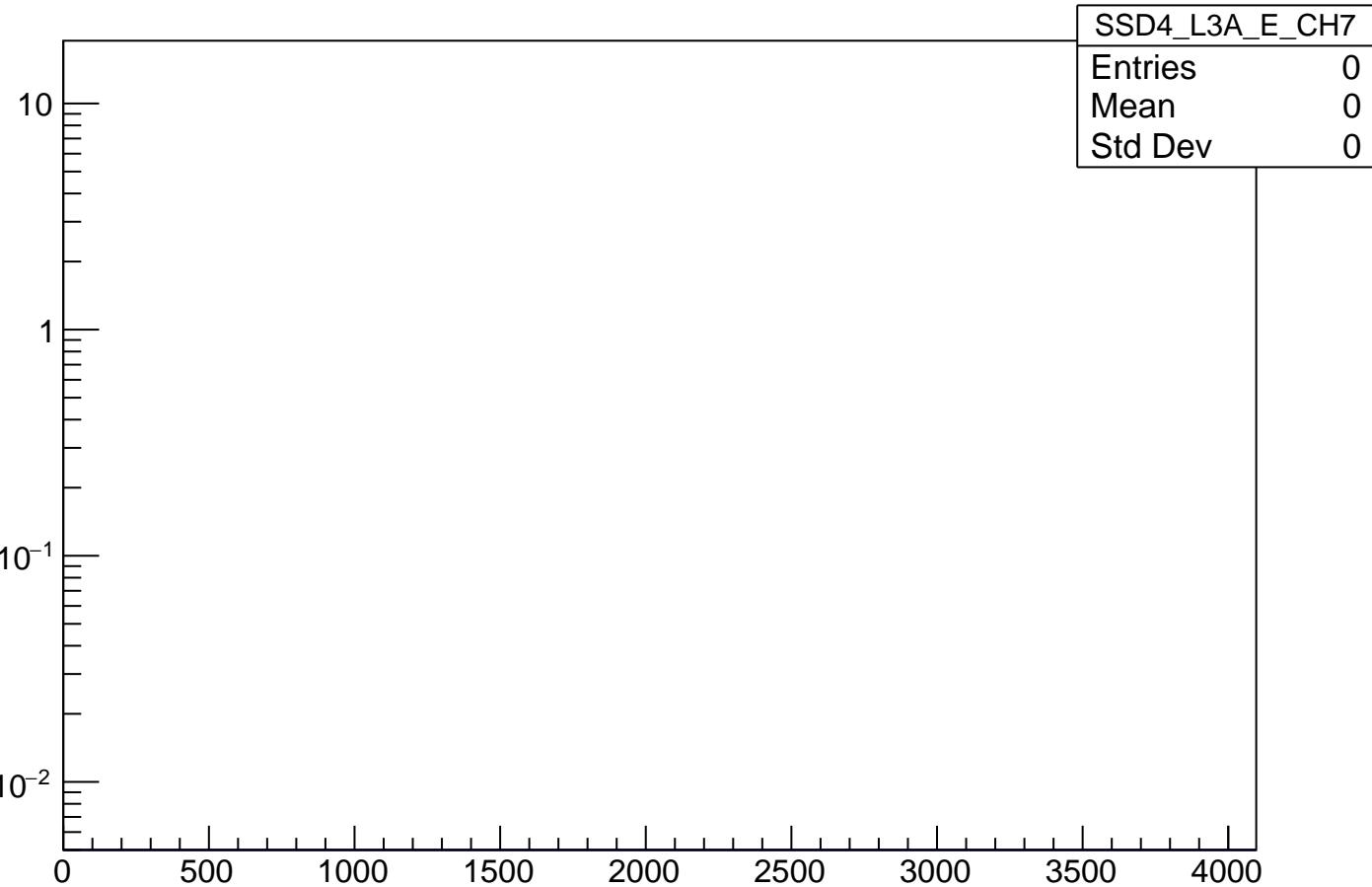
SSD4_L3A_E_CH5



SSD4_L3A_E_CH6



SSD4_L3A_E_CH7



SSD4_L3A_E_CH8

