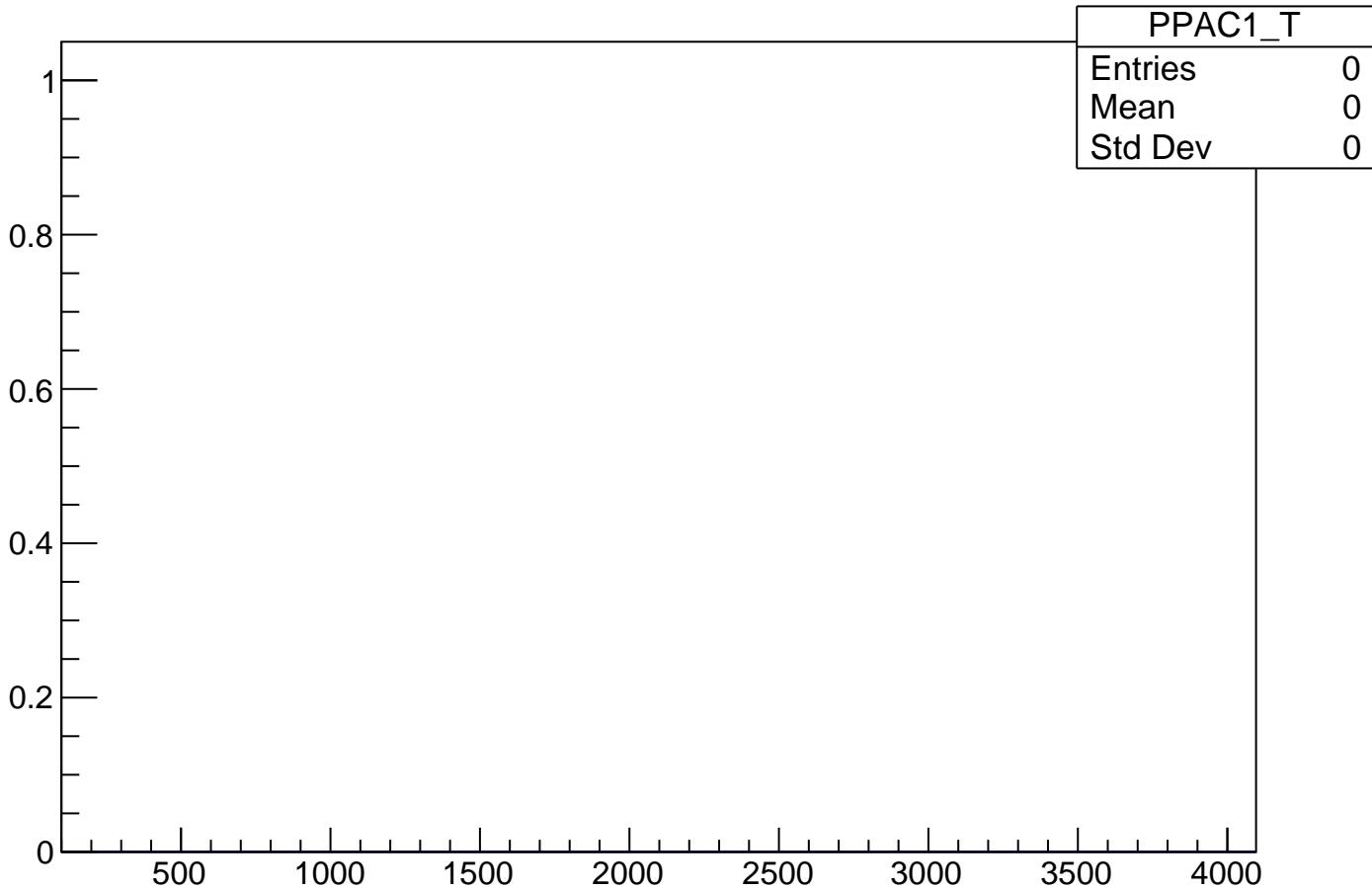
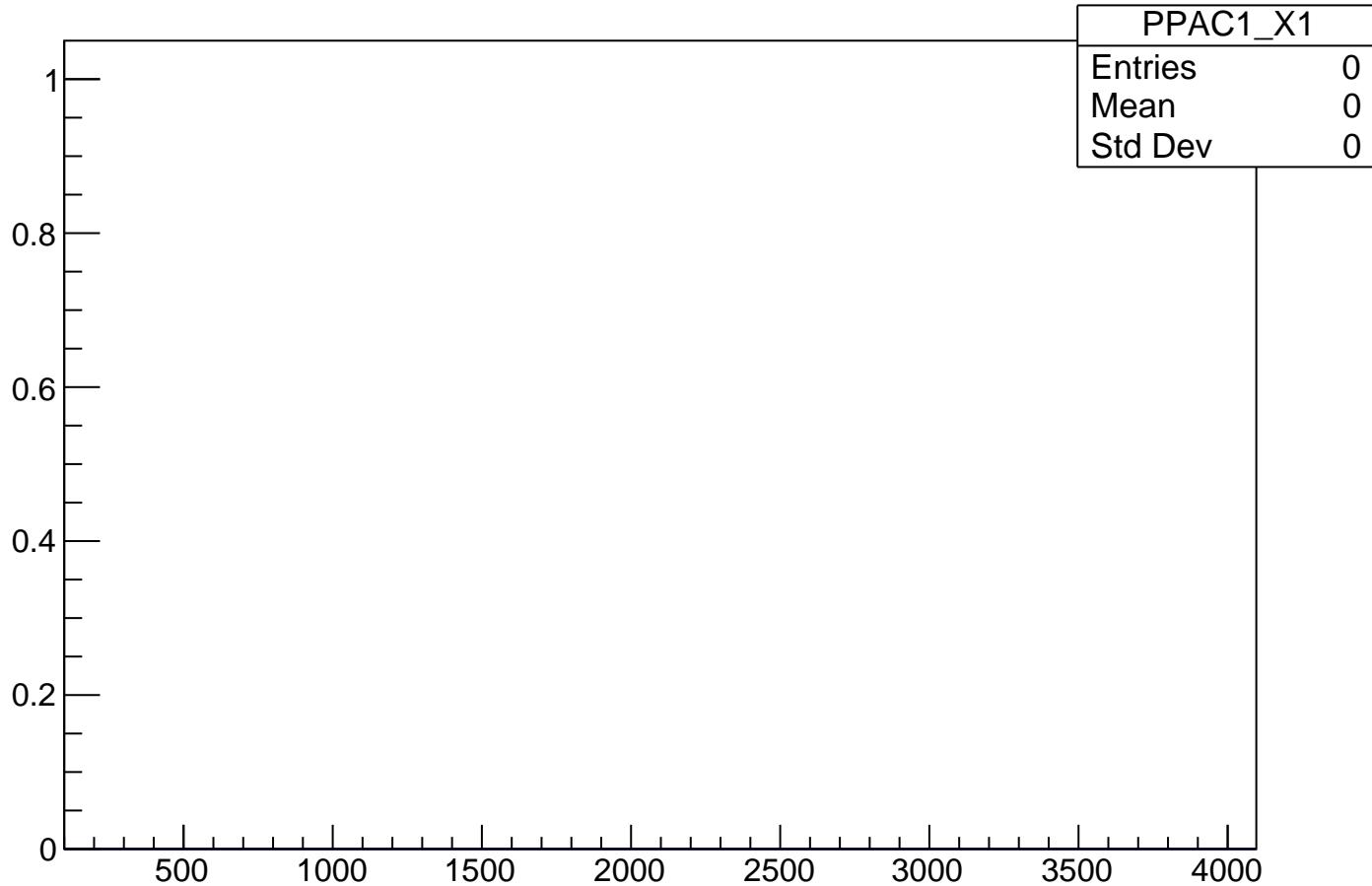


PPAC1_T

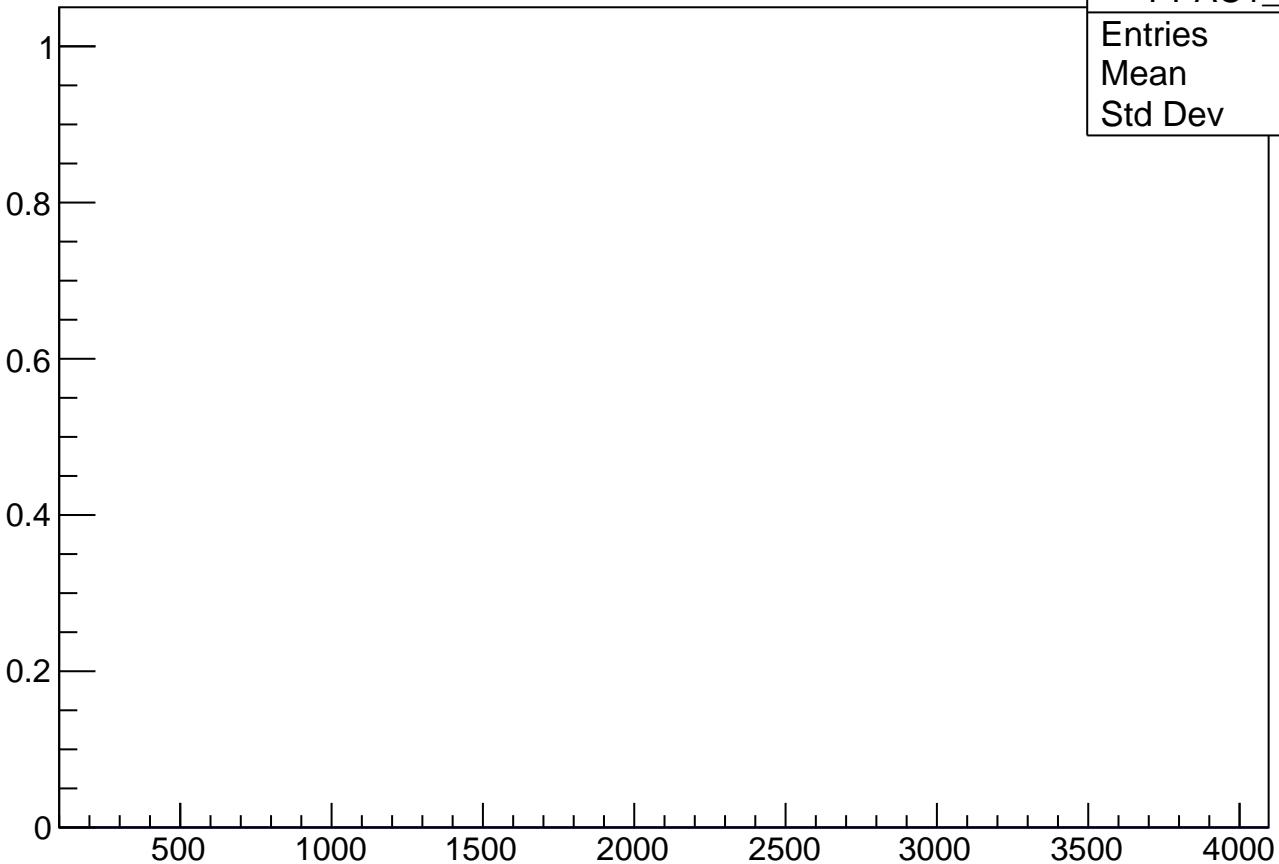


PPAC1_X1

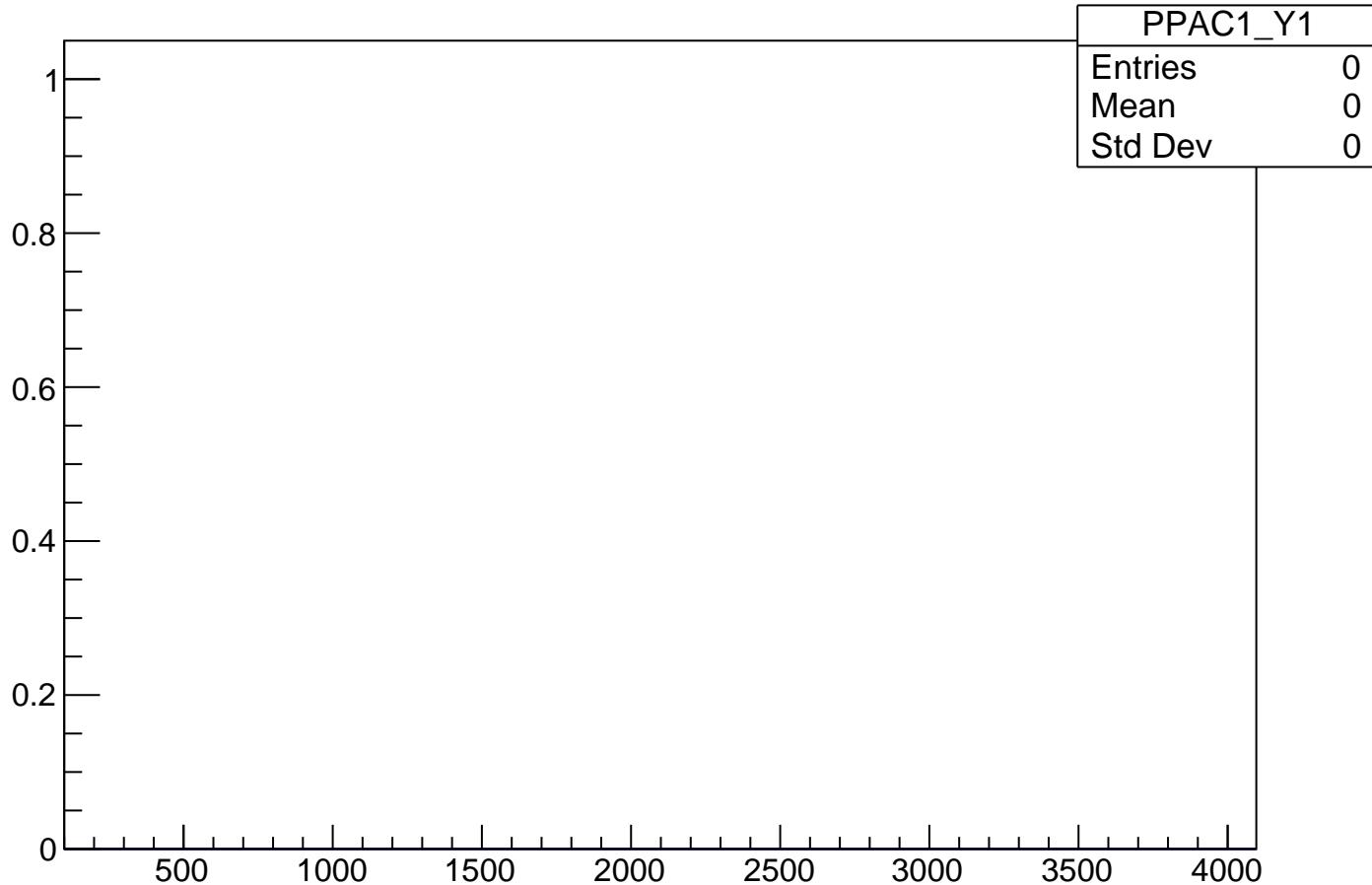


PPAC1_X2

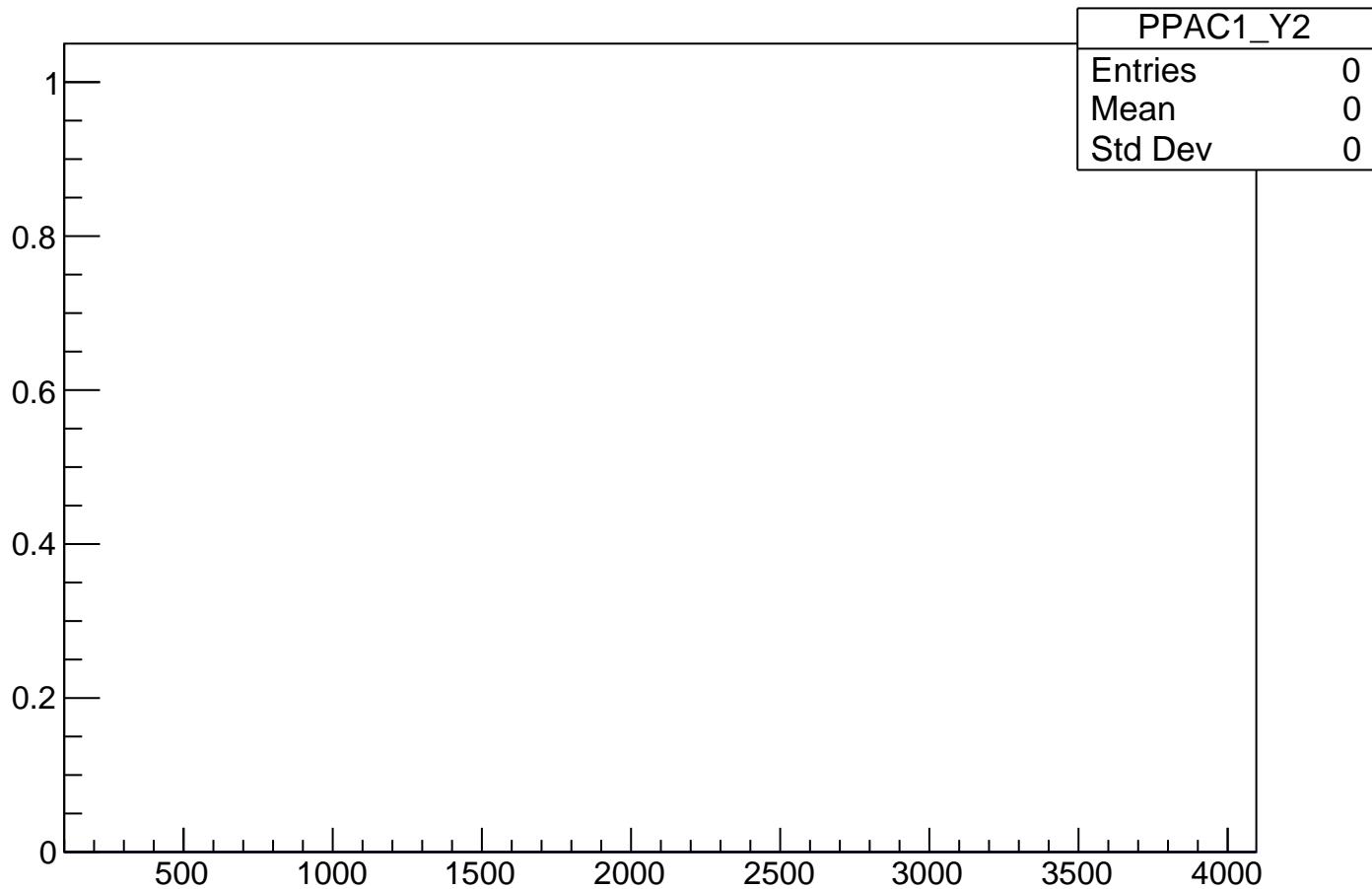
PPAC1_X2	
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Mean	0
Std Dev	0



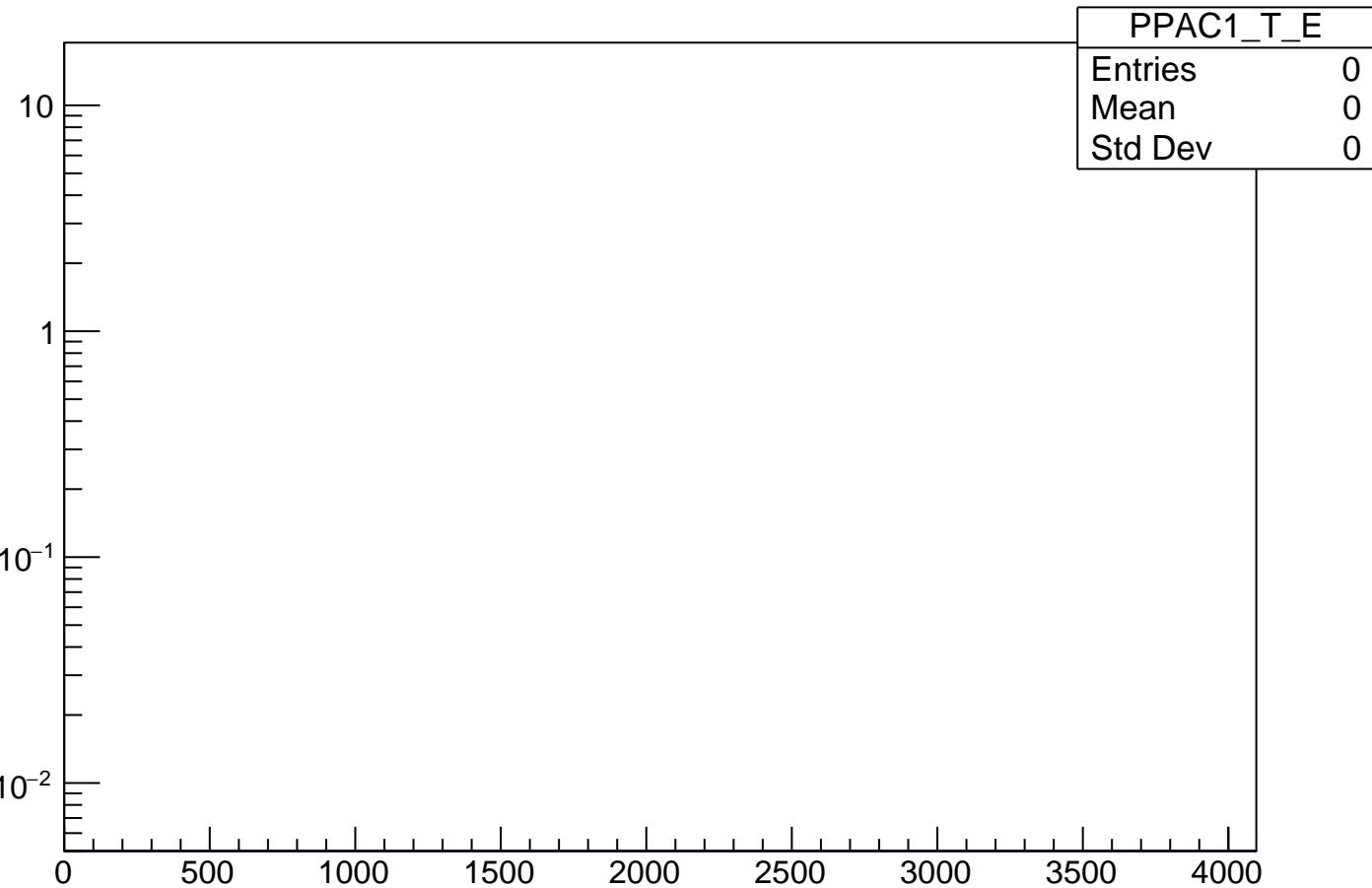
PPAC1_Y1



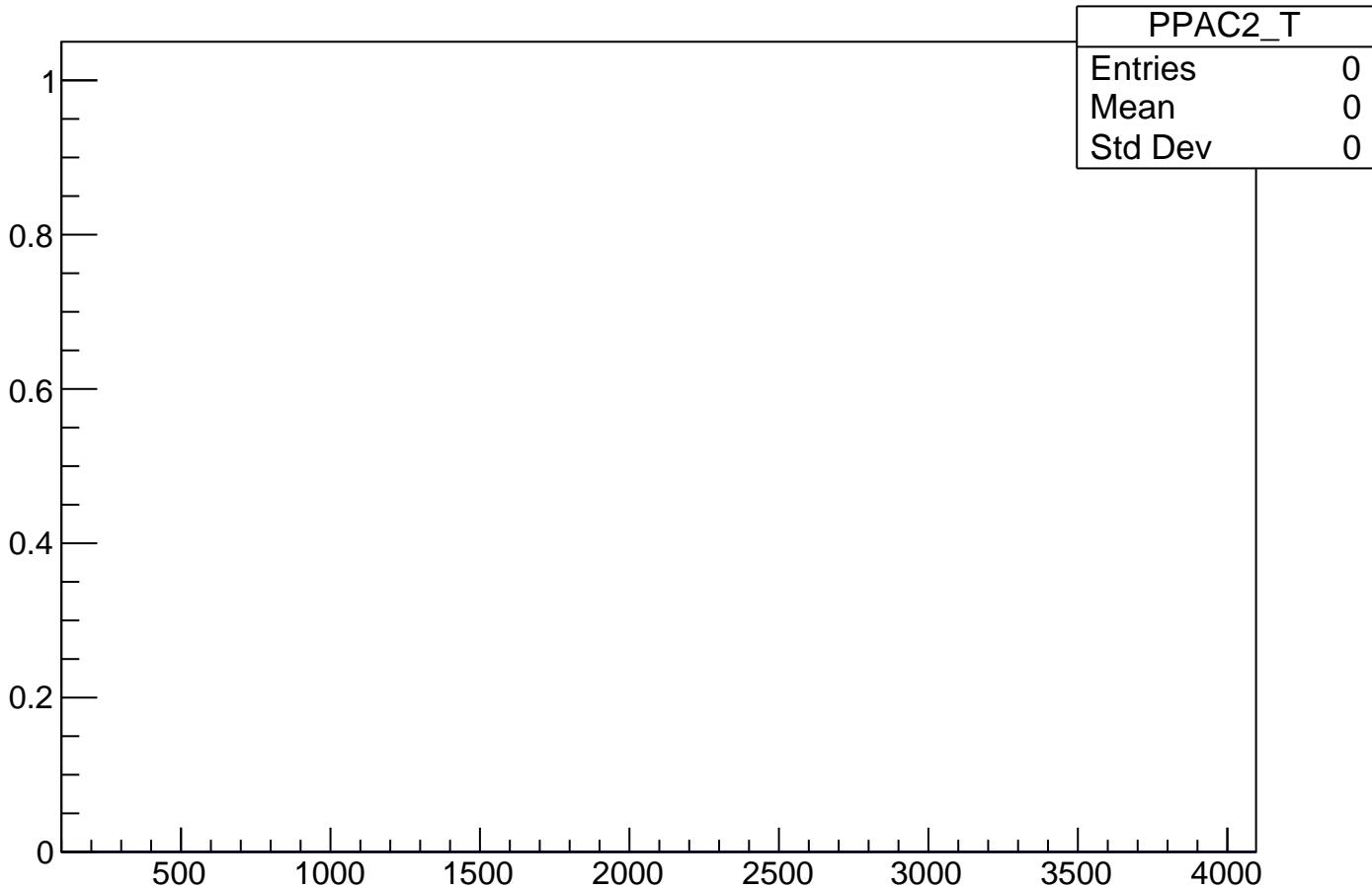
PPAC1_Y2



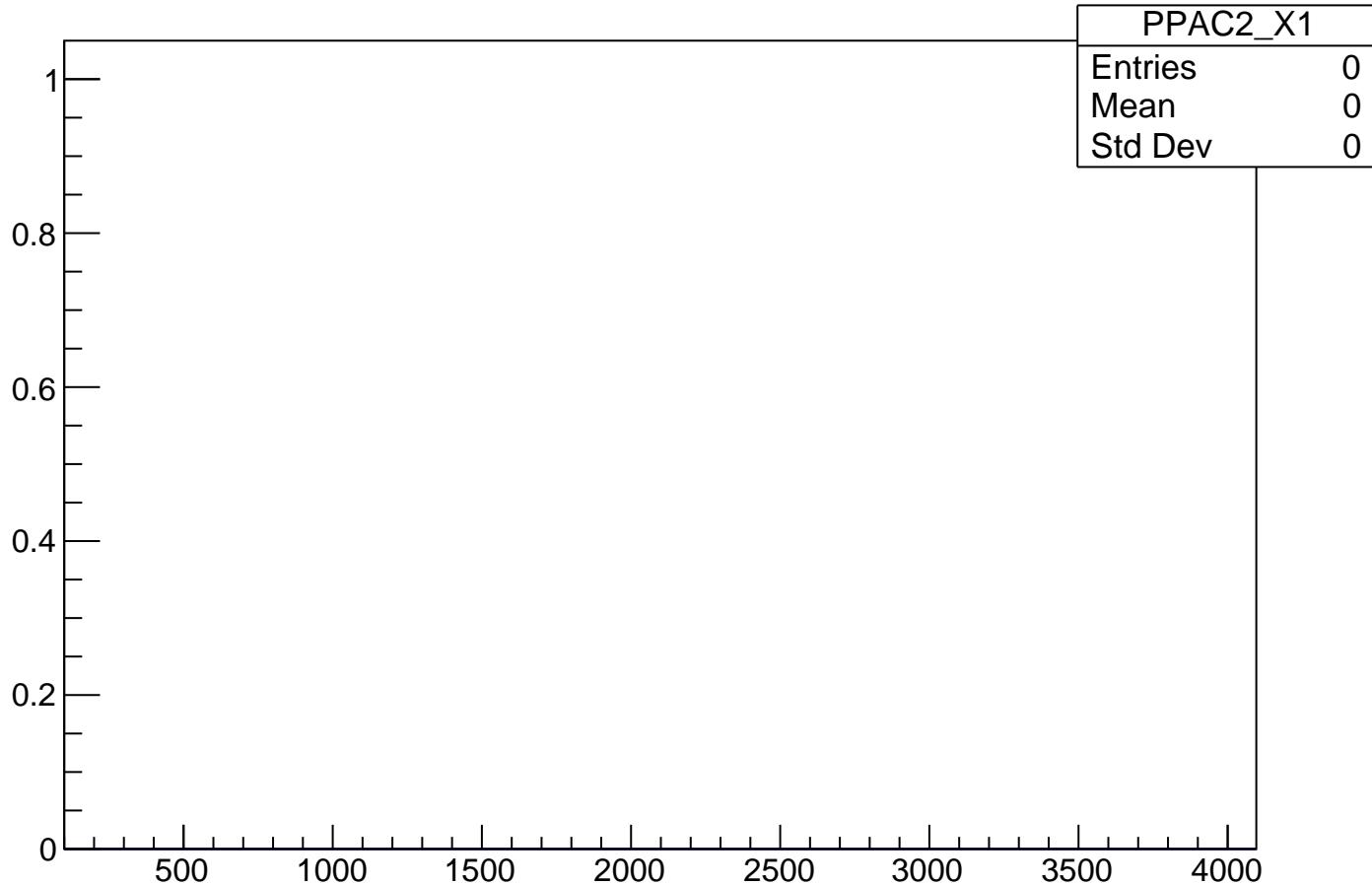
PPAC1_T_E



PPAC2_T

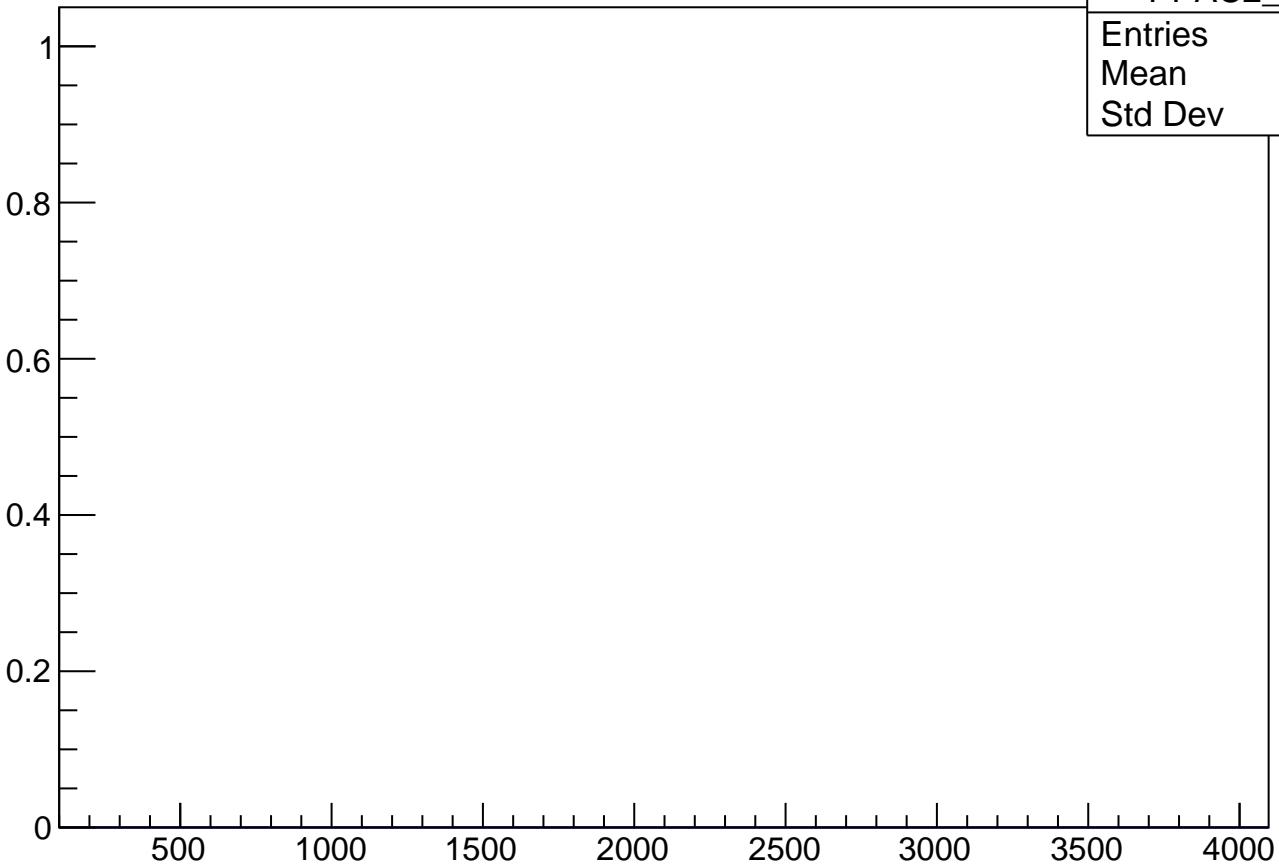


PPAC2_X1

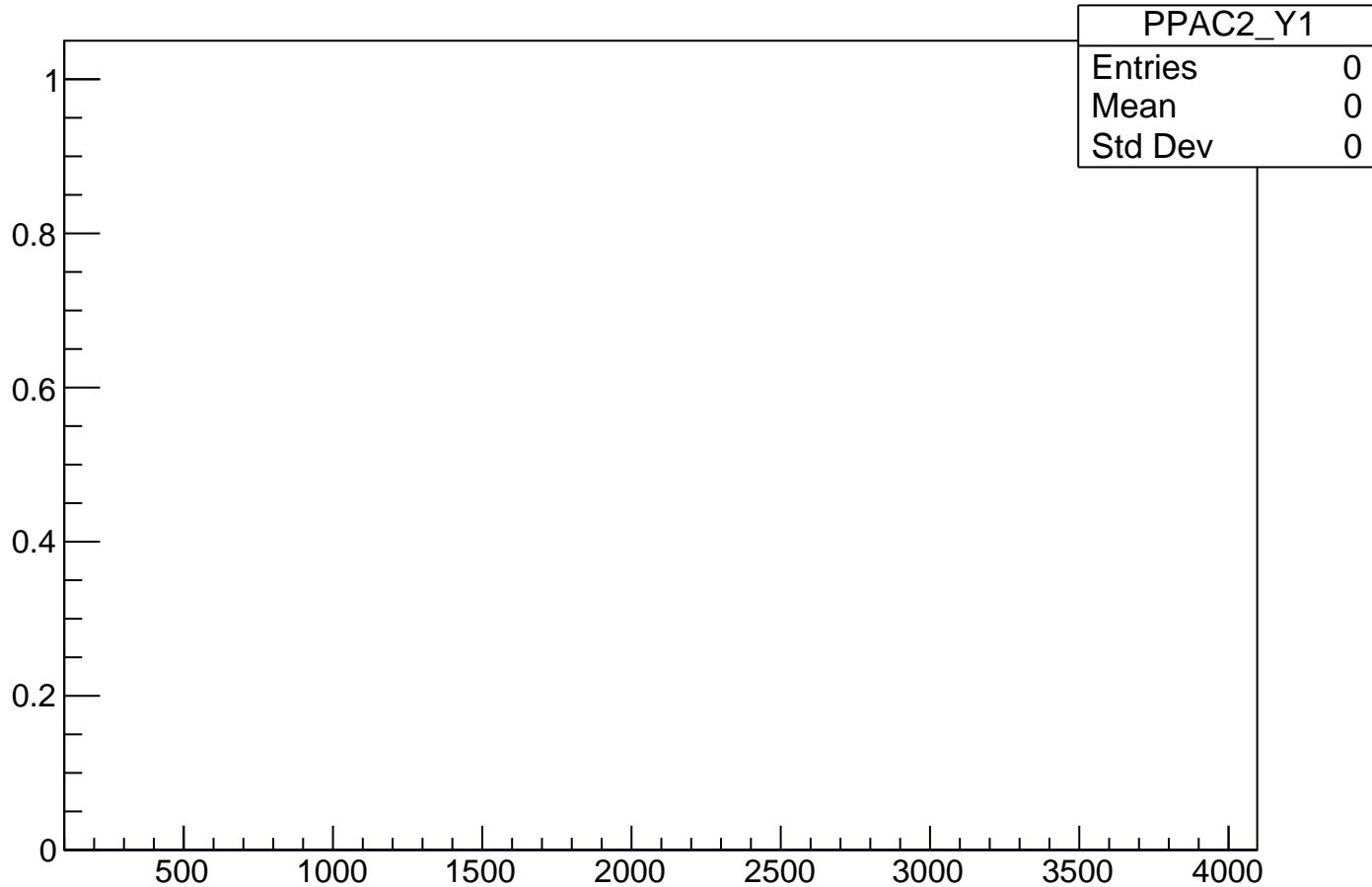


PPAC2_X2

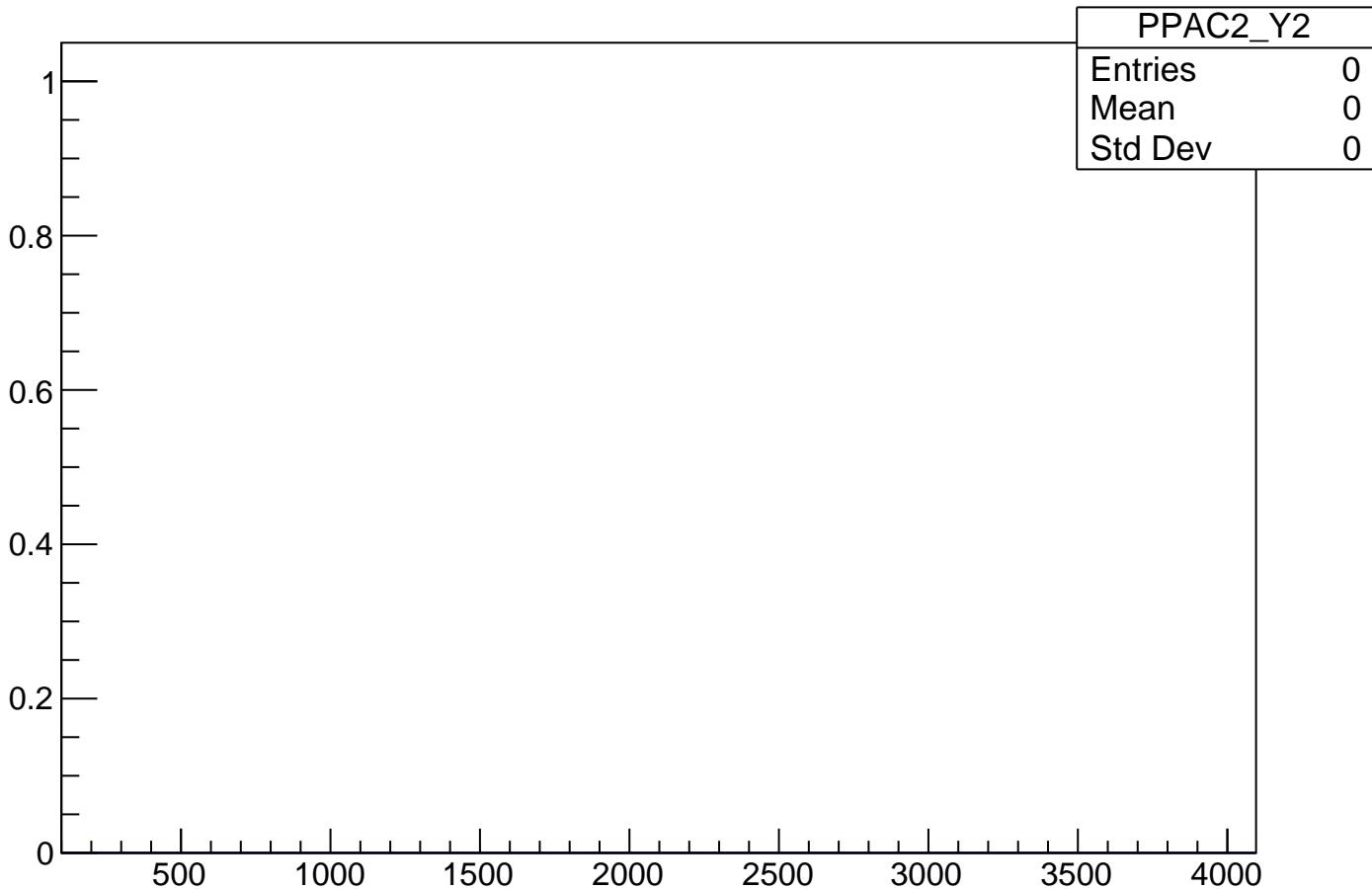
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Mean	0
Std Dev	0



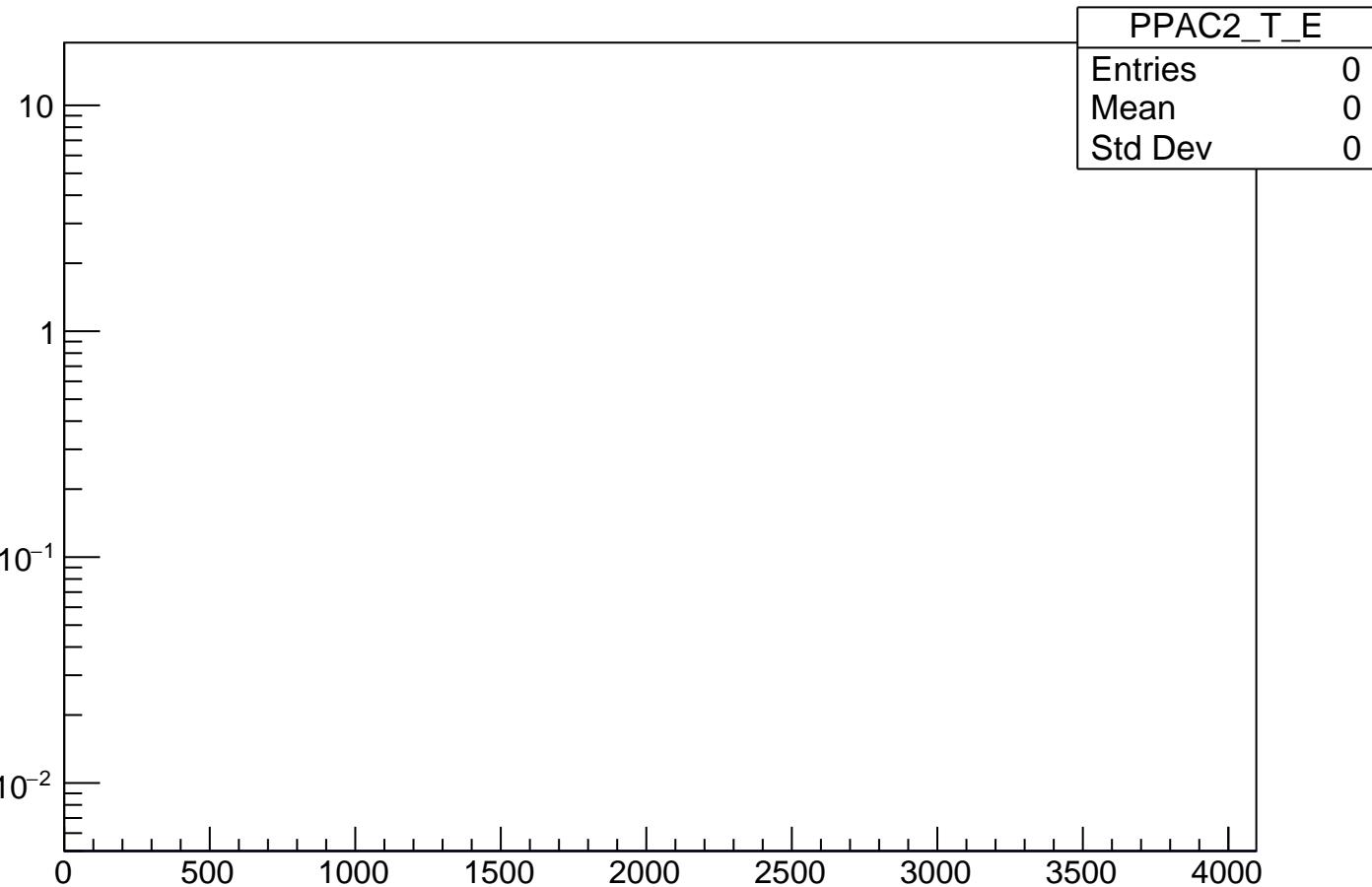
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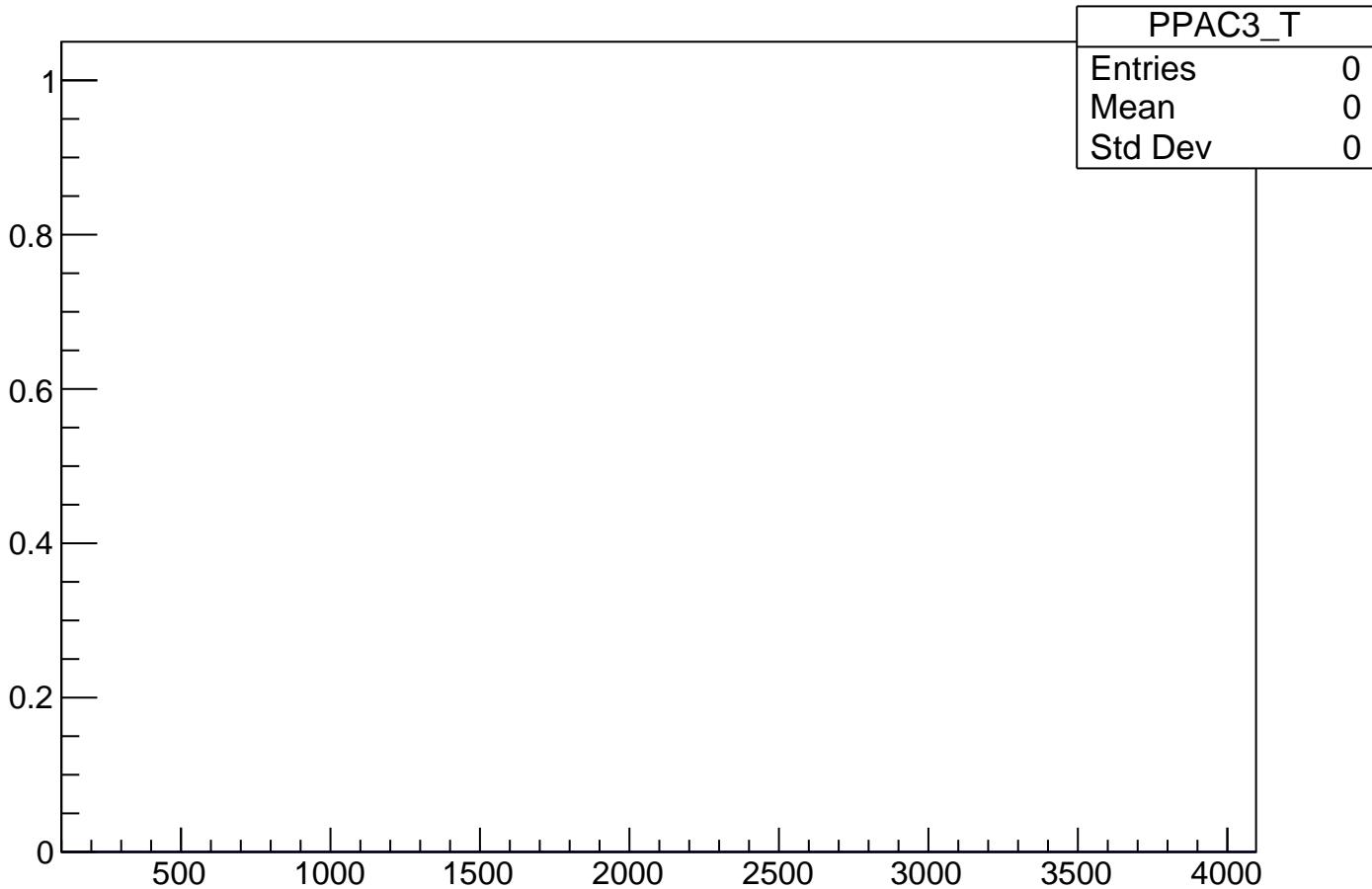
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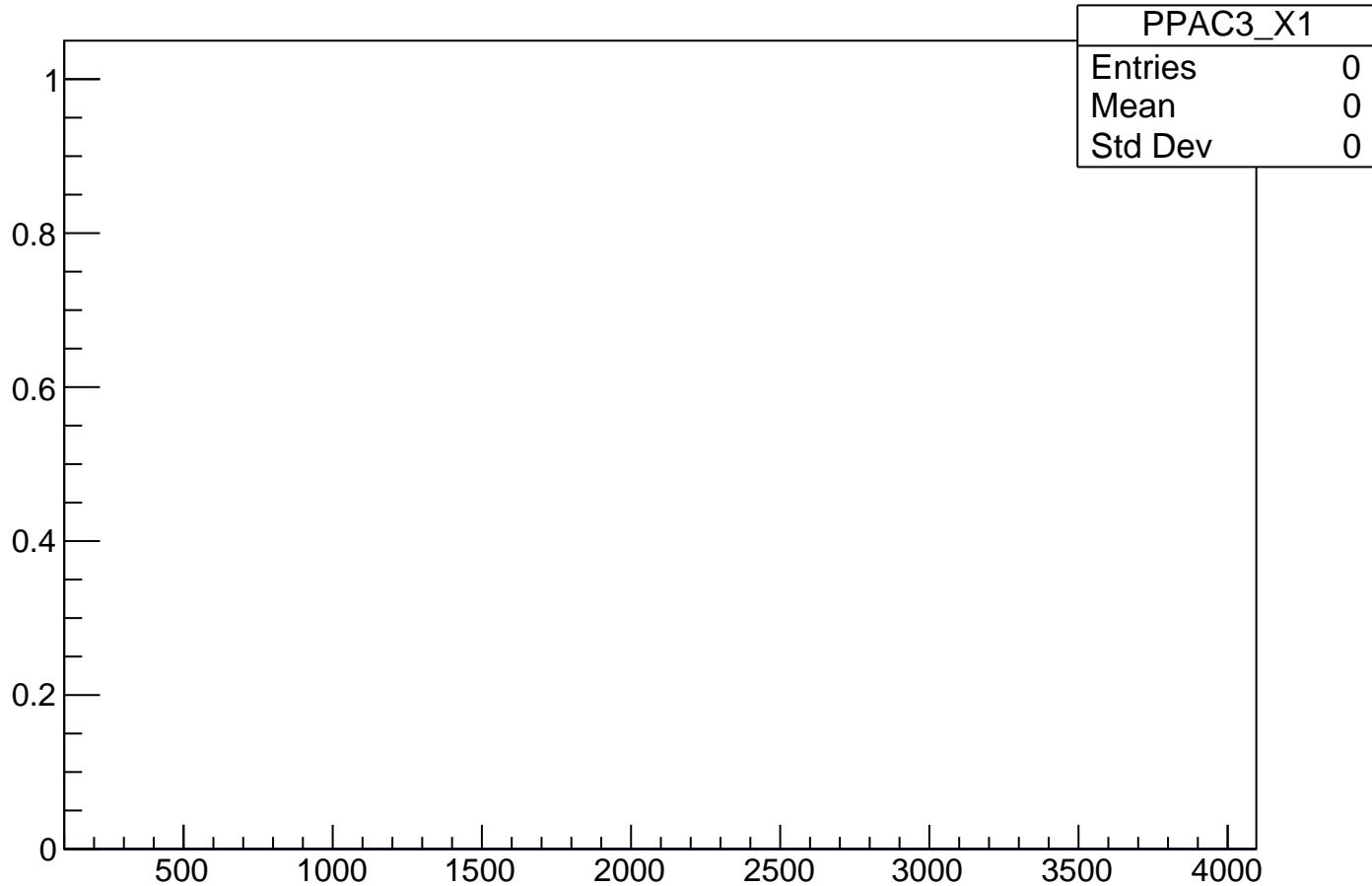
PPAC2_T_E



PPAC3_T

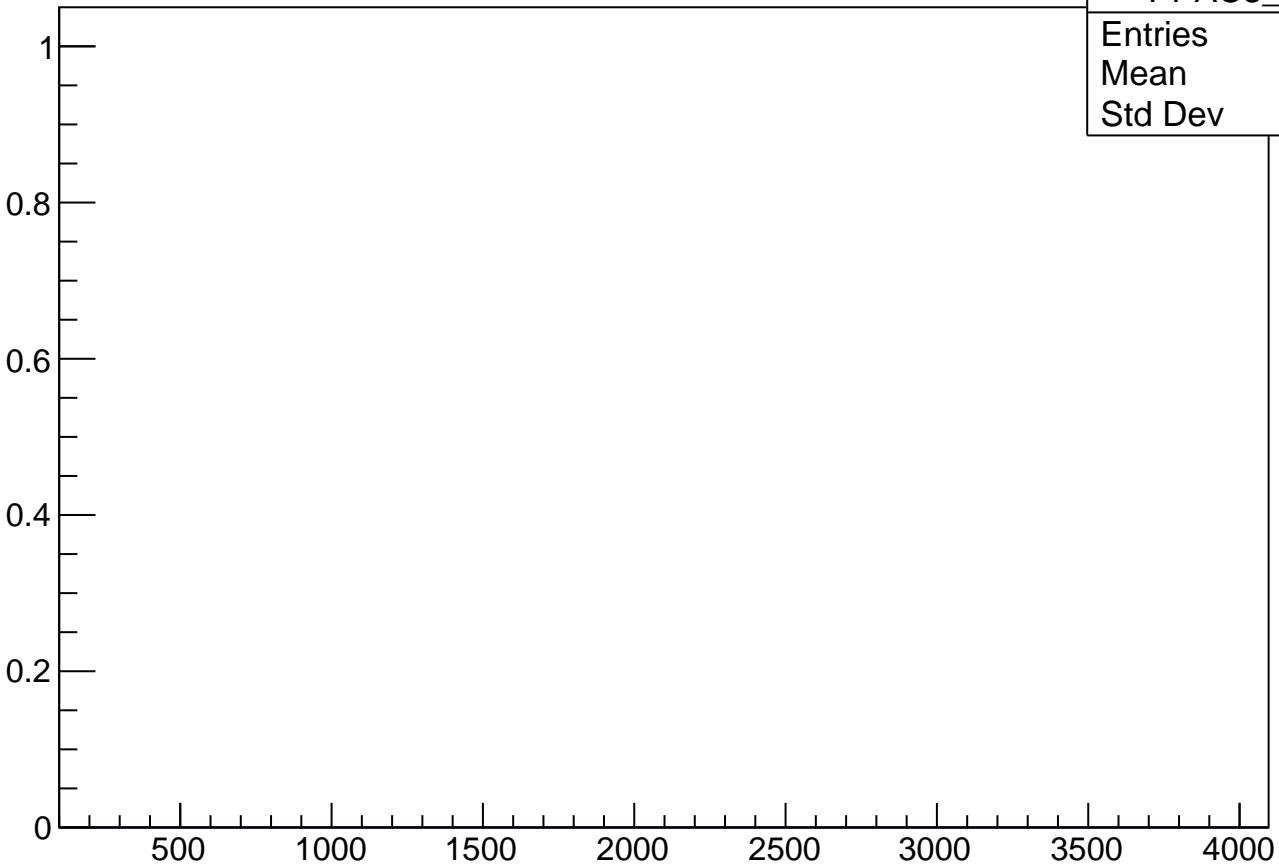


PPAC3_X1

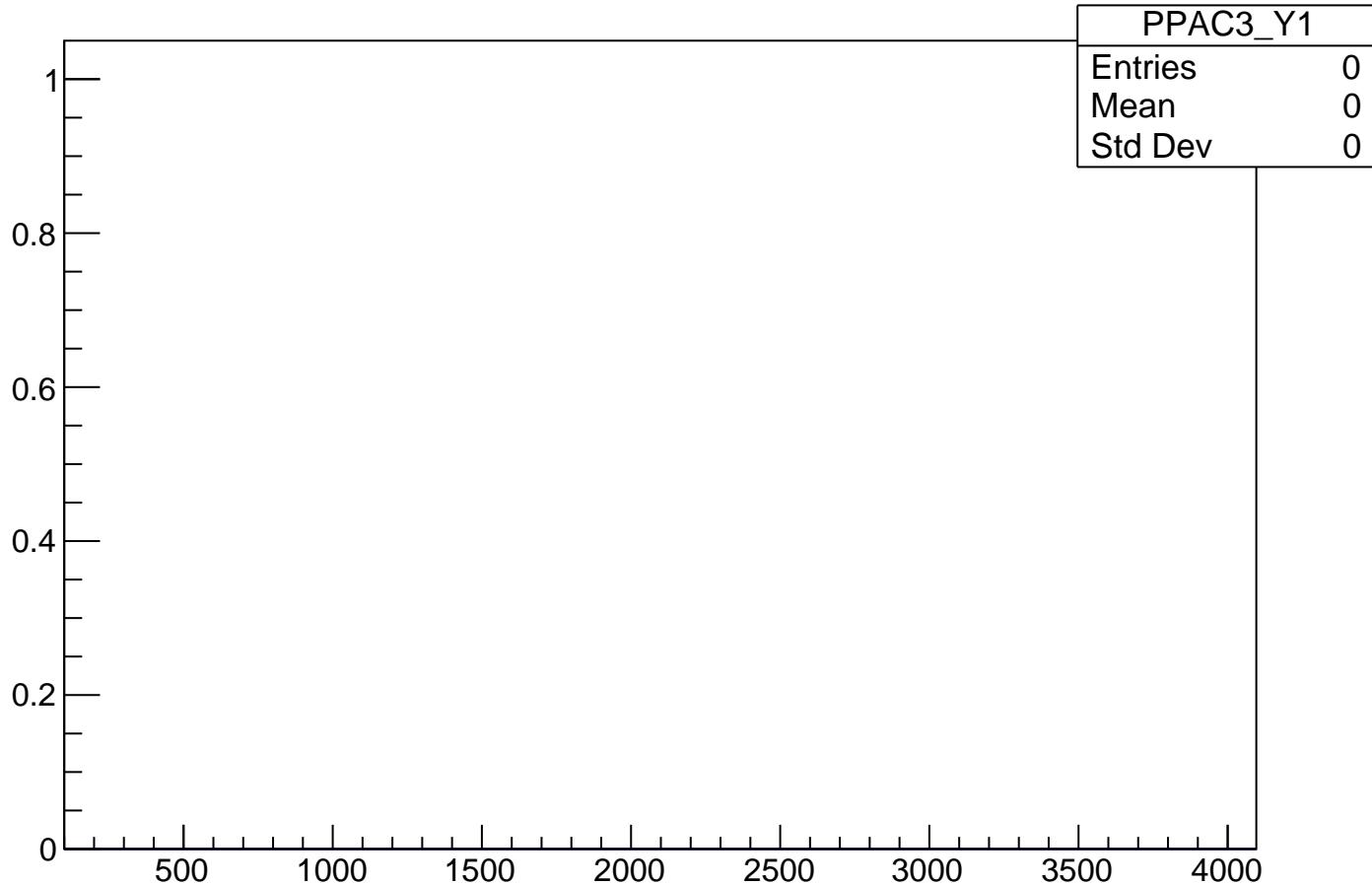


PPAC3_X2

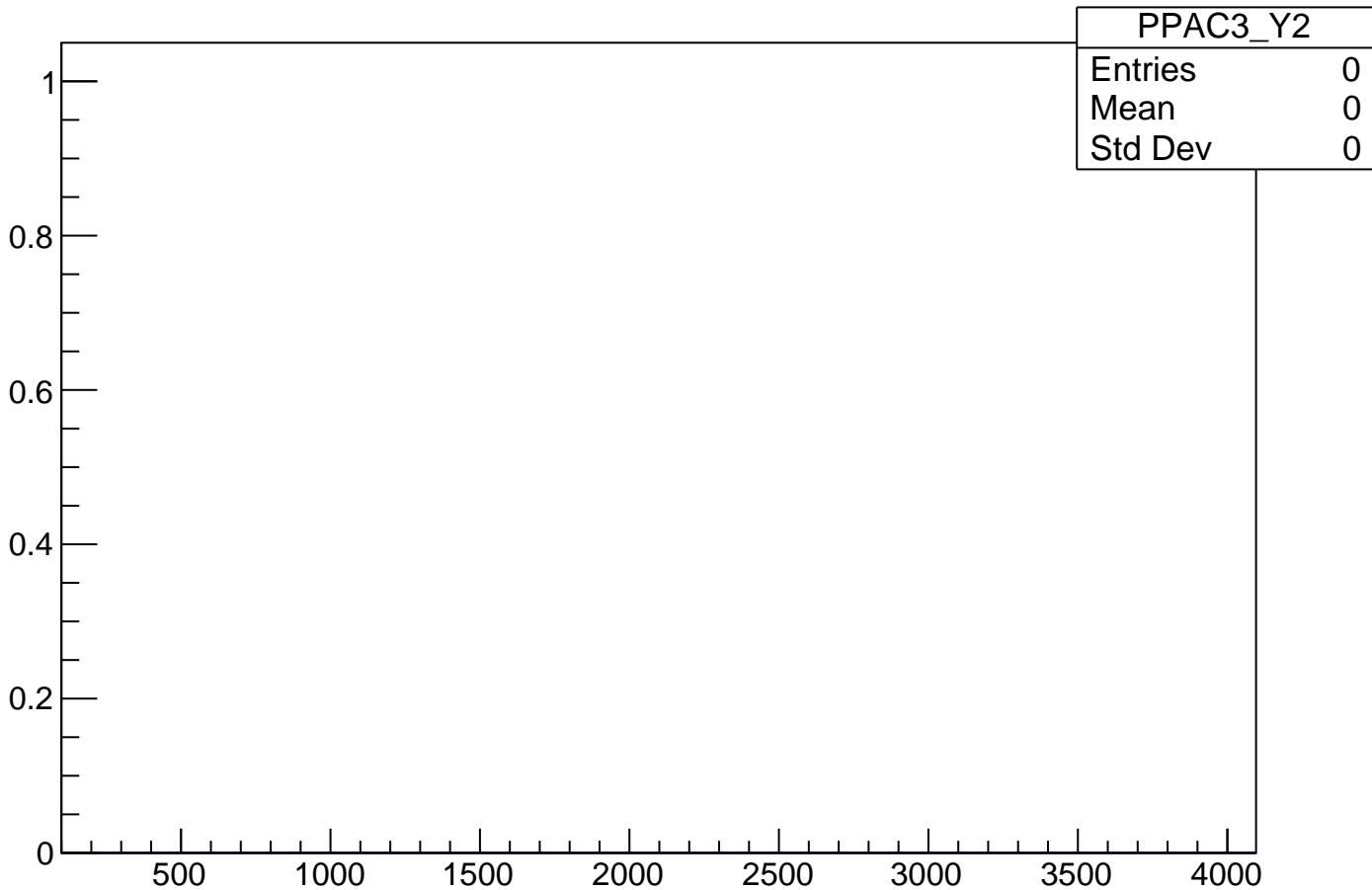
PPAC3_X2	
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Mean	0
Std Dev	0



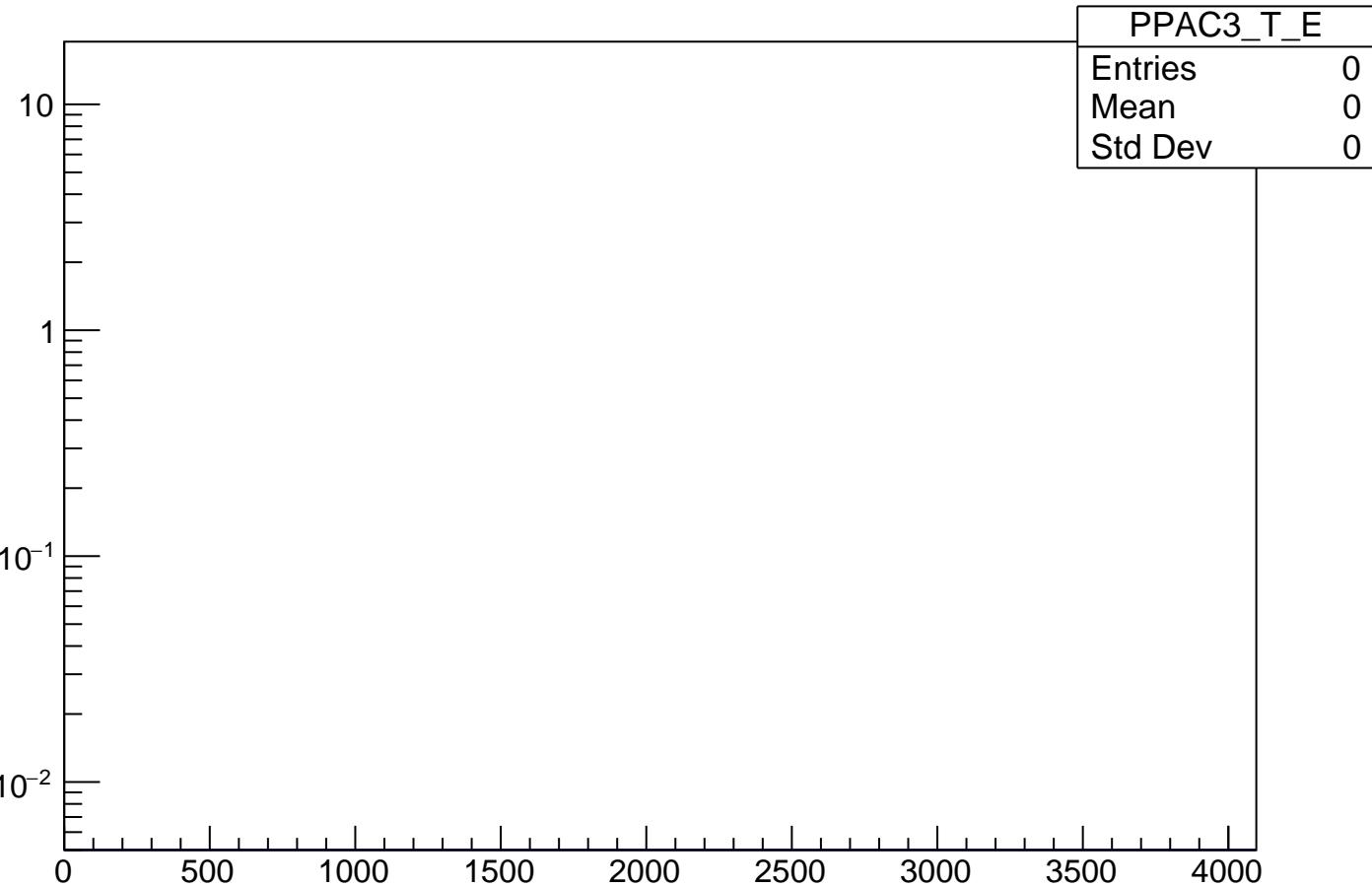
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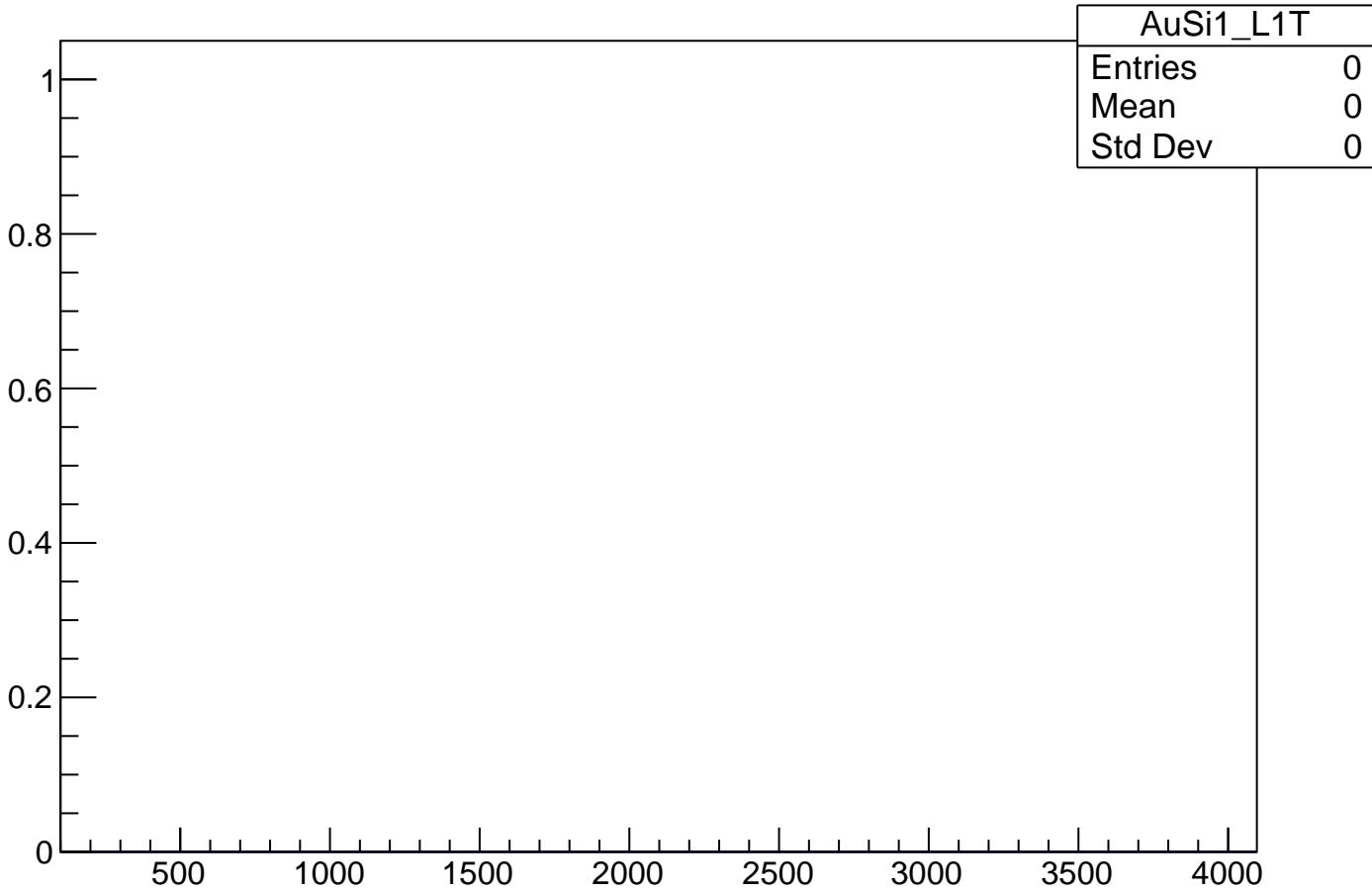
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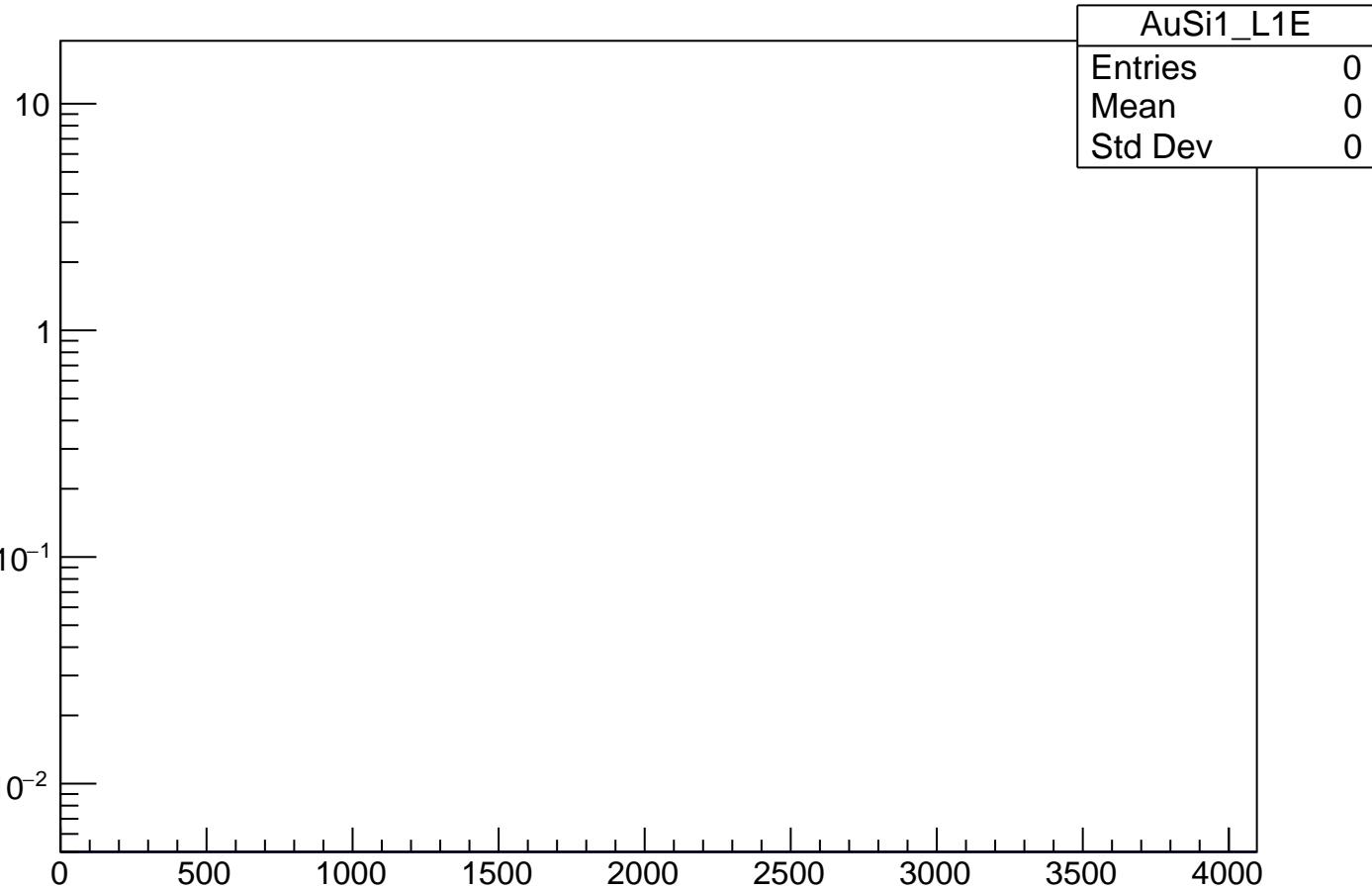
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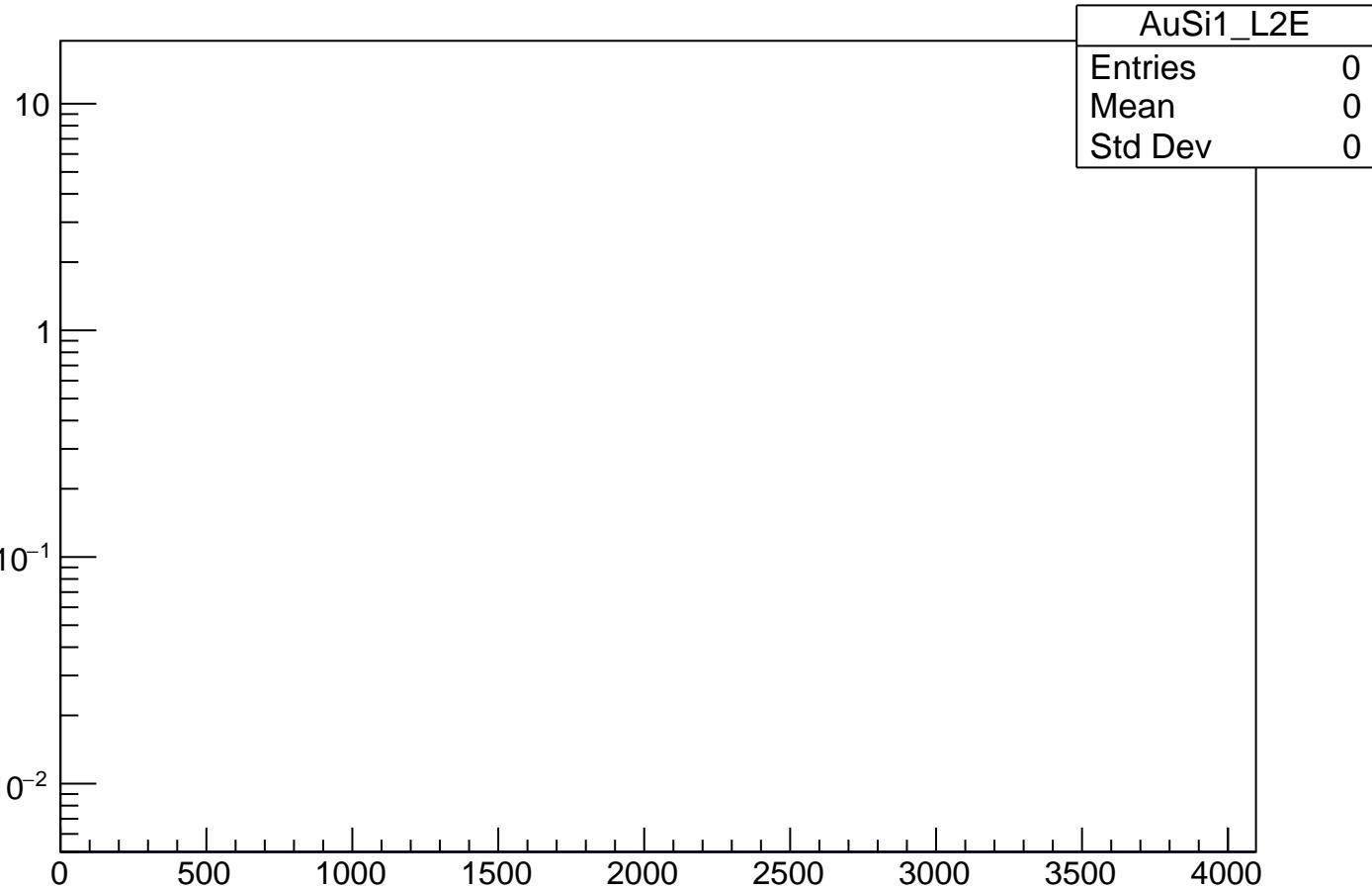
AuSi1_L1T



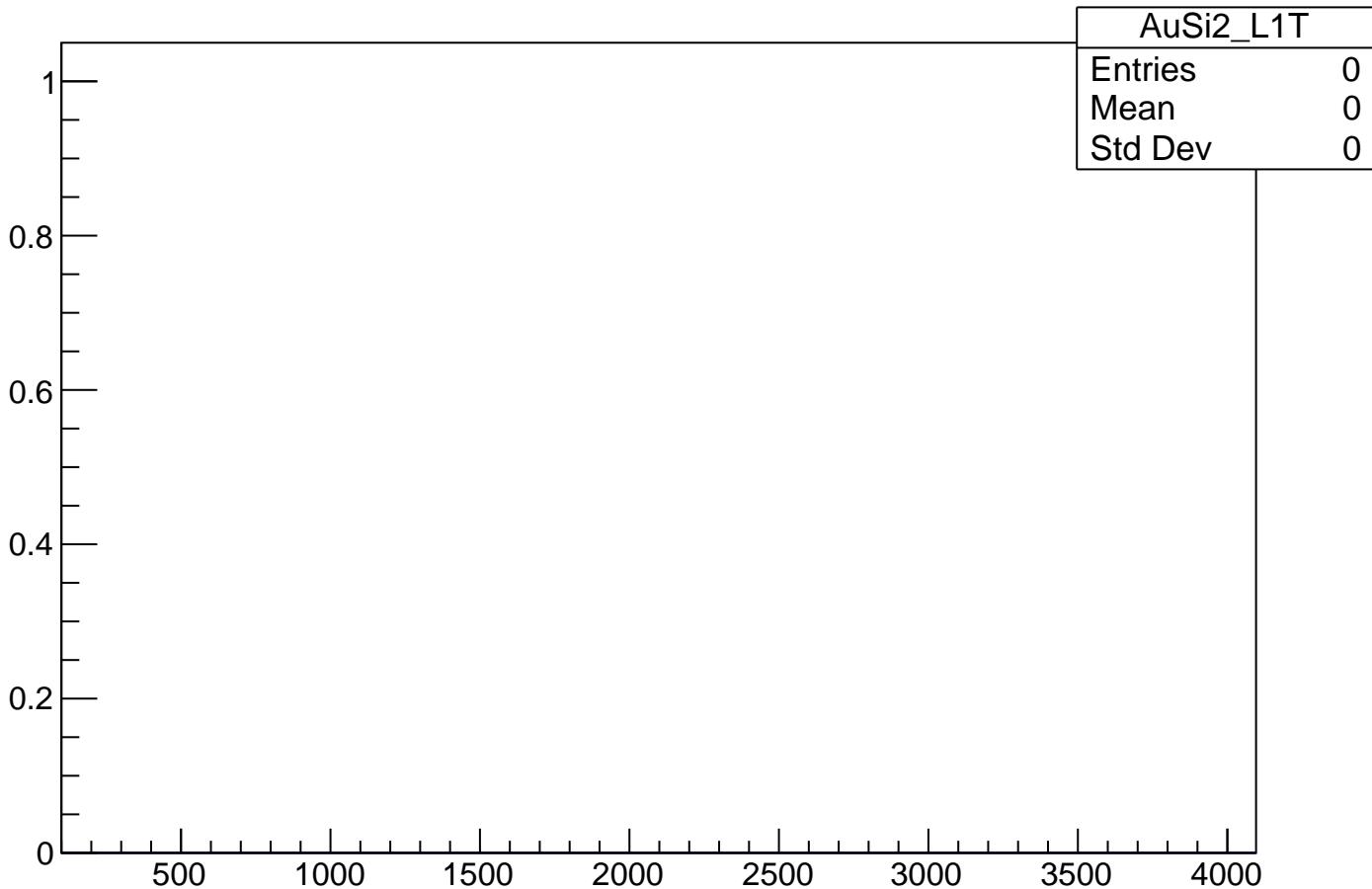
AuSi1_L1E



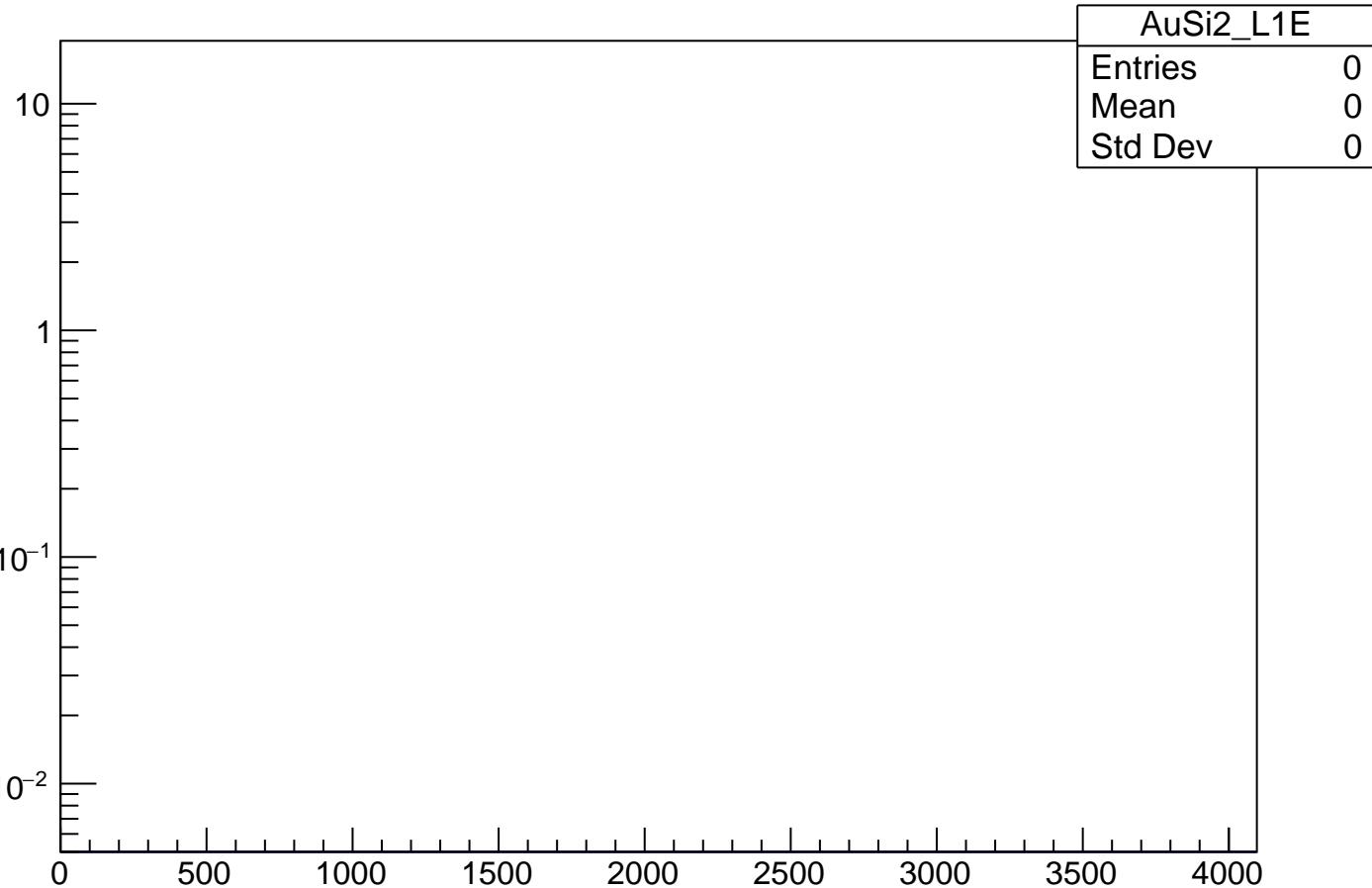
AuSi1_L2E



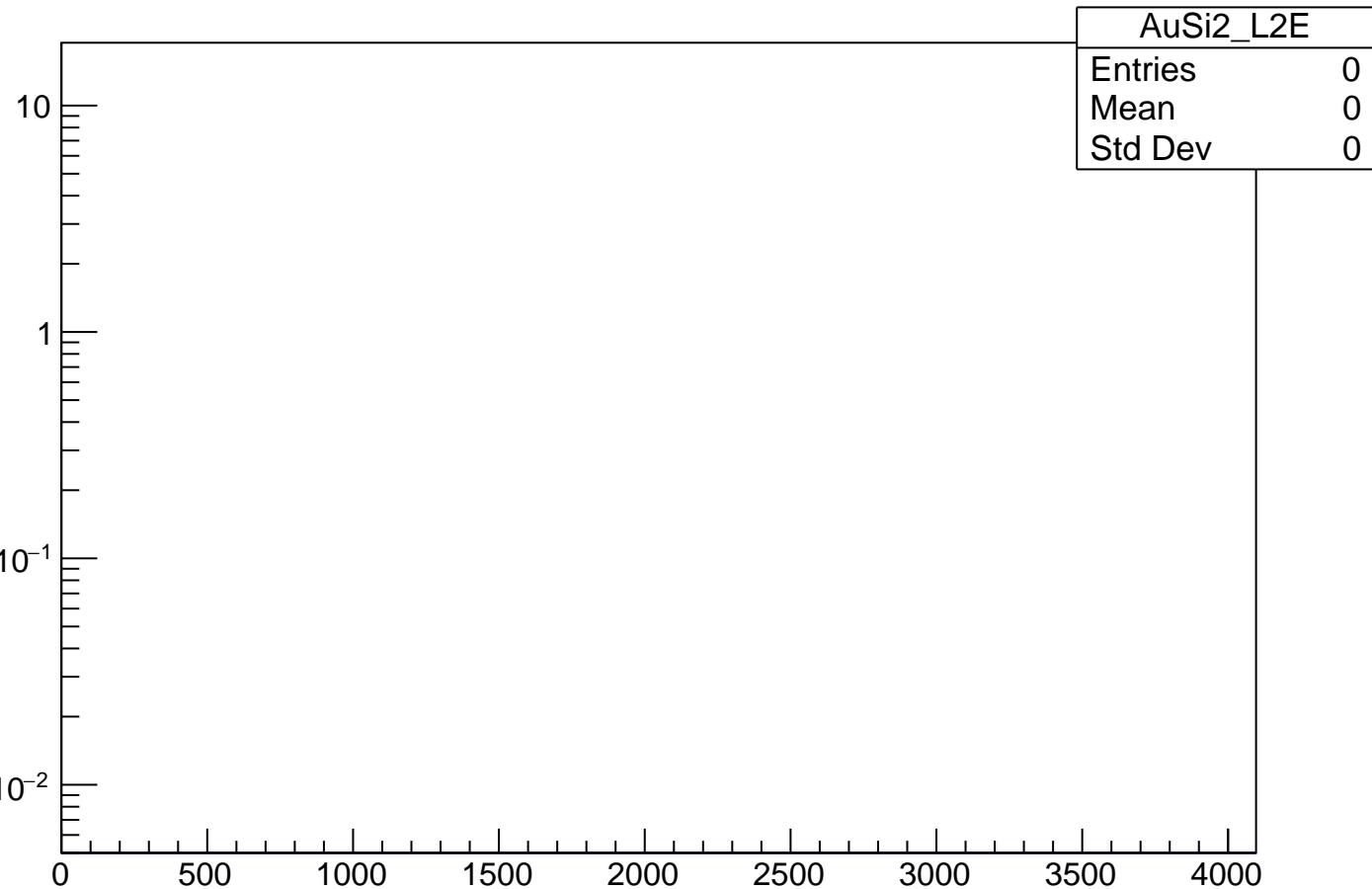
AuSi2_L1T



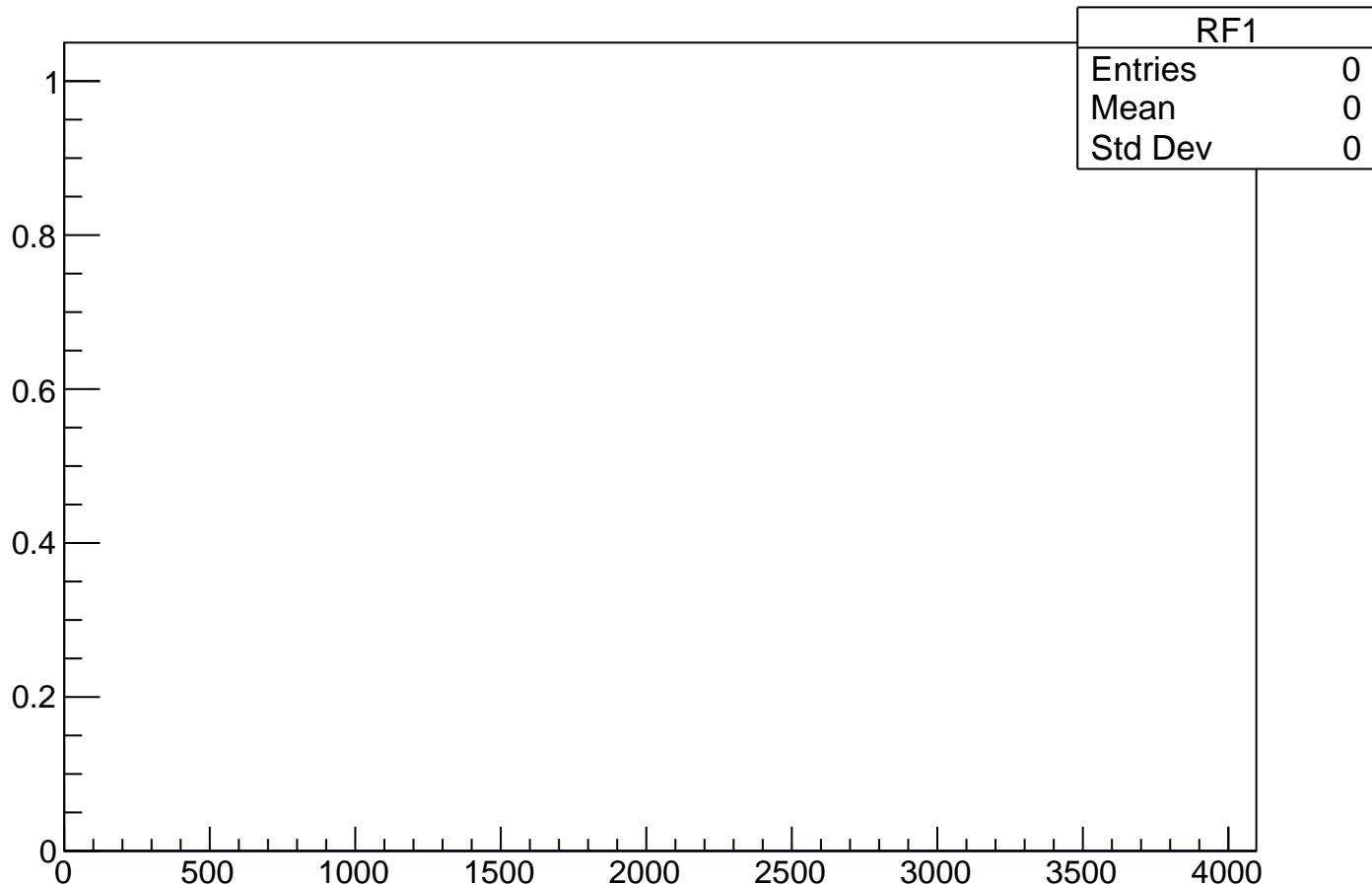
AuSi2_L1E



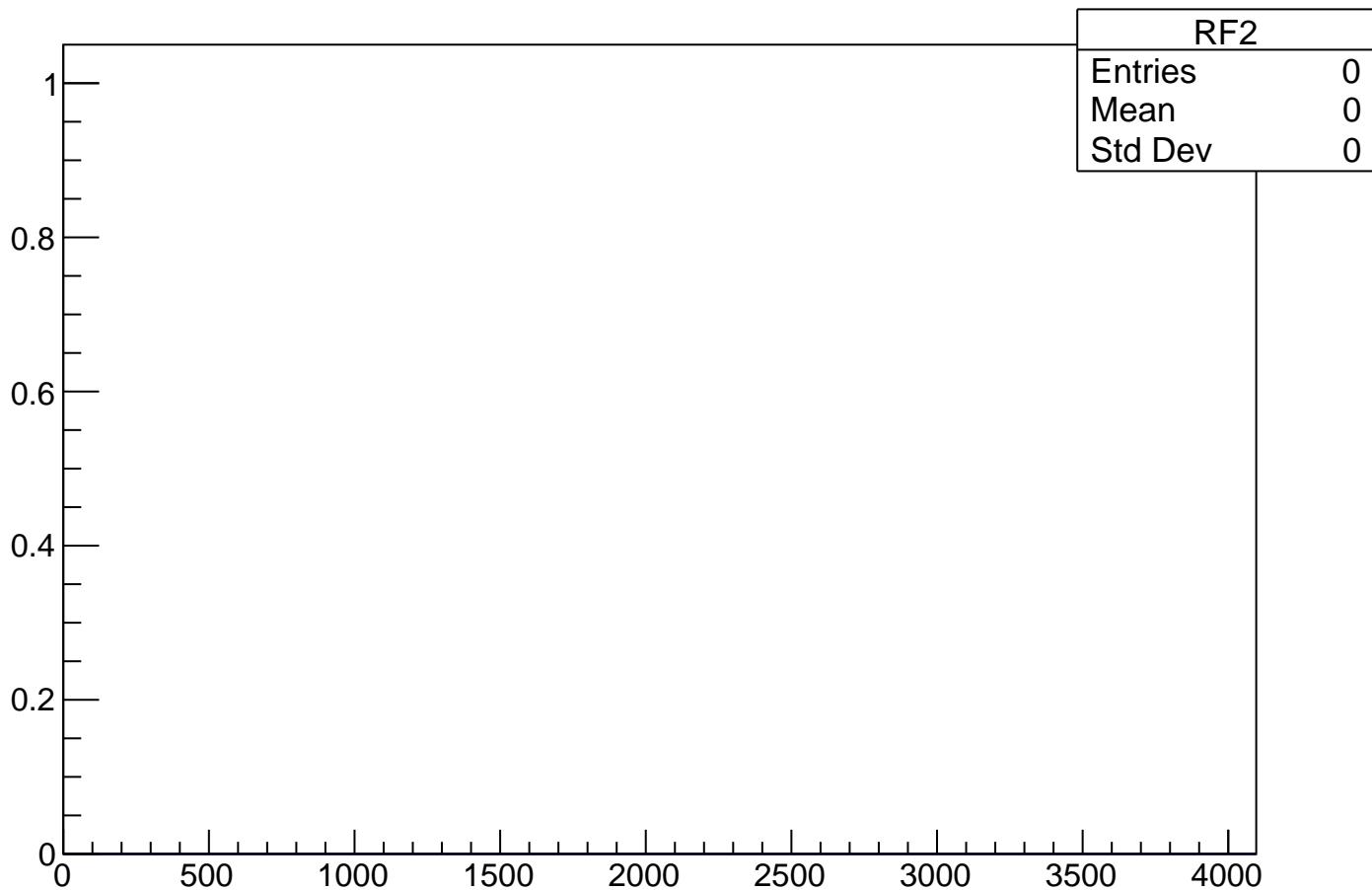
AuSi2_L2E



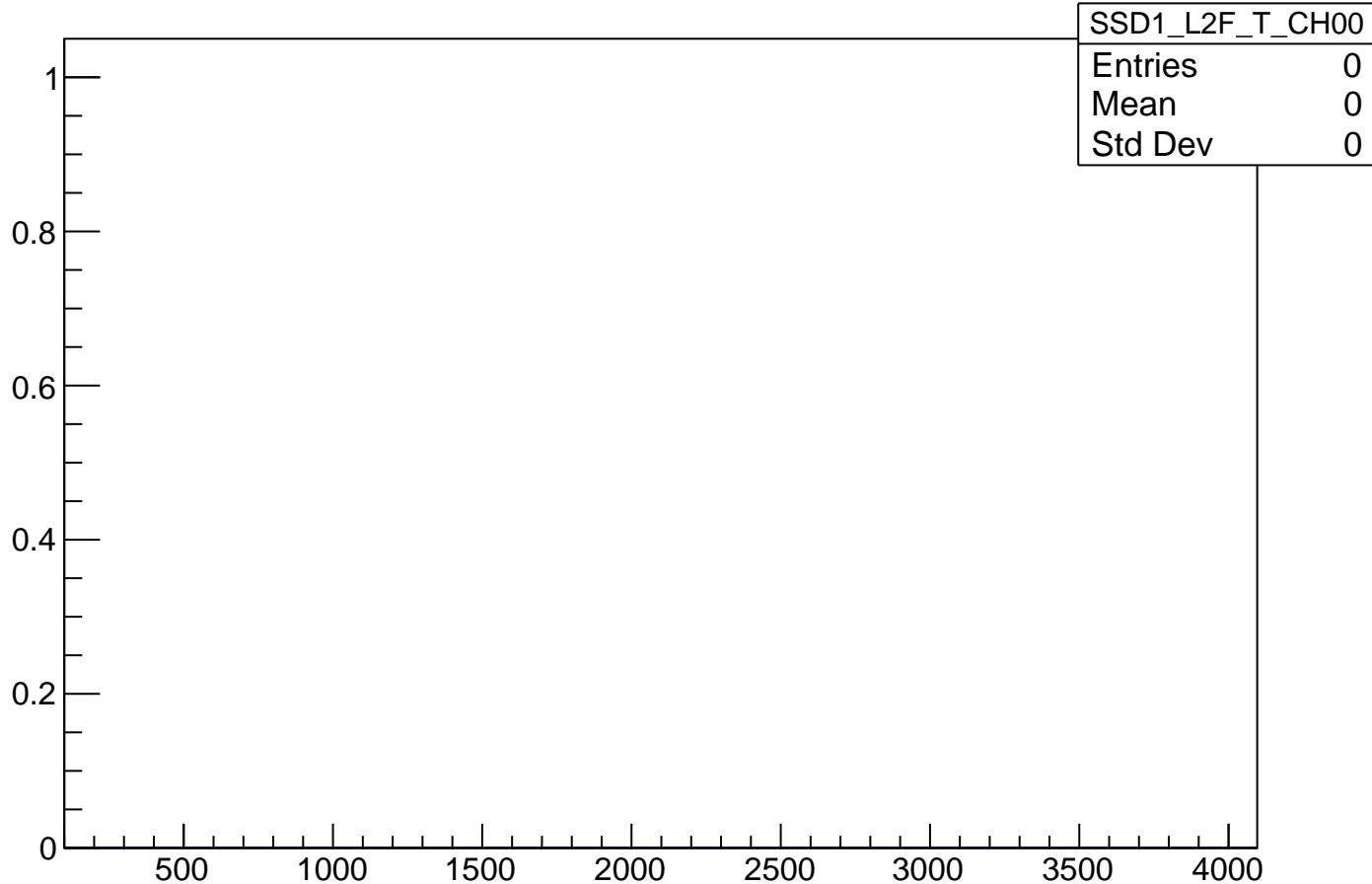
RF1



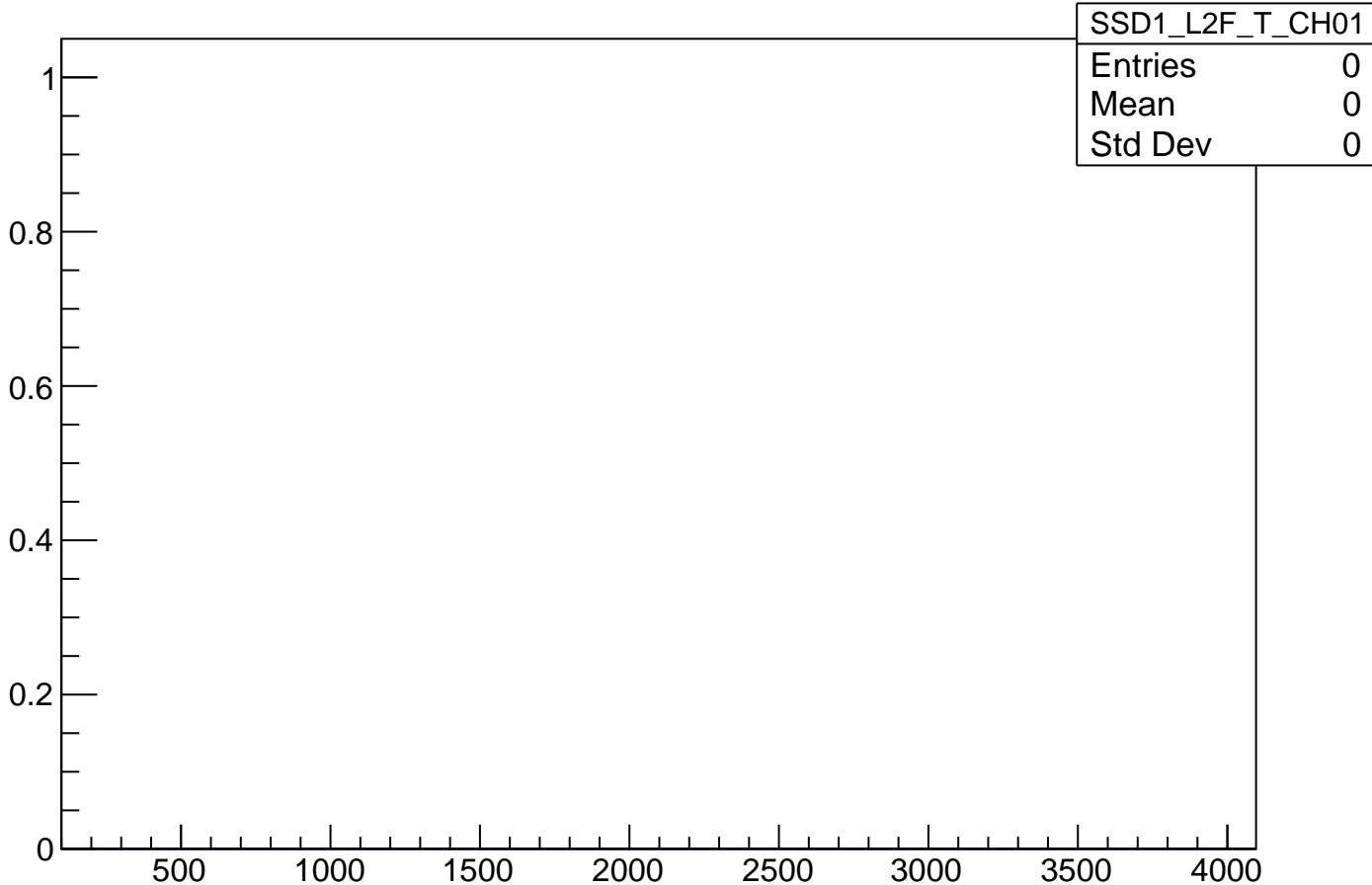
RF2



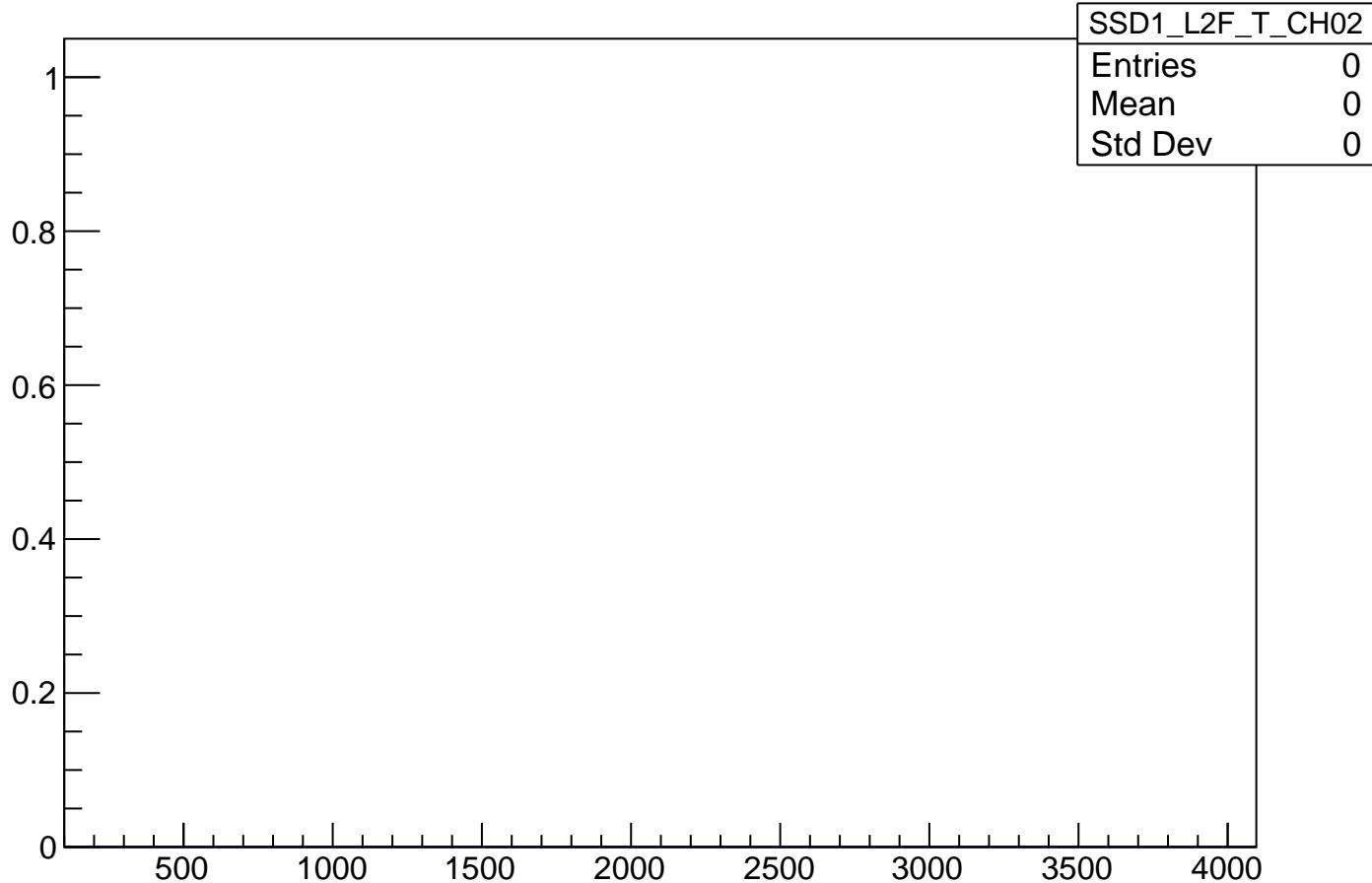
SSD1_L2F_T_CH00



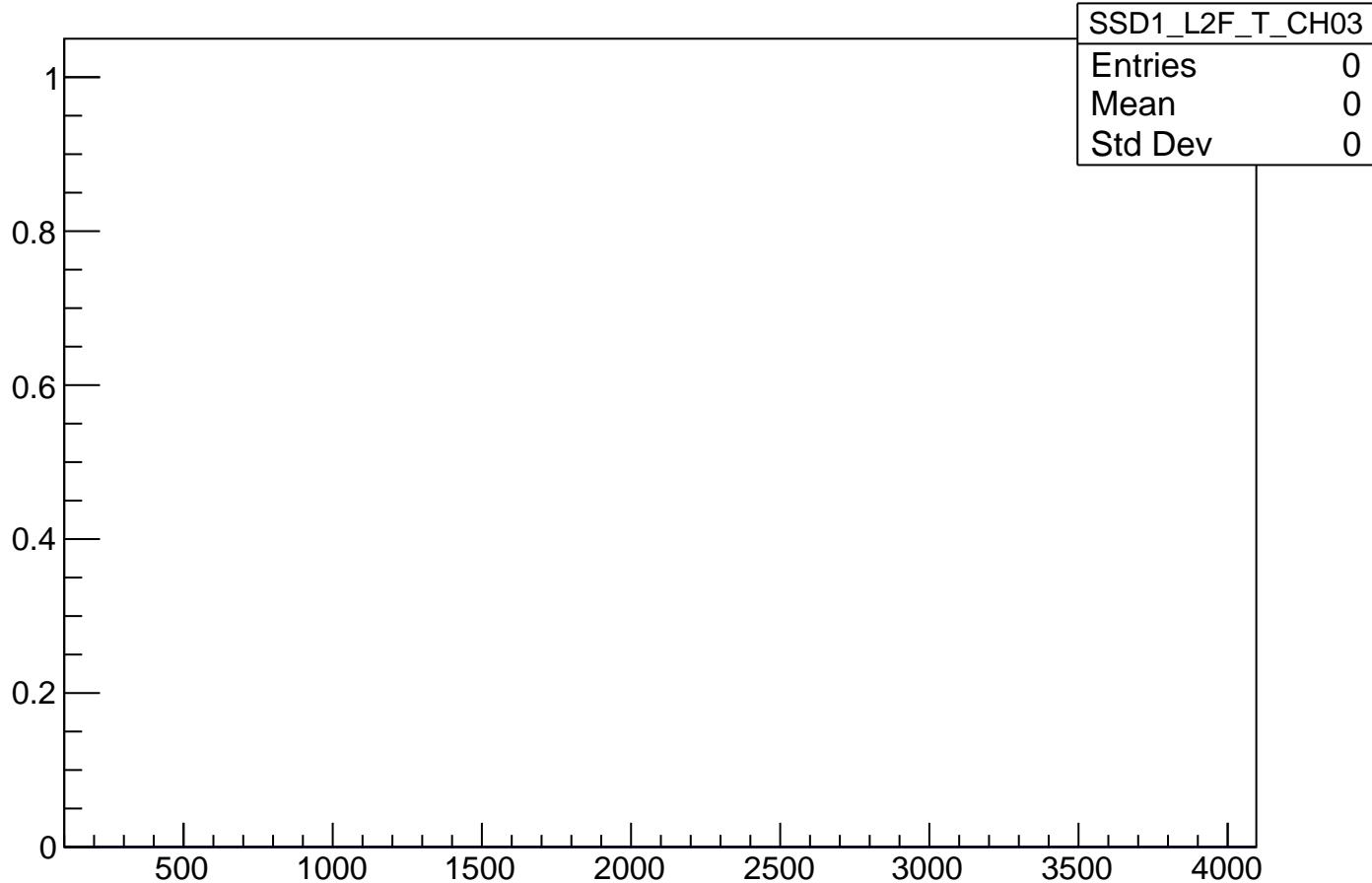
SSD1_L2F_T_CH01



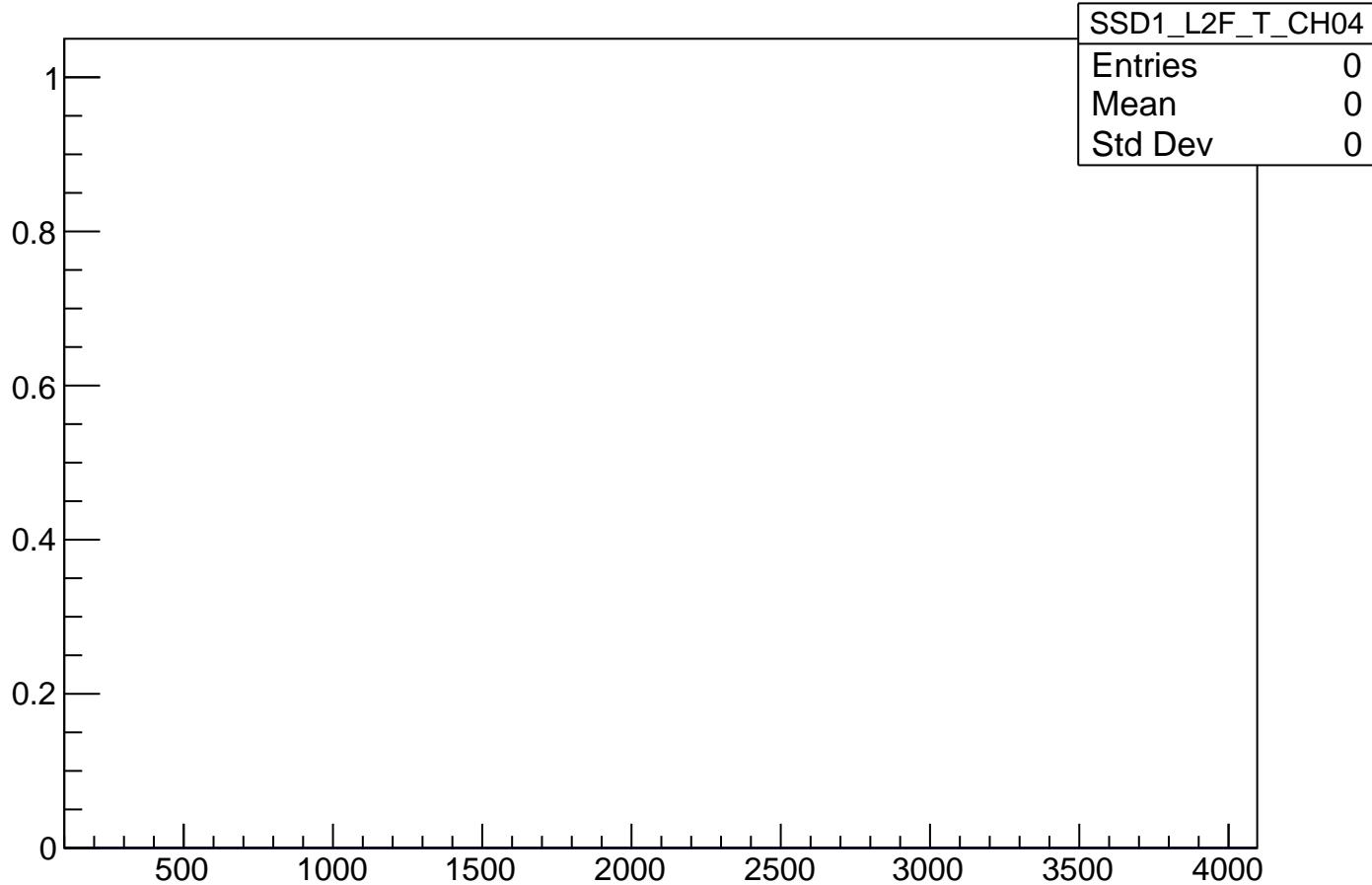
SSD1_L2F_T_CH02



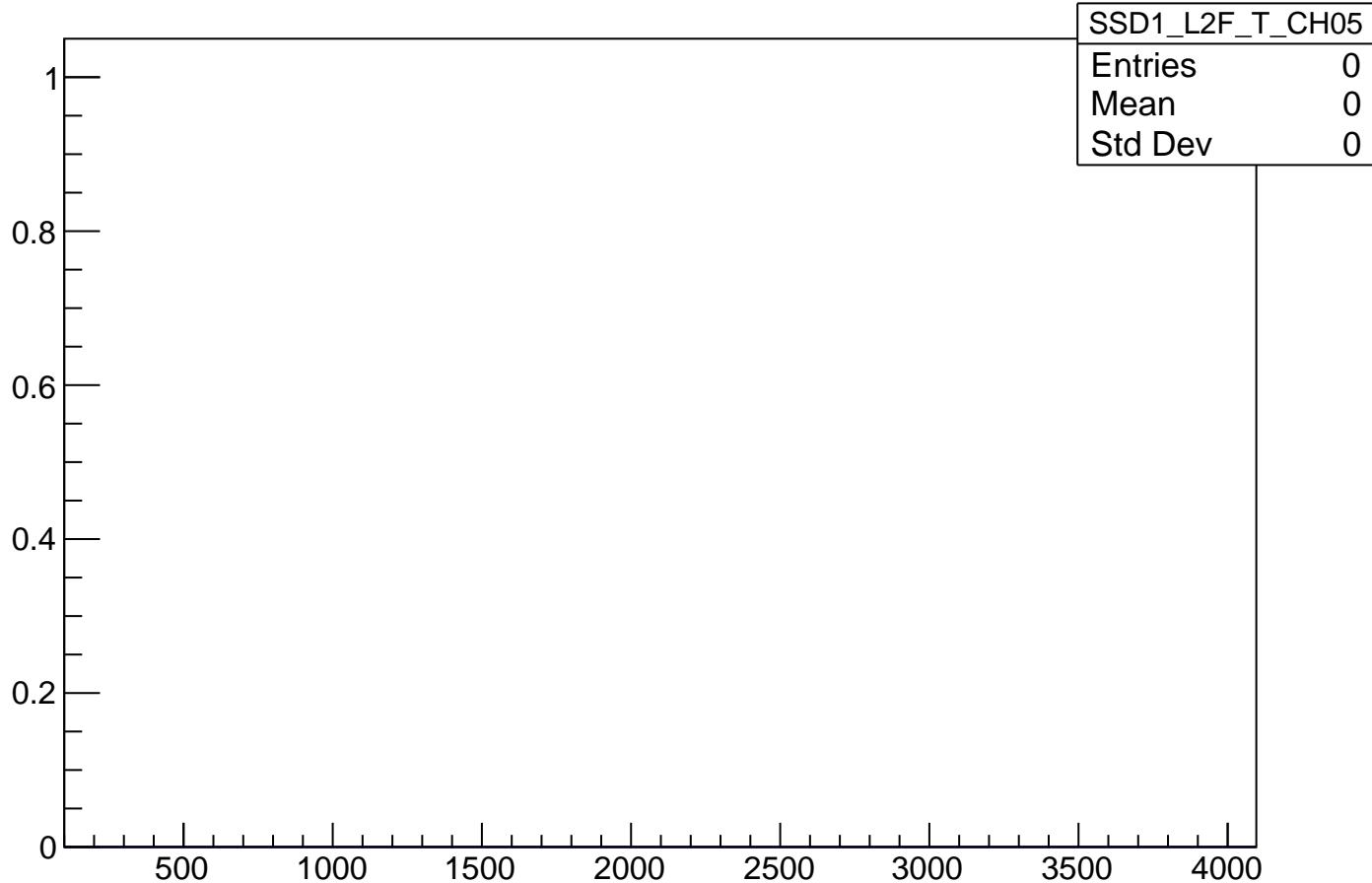
SSD1_L2F_T_CH03



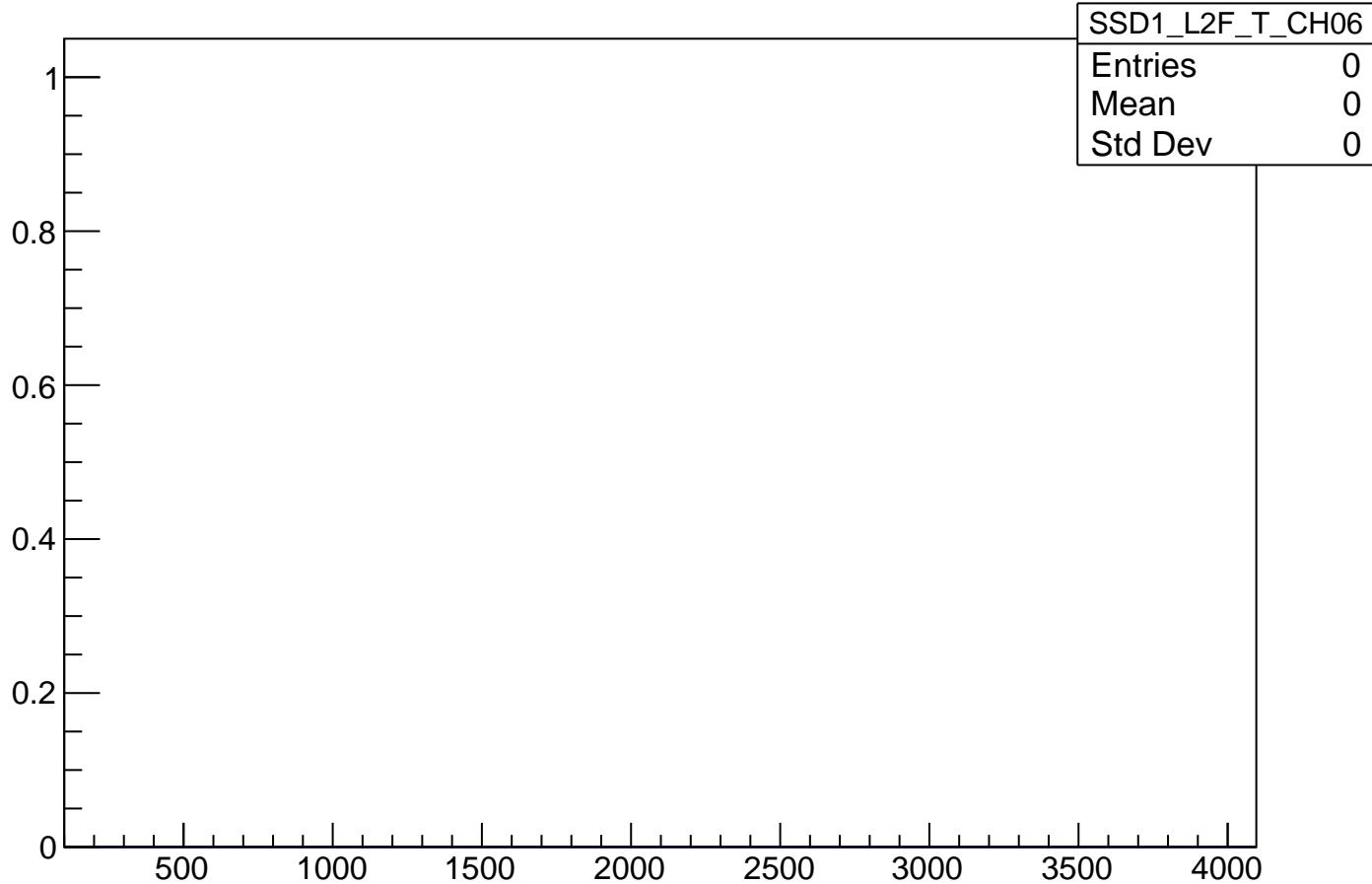
SSD1_L2F_T_CH04



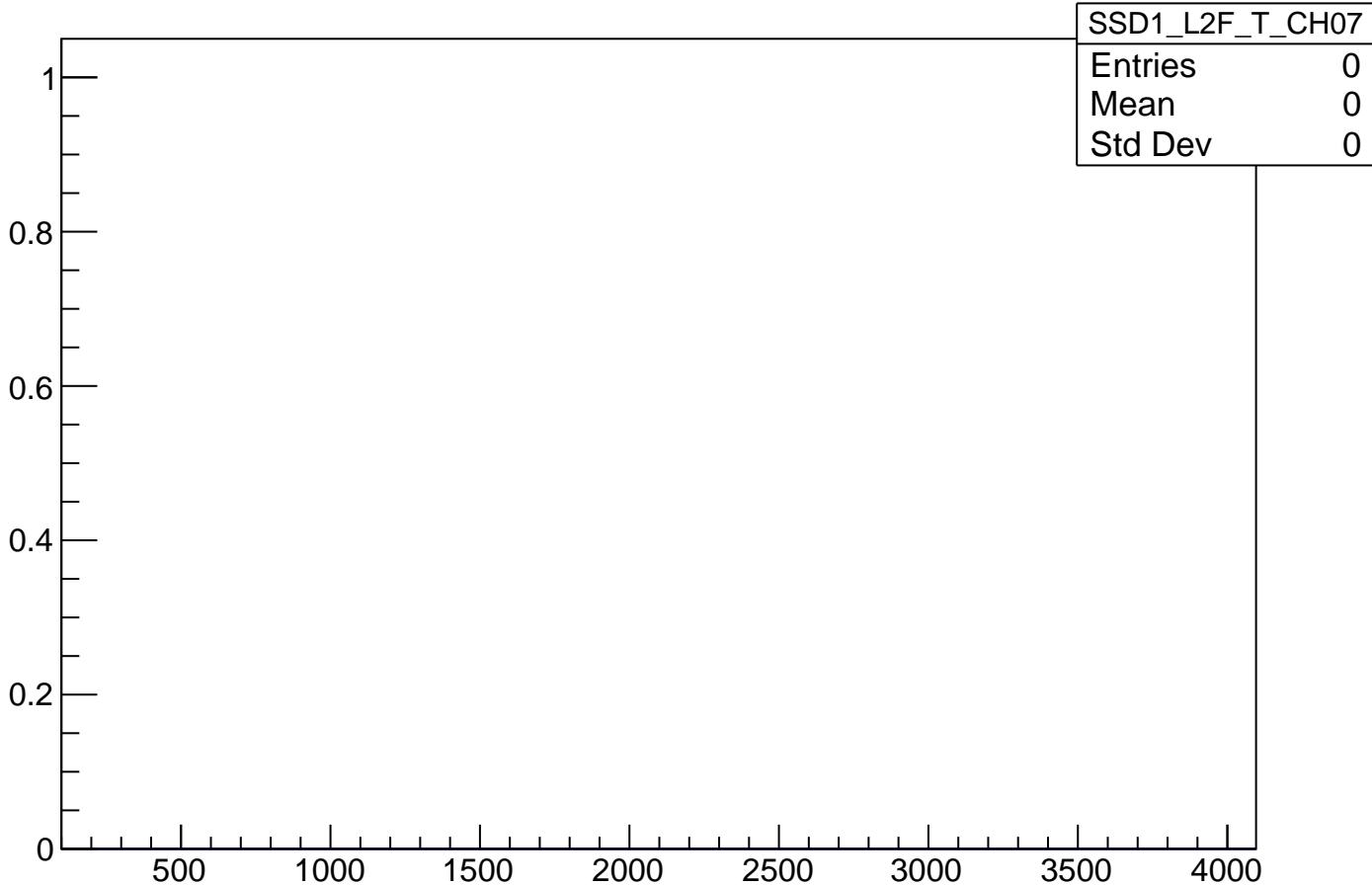
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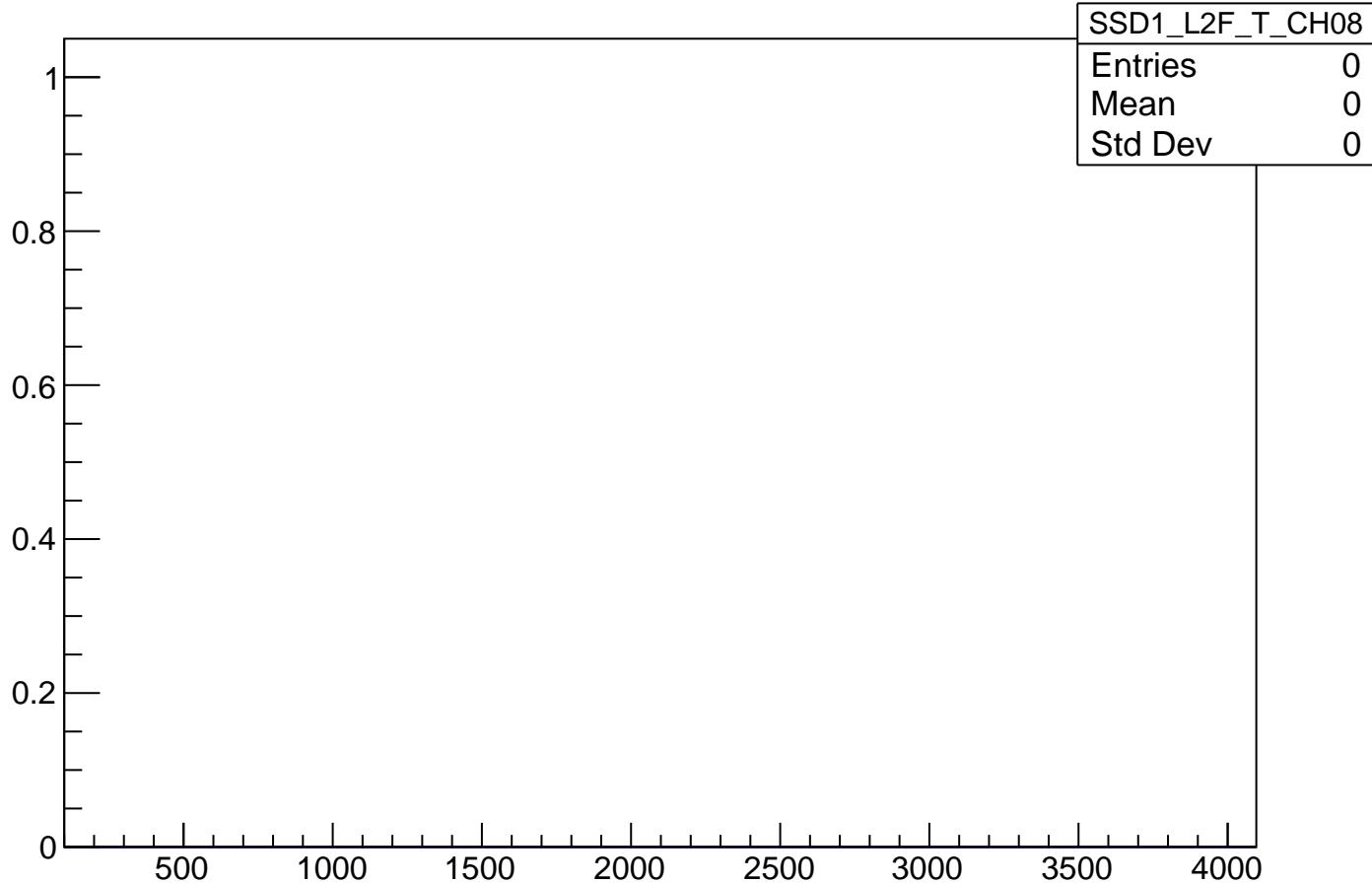
SSD1_L2F_T_CH06



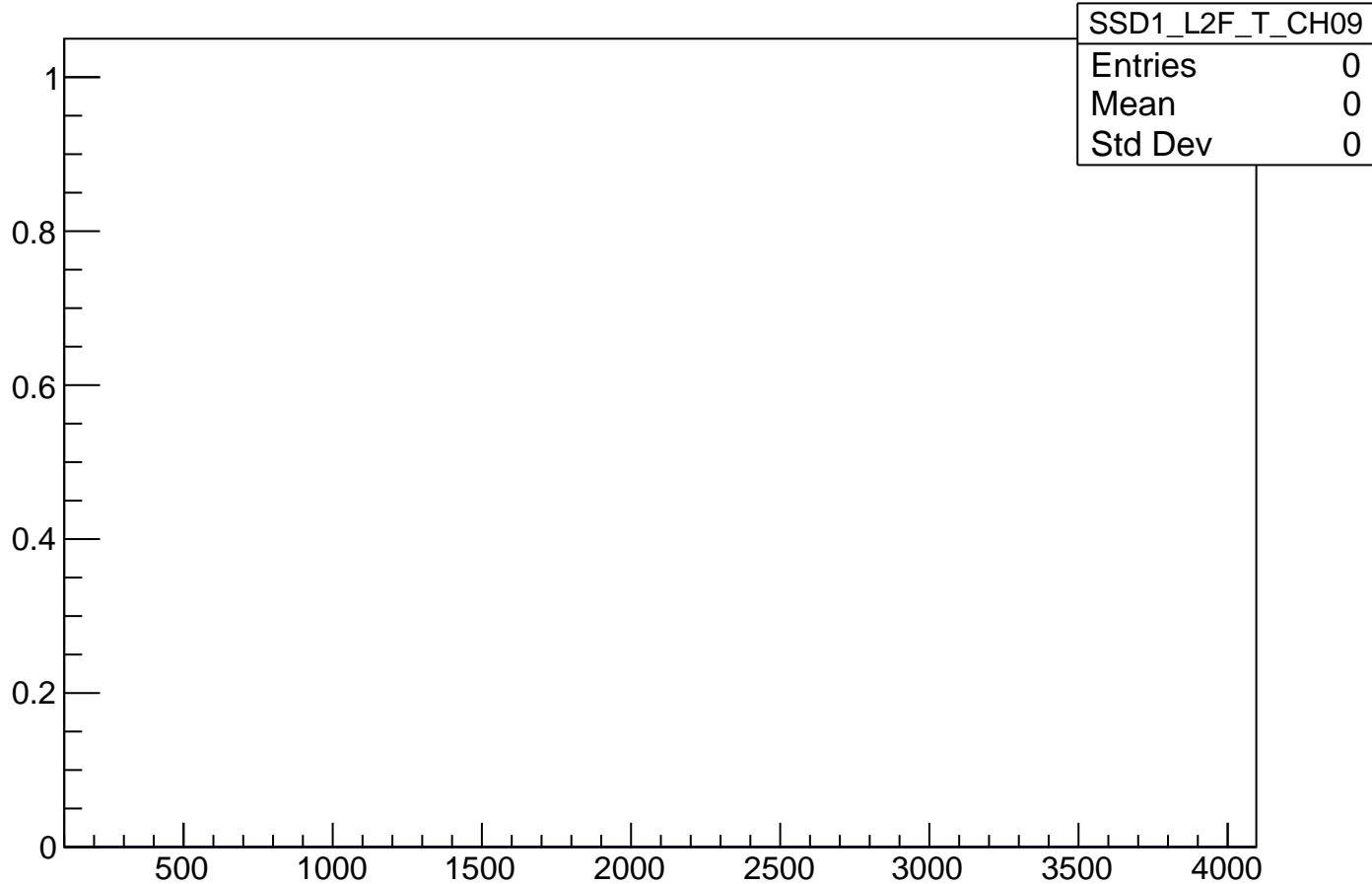
SSD1_L2F_T_CH07



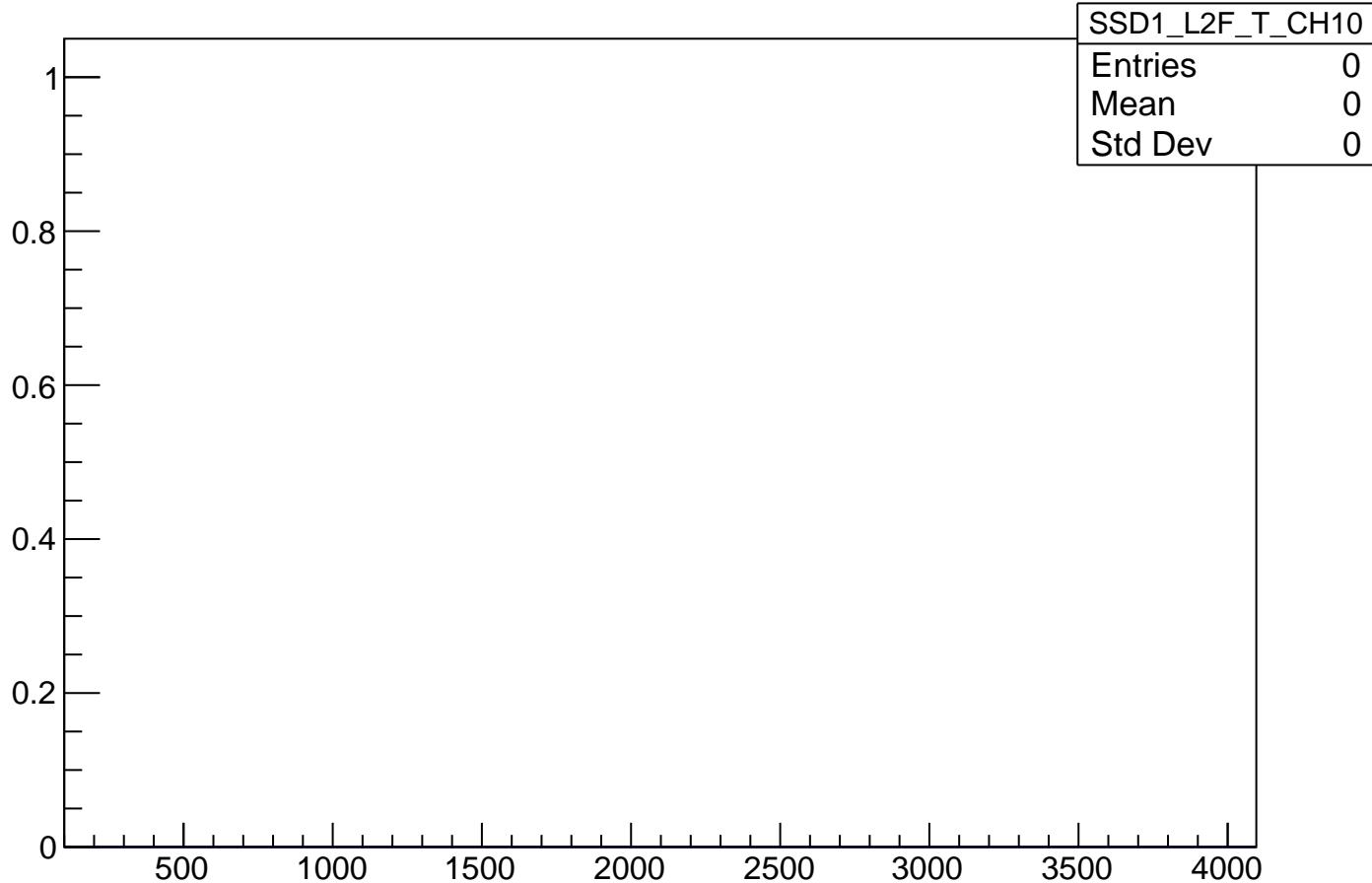
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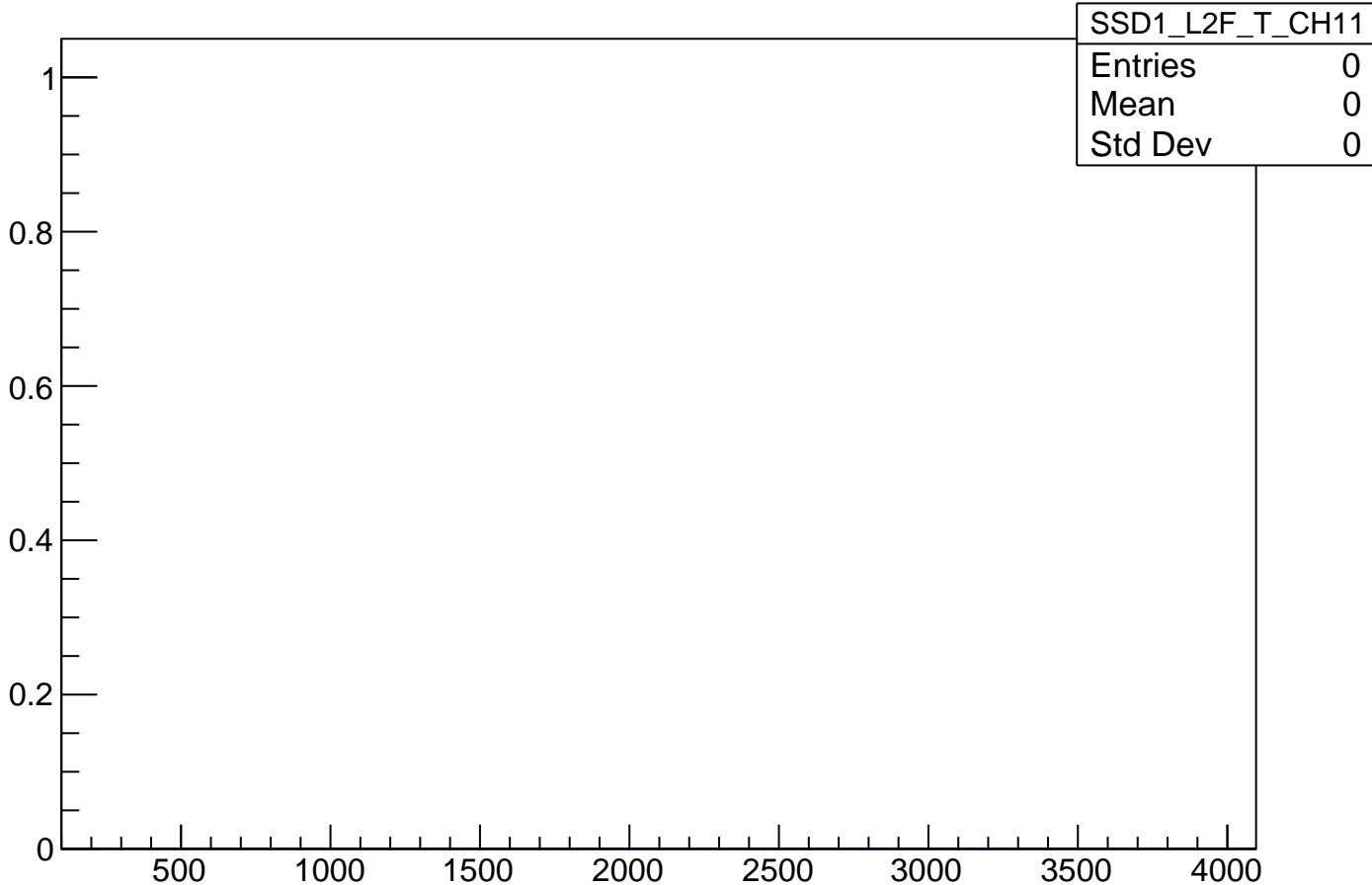
SSD1_L2F_T_CH09



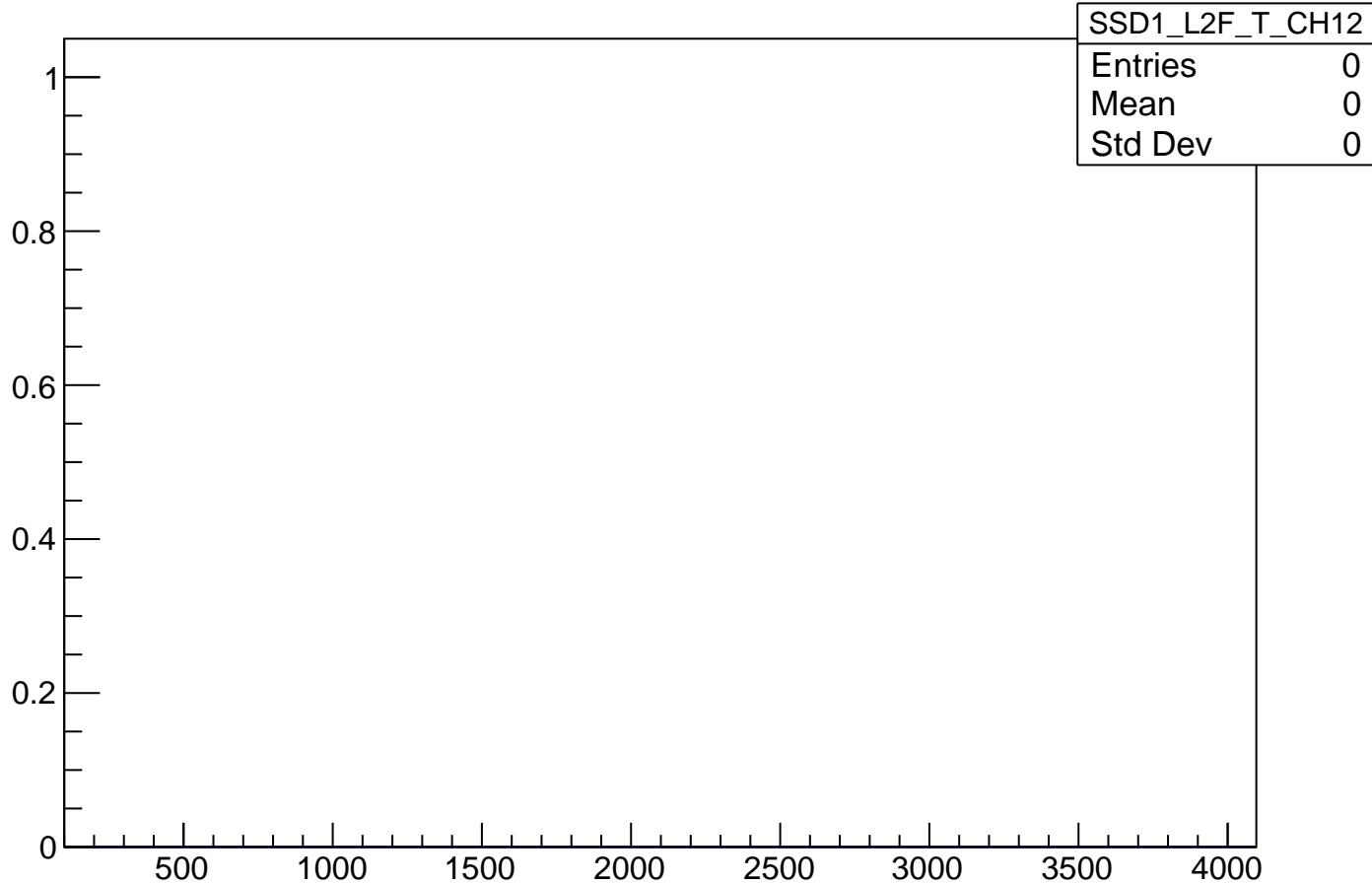
SSD1_L2F_T_CH10



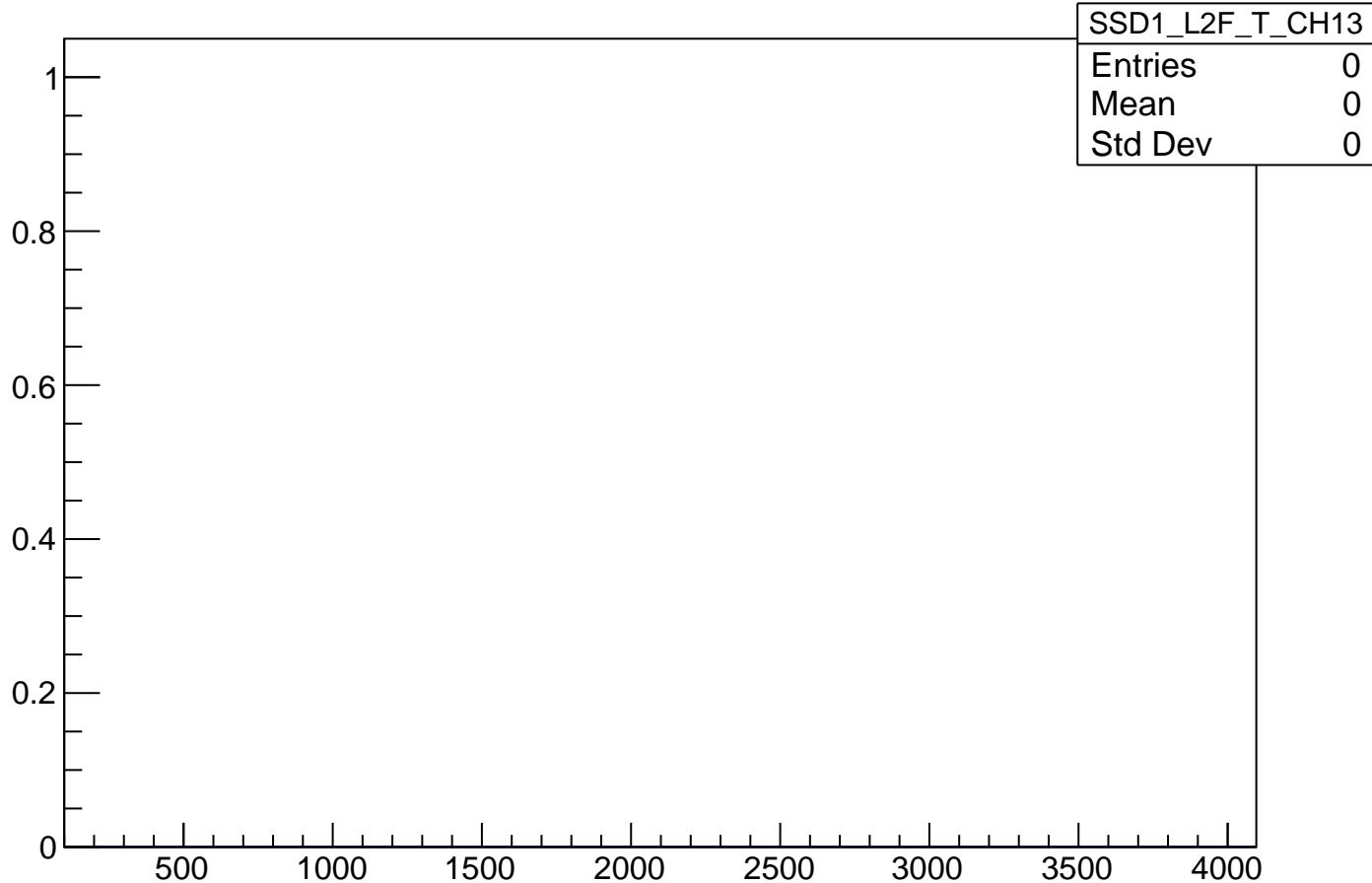
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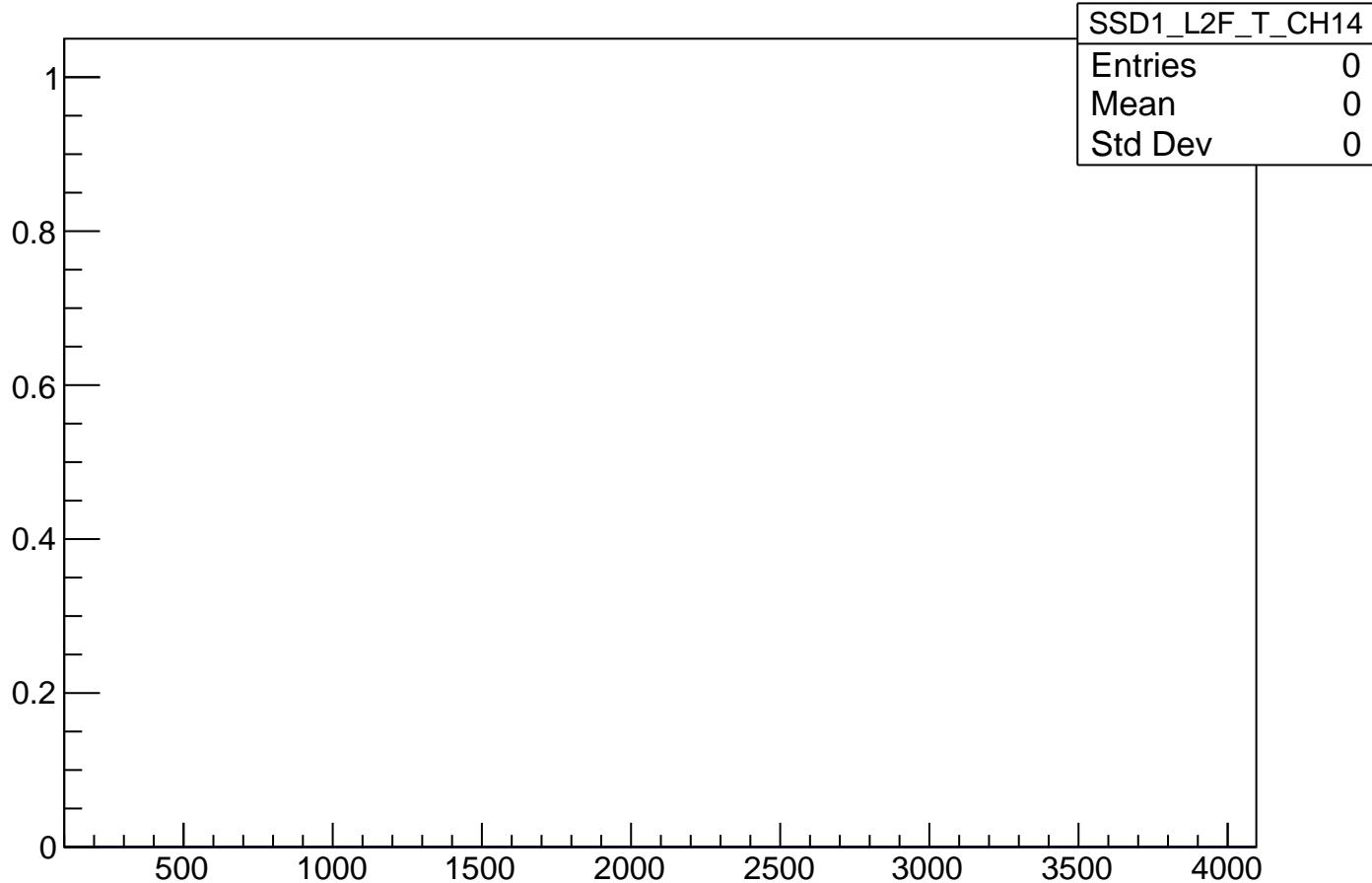
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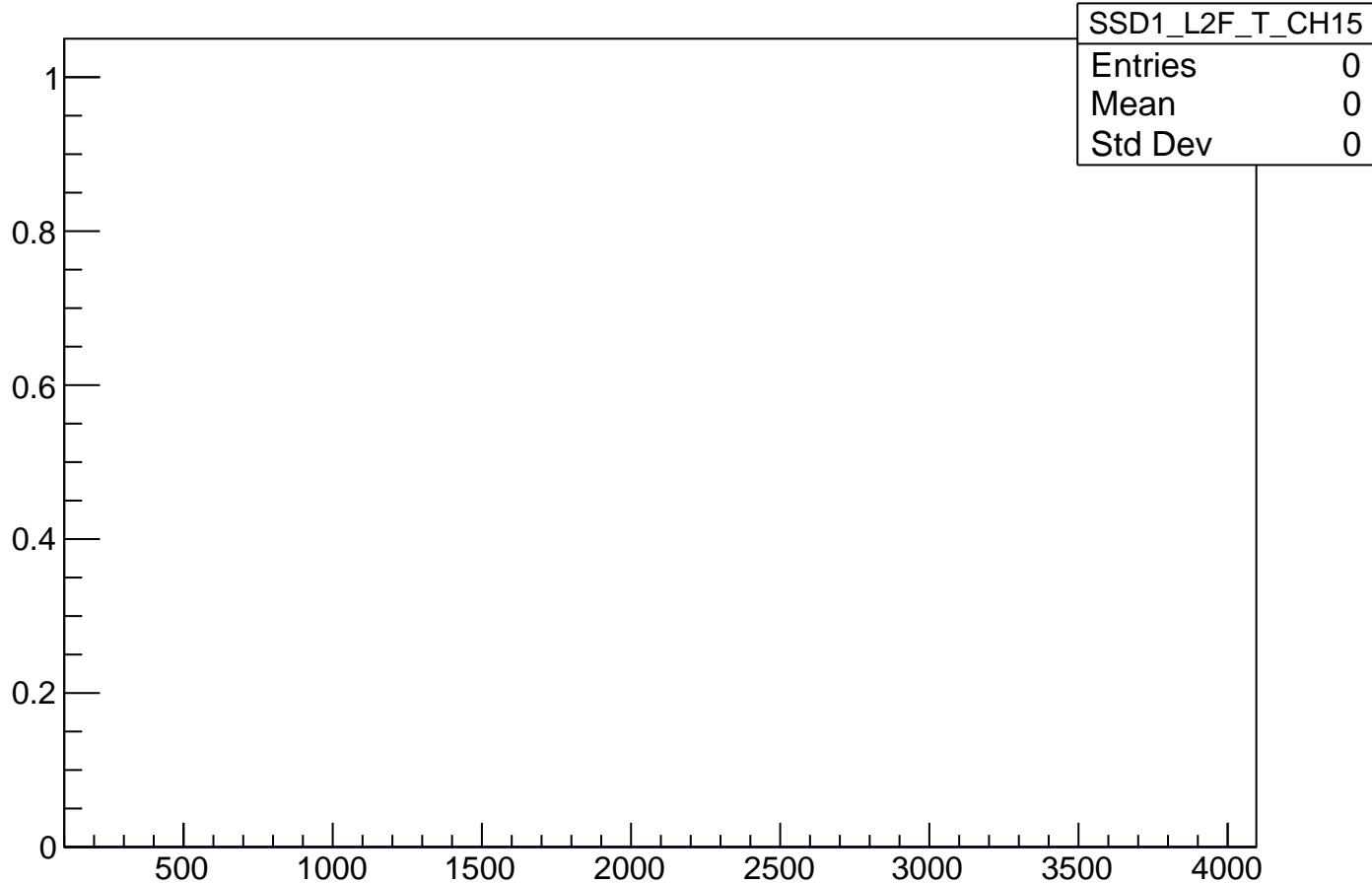
SSD1_L2F_T_CH13



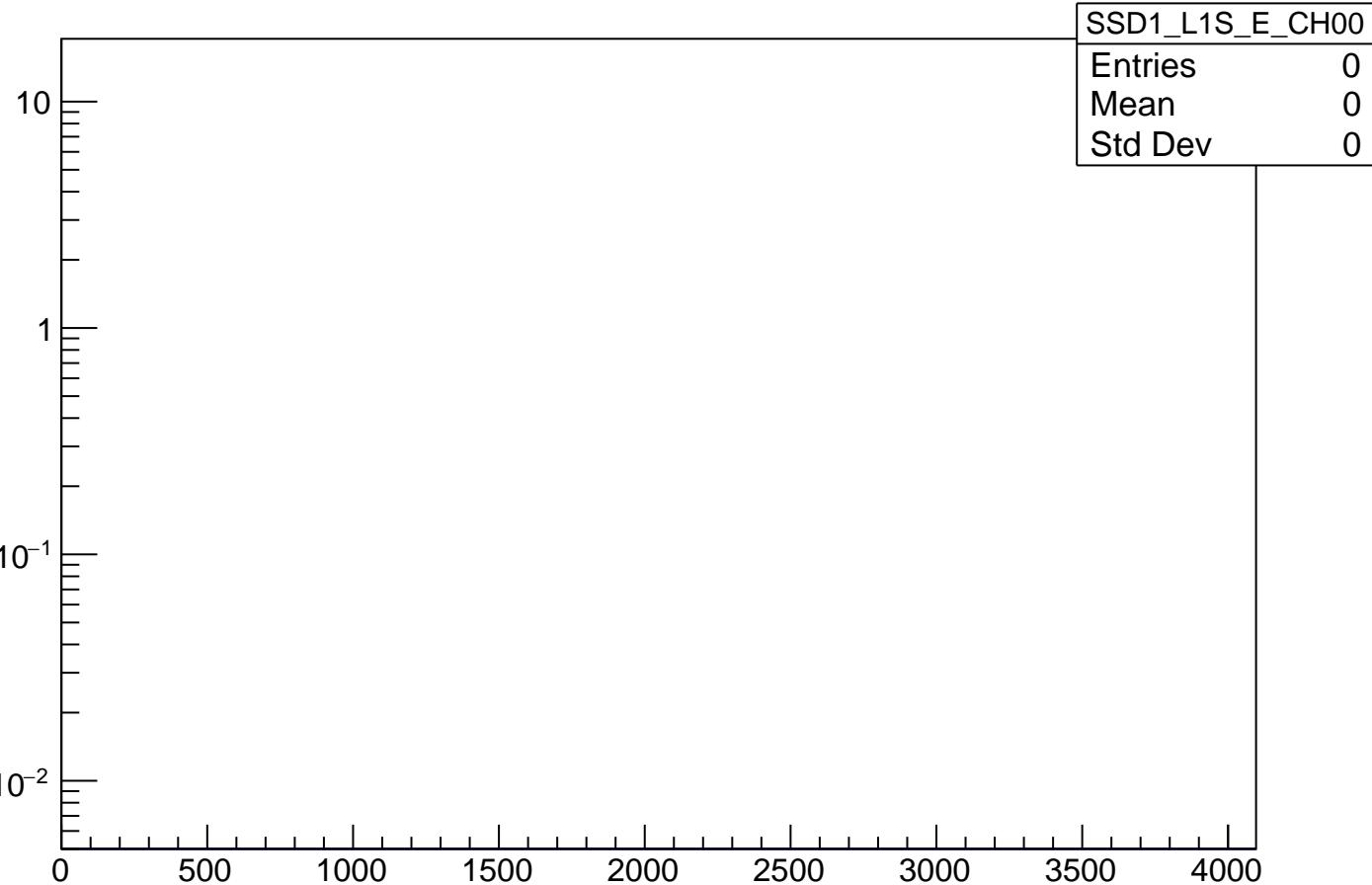
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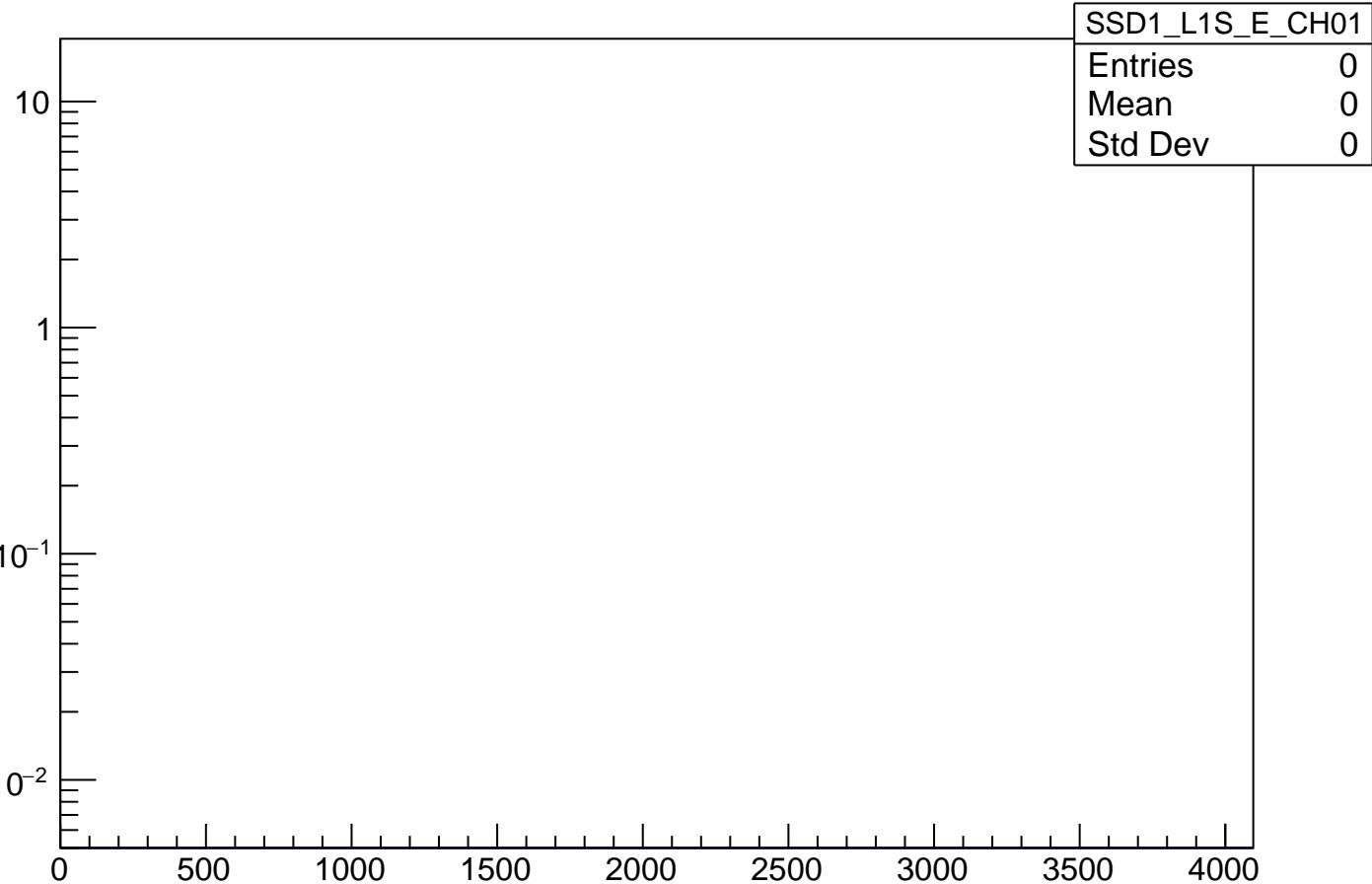
SSD1_L2F_T_CH15



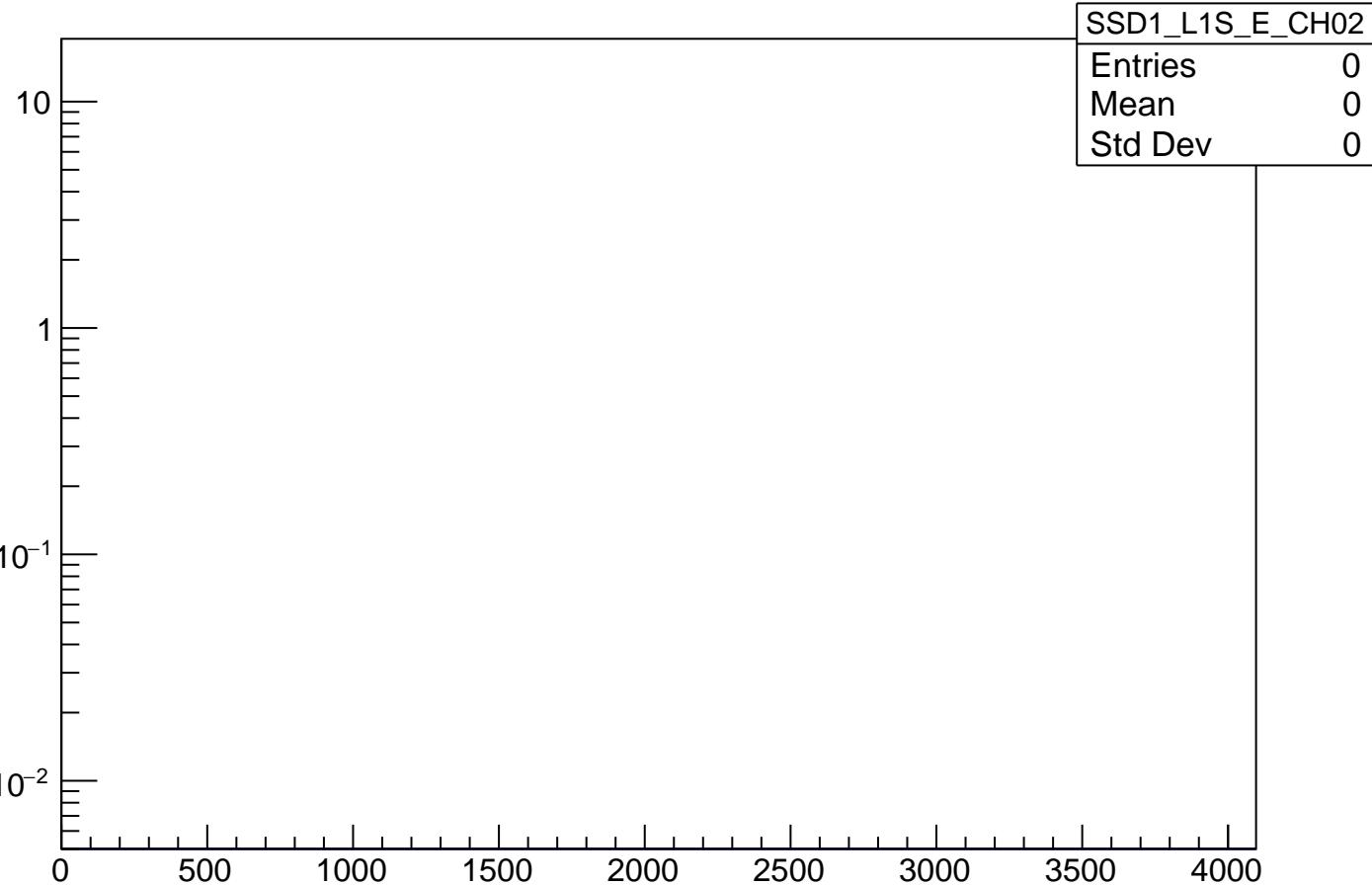
SSD1_L1S_E_CH00



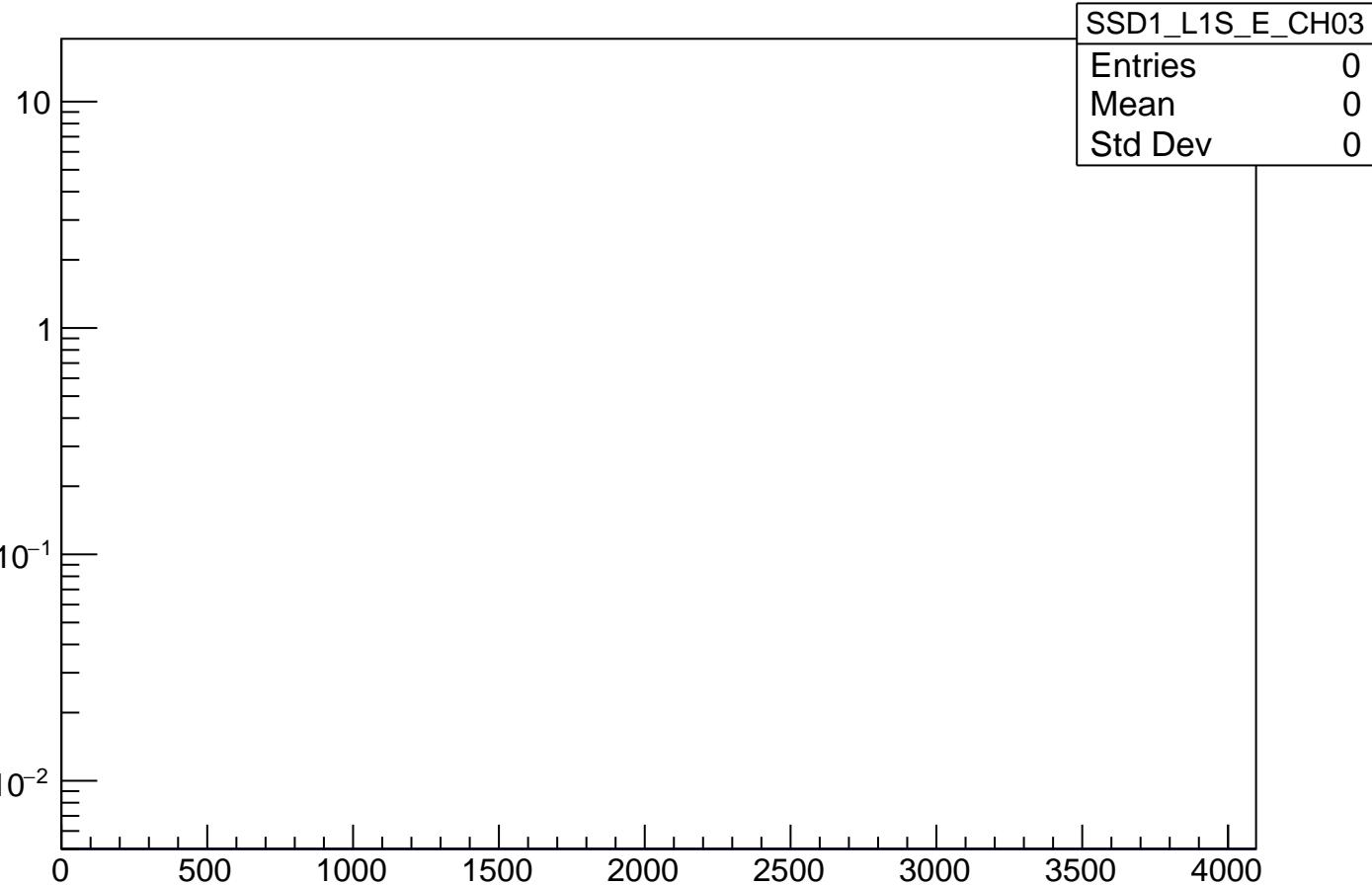
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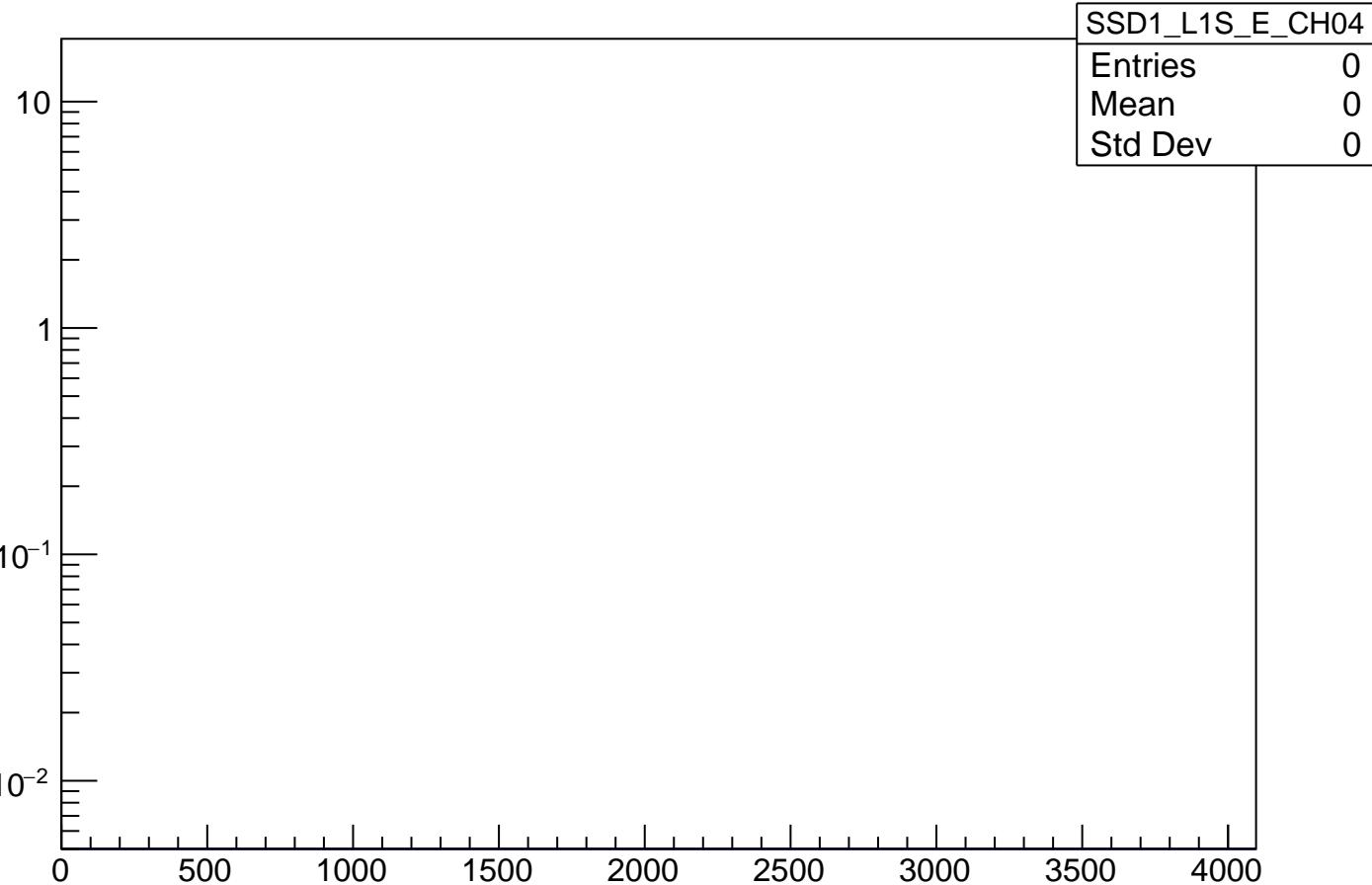
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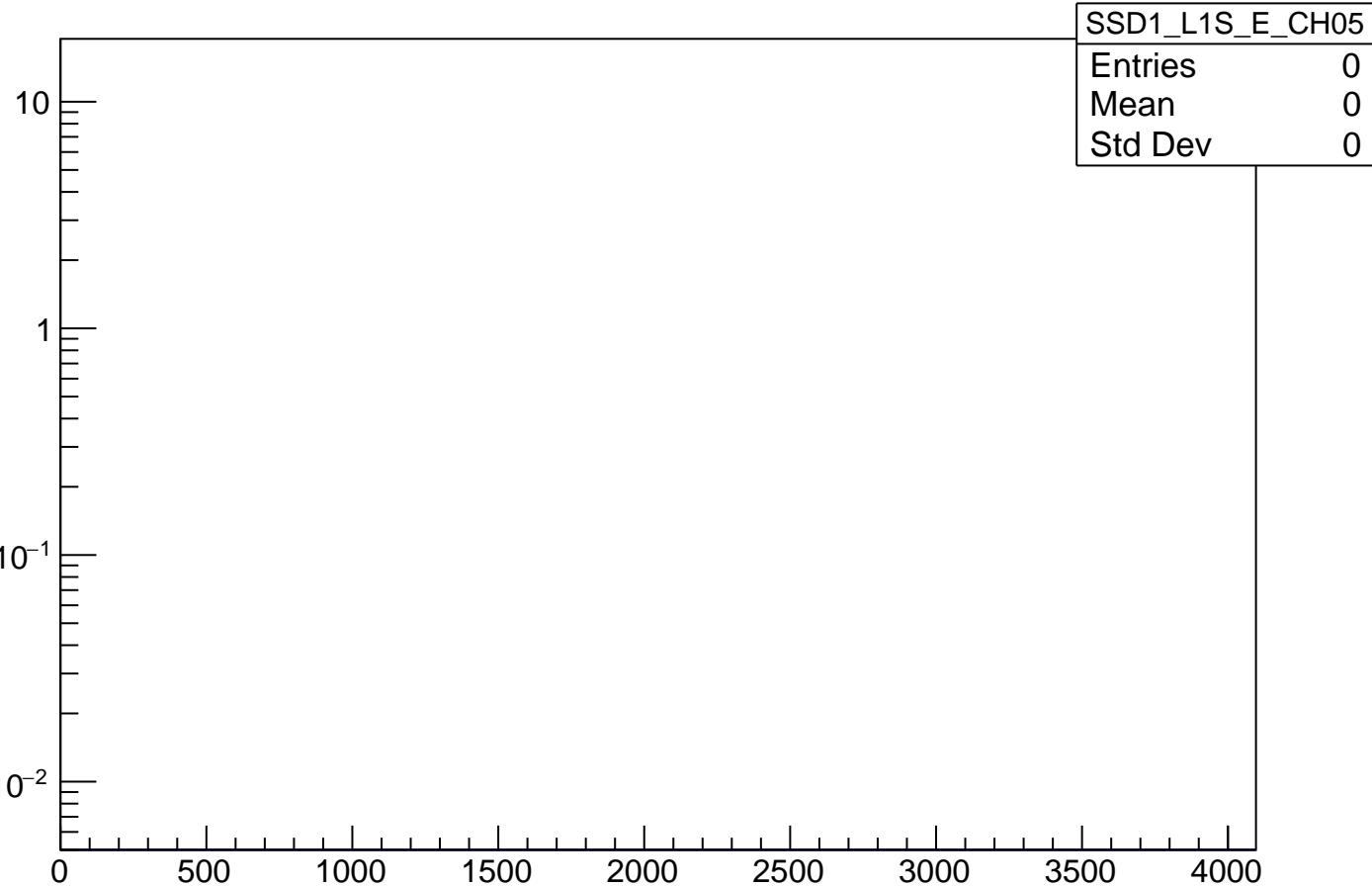
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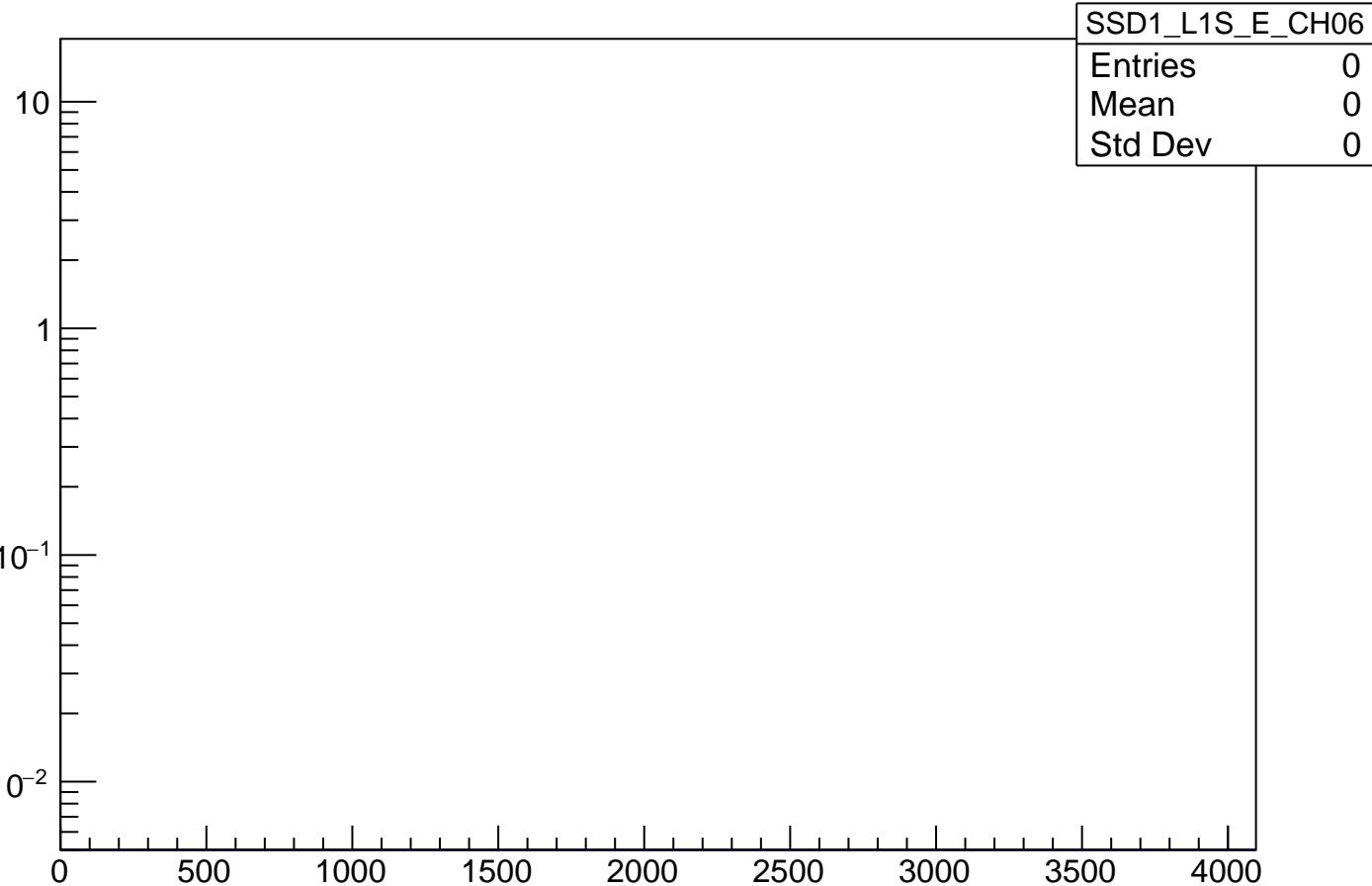
SSD1_L1S_E_CH04



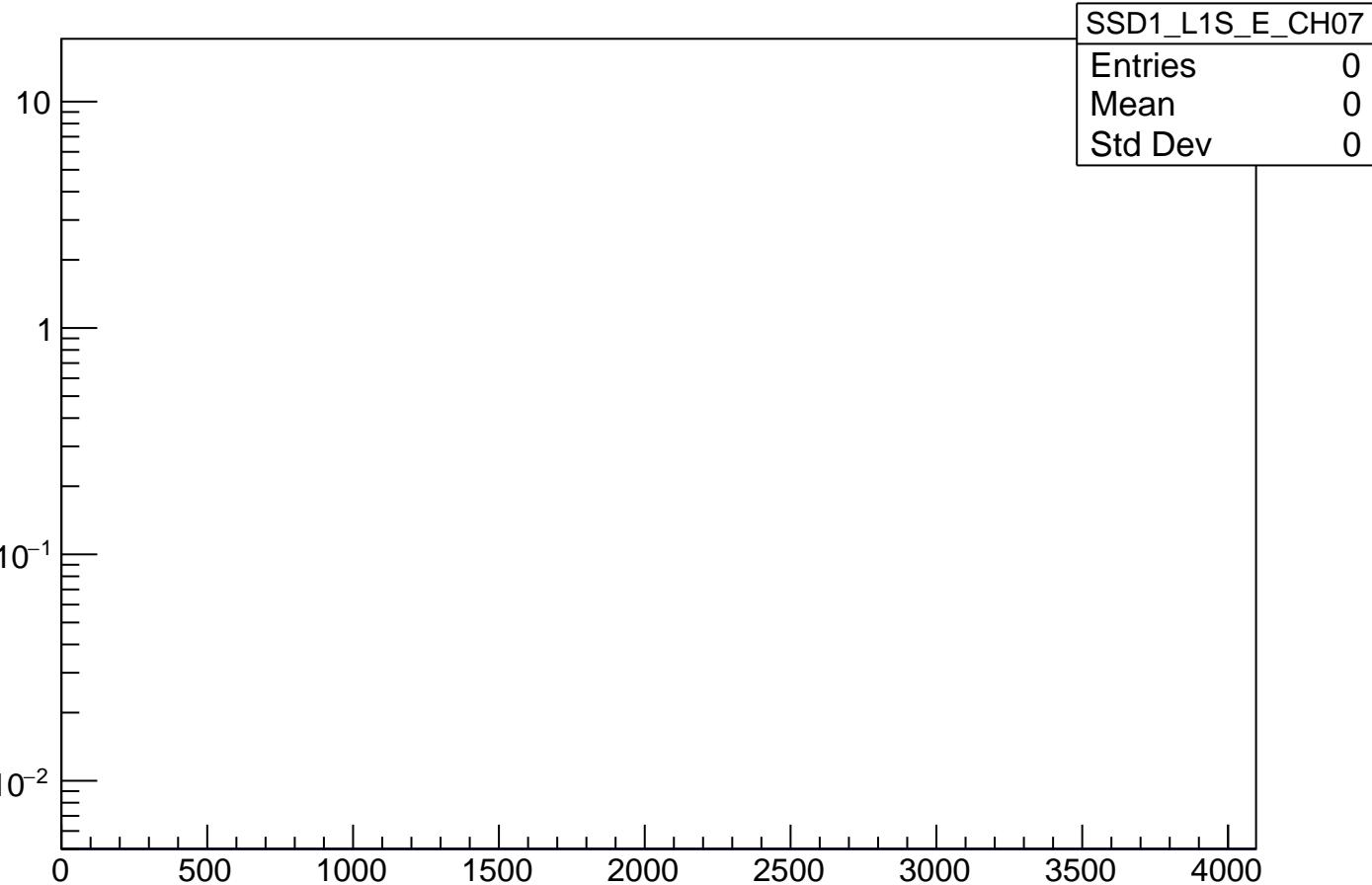
SSD1_L1S_E_CH05



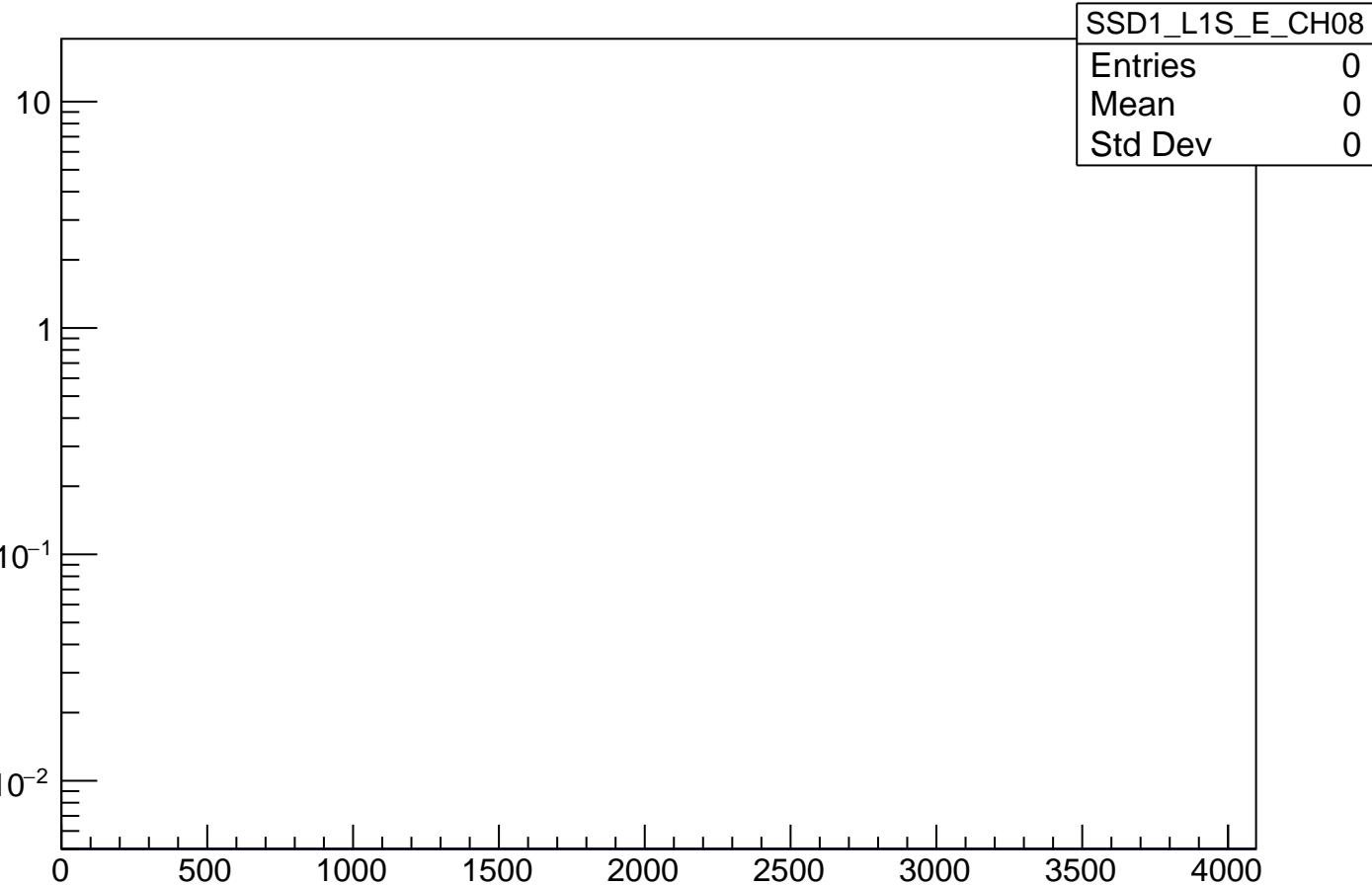
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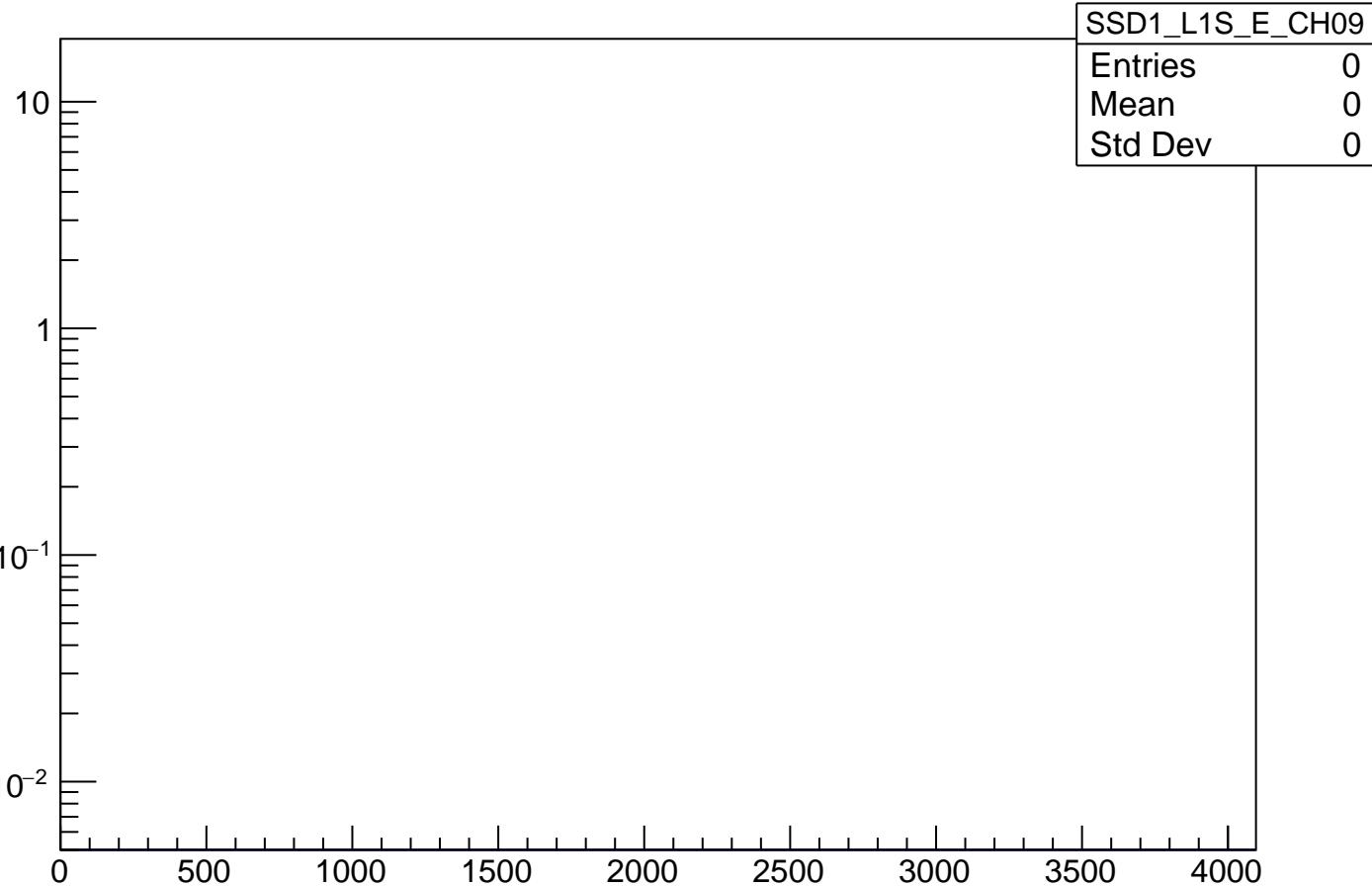
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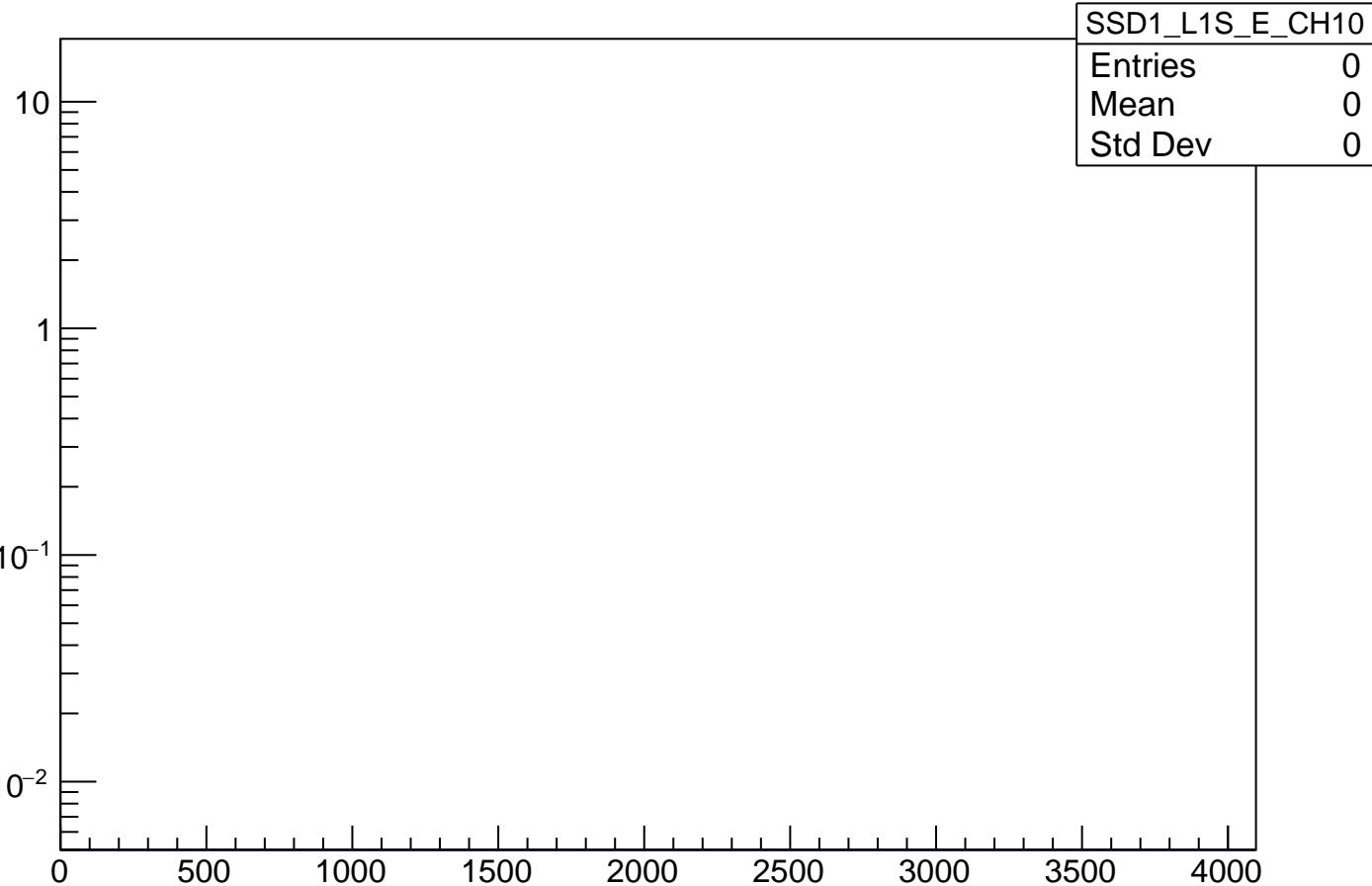
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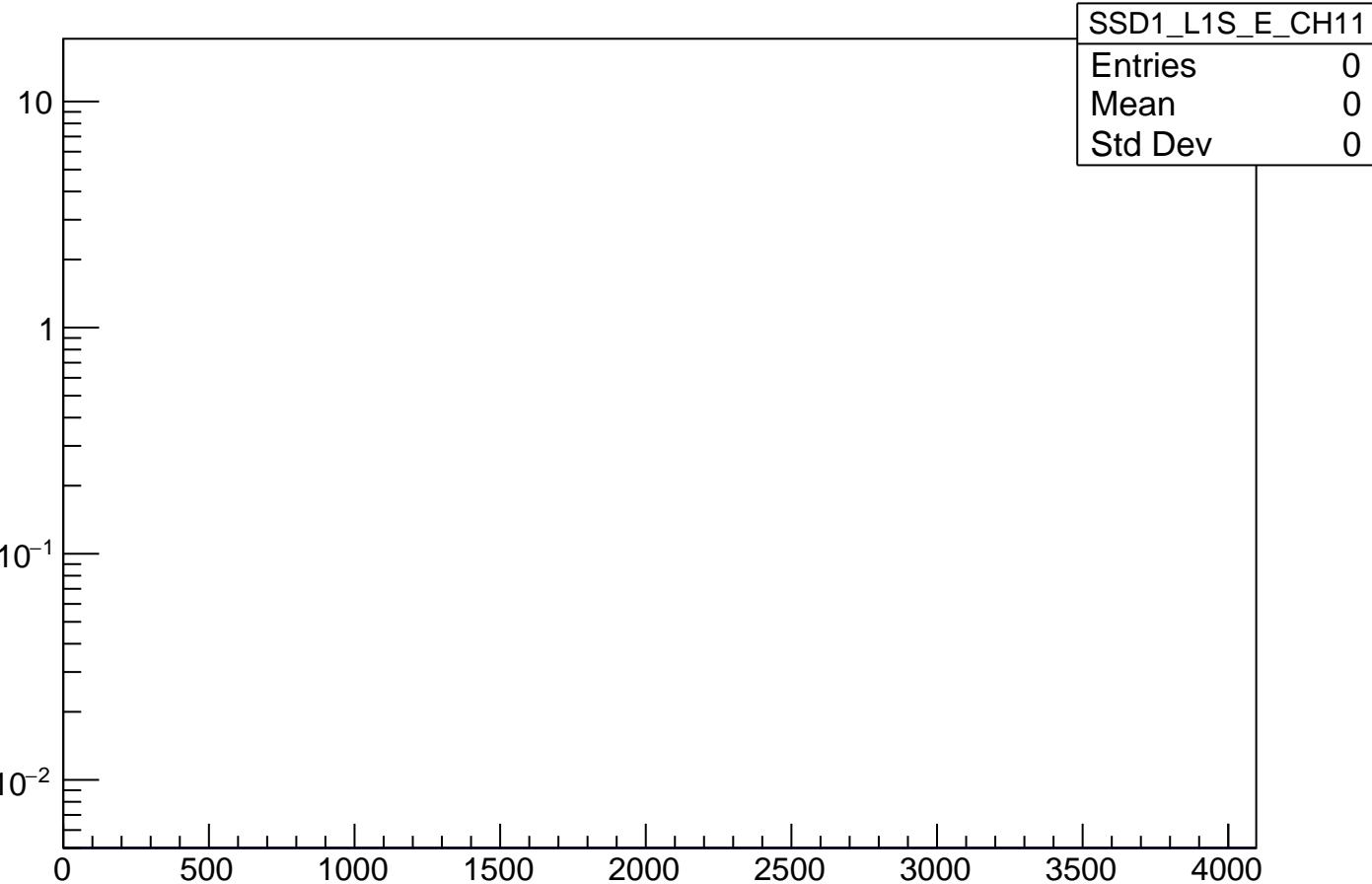
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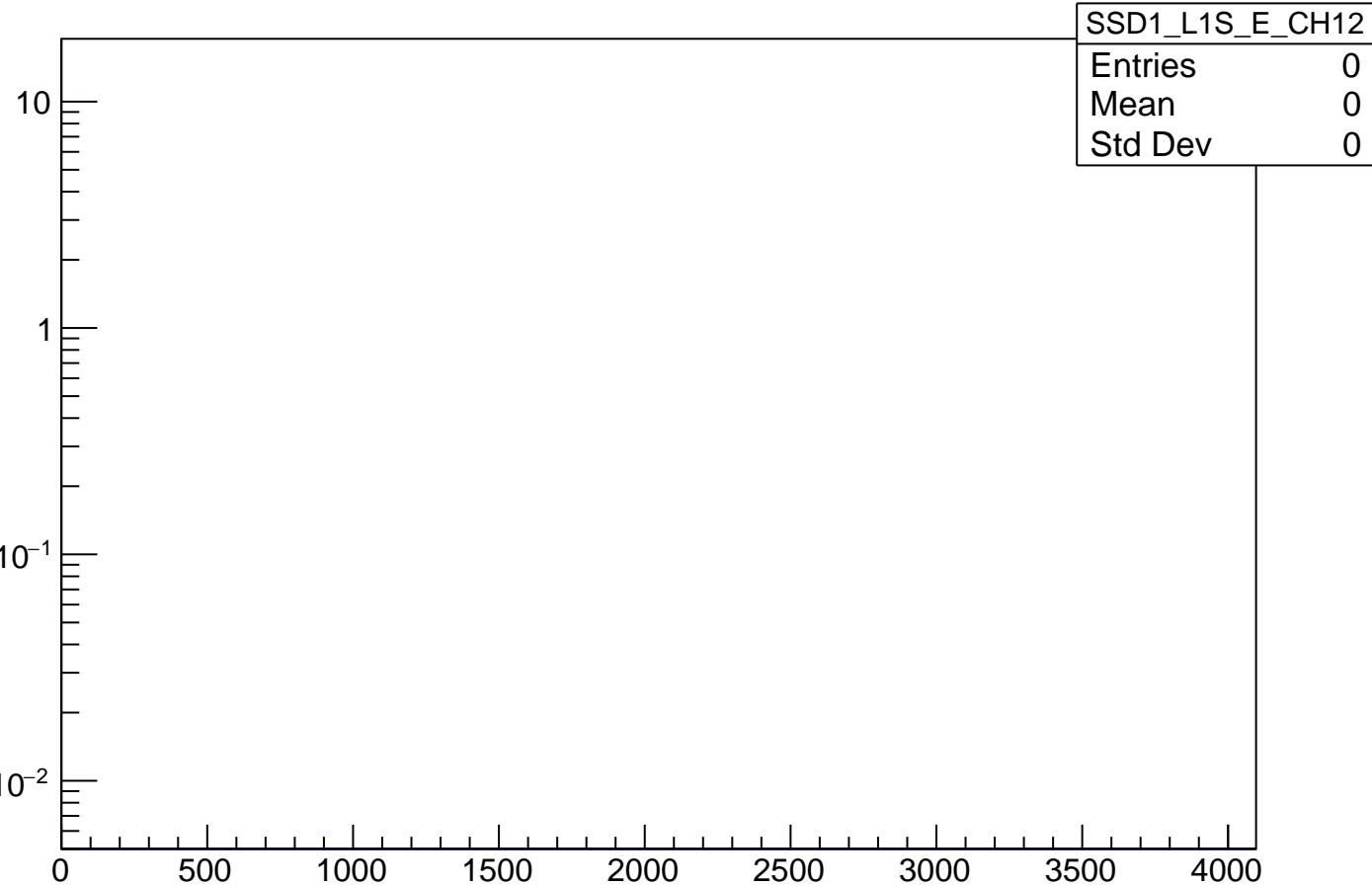
SSD1_L1S_E_CH10



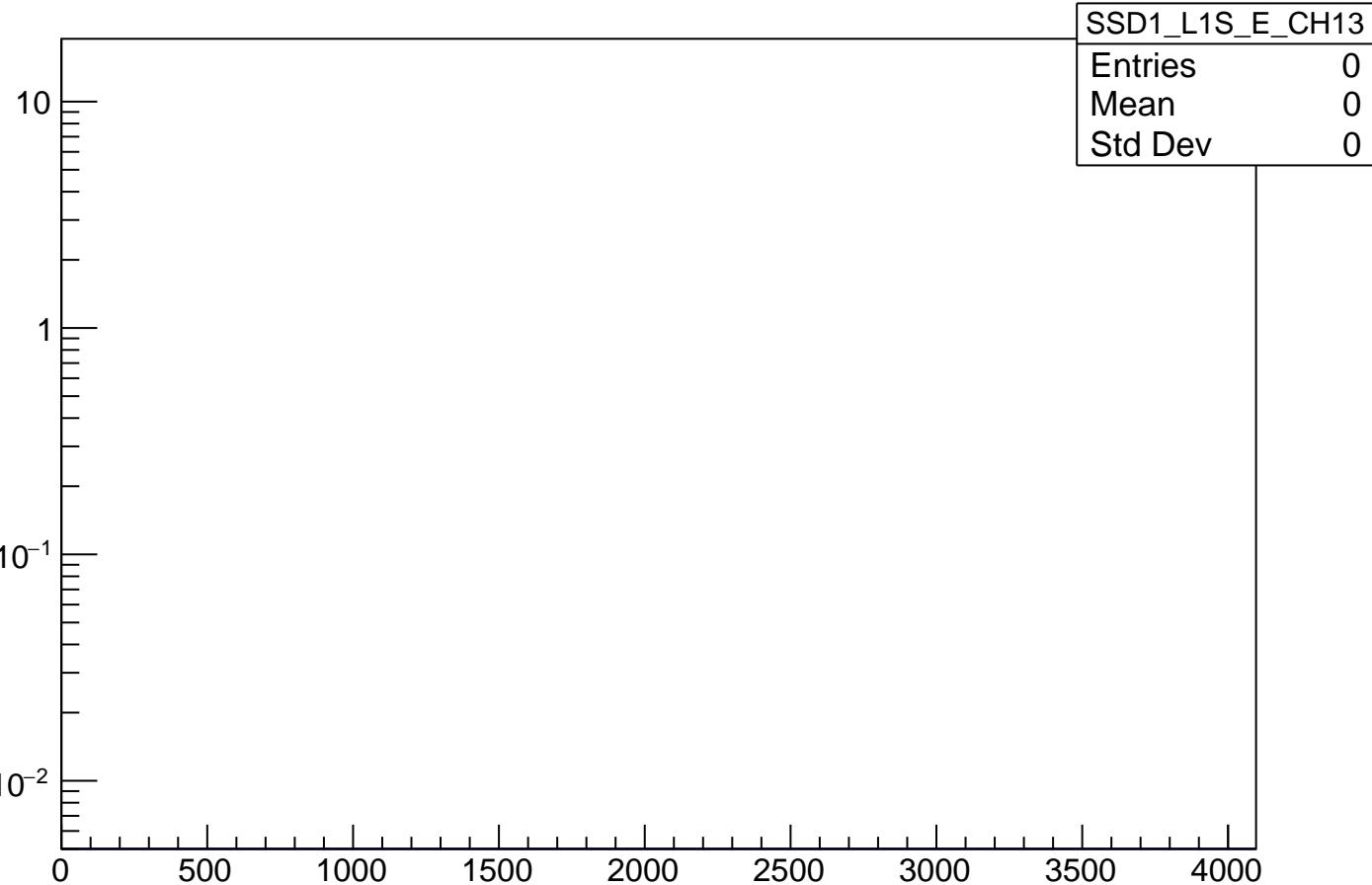
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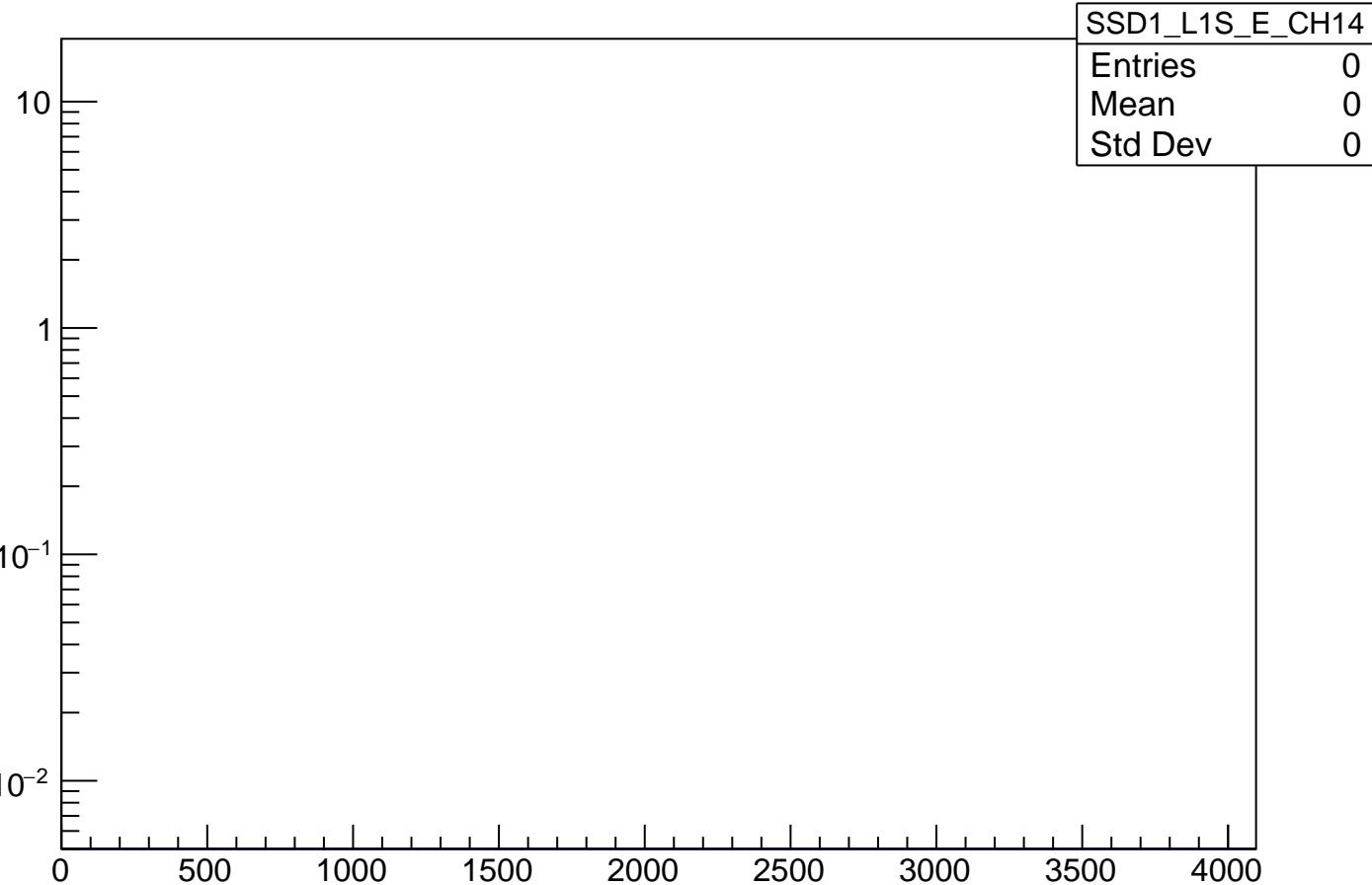
SSD1_L1S_E_CH12



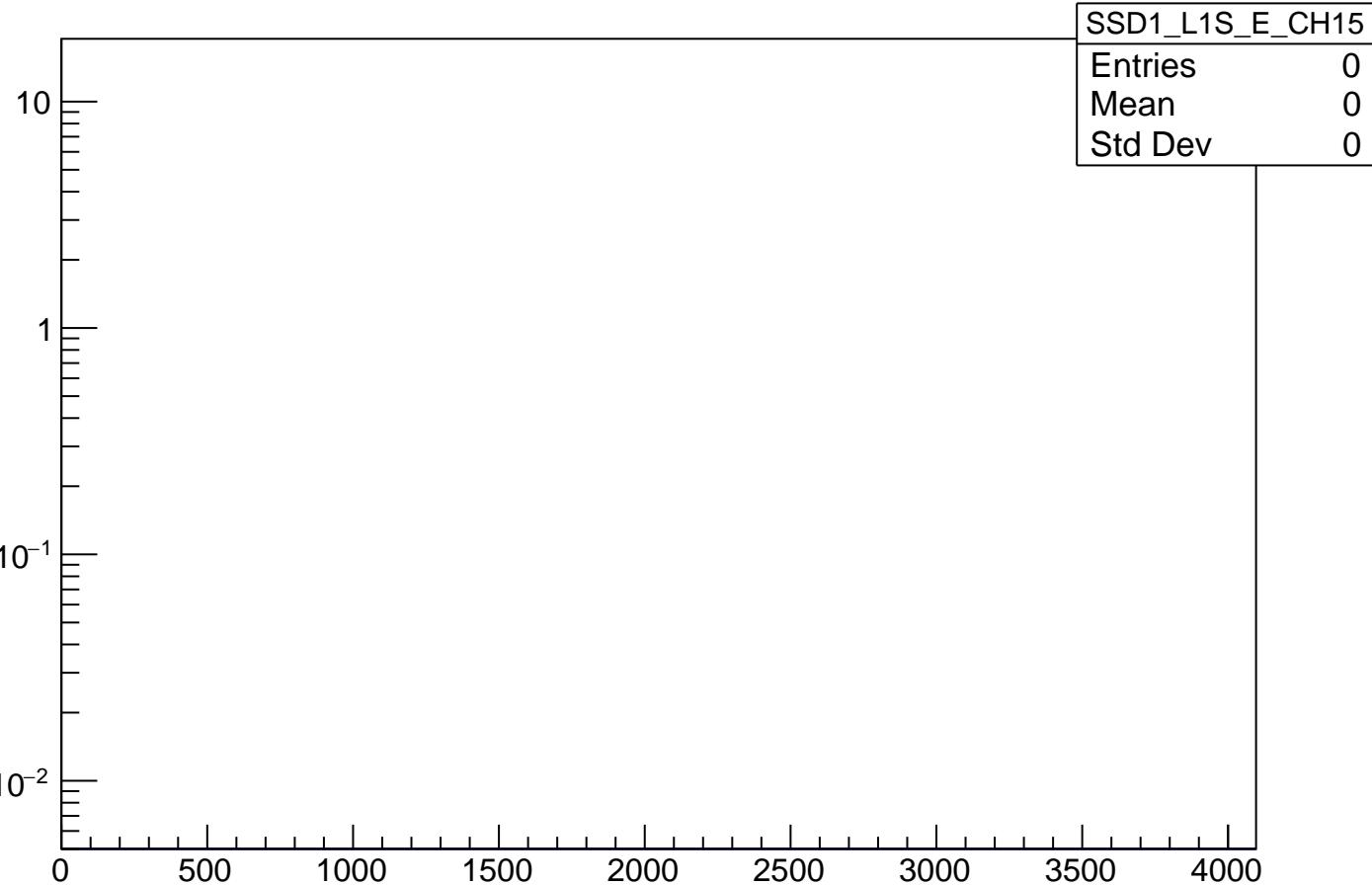
SSD1_L1S_E_CH13



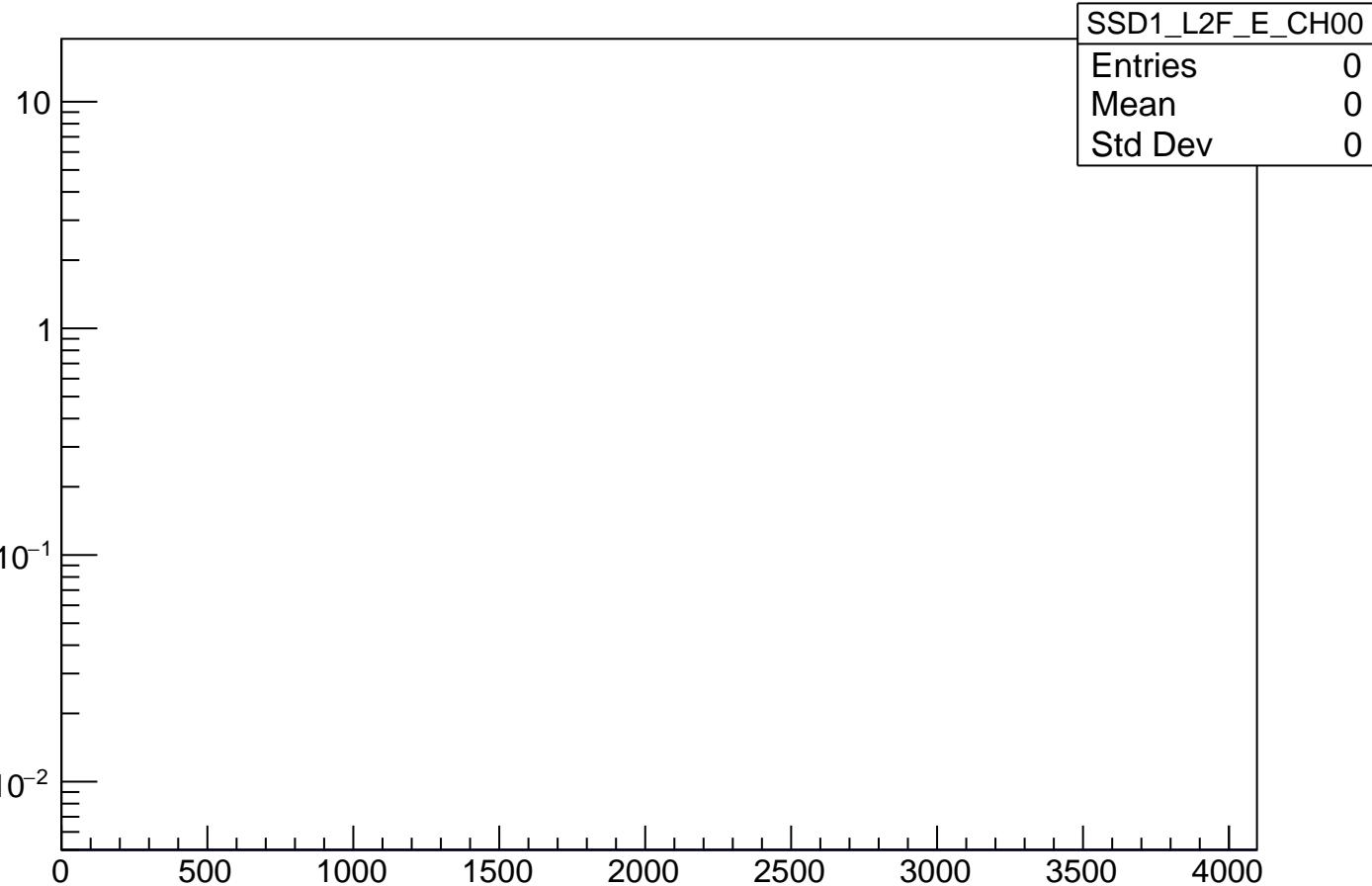
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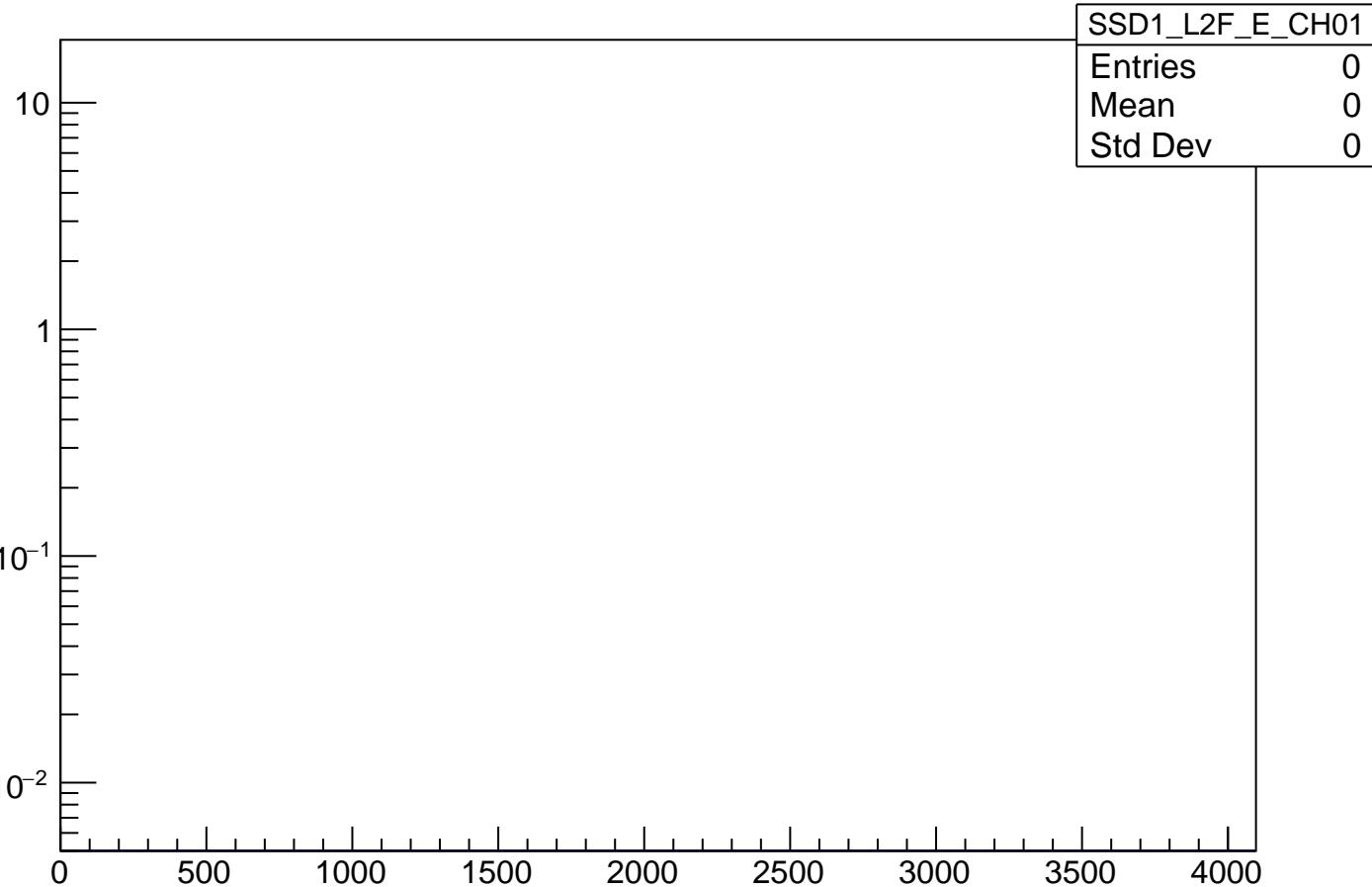
SSD1_L1S_E_CH15



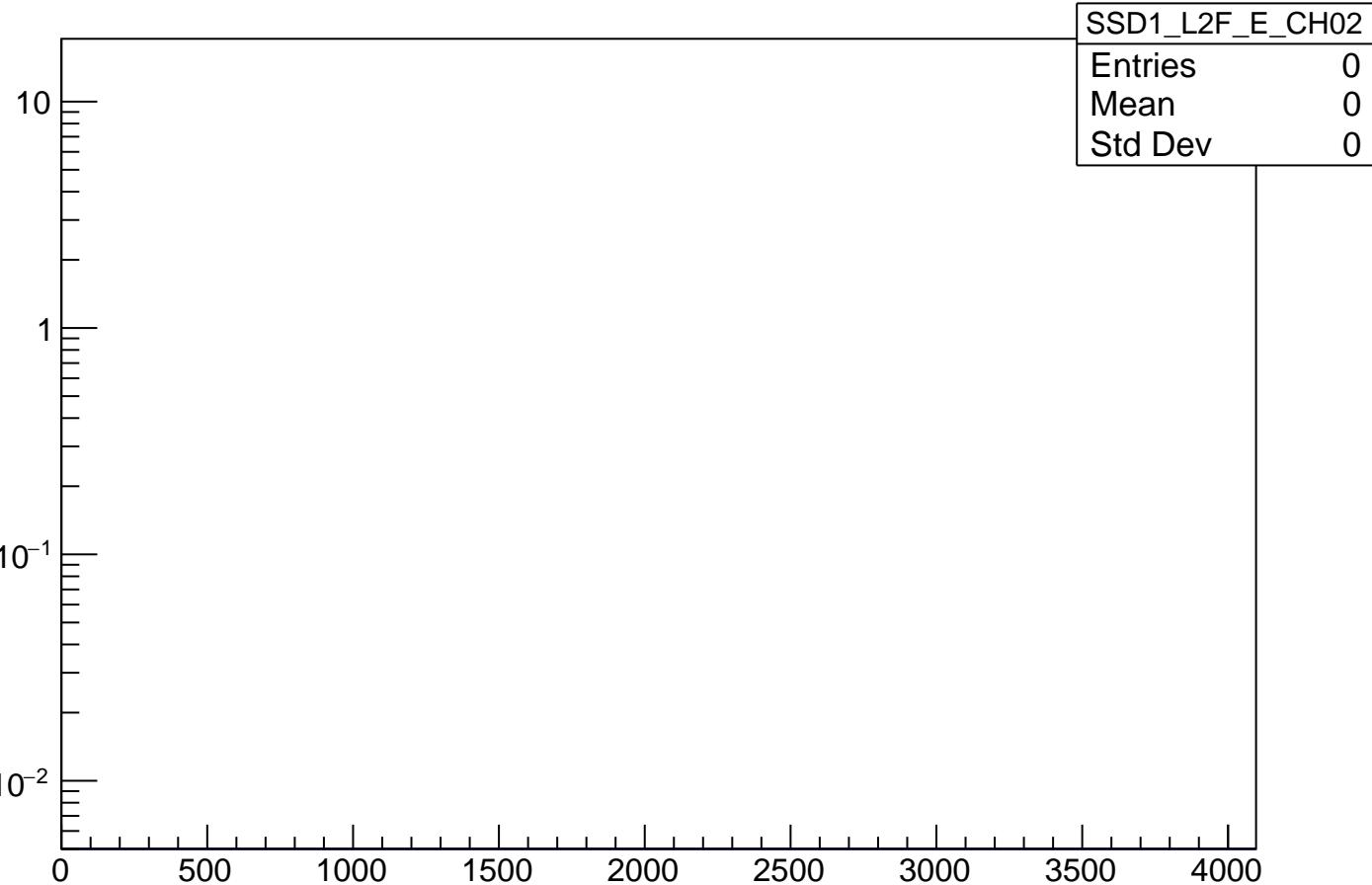
SSD1_L2F_E_CH00



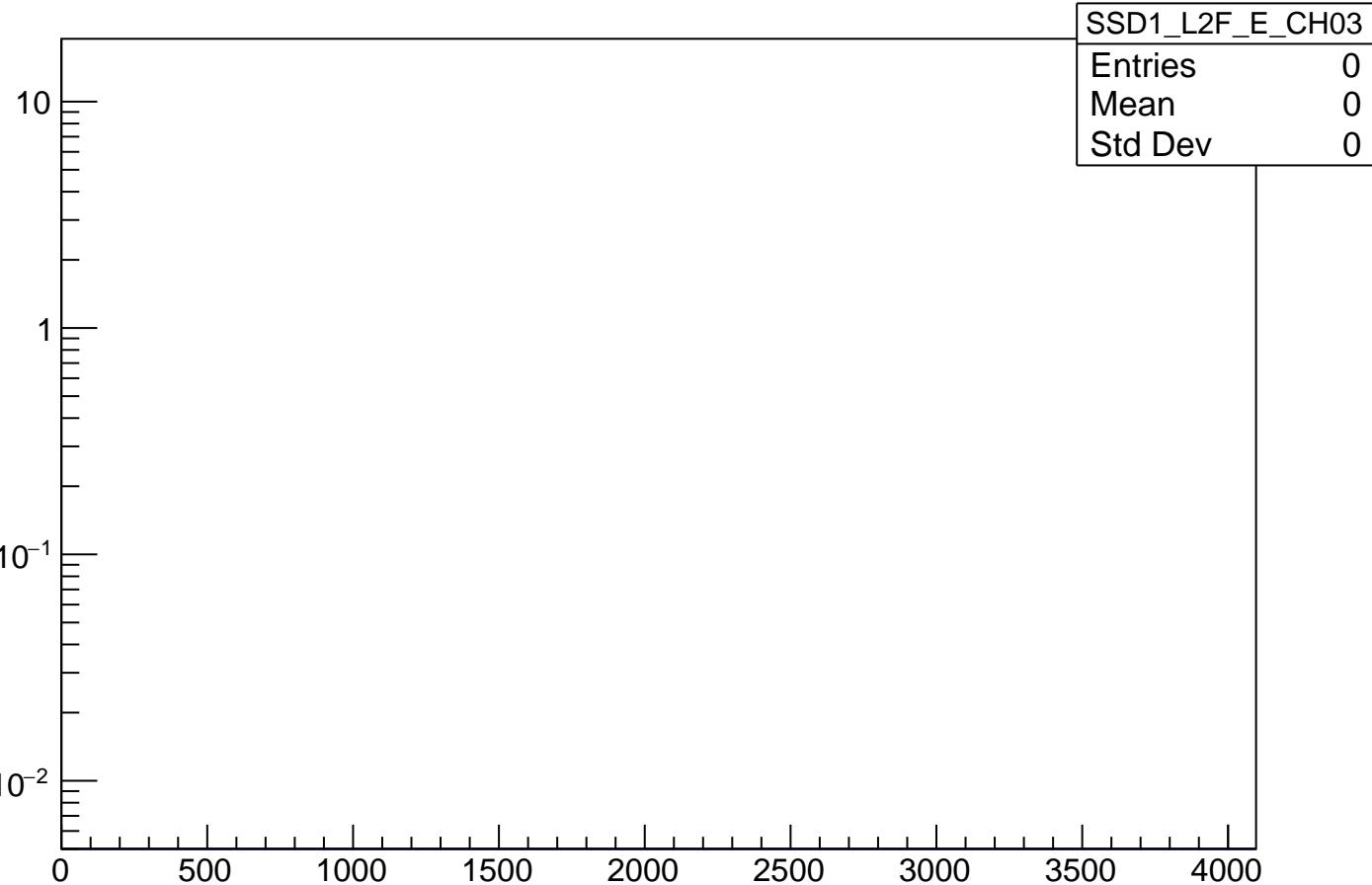
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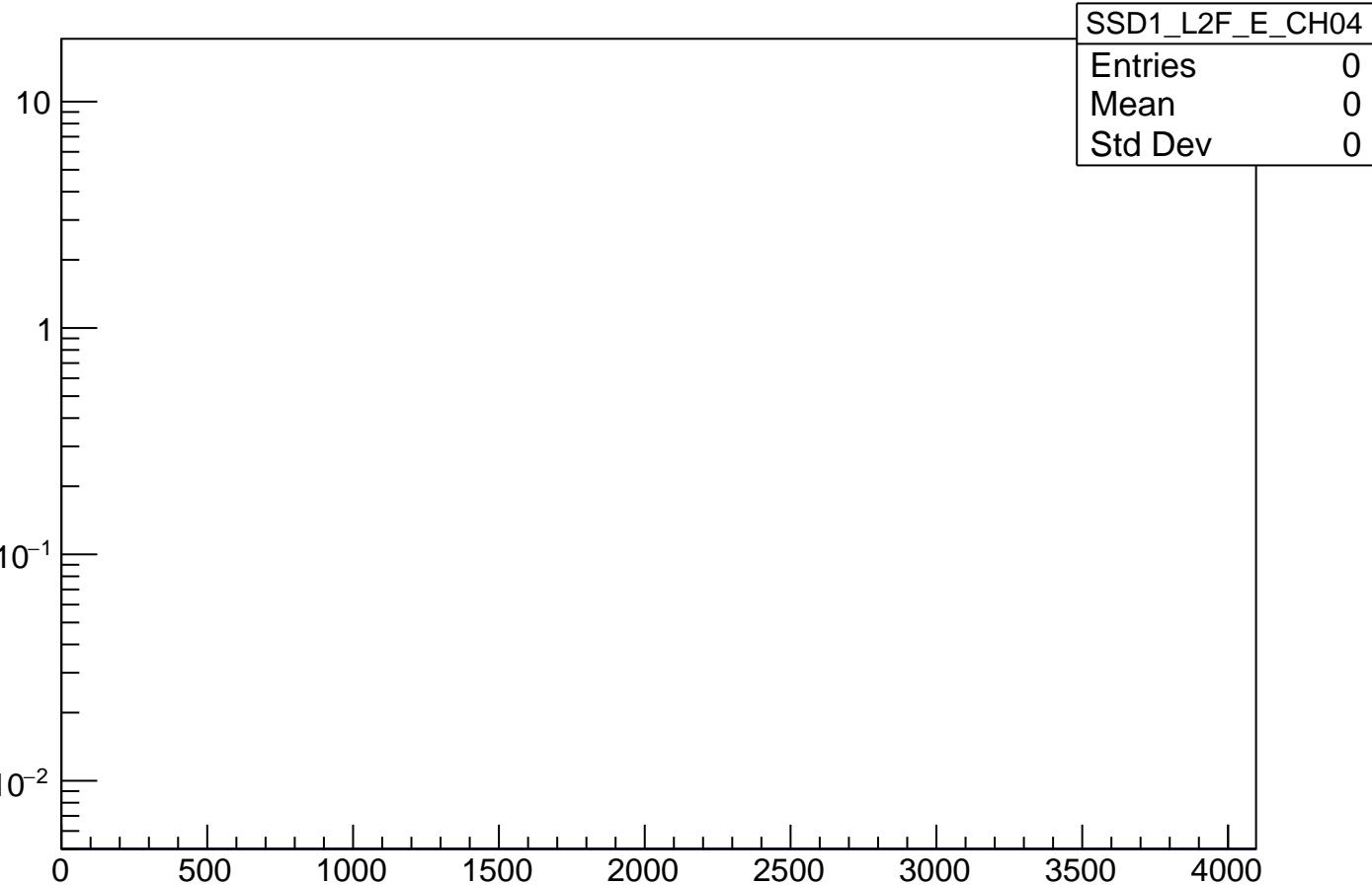
SSD1_L2F_E_CH02



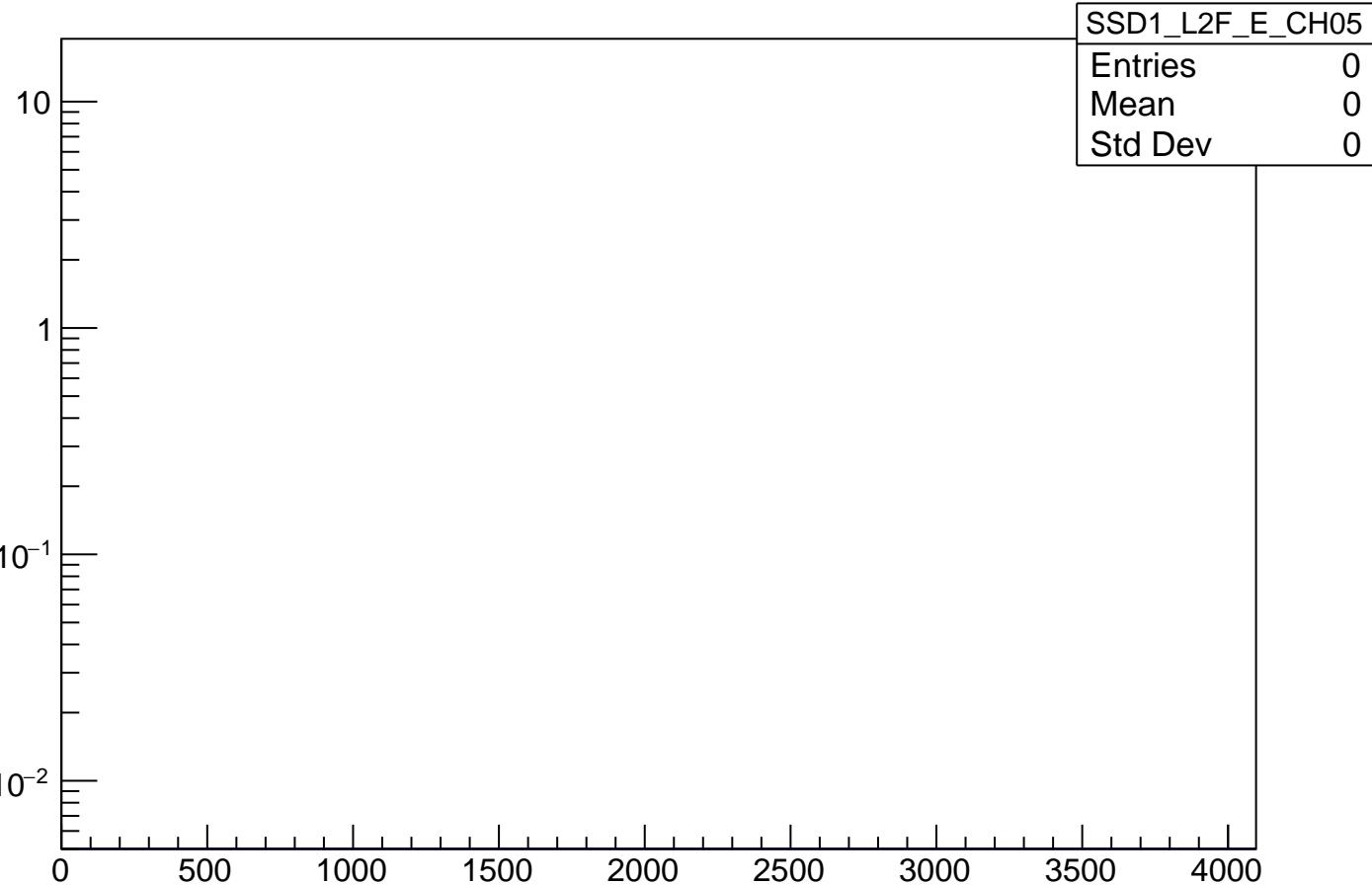
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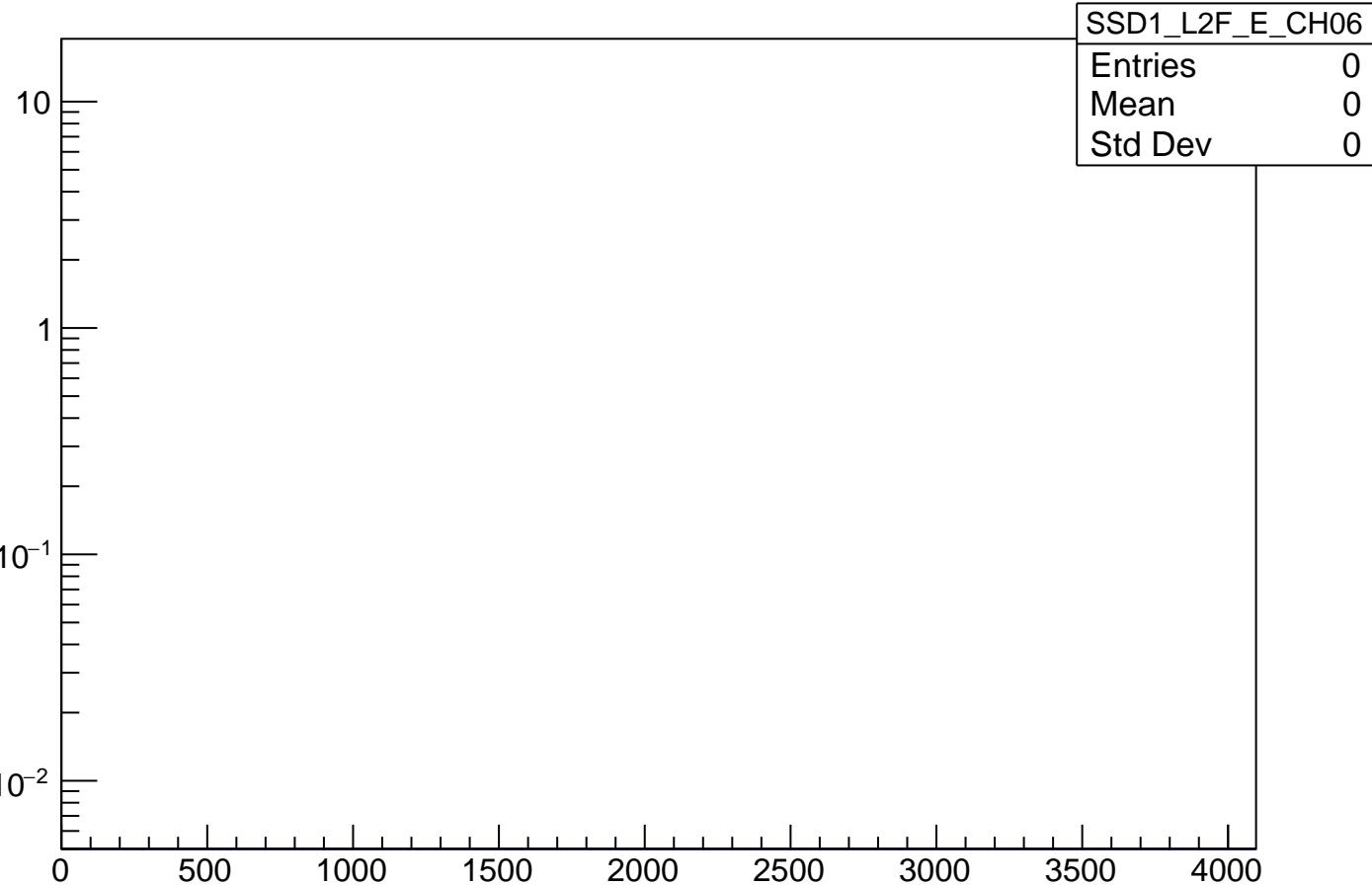
SSD1_L2F_E_CH04



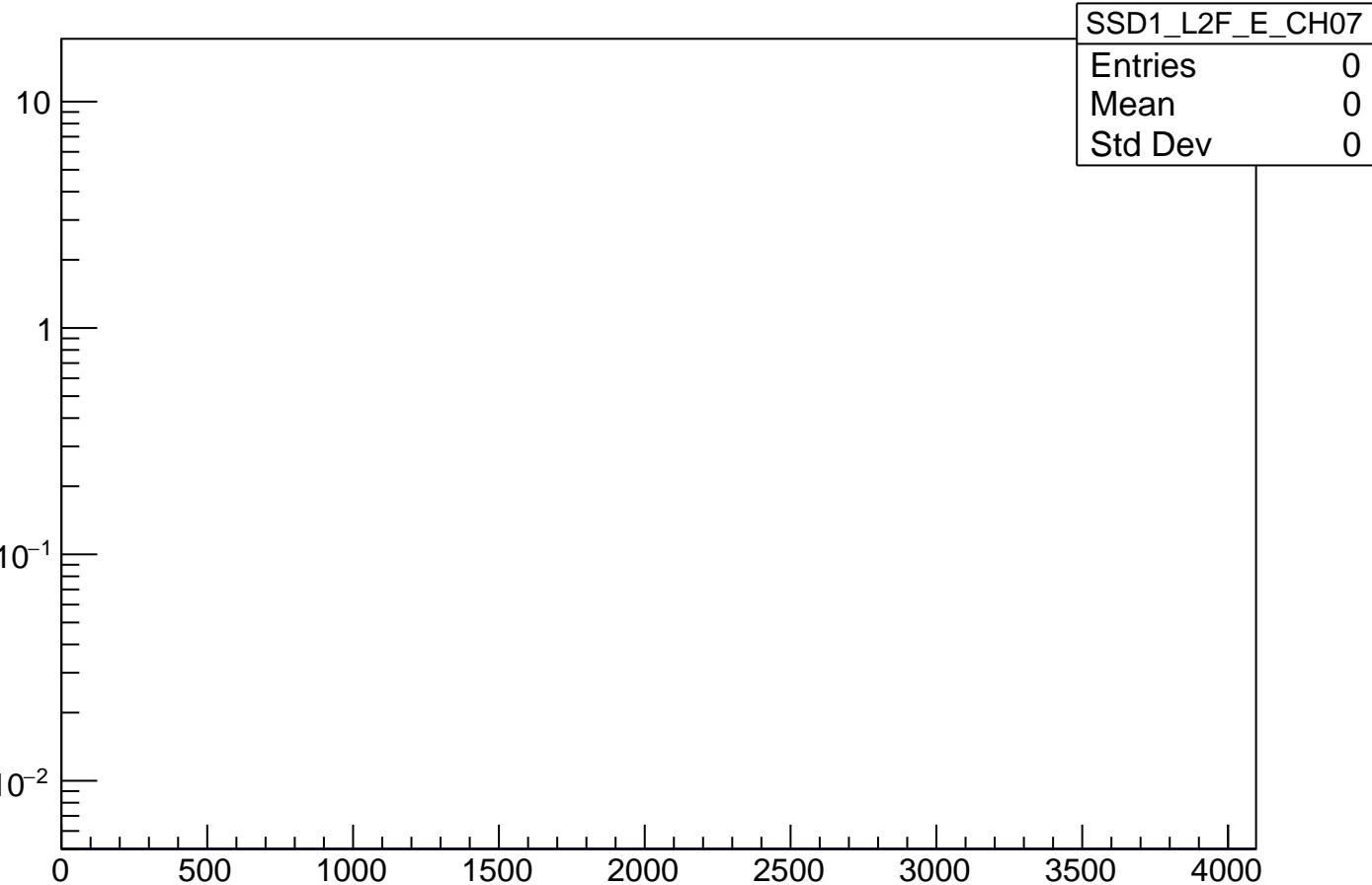
SSD1_L2F_E_CH05



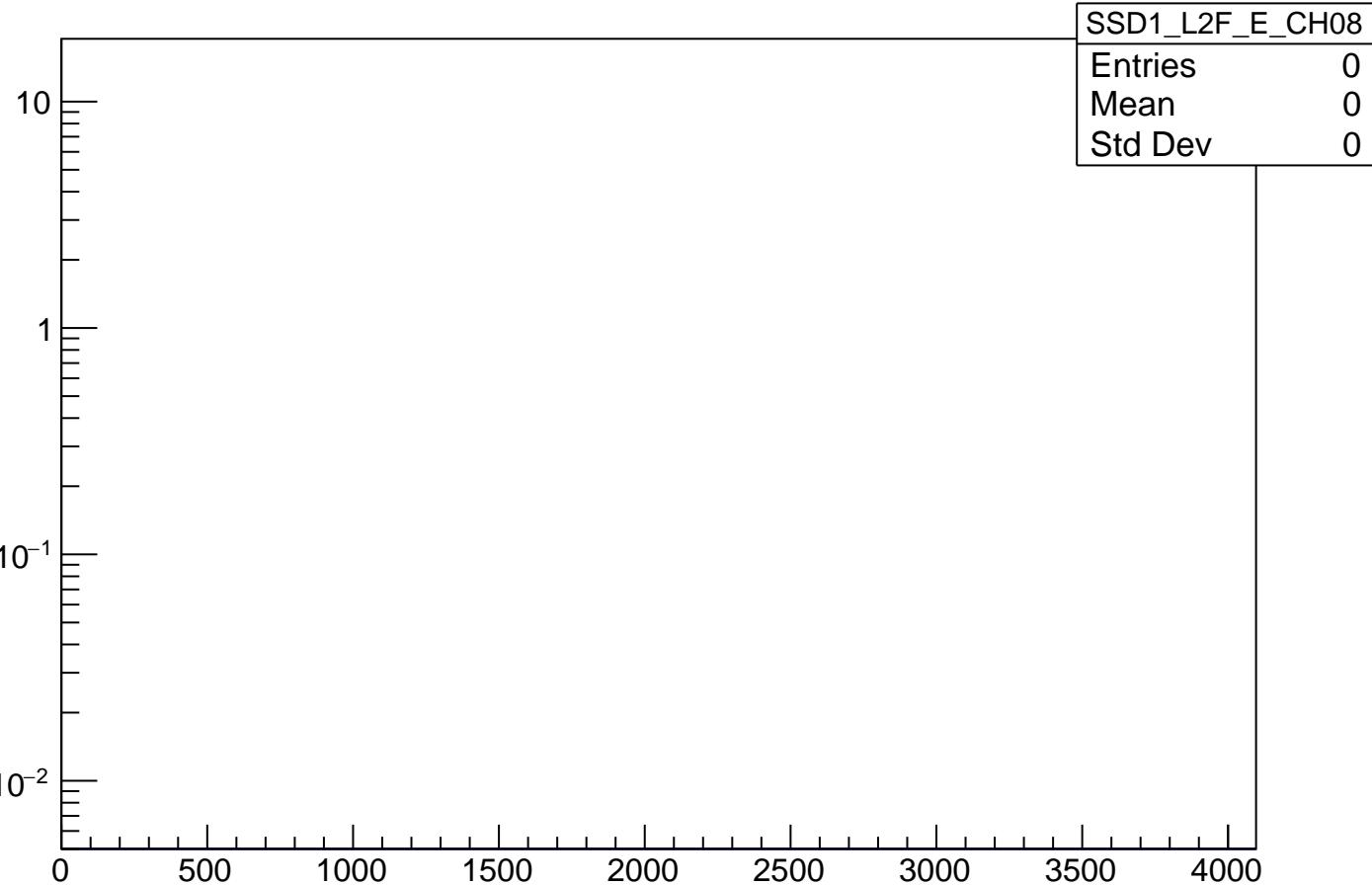
SSD1_L2F_E_CH06



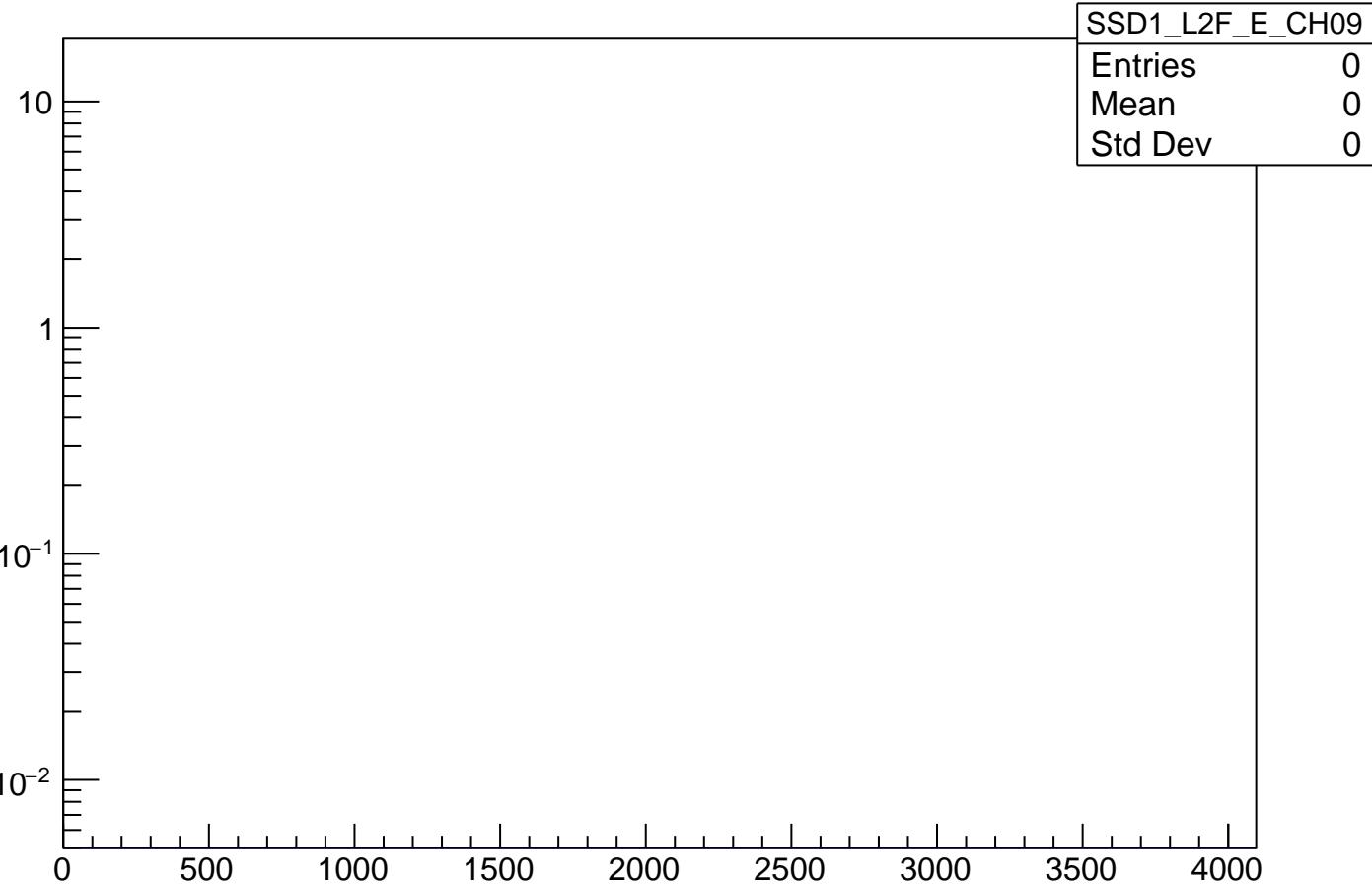
SSD1_L2F_E_CH07



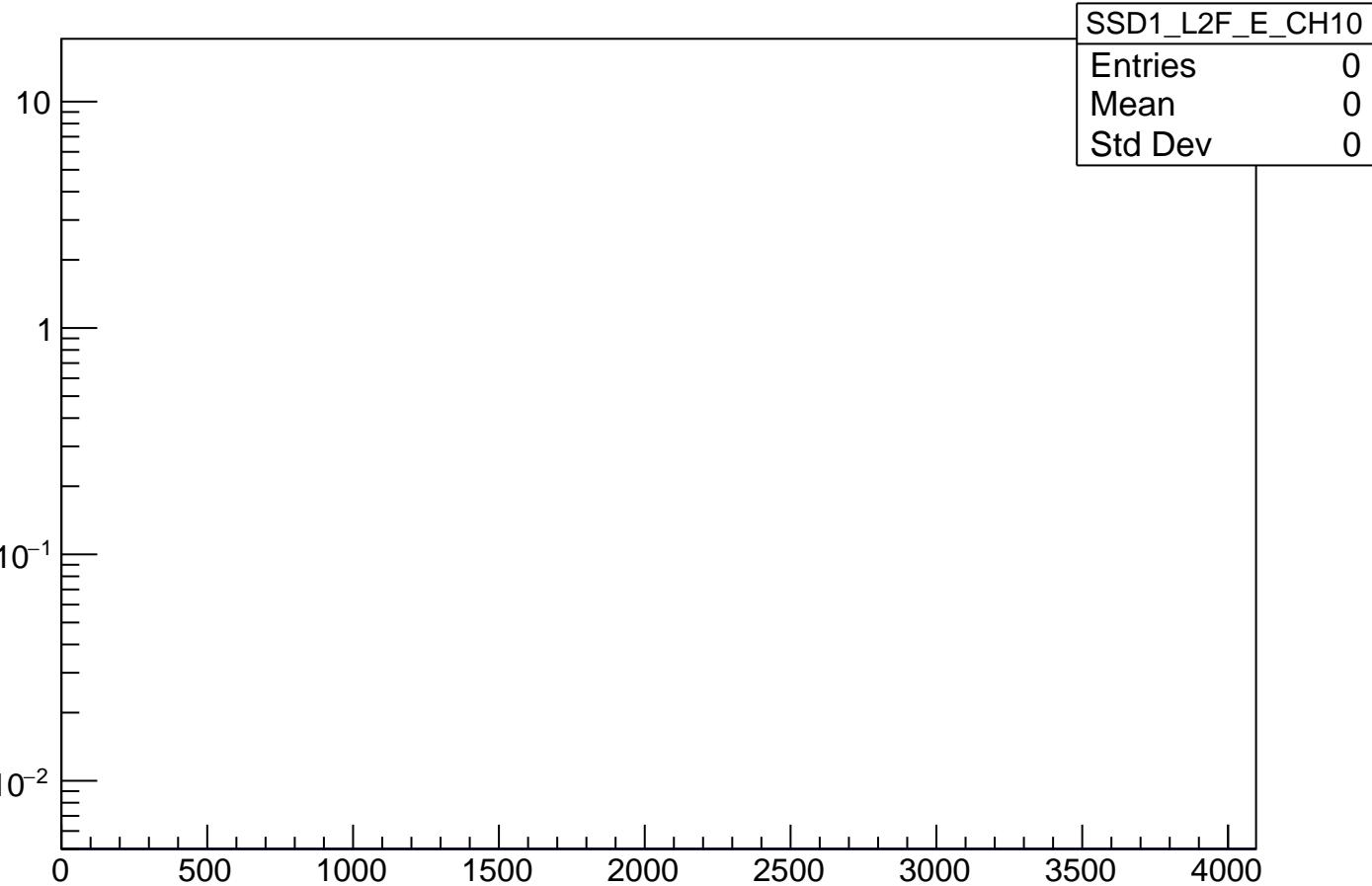
SSD1_L2F_E_CH08



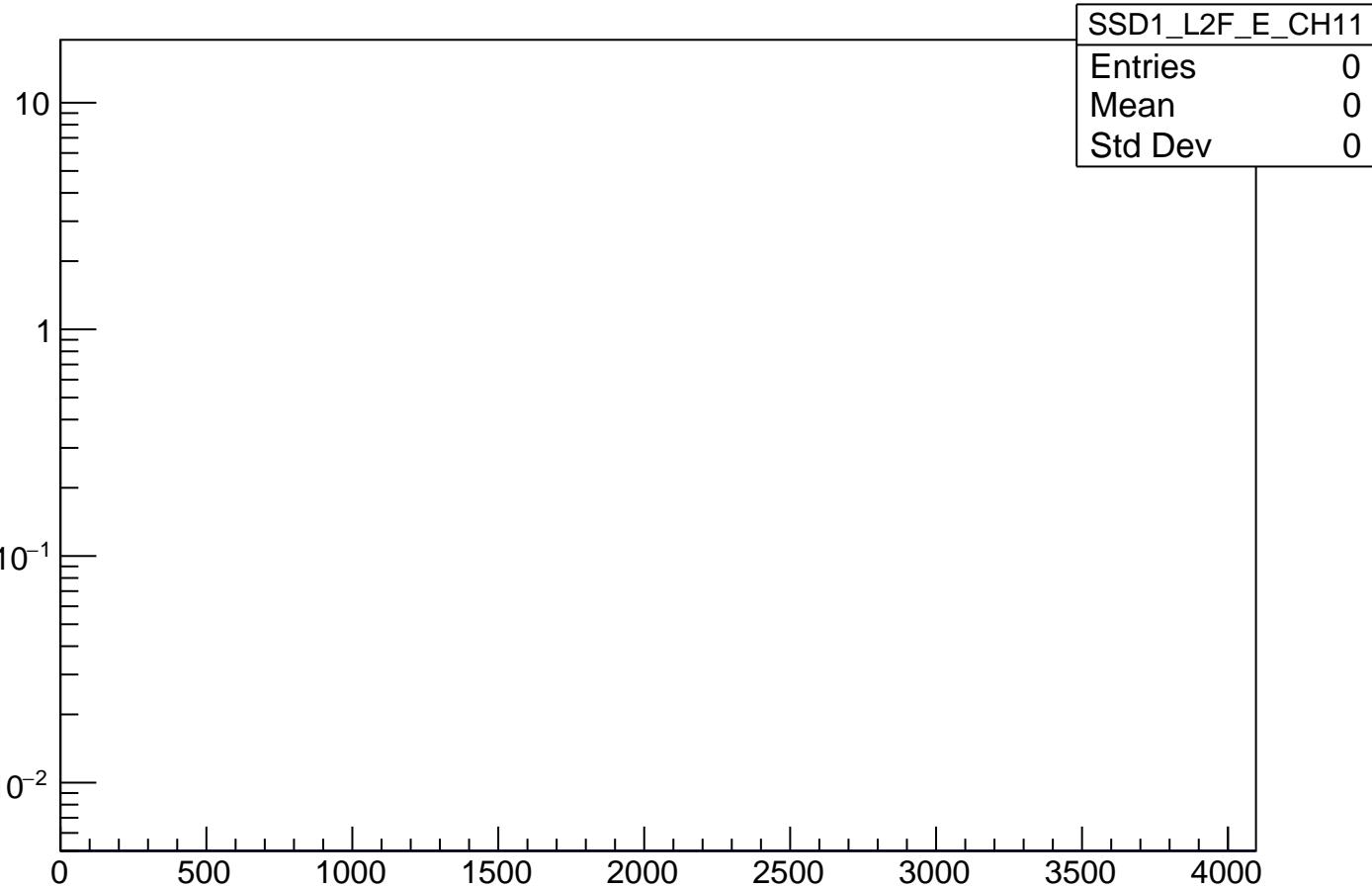
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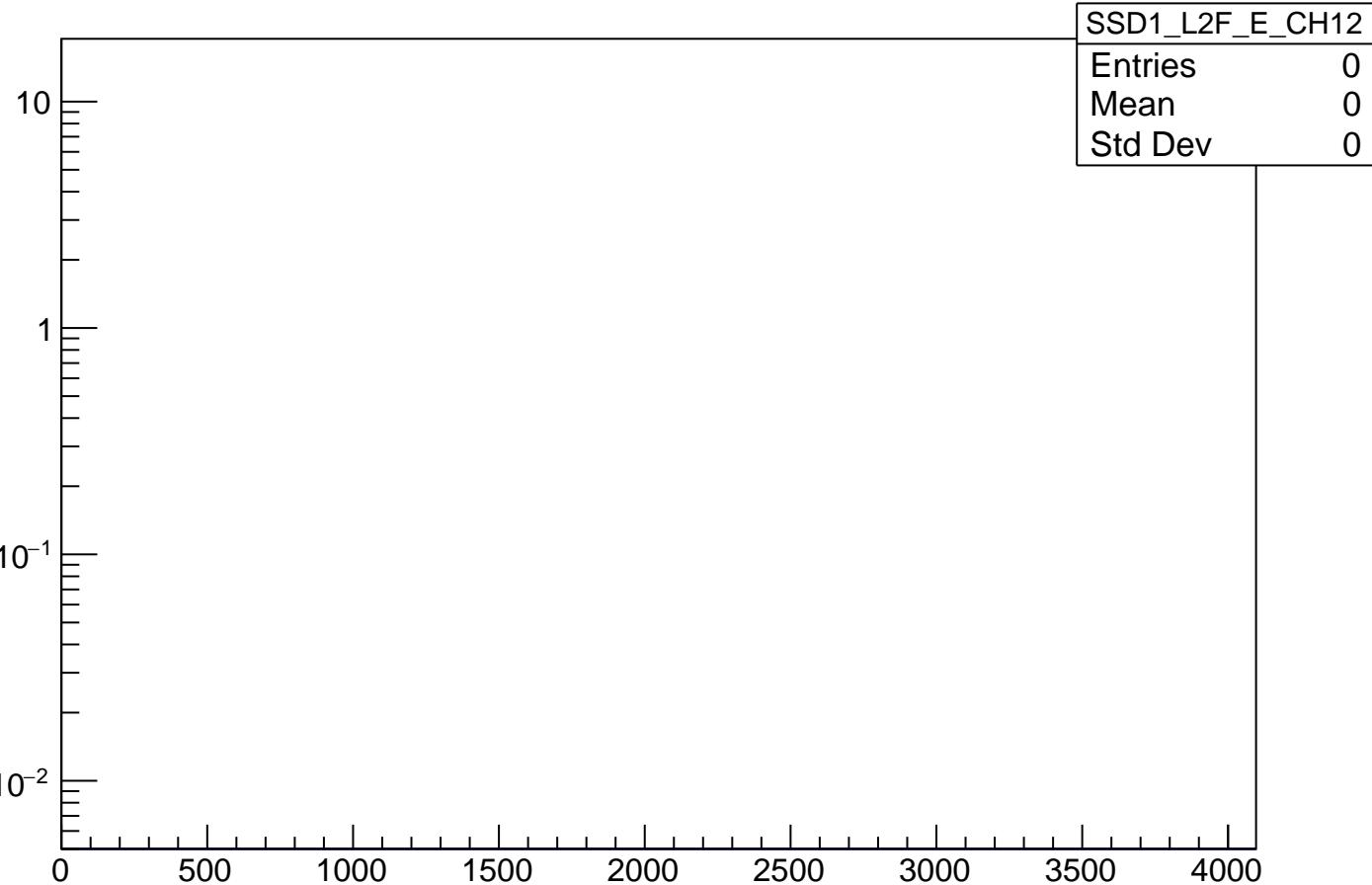
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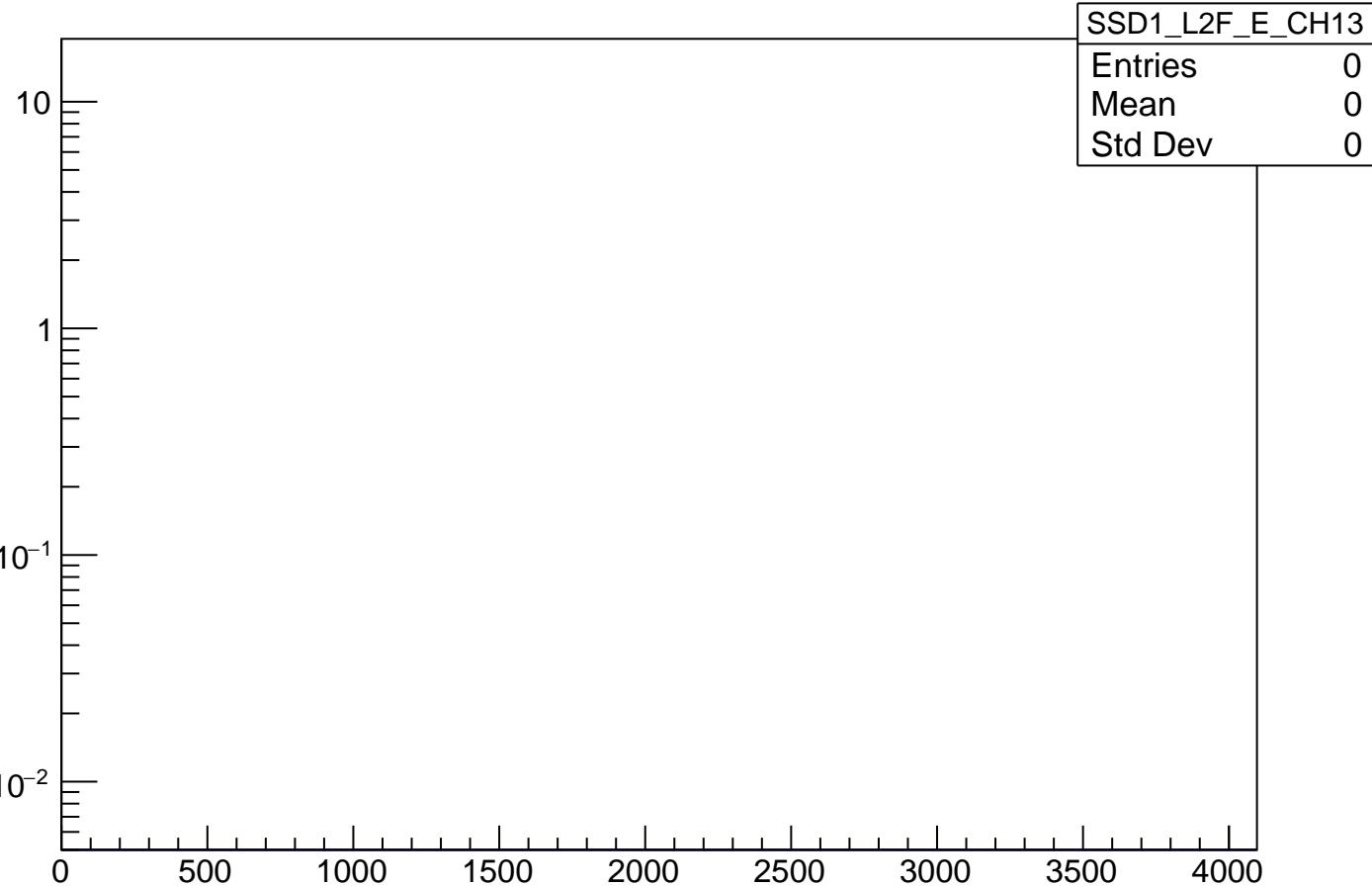
SSD1_L2F_E_CH11



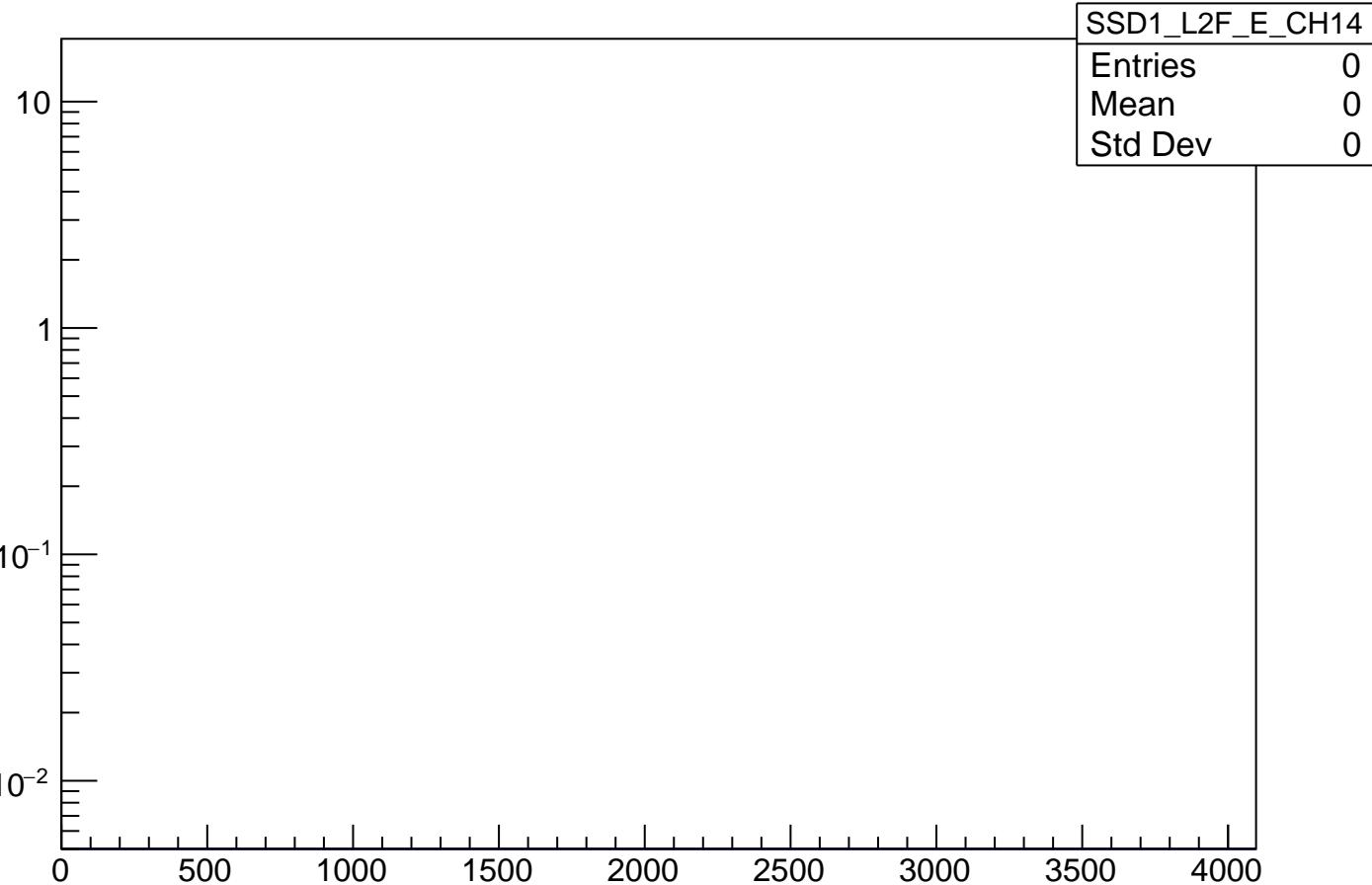
SSD1_L2F_E_CH12



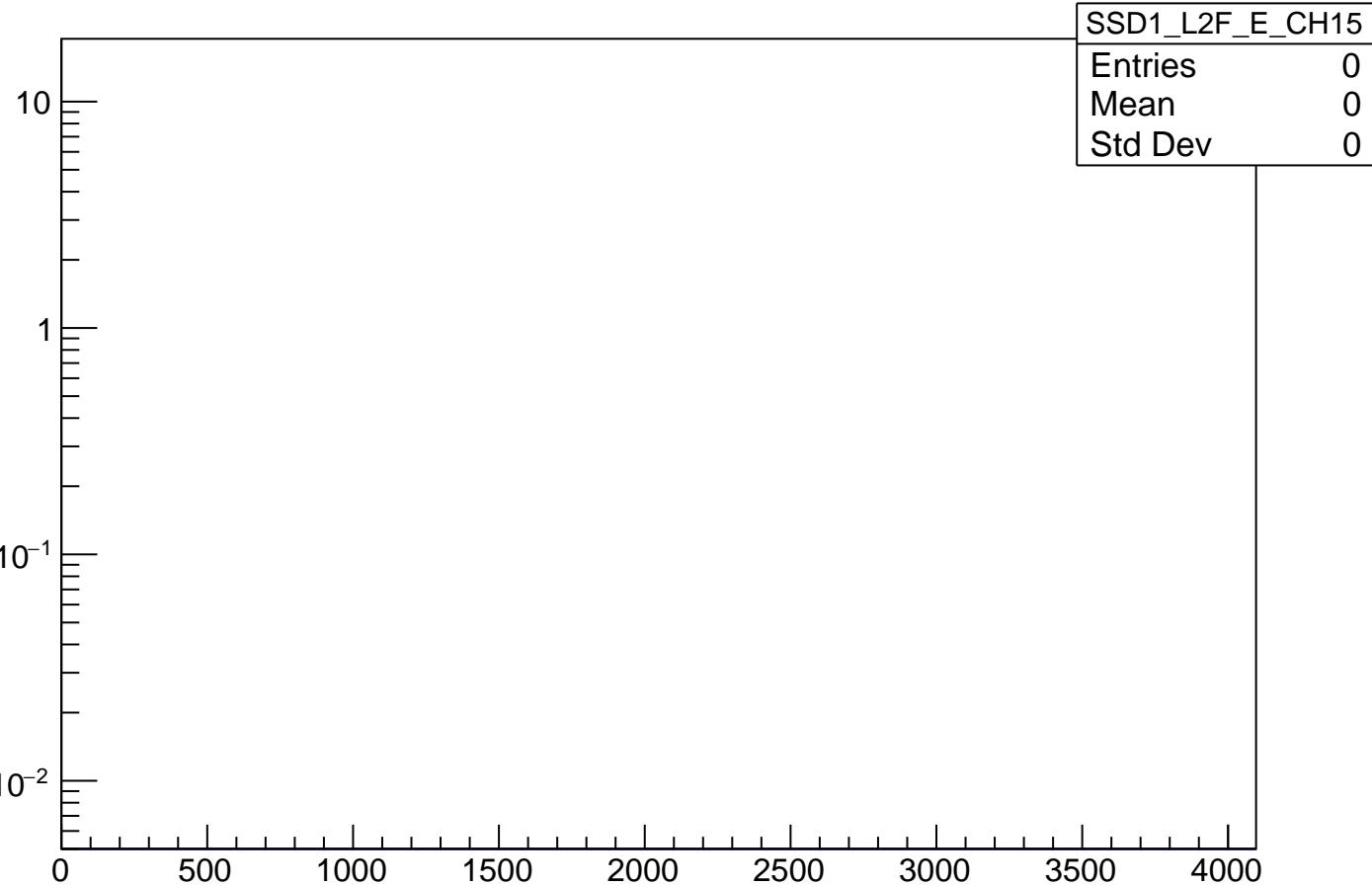
SSD1_L2F_E_CH13



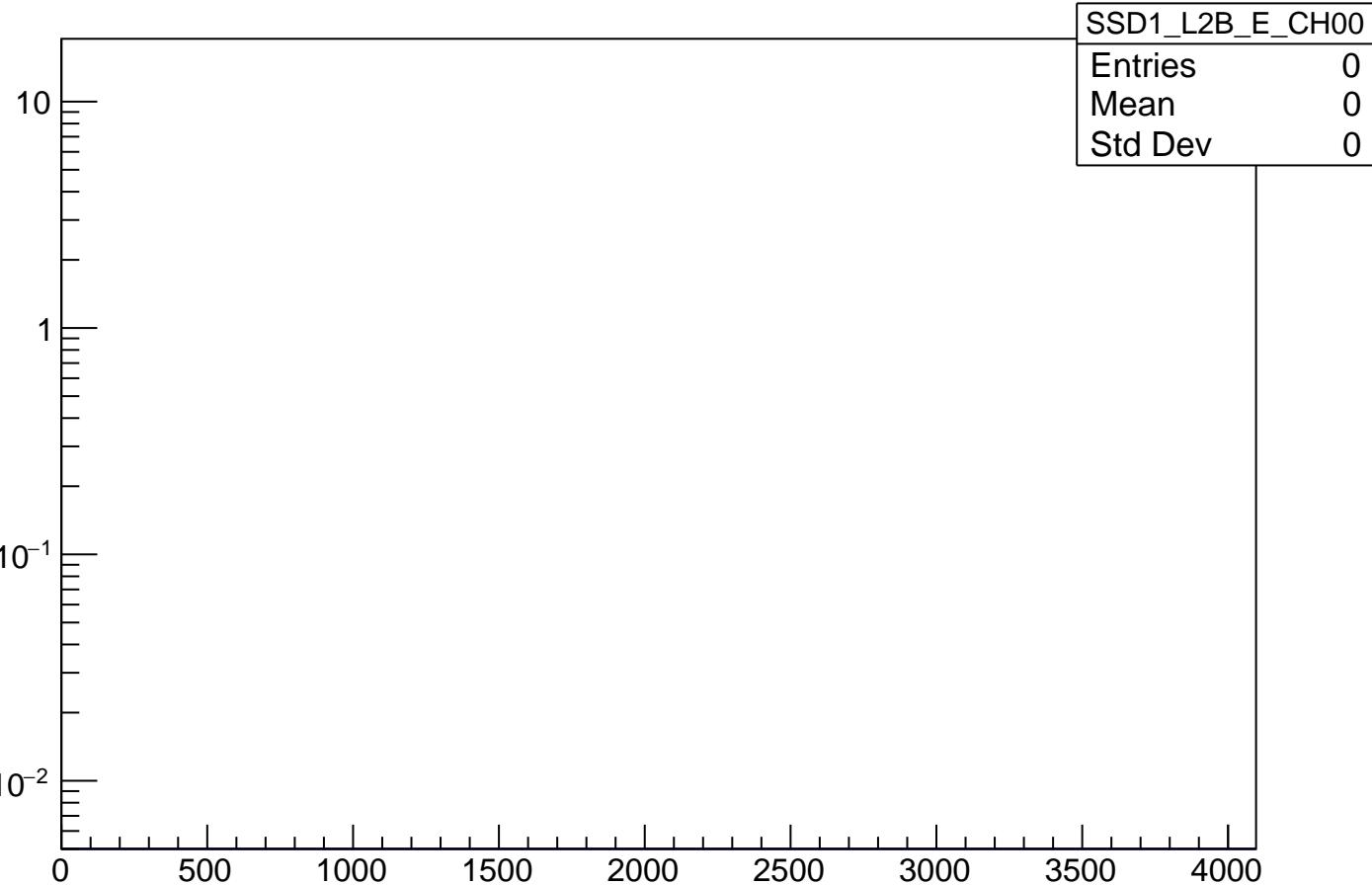
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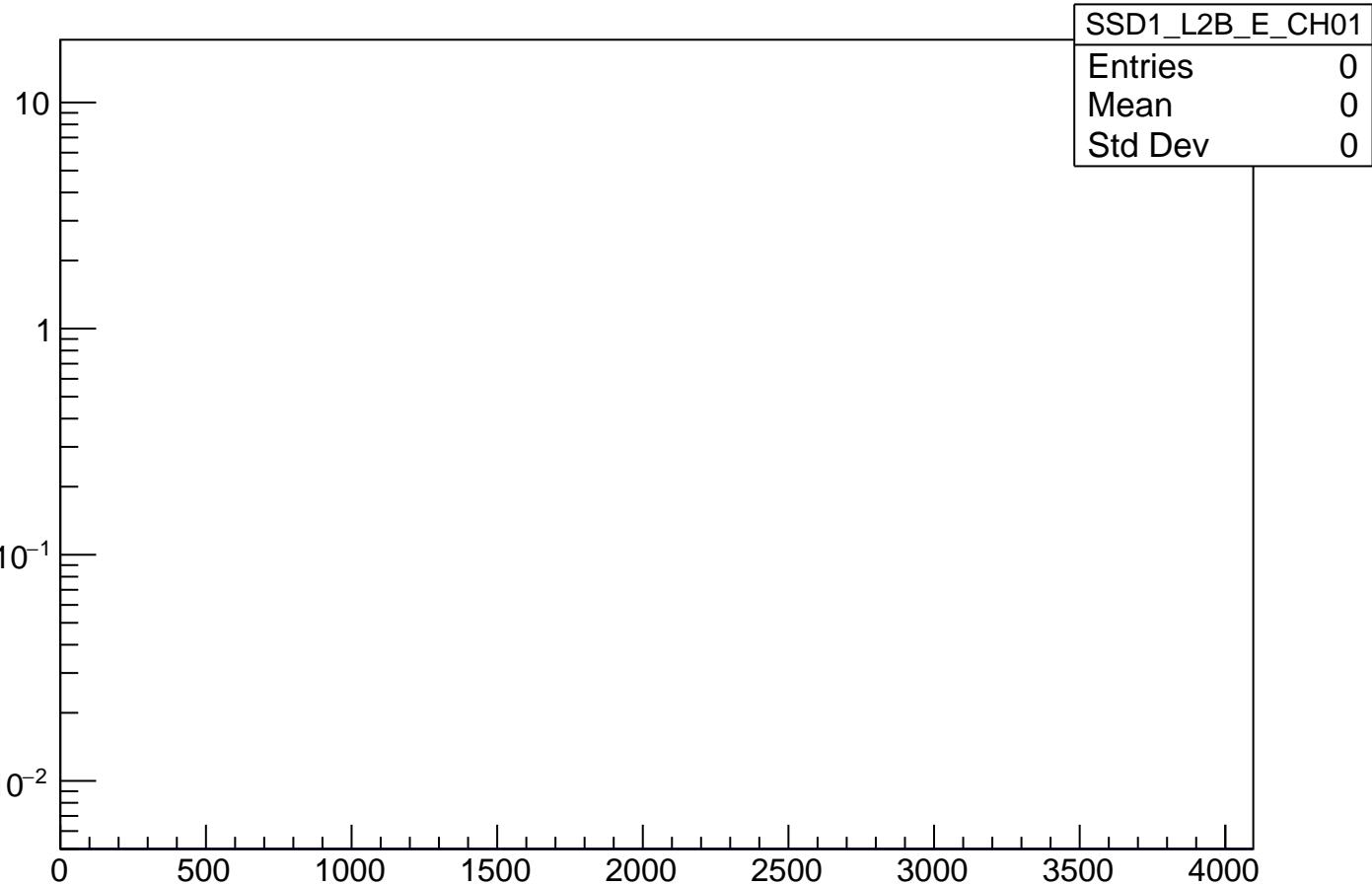
SSD1_L2F_E_CH15



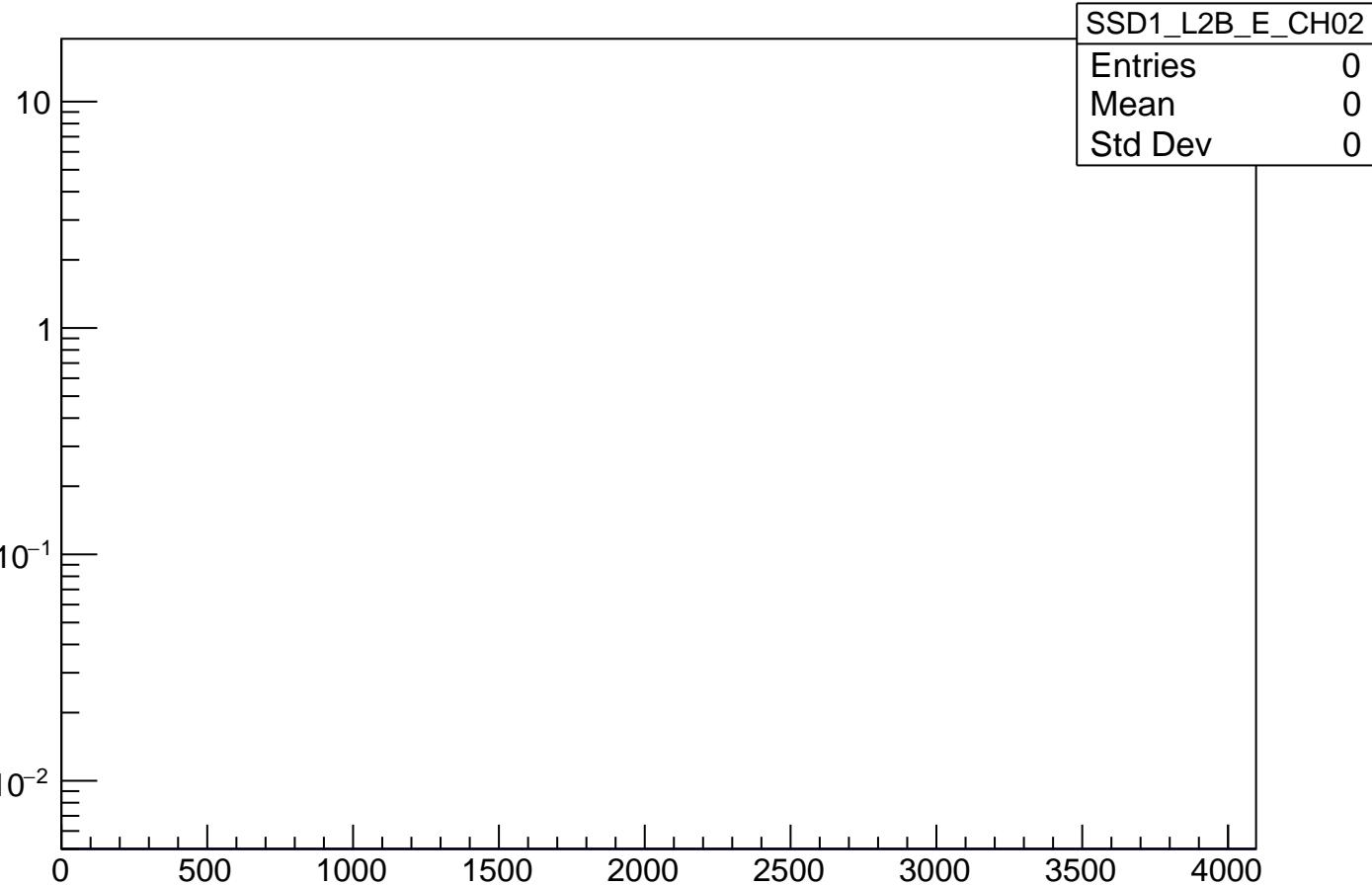
SSD1_L2B_E_CH00



SSD1_L2B_E_CH01

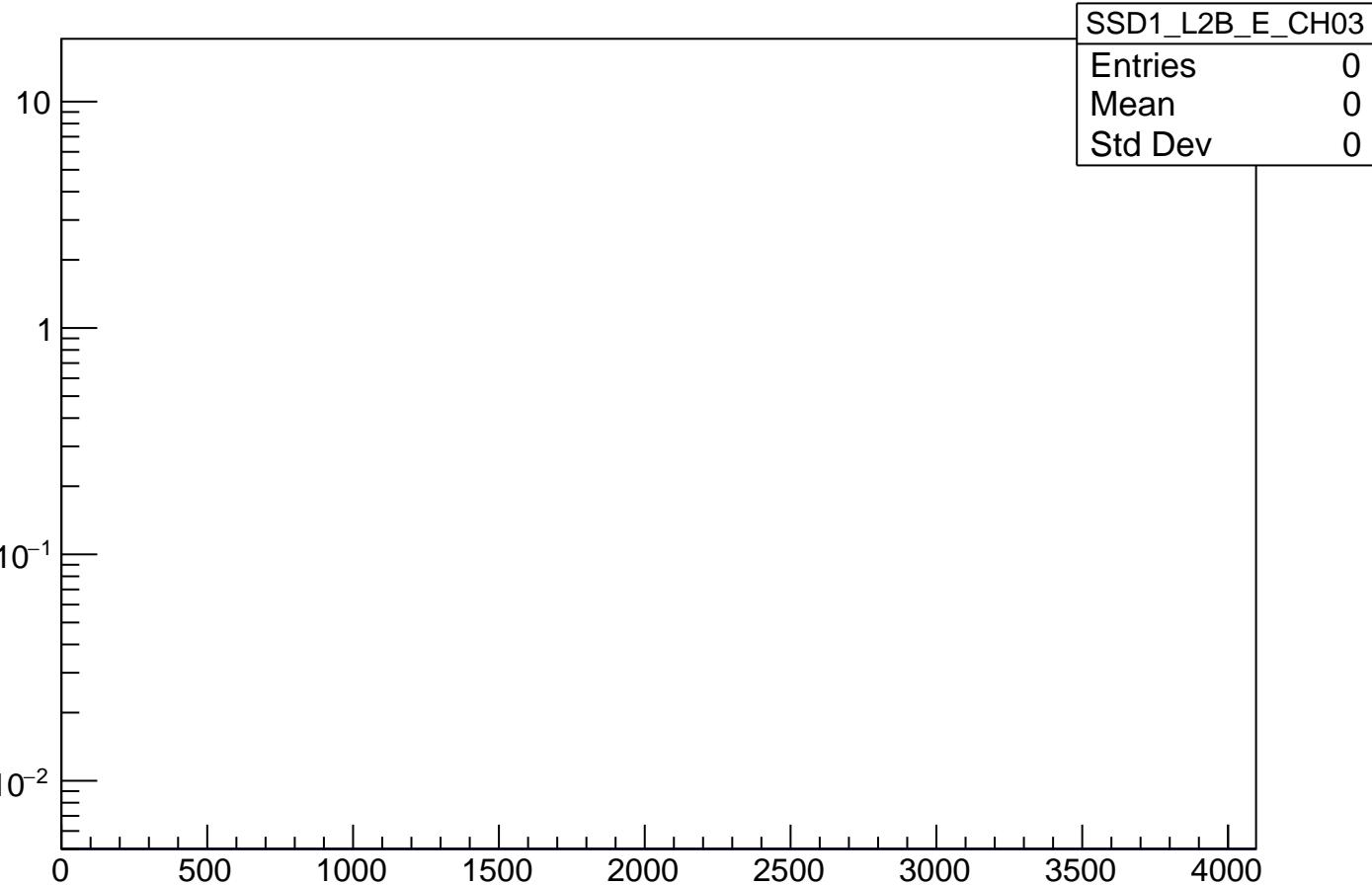


SSD1_L2B_E_CH02

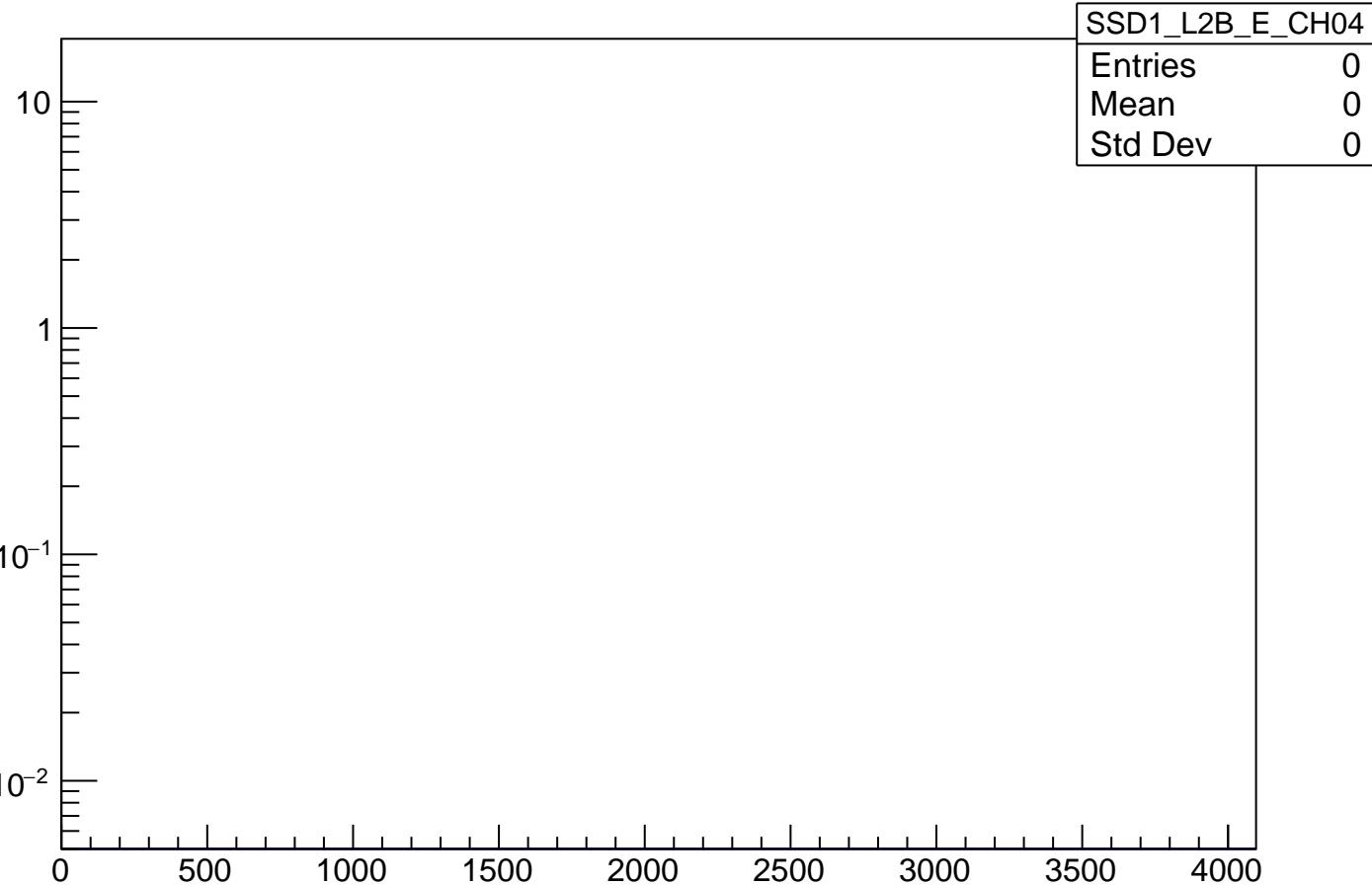


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Mean	0
Std Dev	0

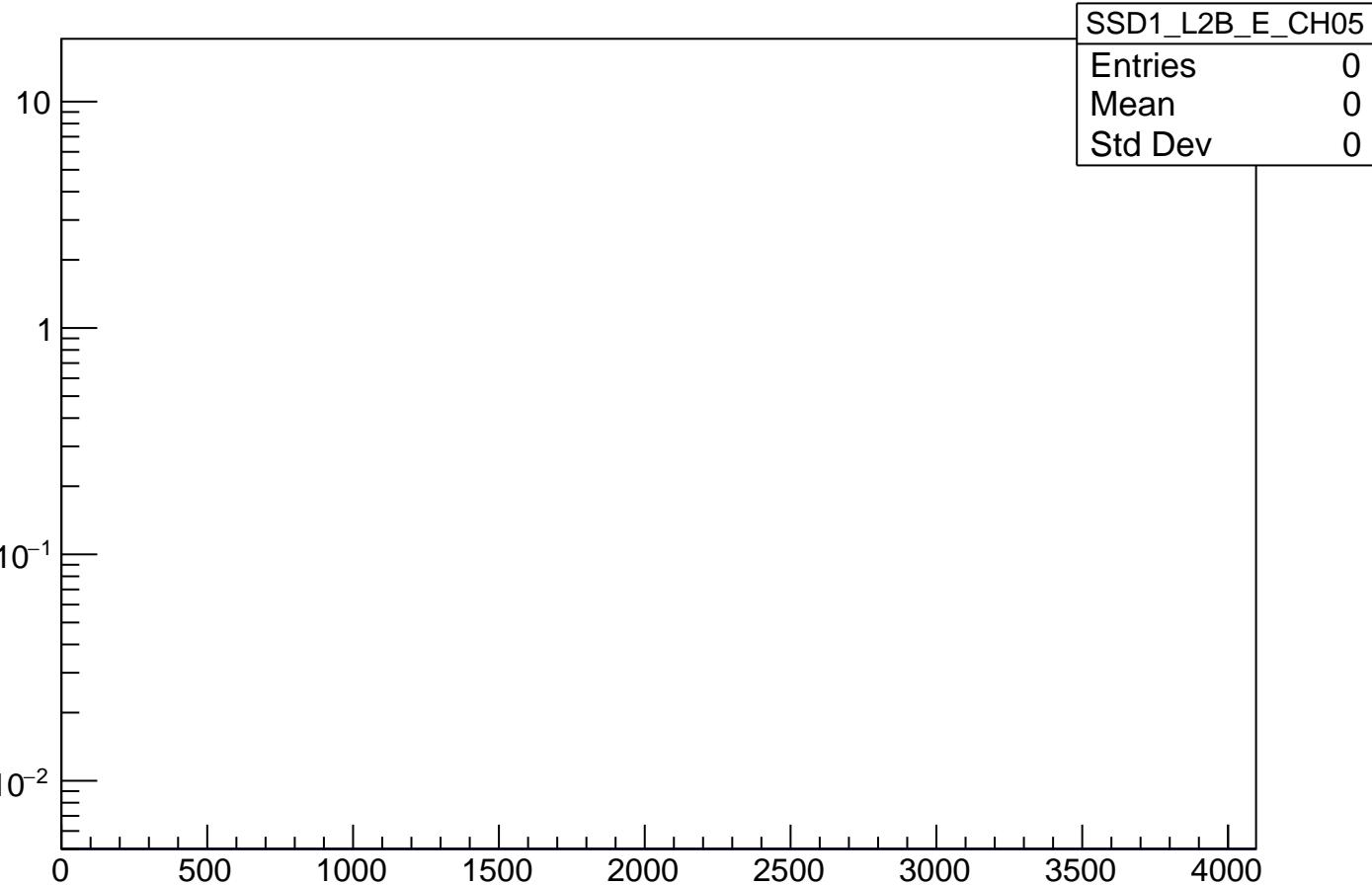
SSD1_L2B_E_CH03



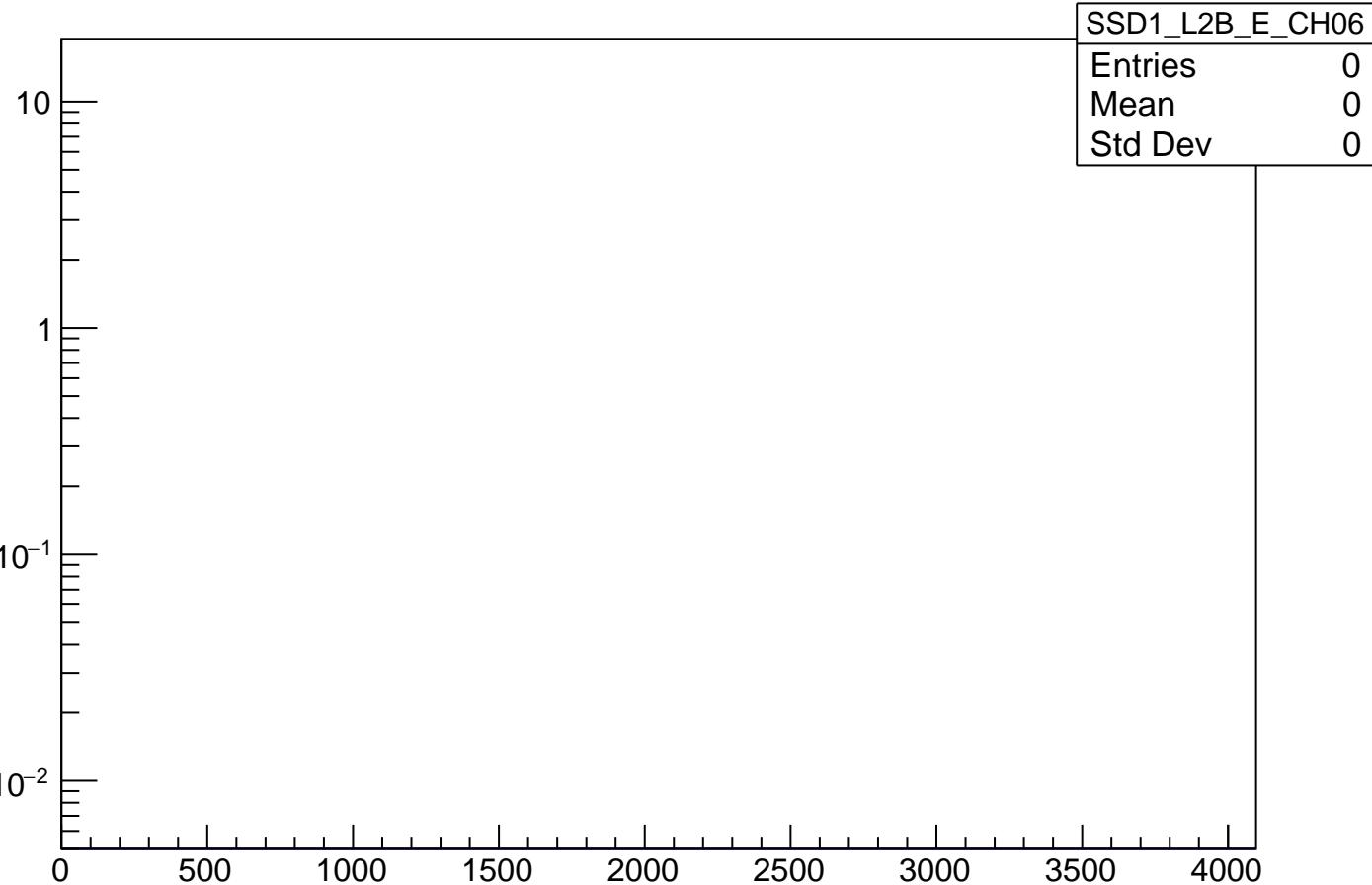
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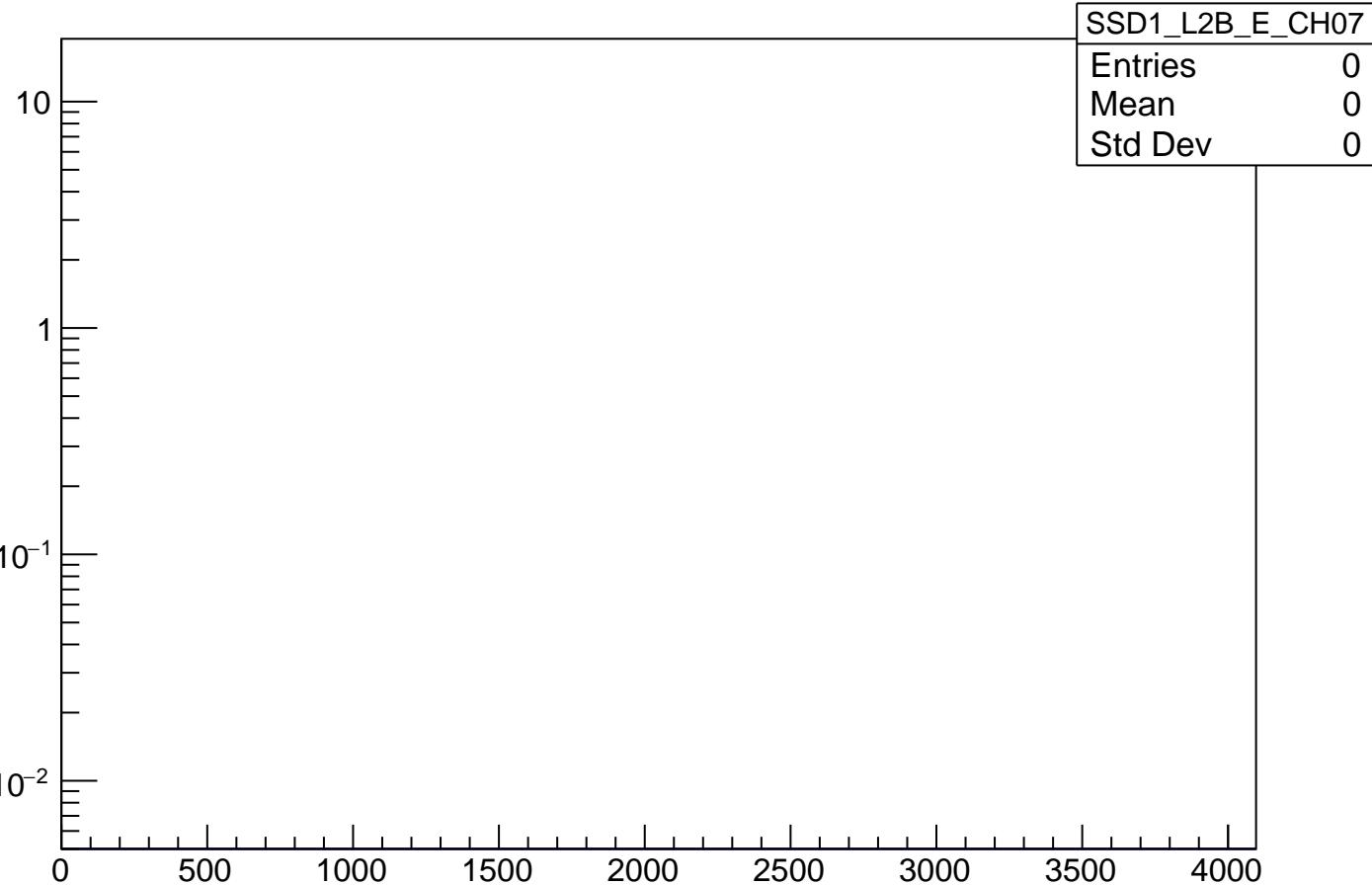
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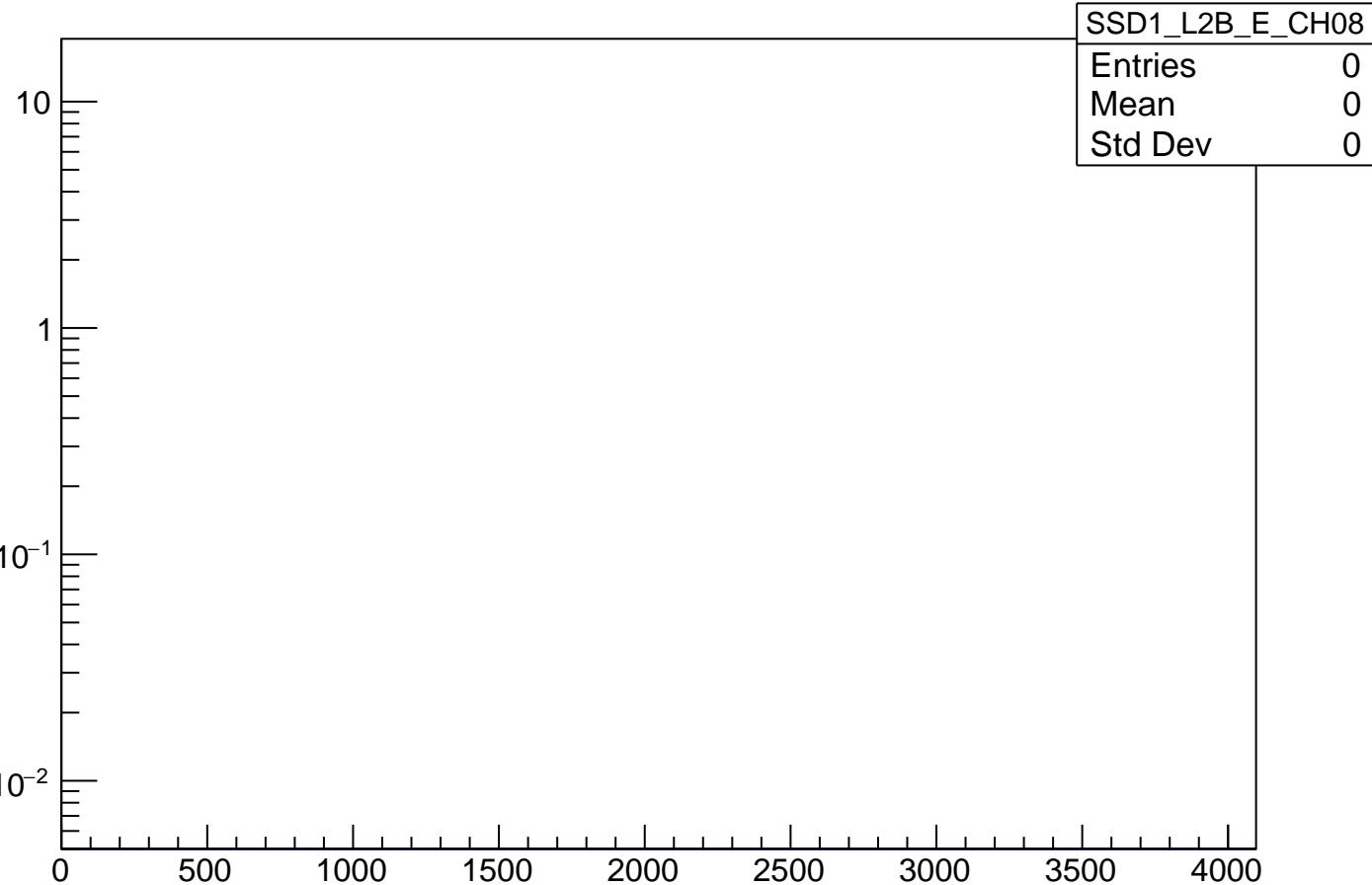
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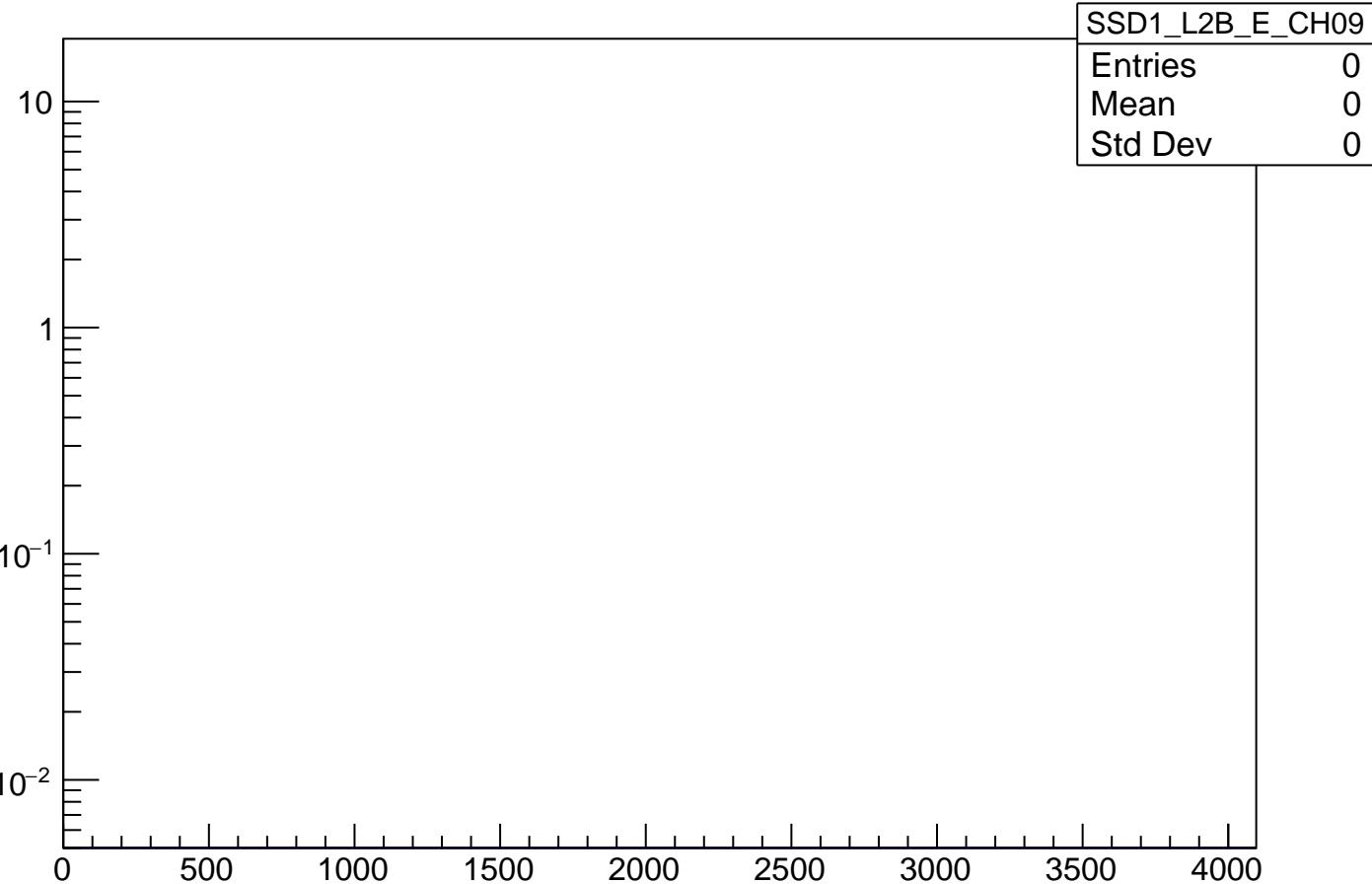
SSD1_L2B_E_CH07



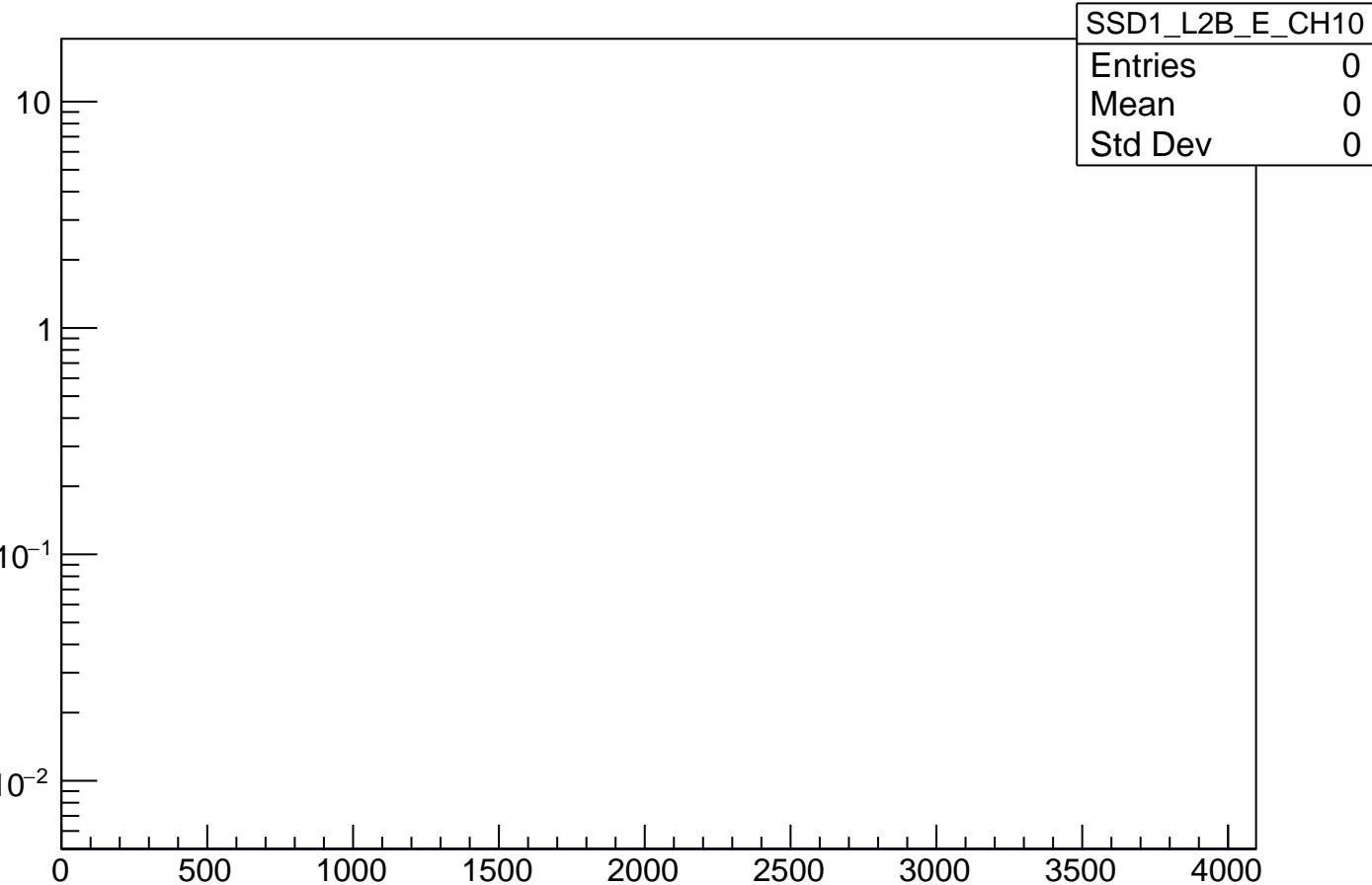
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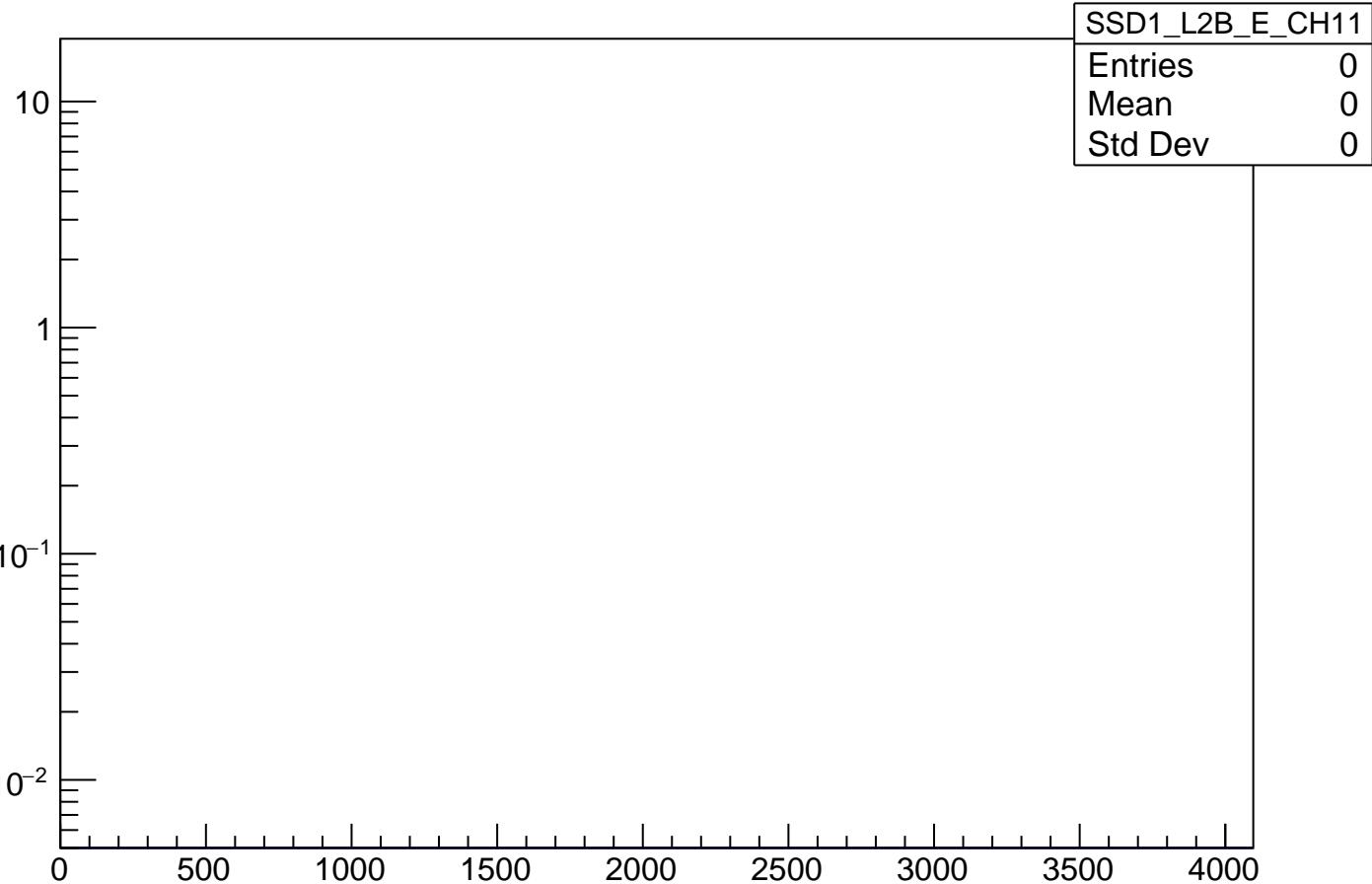
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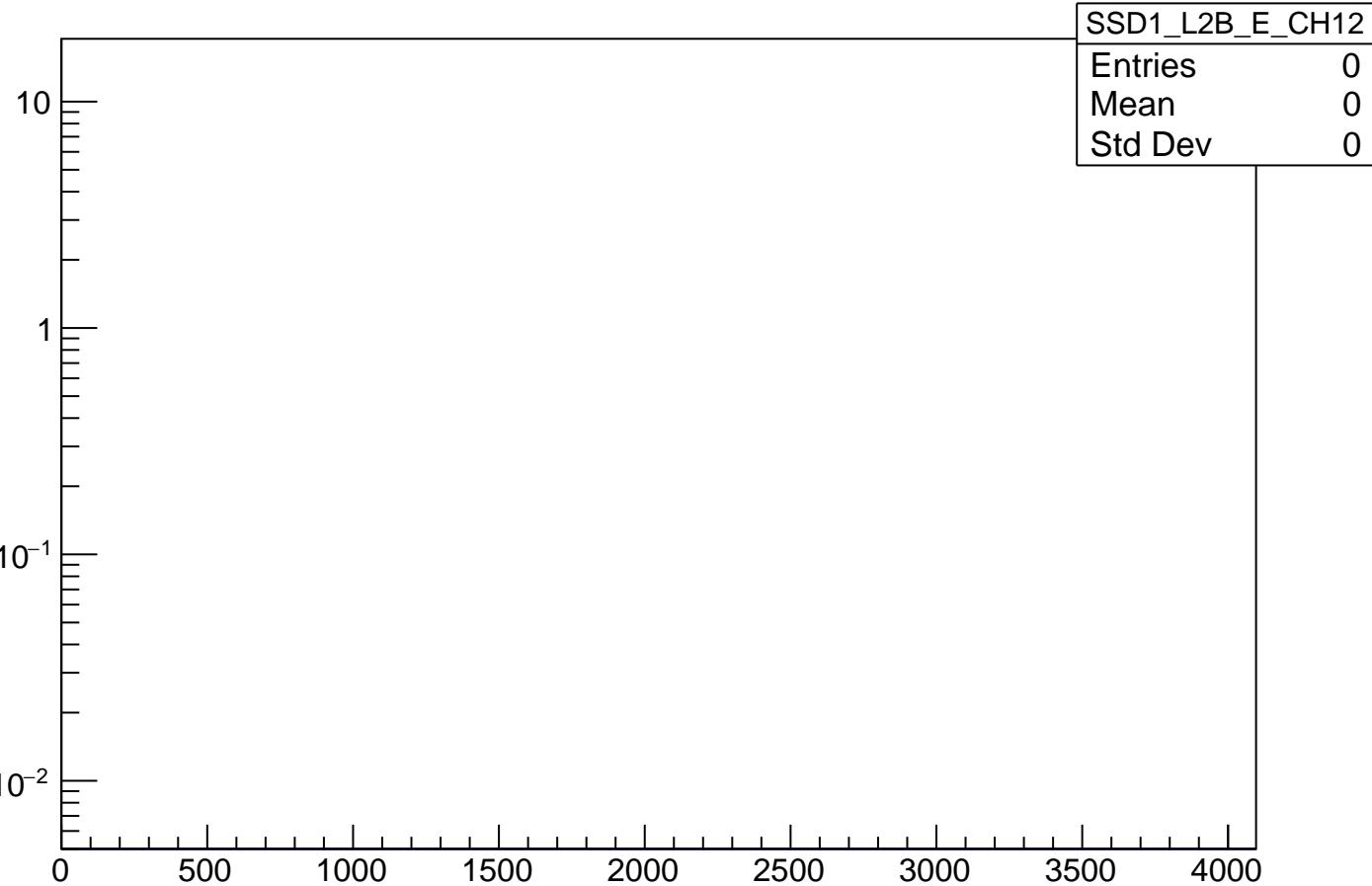
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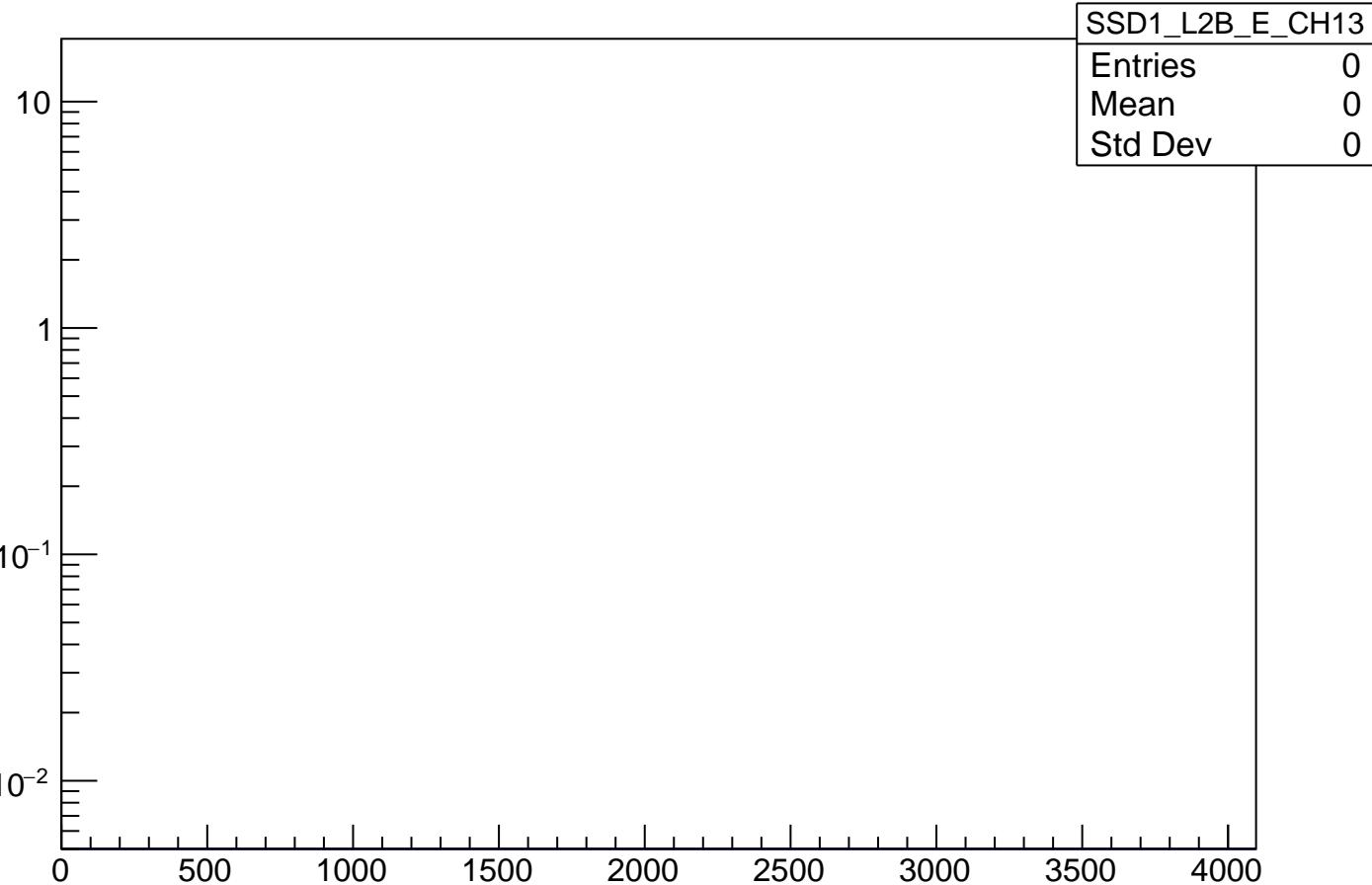
SSD1_L2B_E_CH11



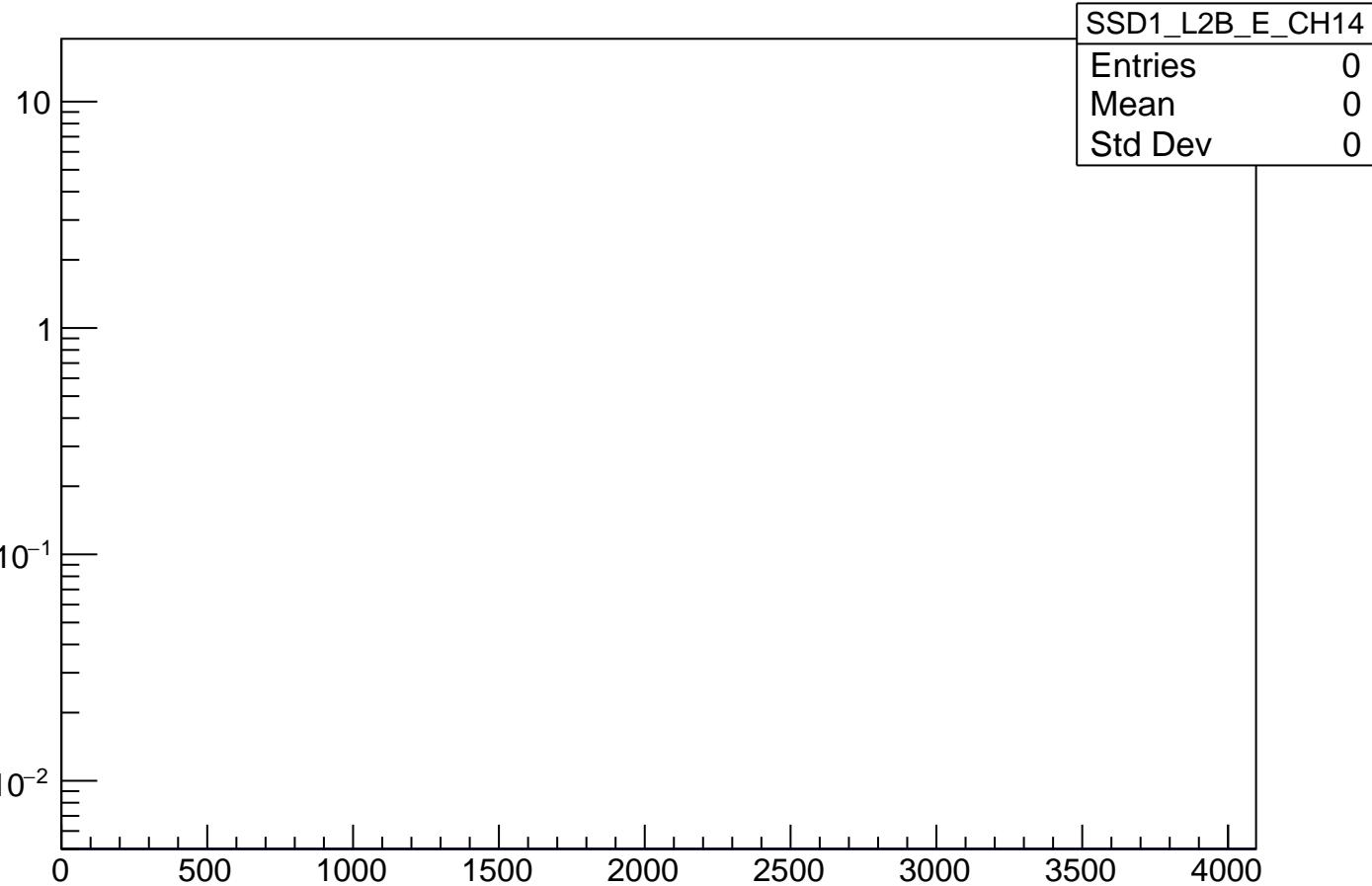
SSD1_L2B_E_CH12



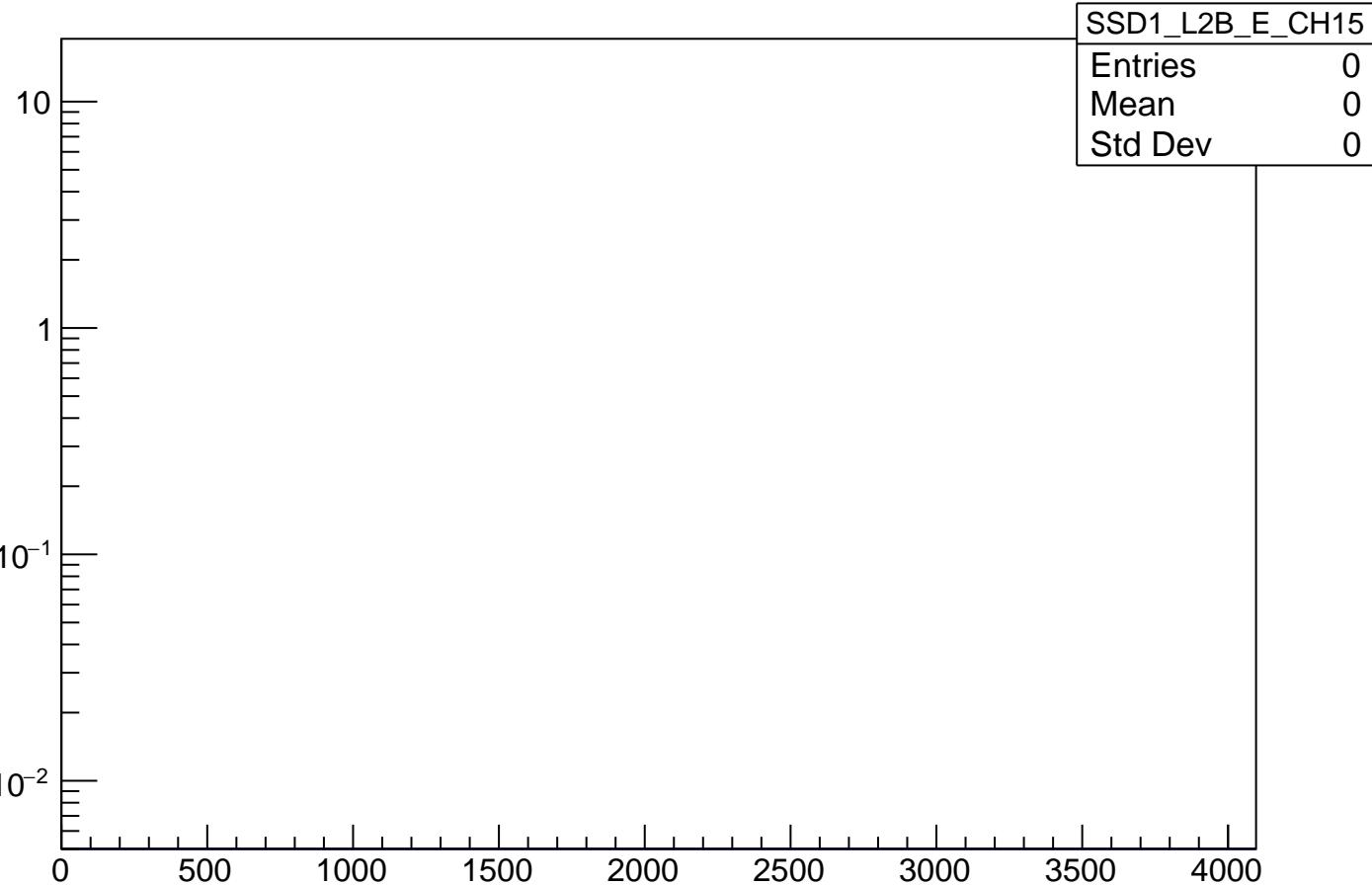
SSD1_L2B_E_CH13



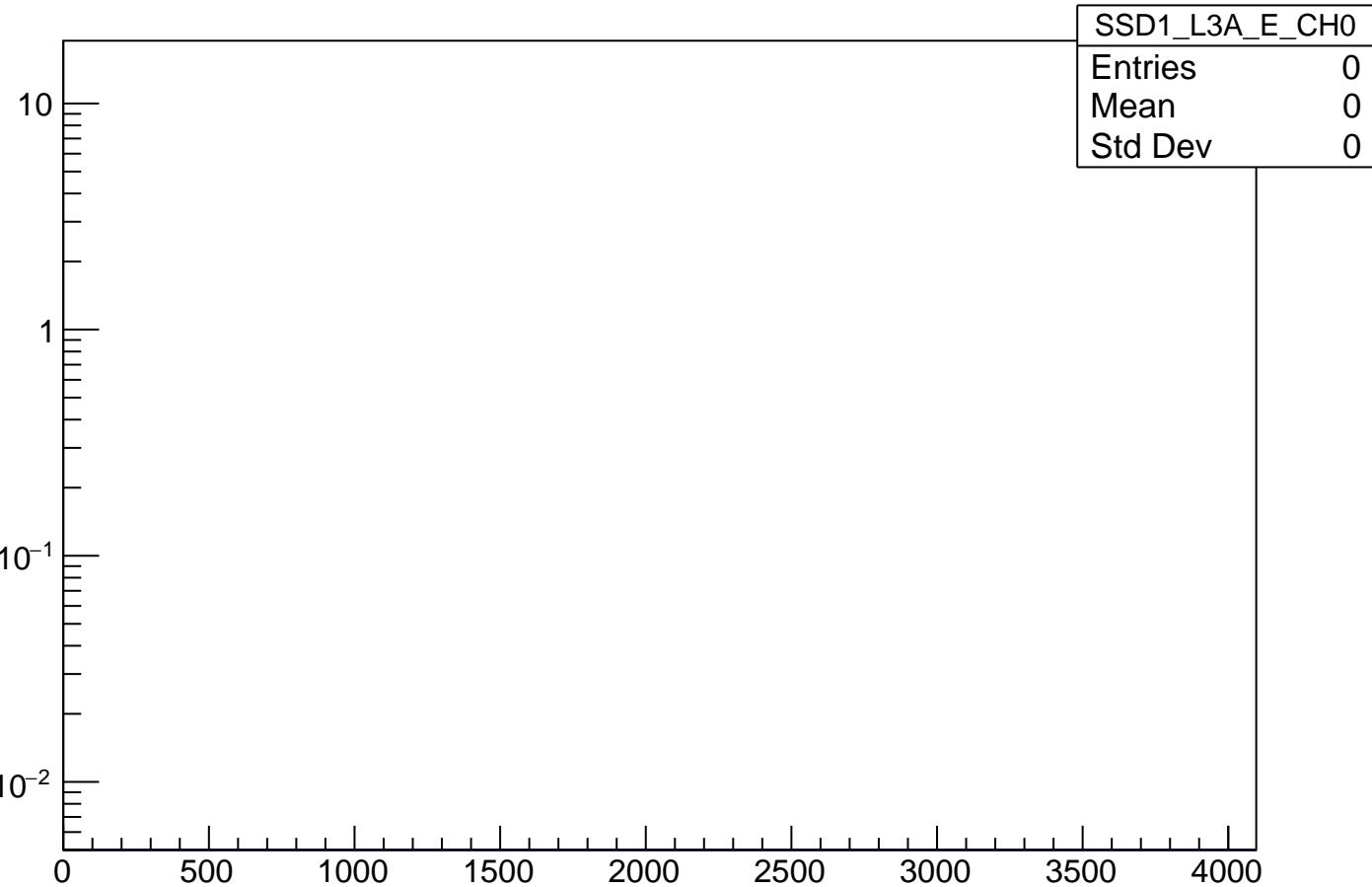
SSD1_L2B_E_CH14



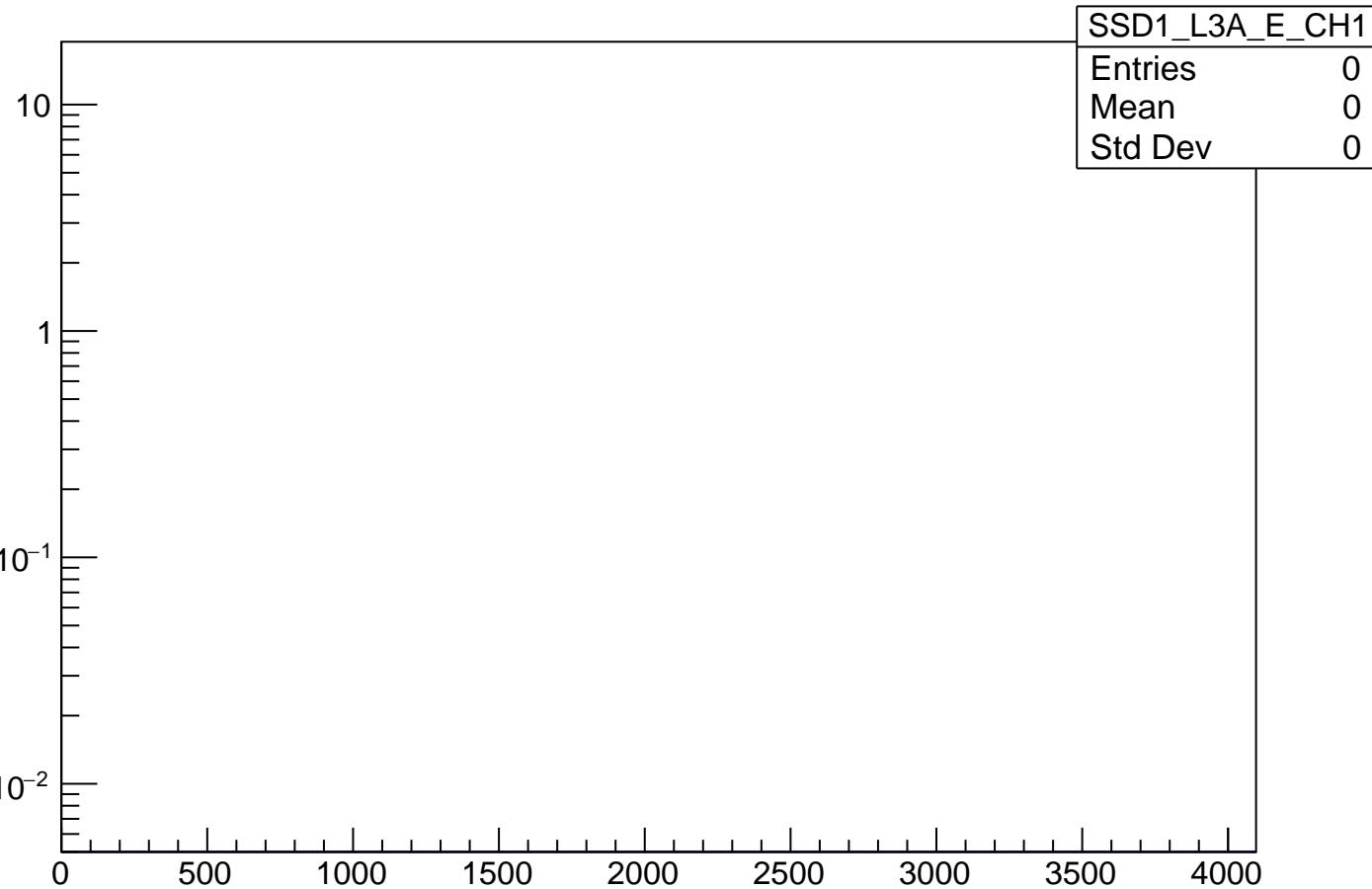
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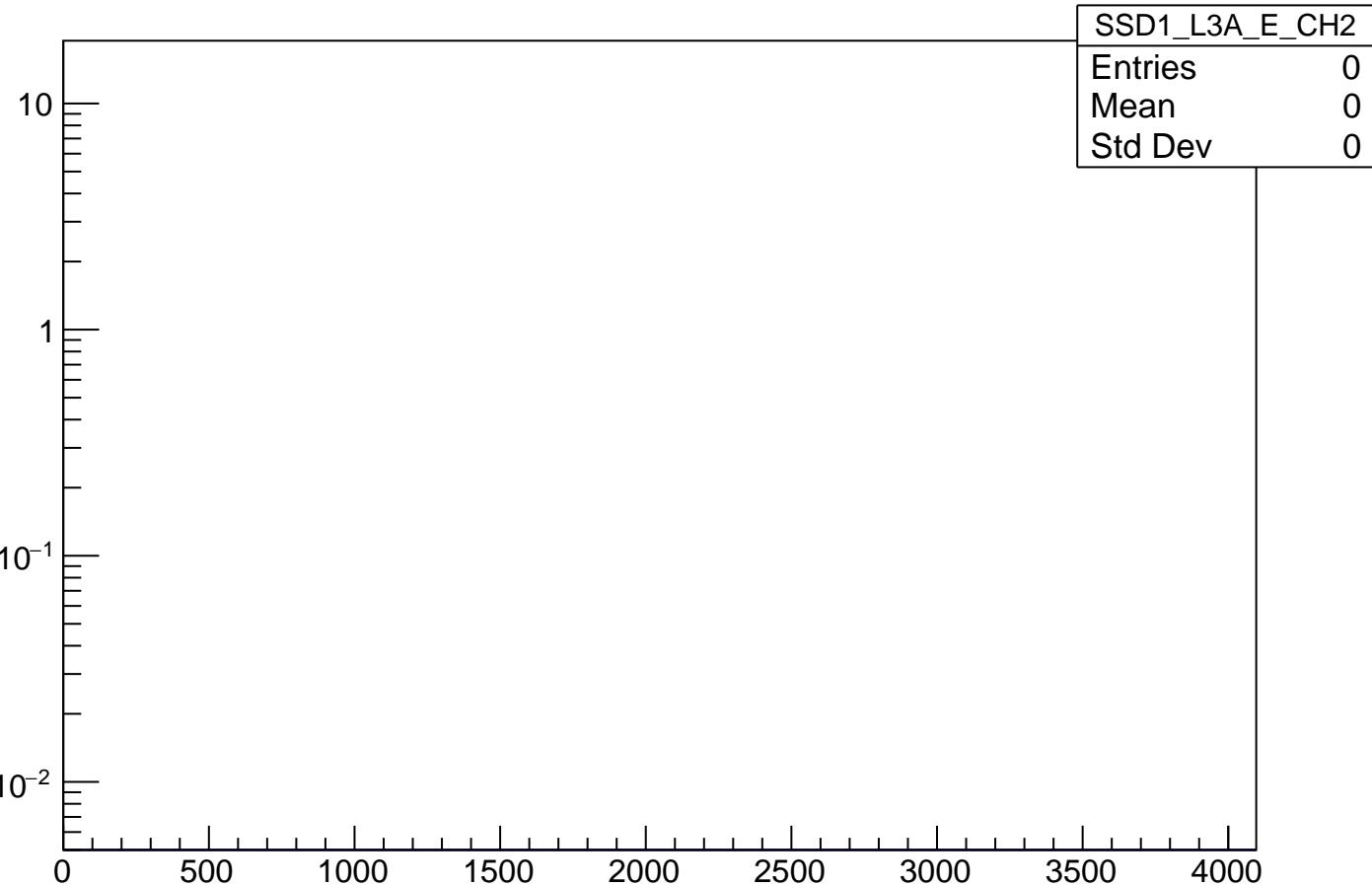
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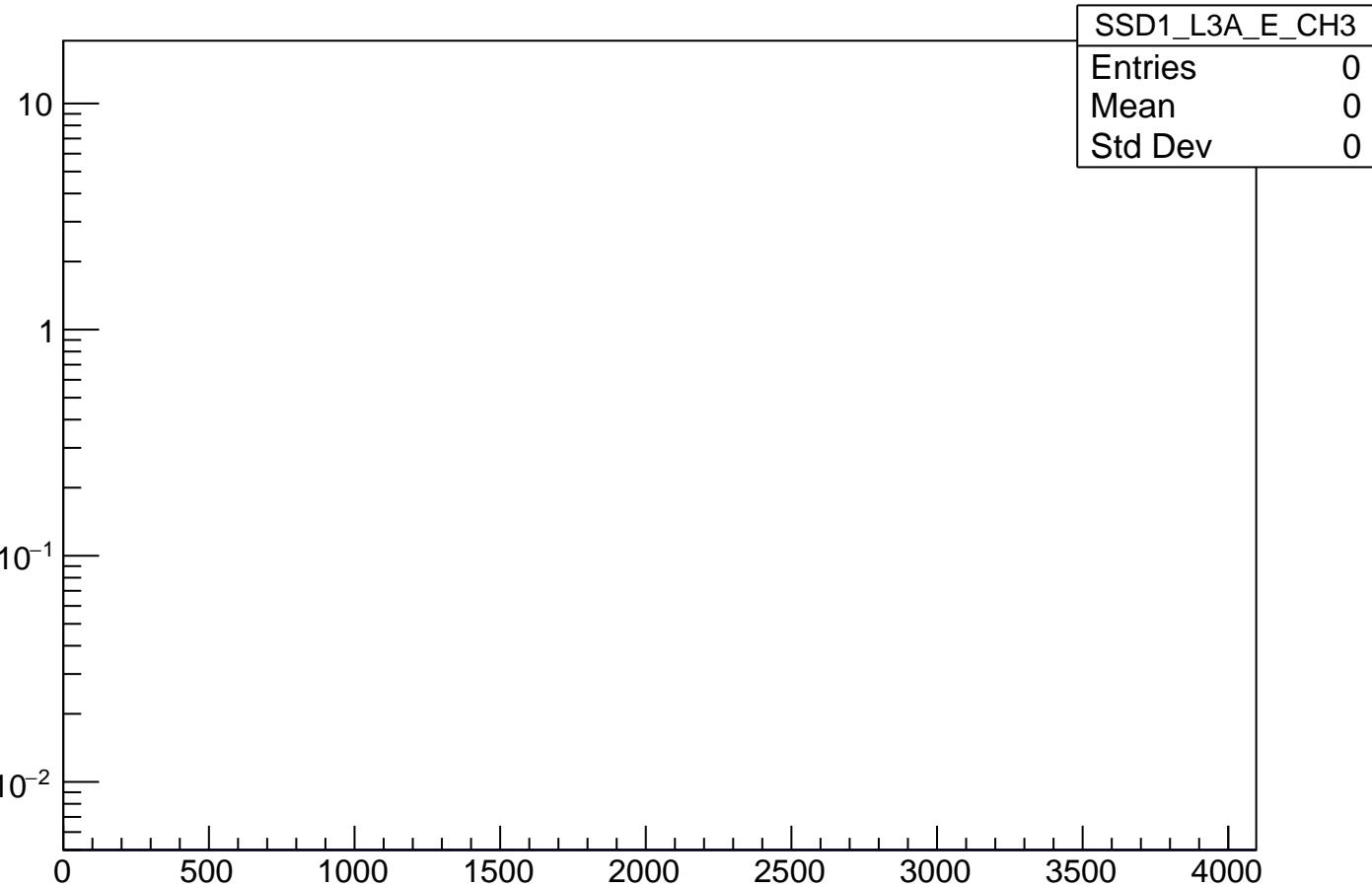
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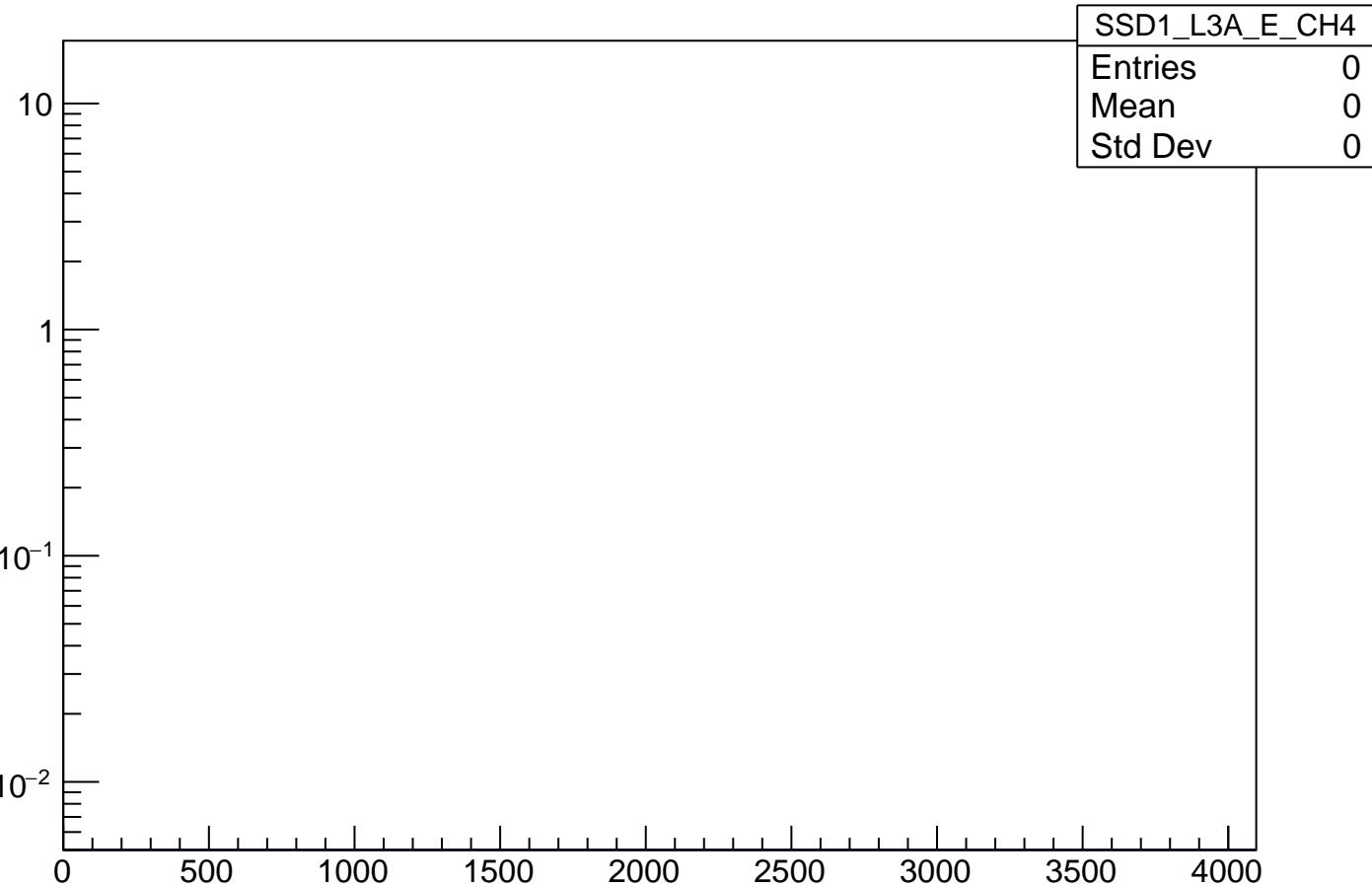
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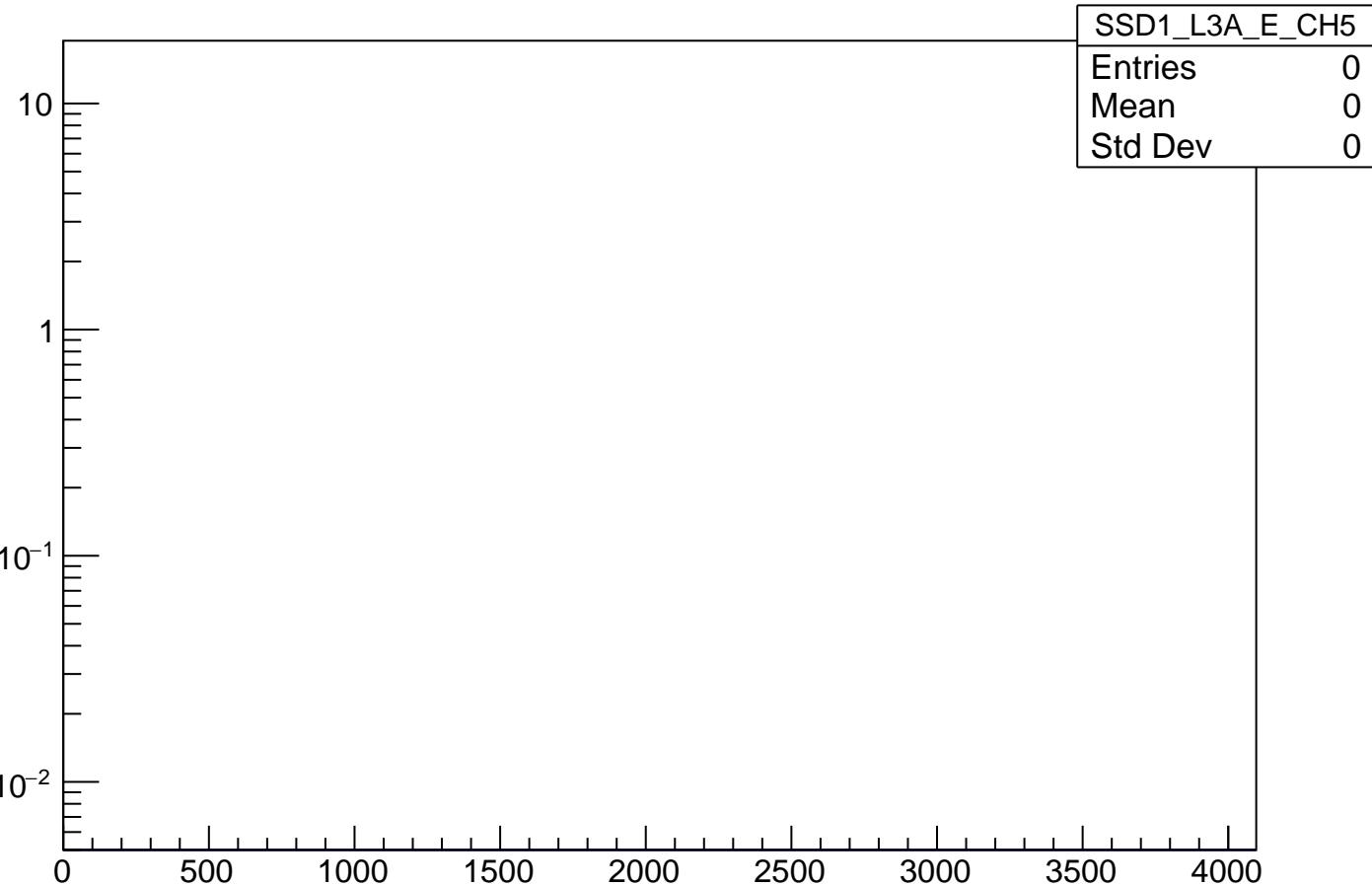
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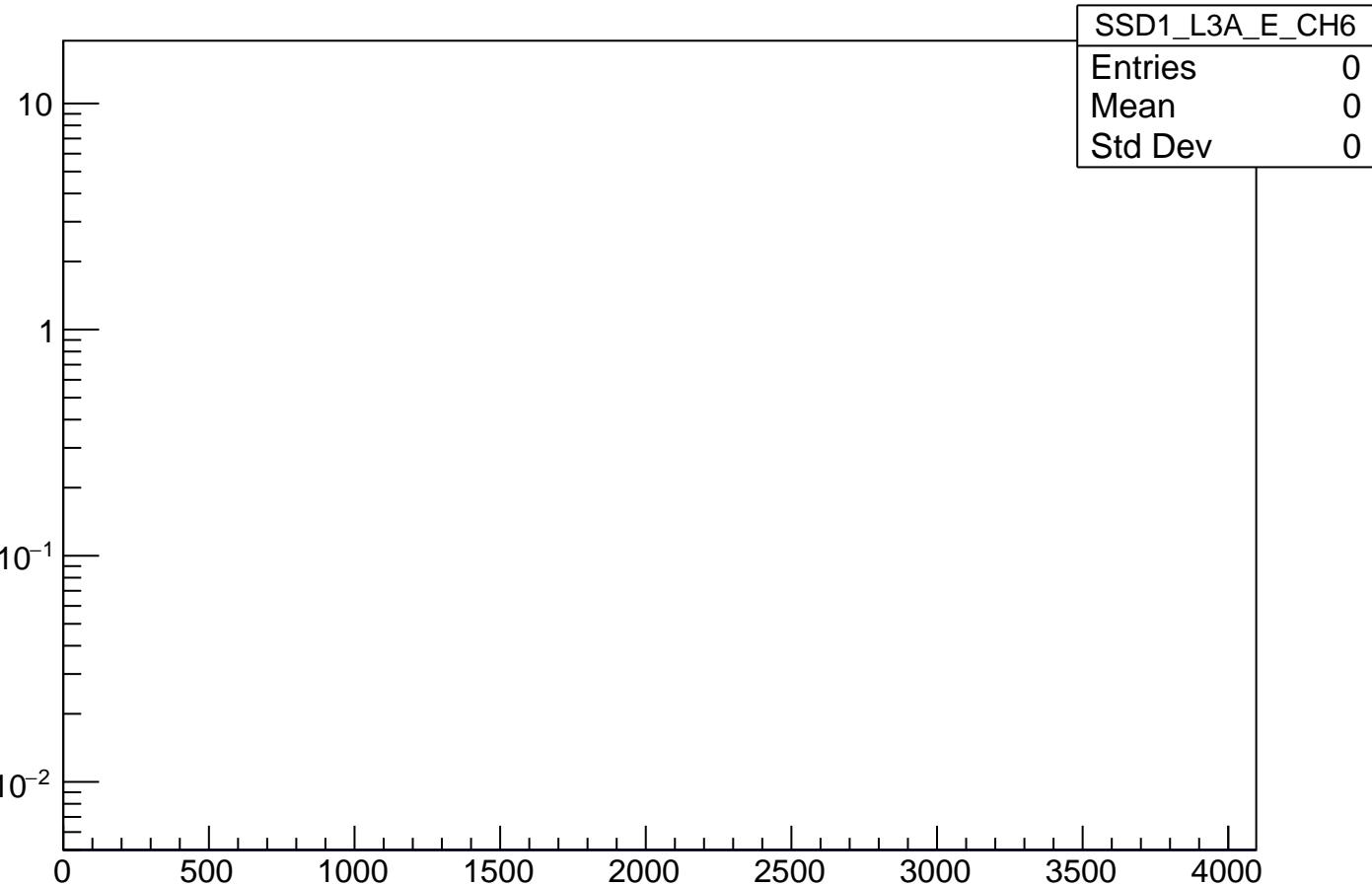
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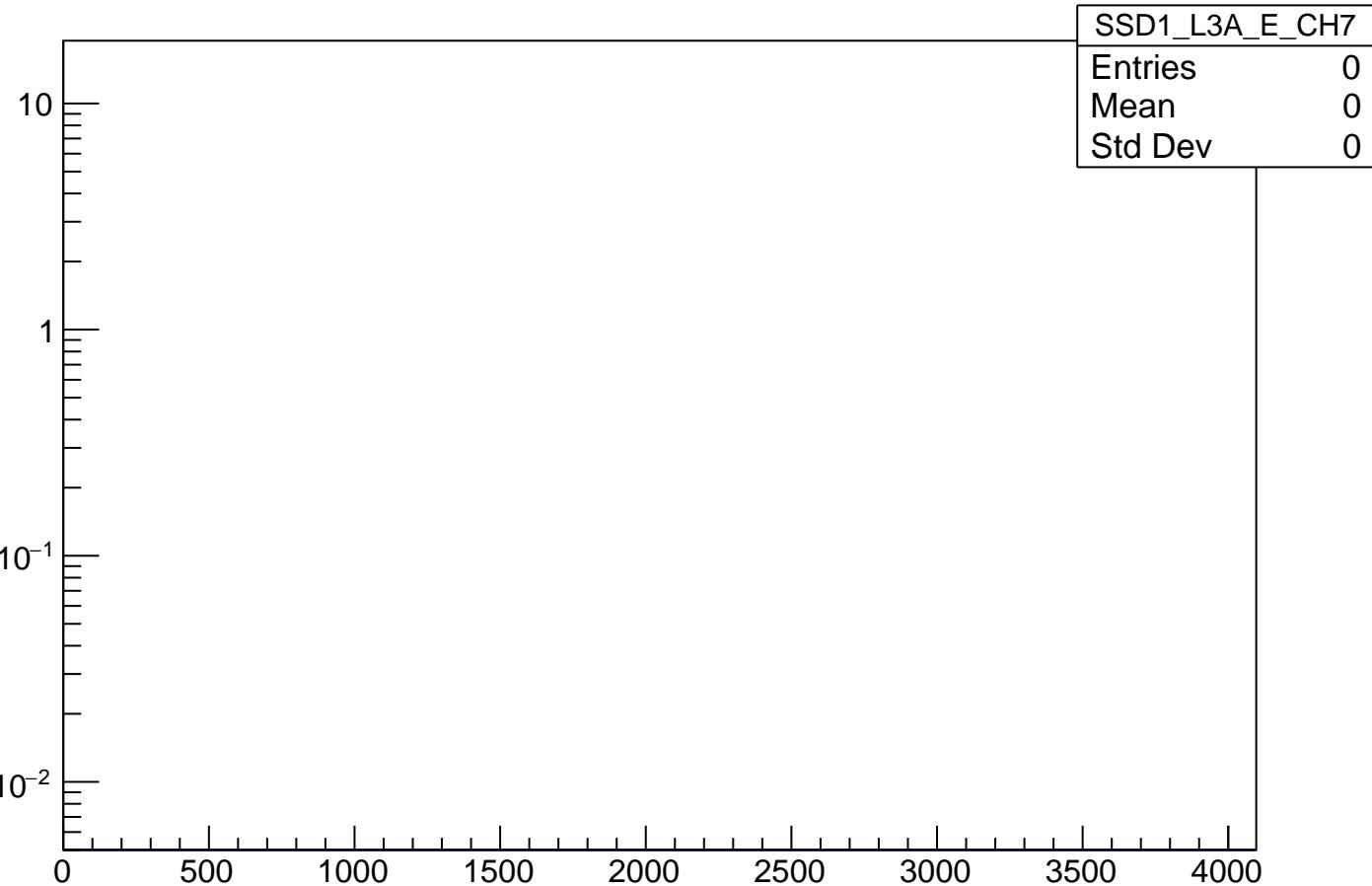
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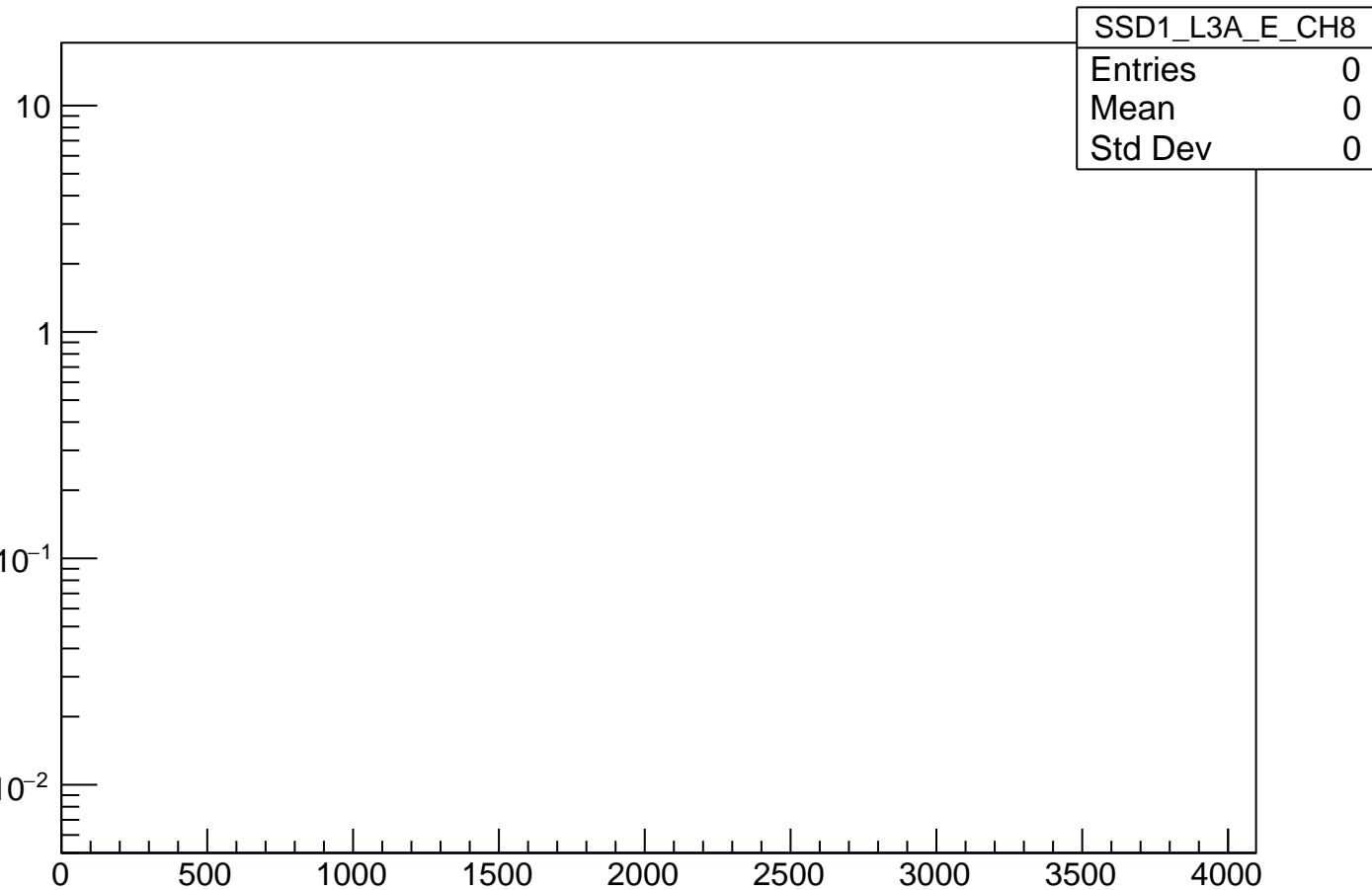
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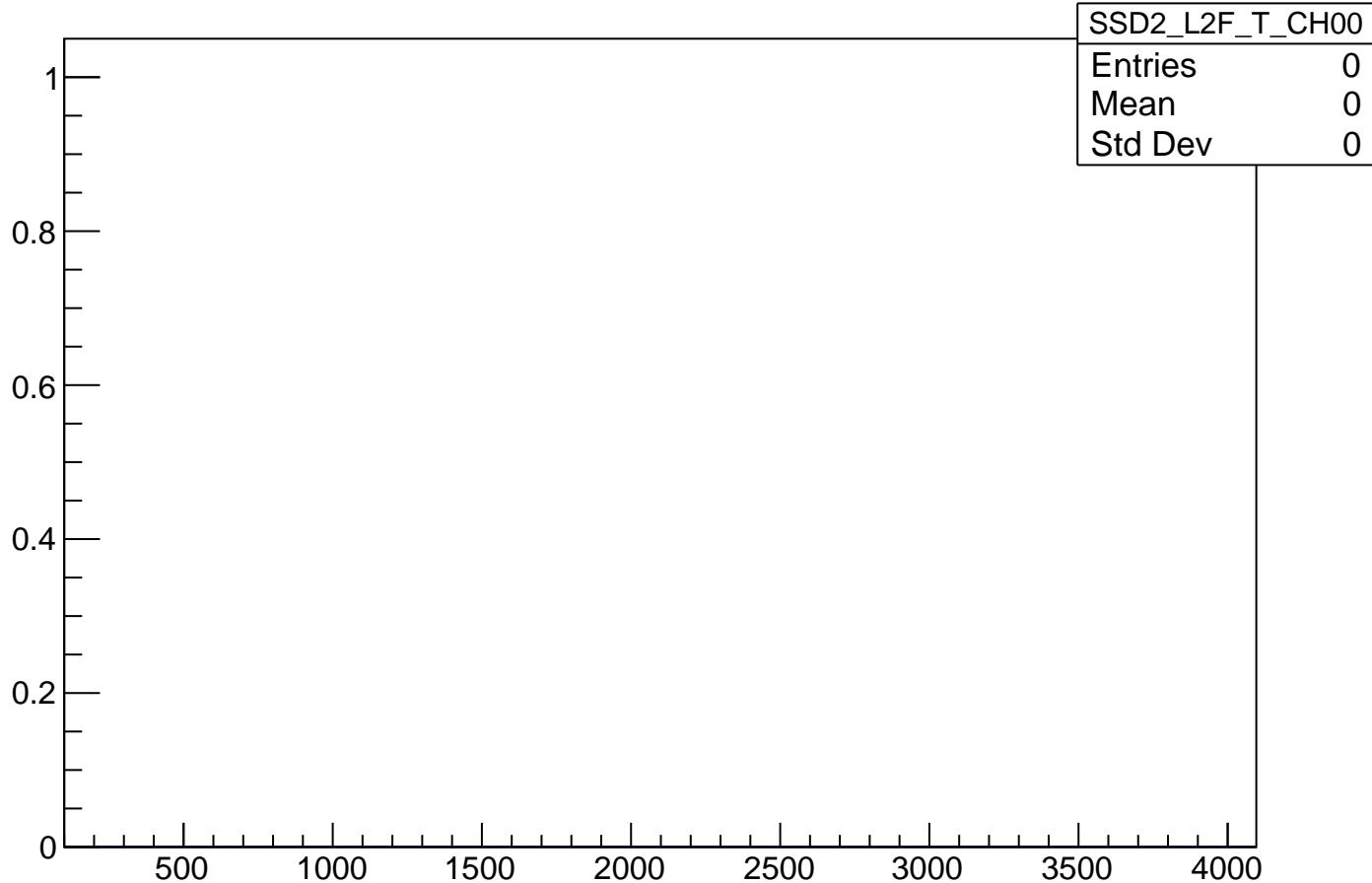
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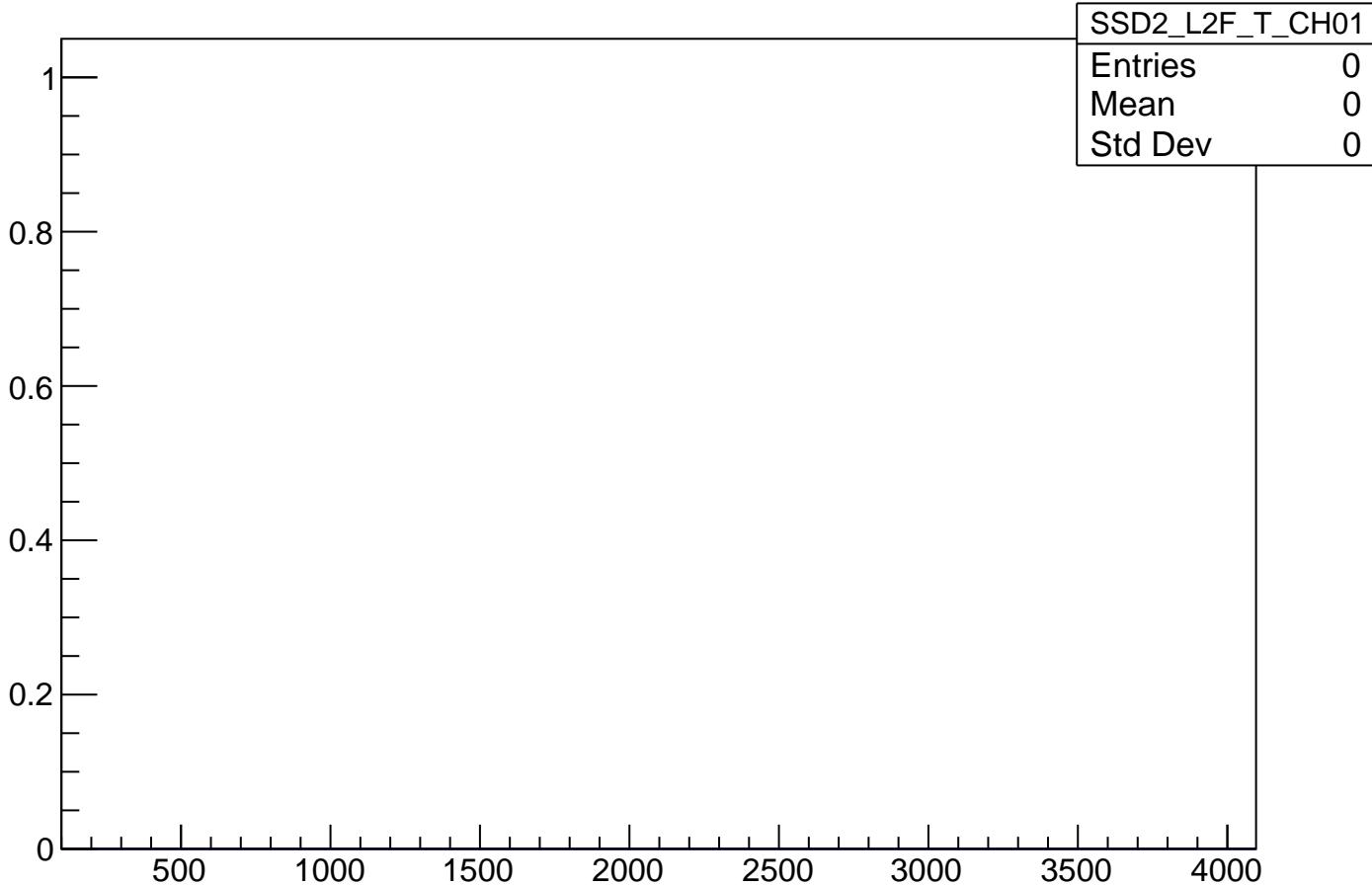
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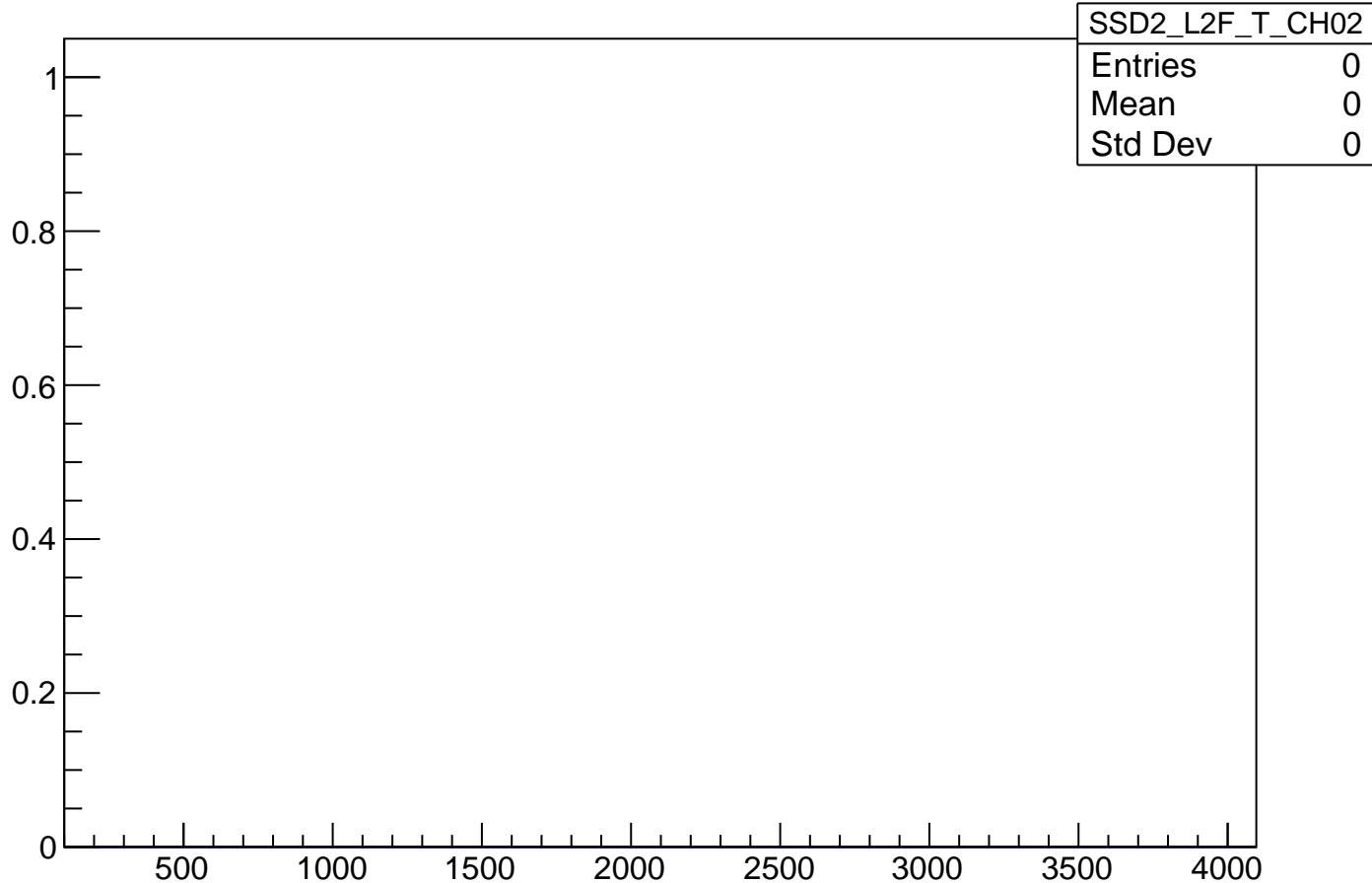
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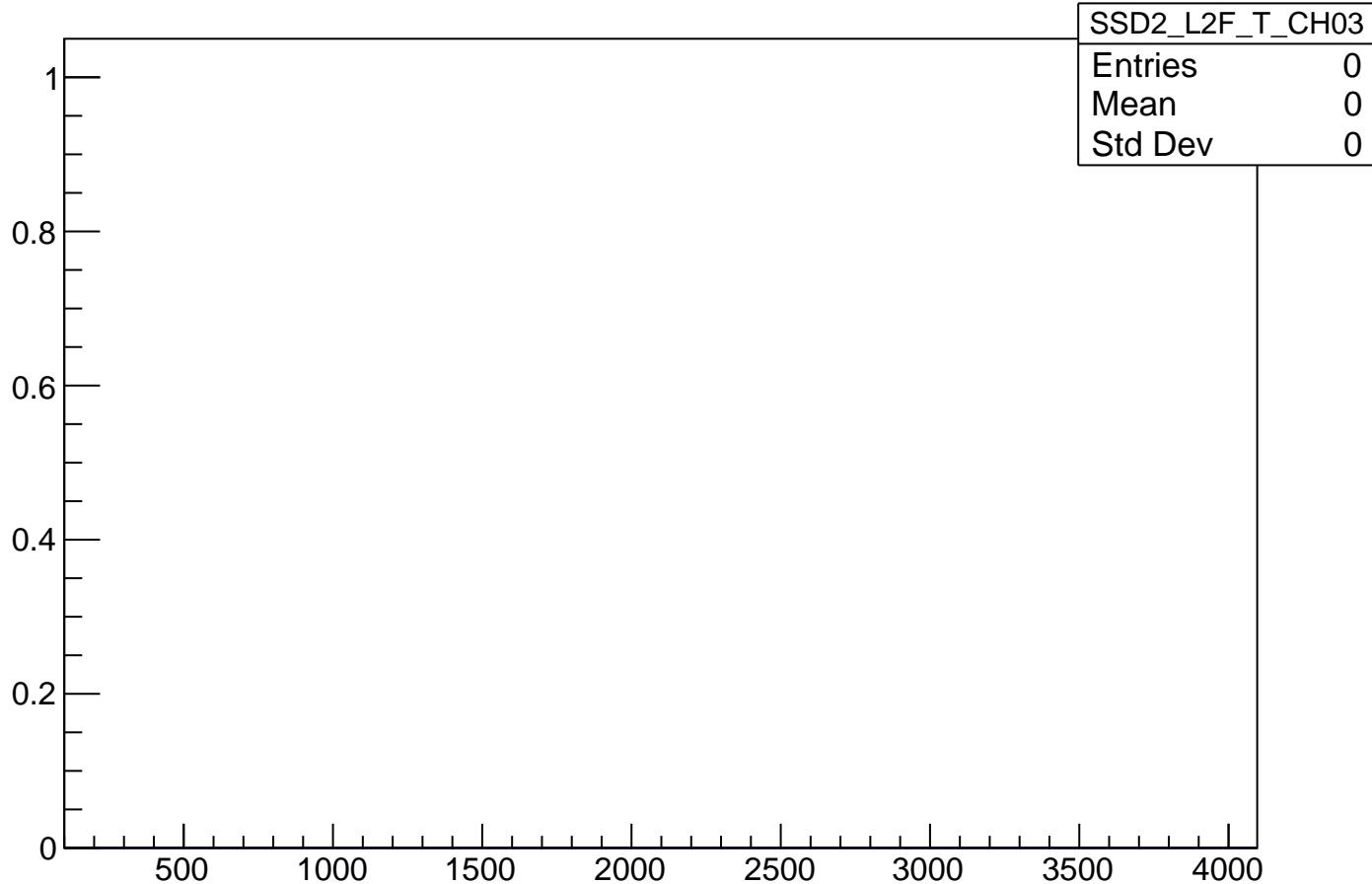
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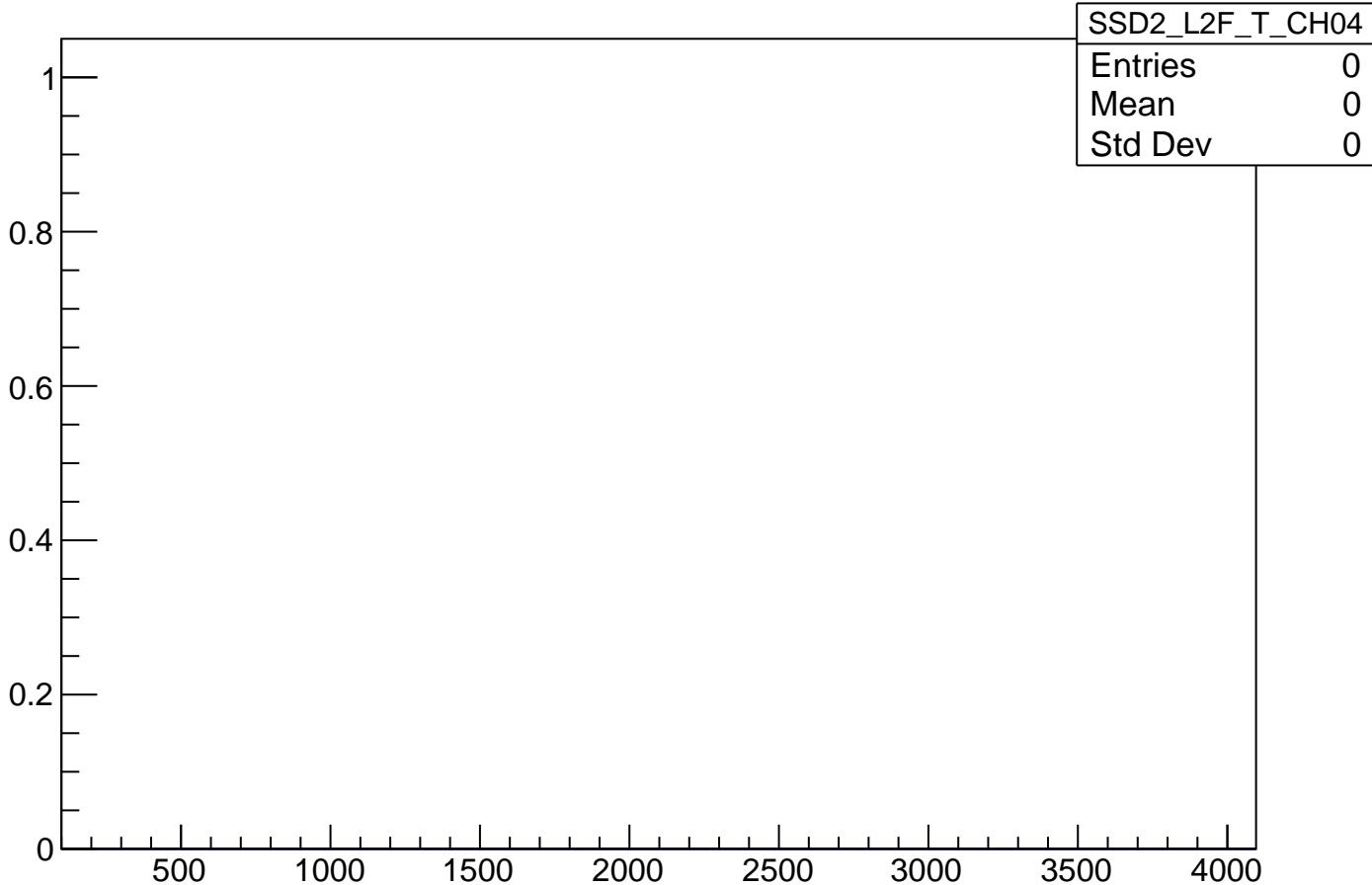
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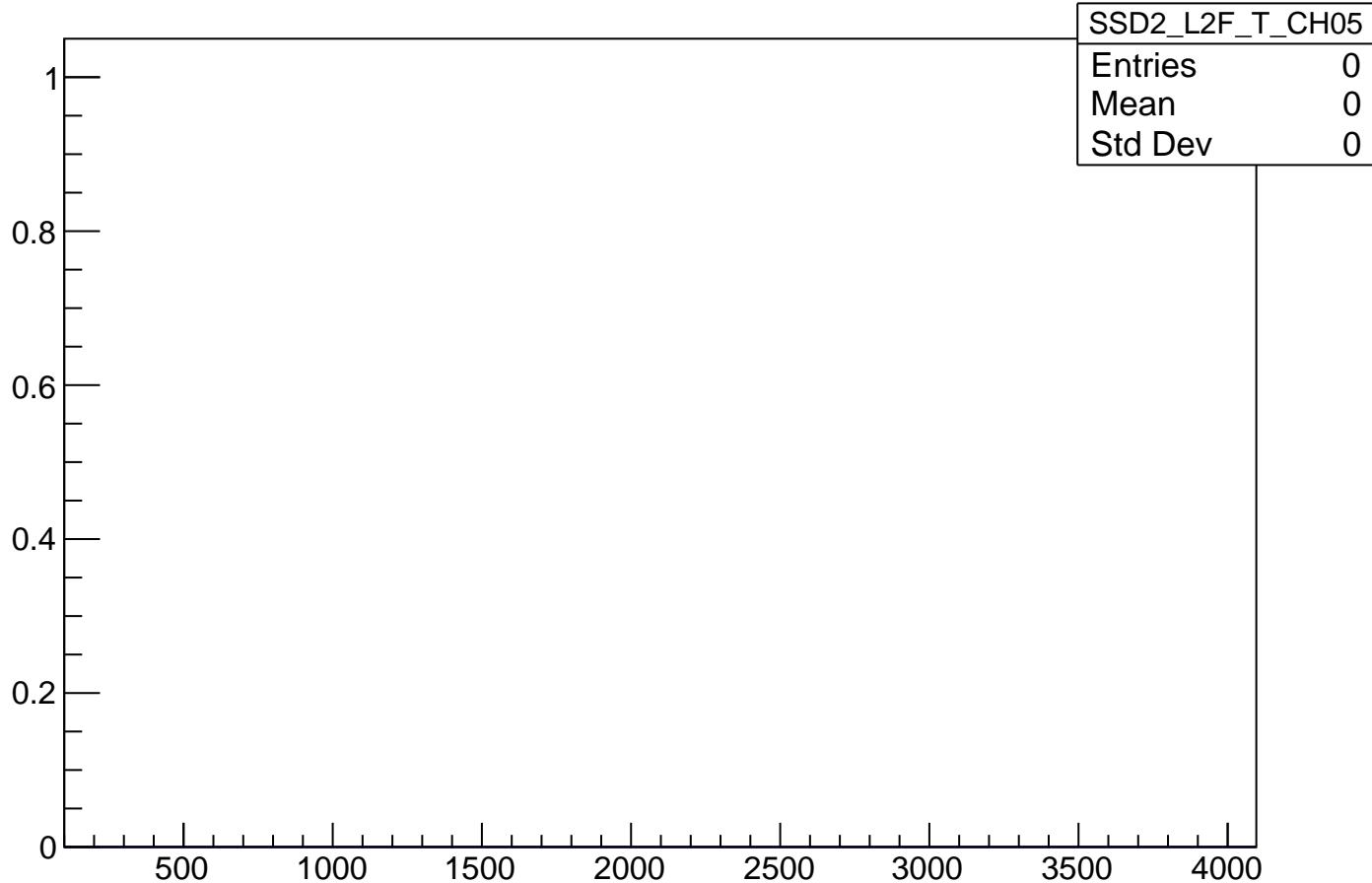
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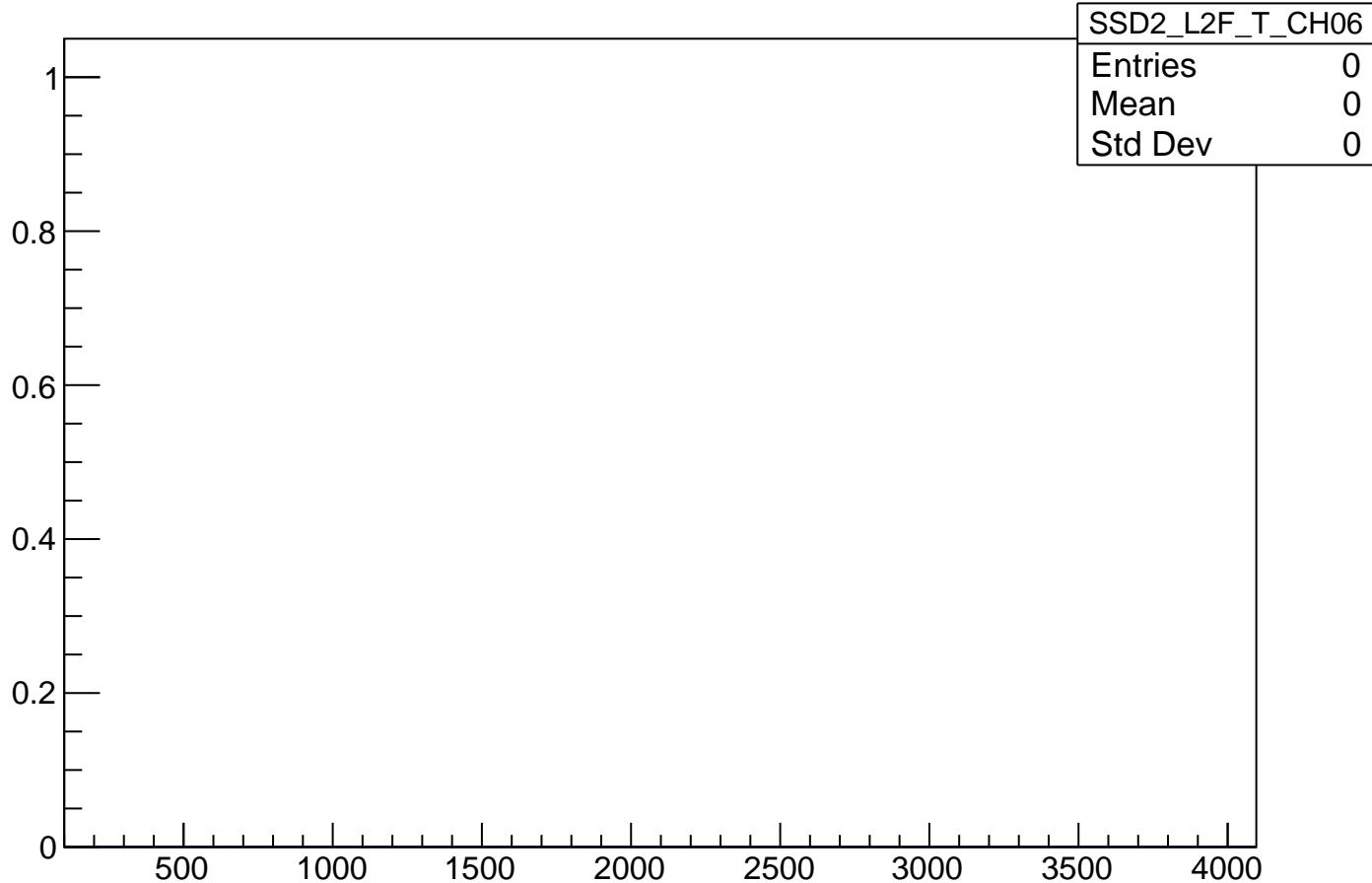
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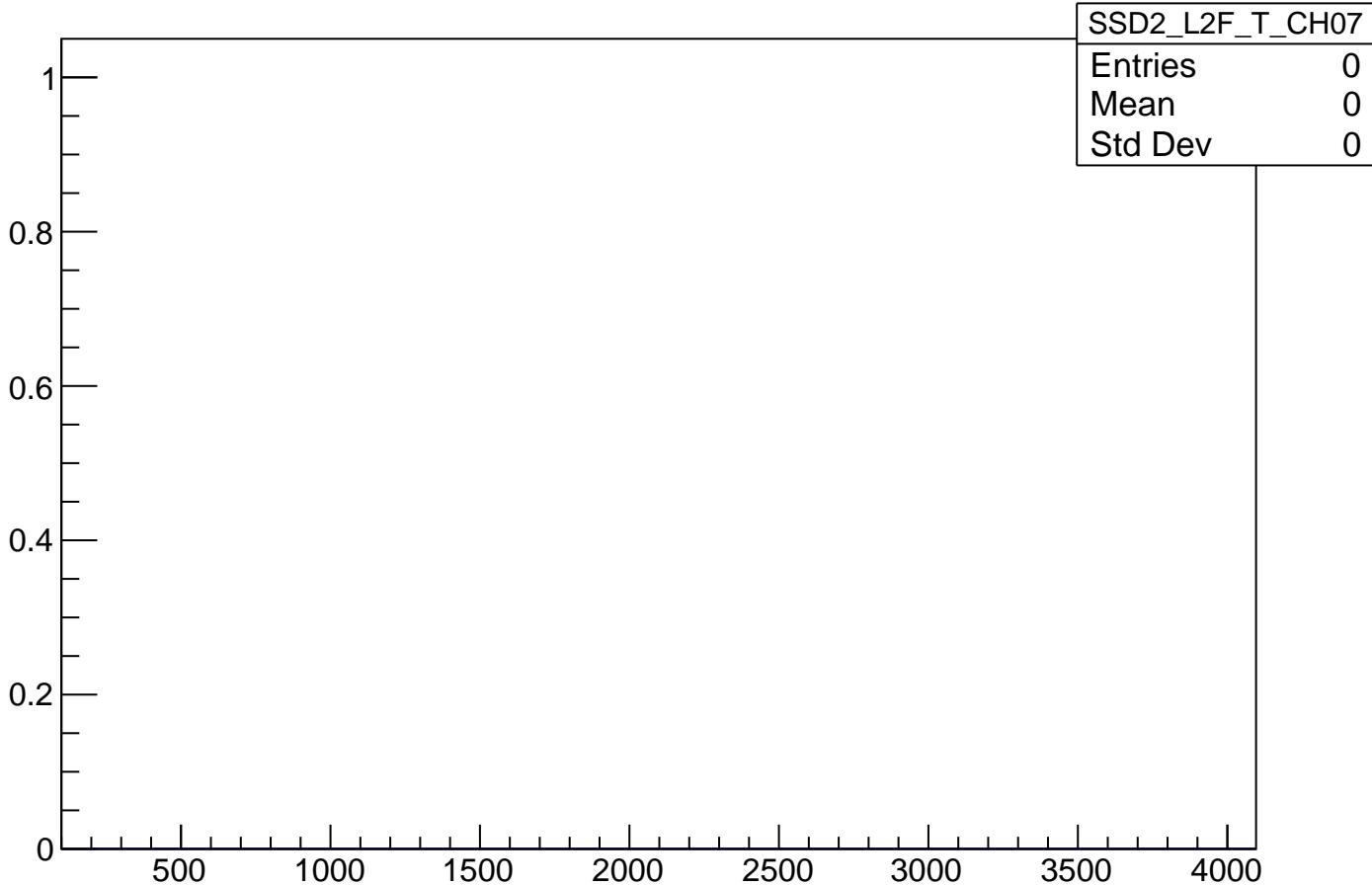
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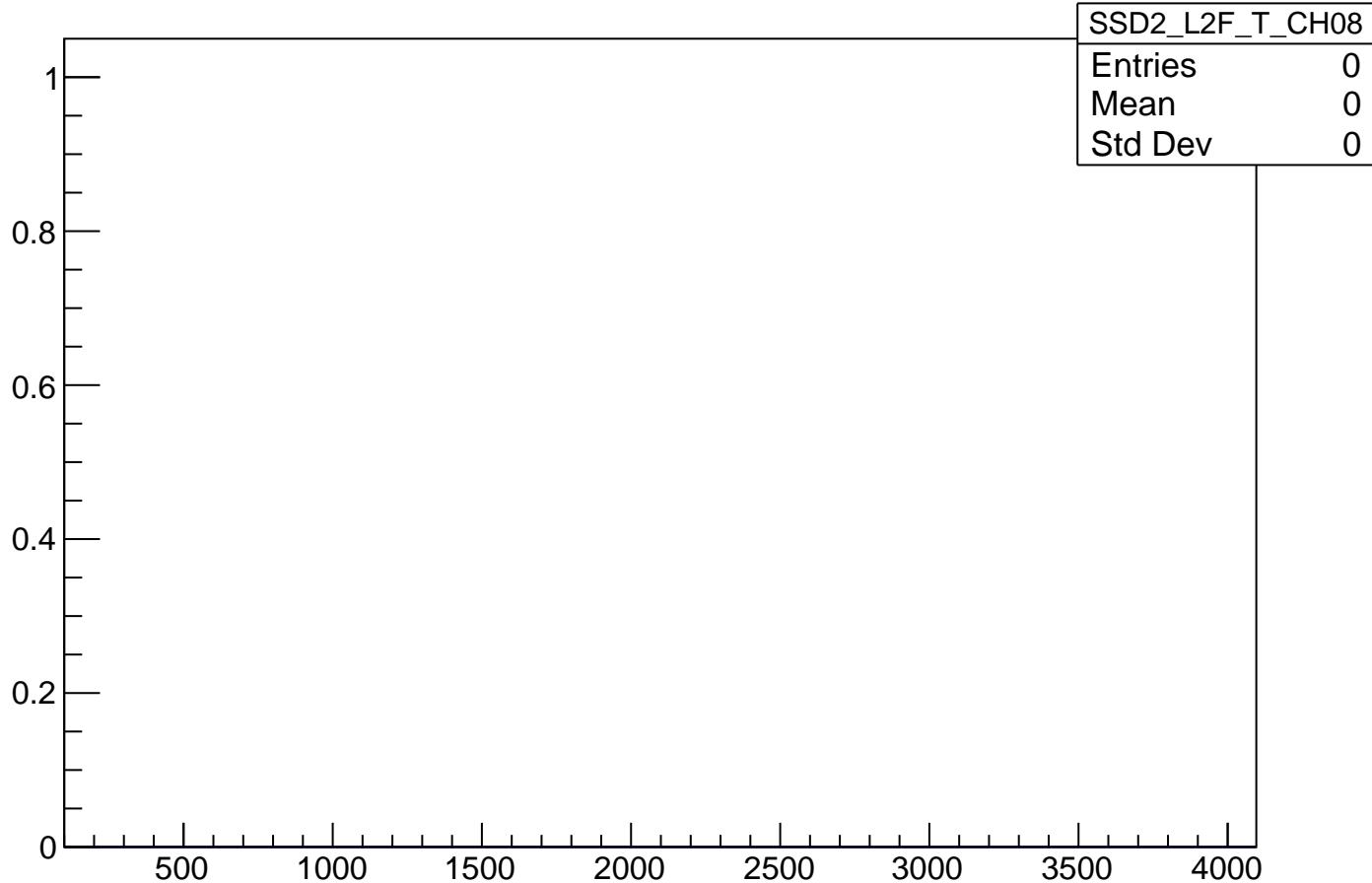
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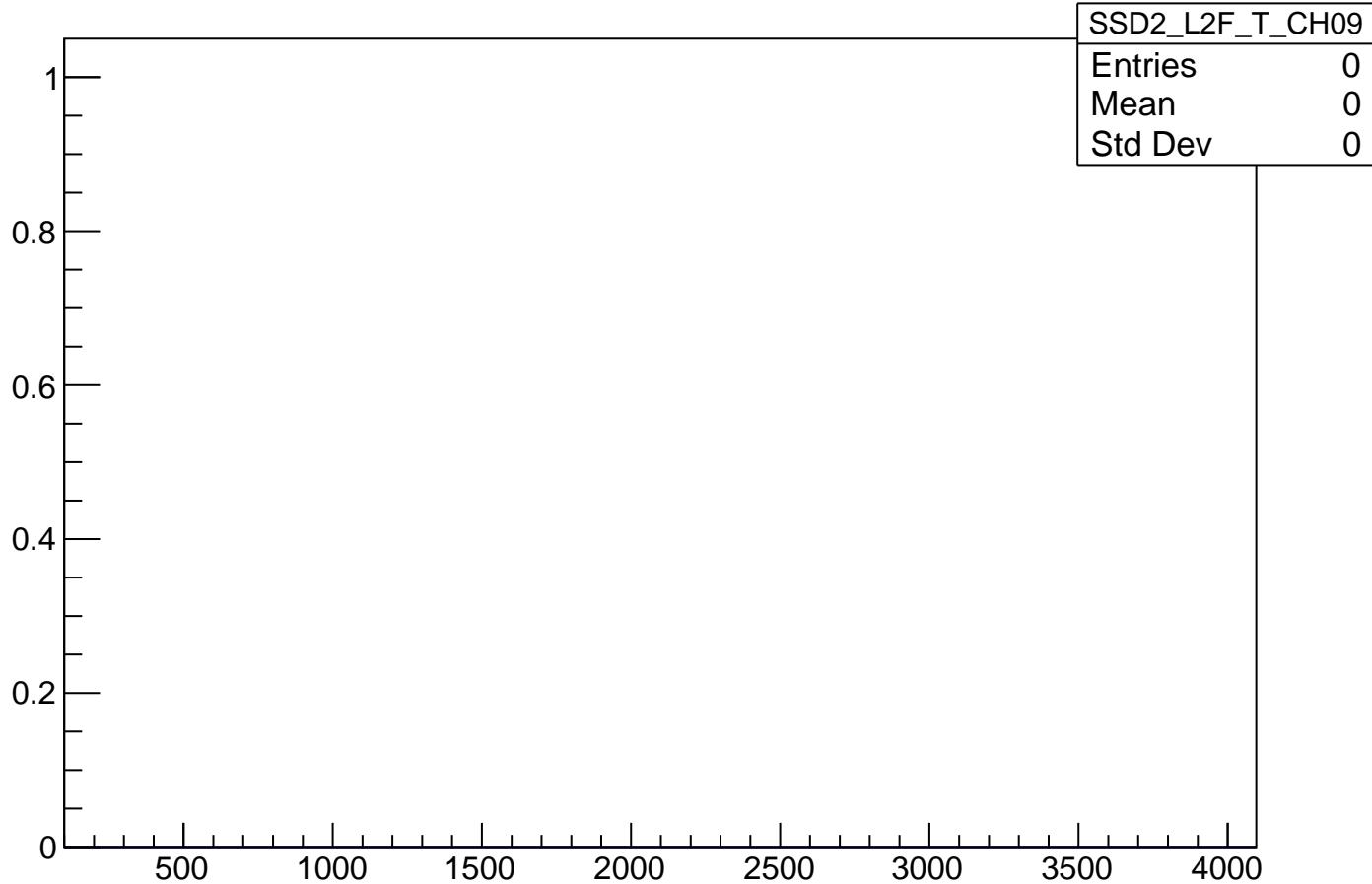
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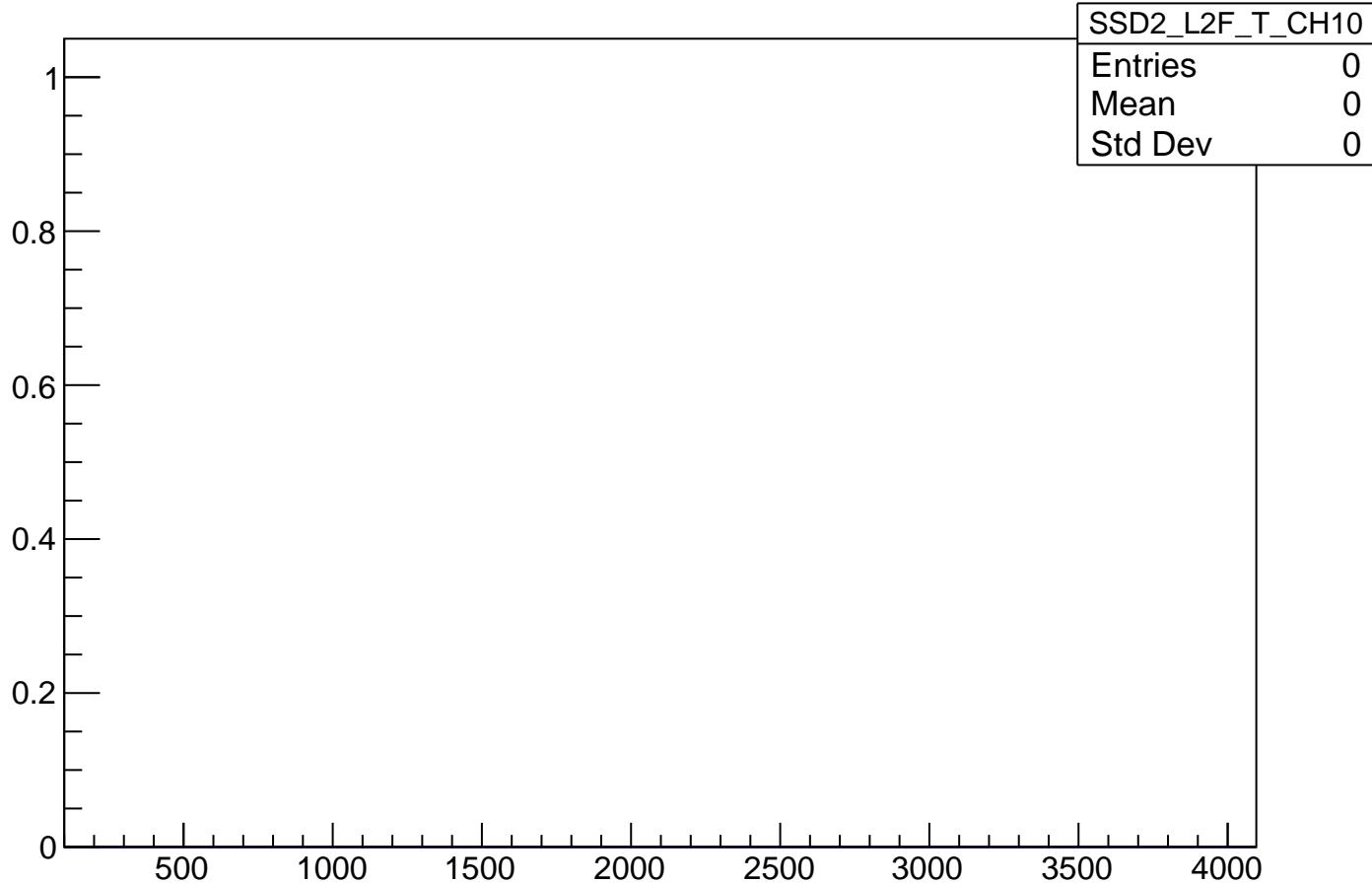
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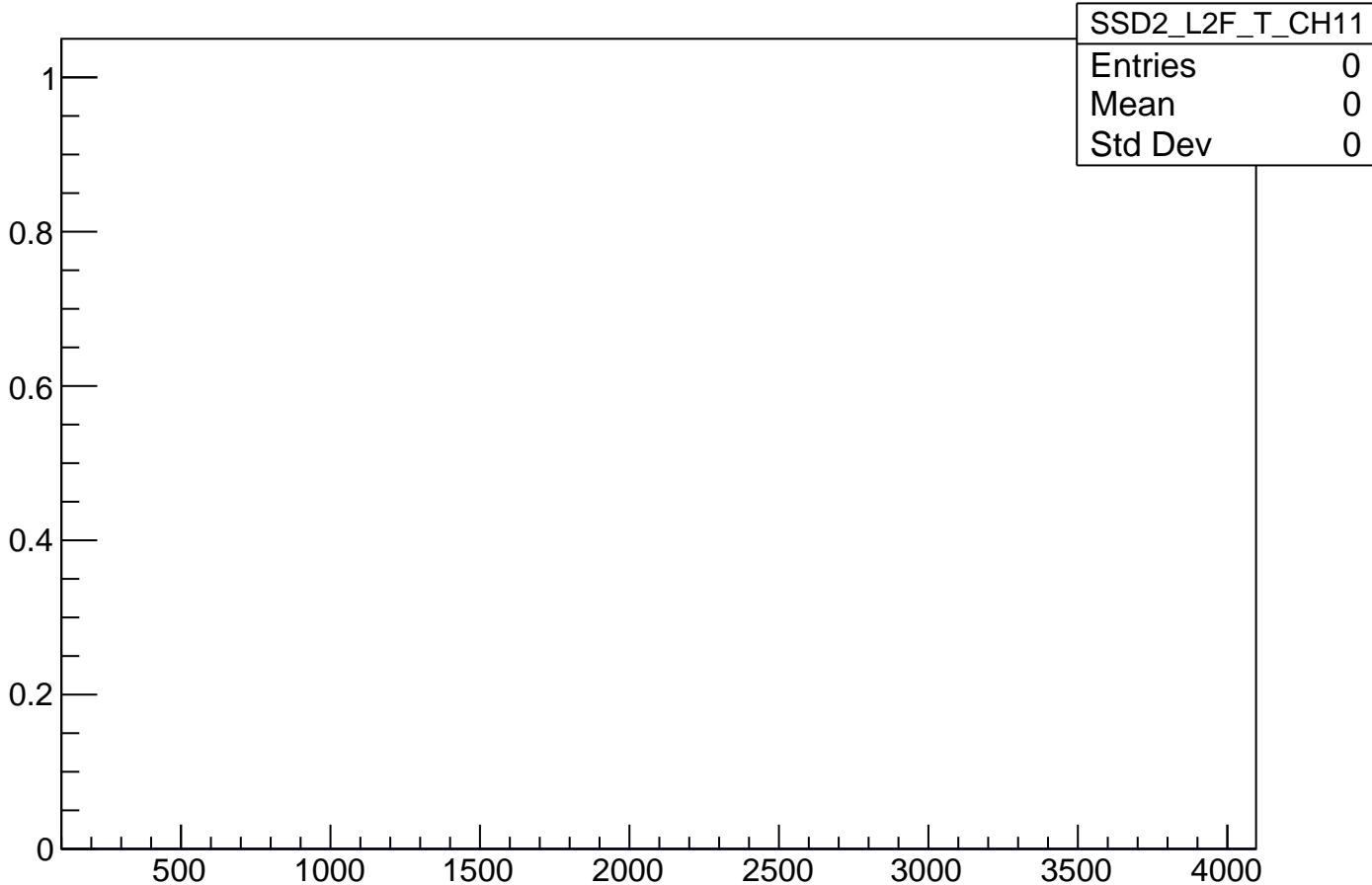
SSD2_L2F_T_CH09



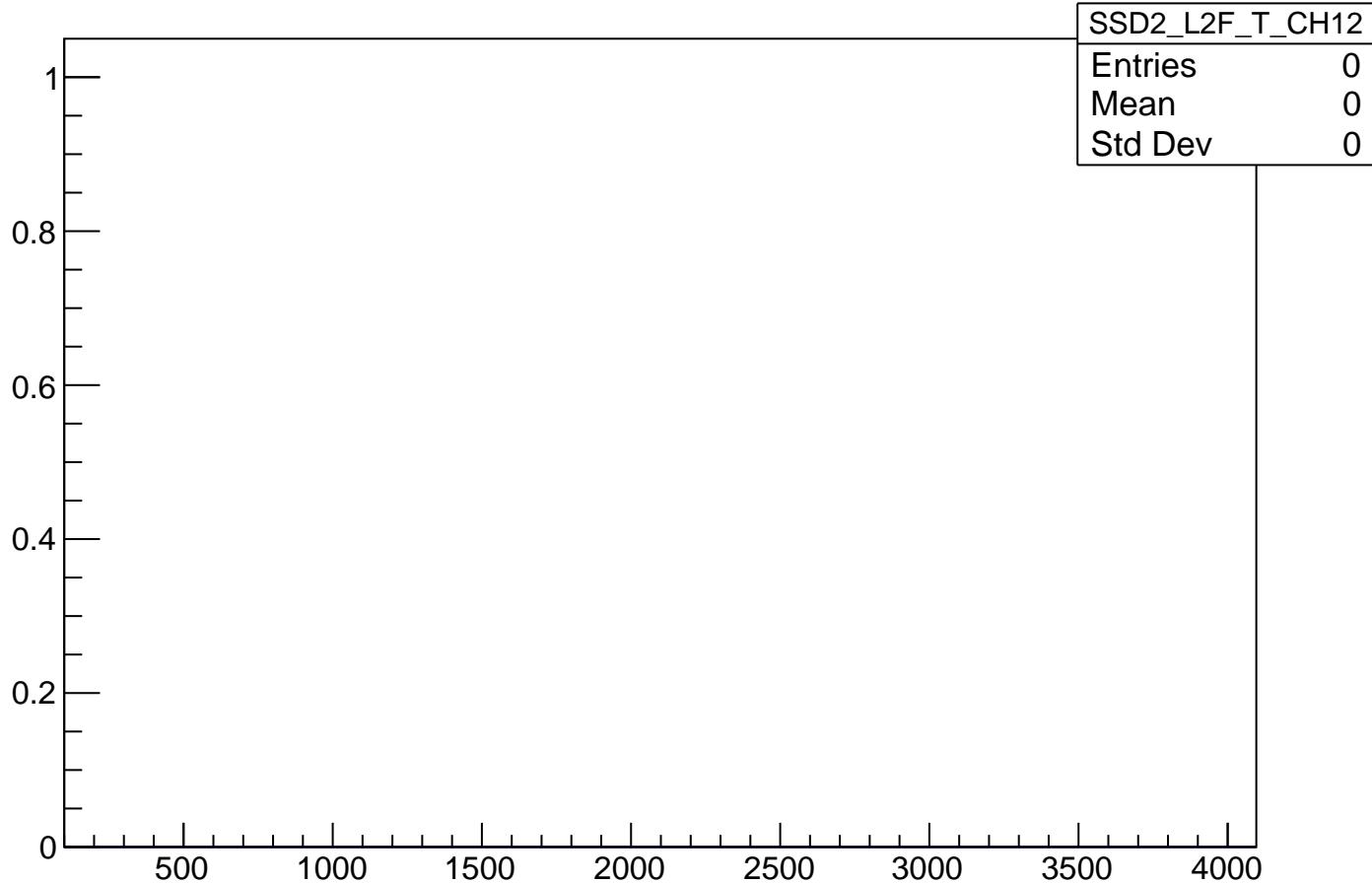
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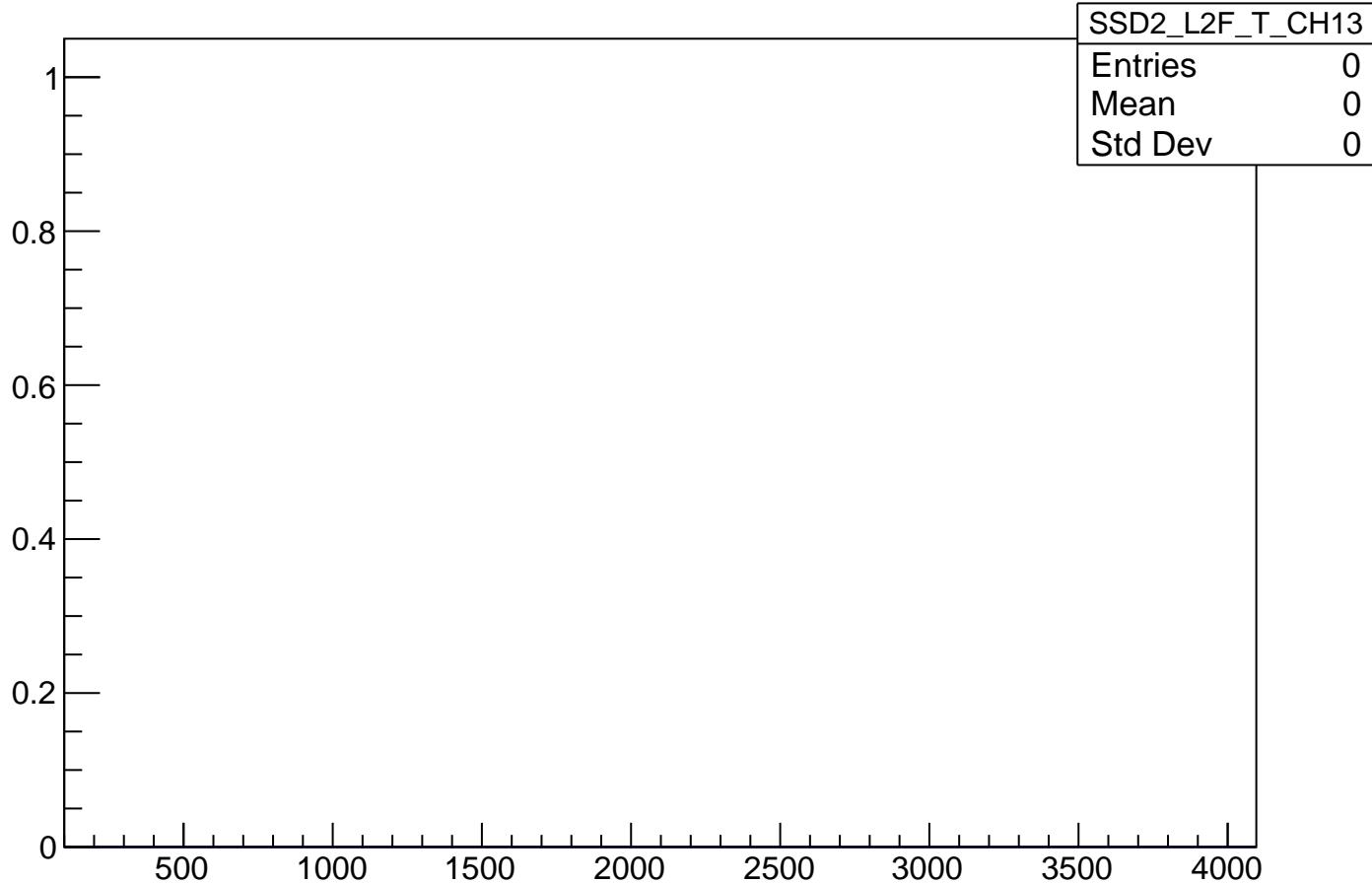
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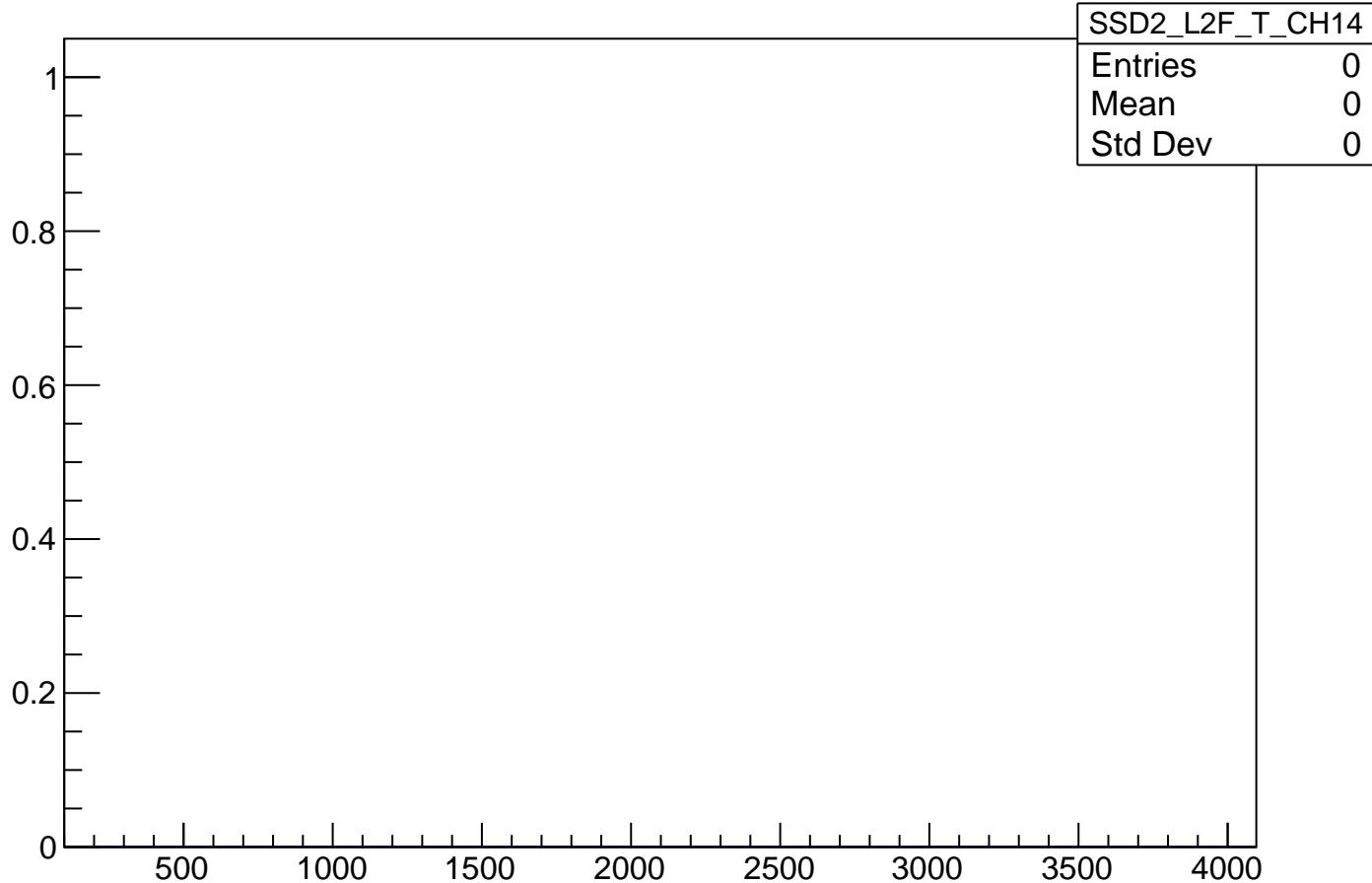
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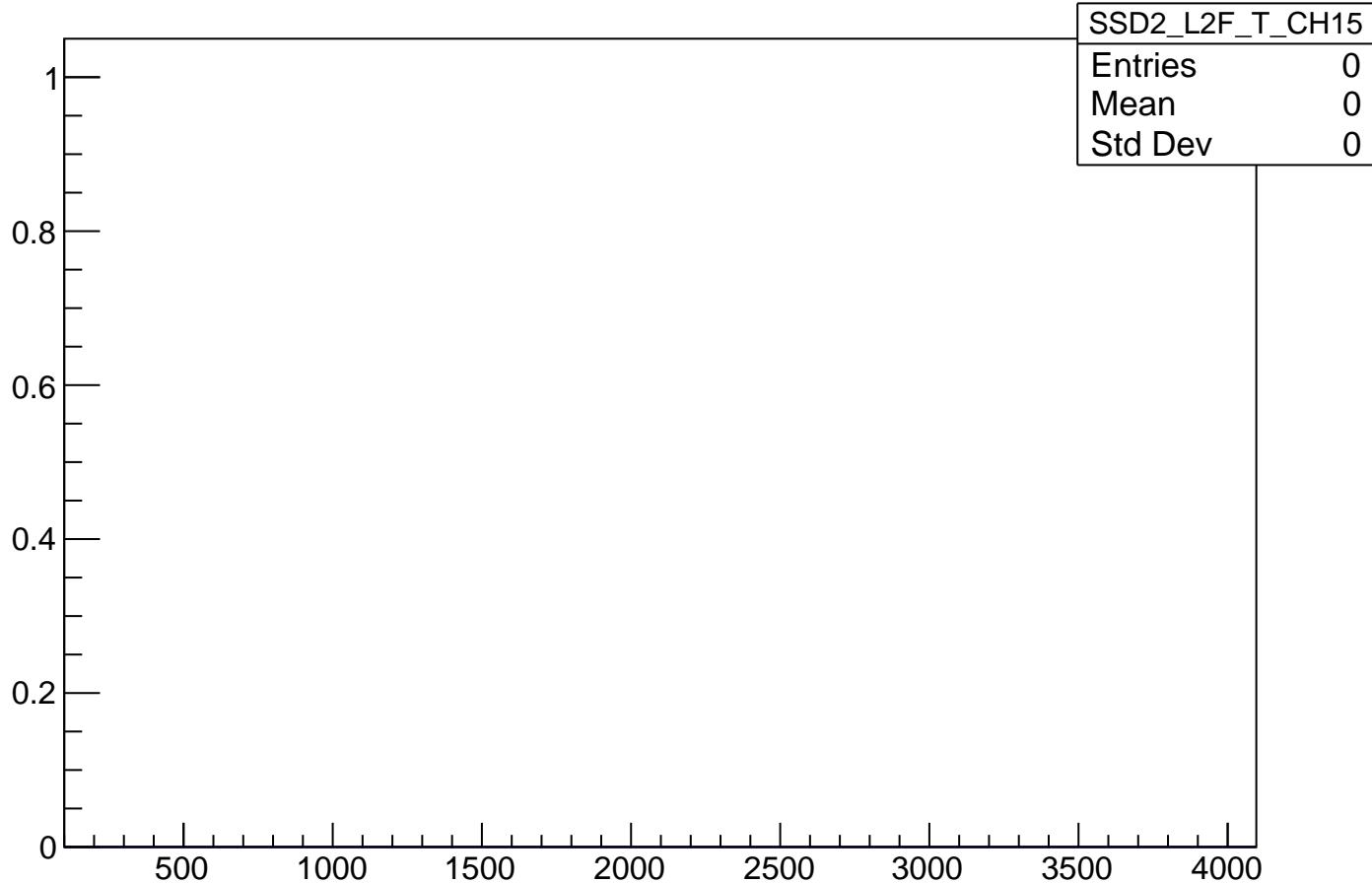
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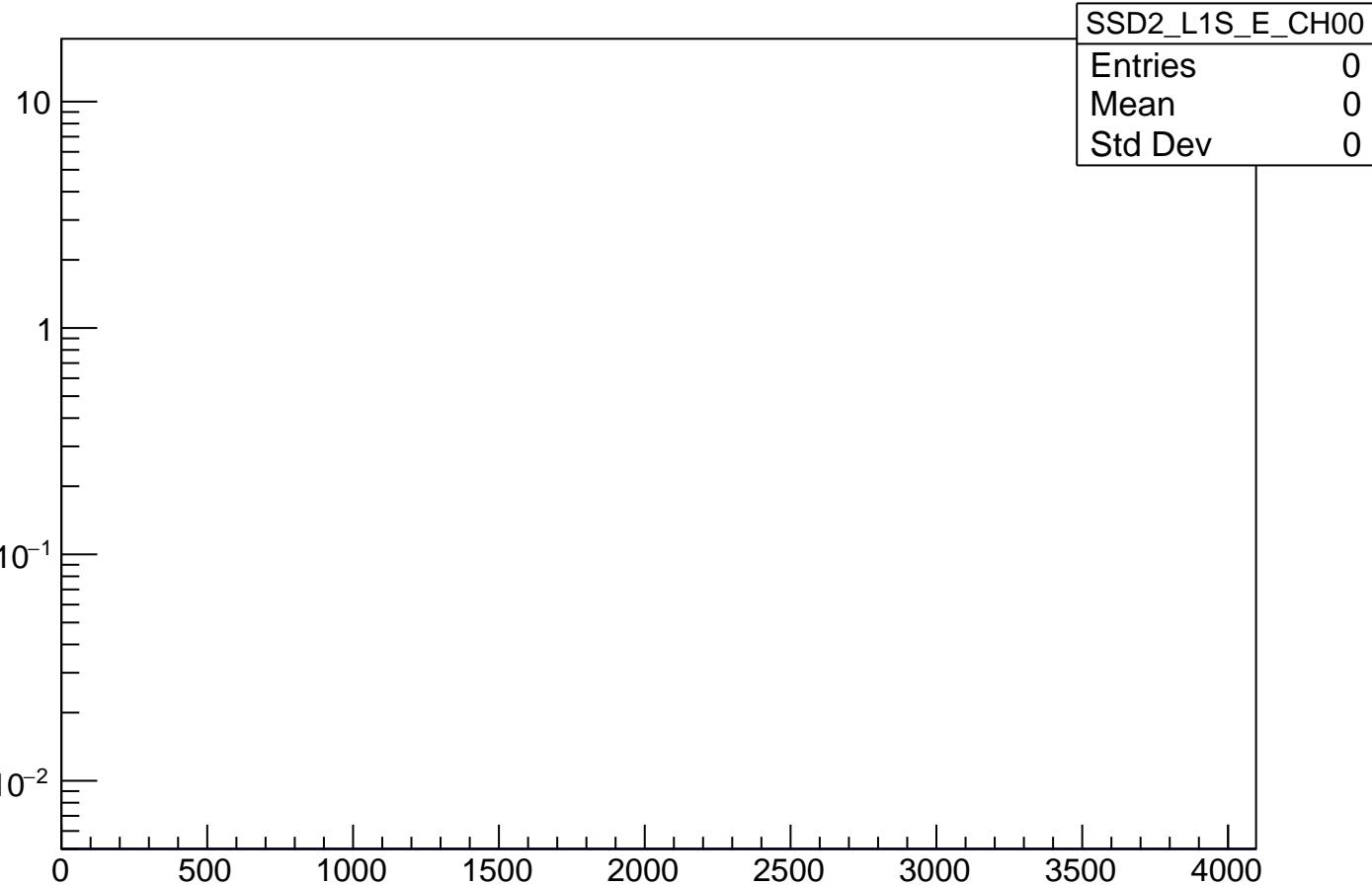
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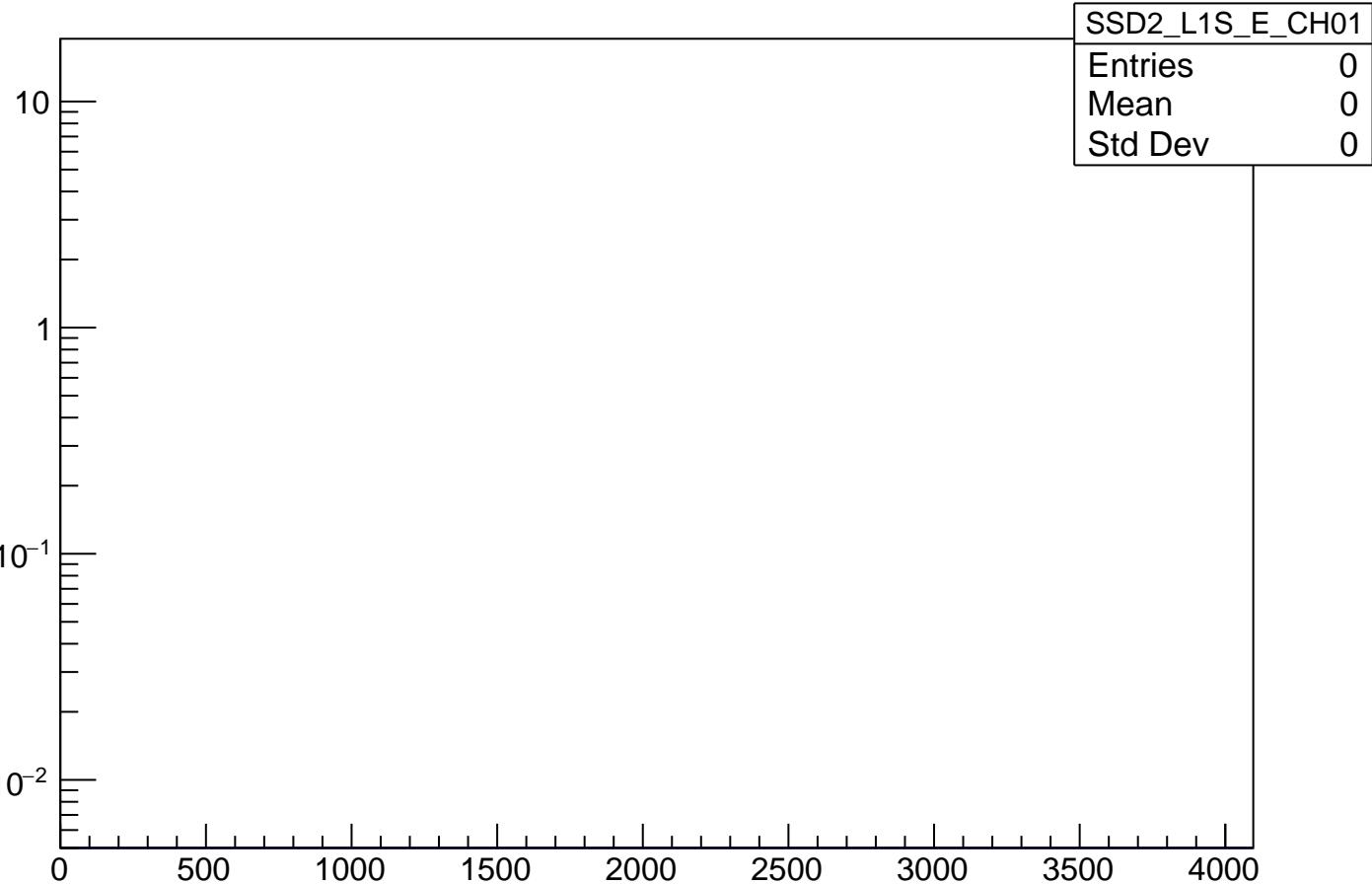
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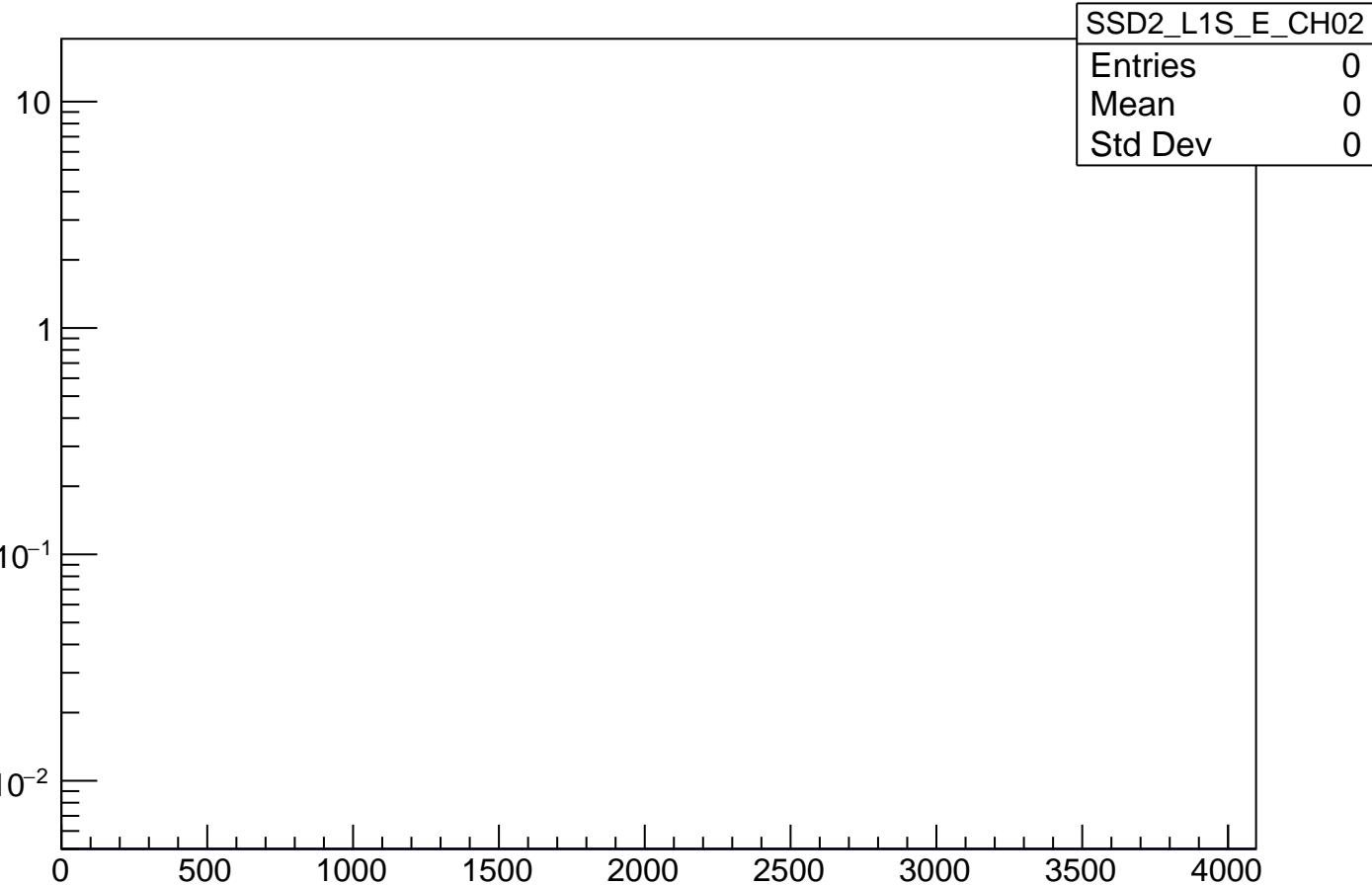
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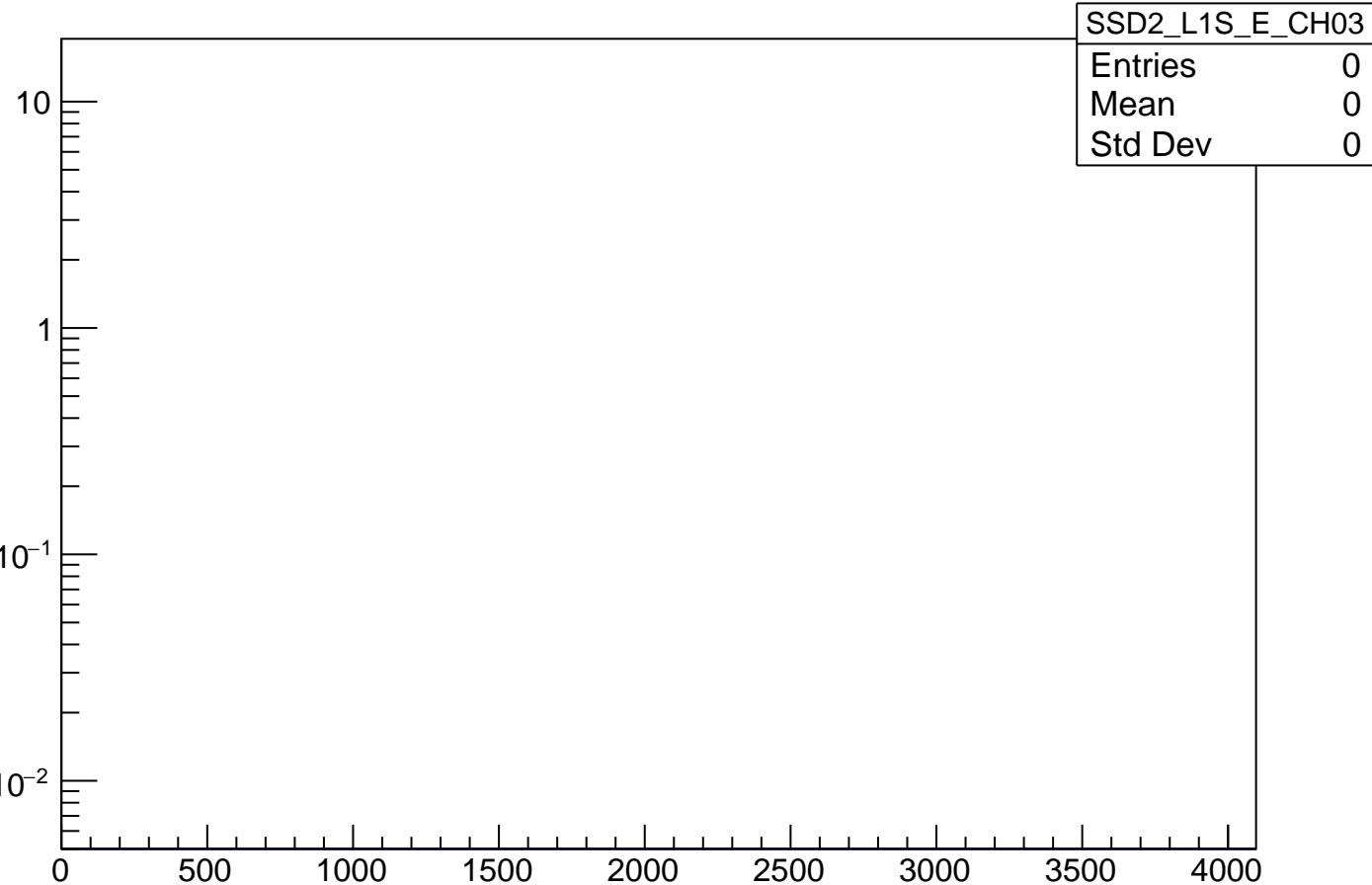
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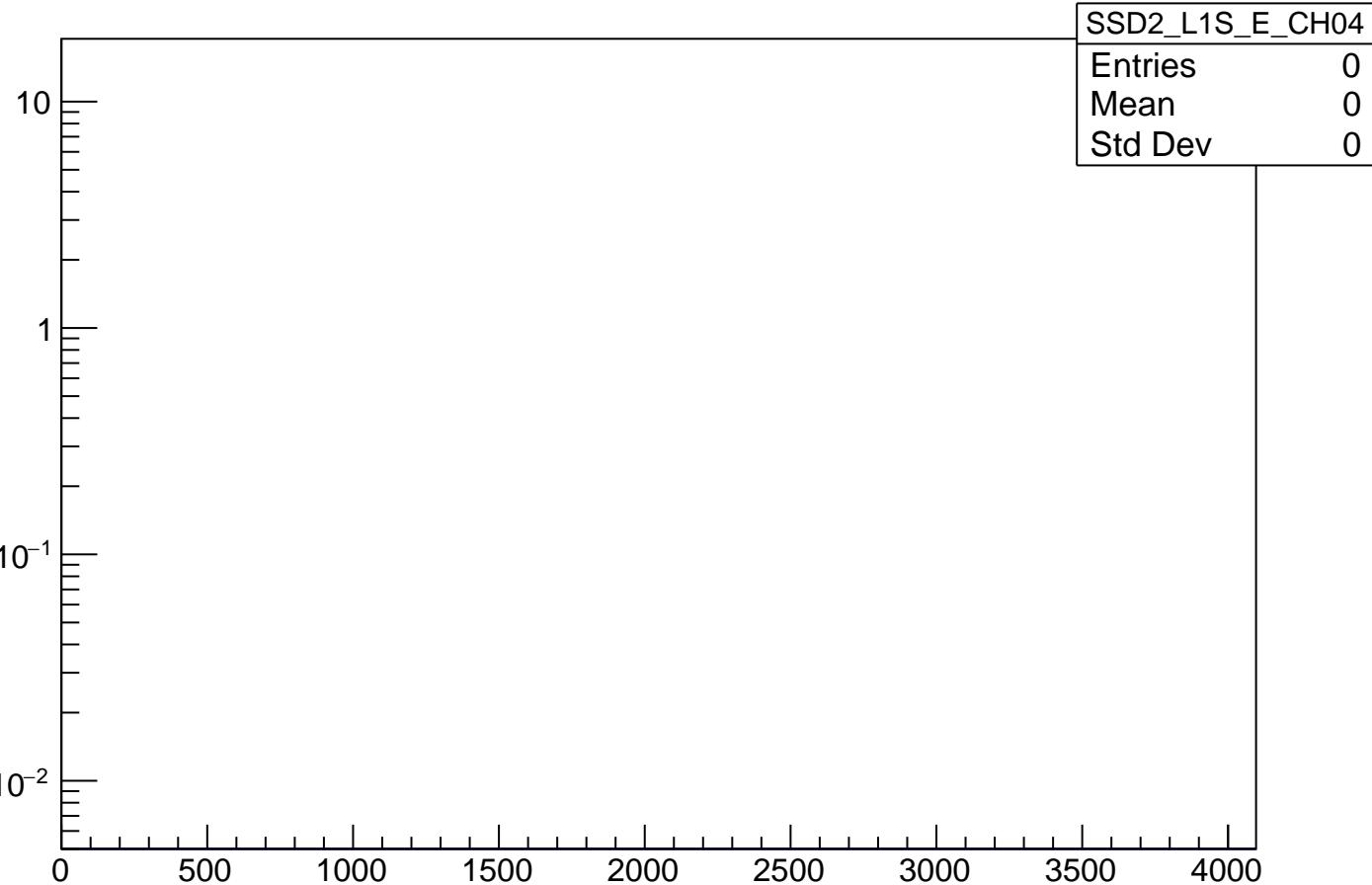
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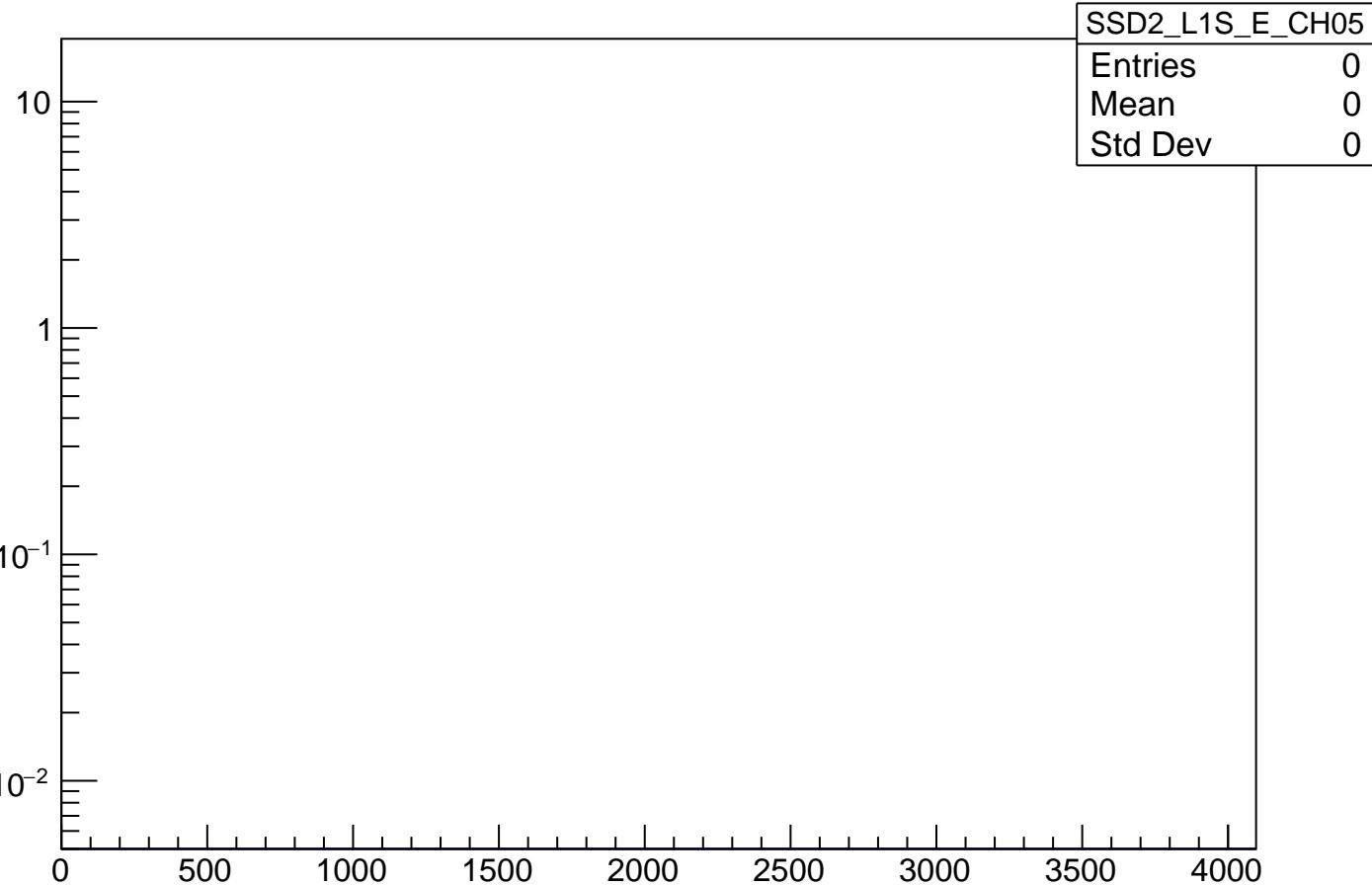
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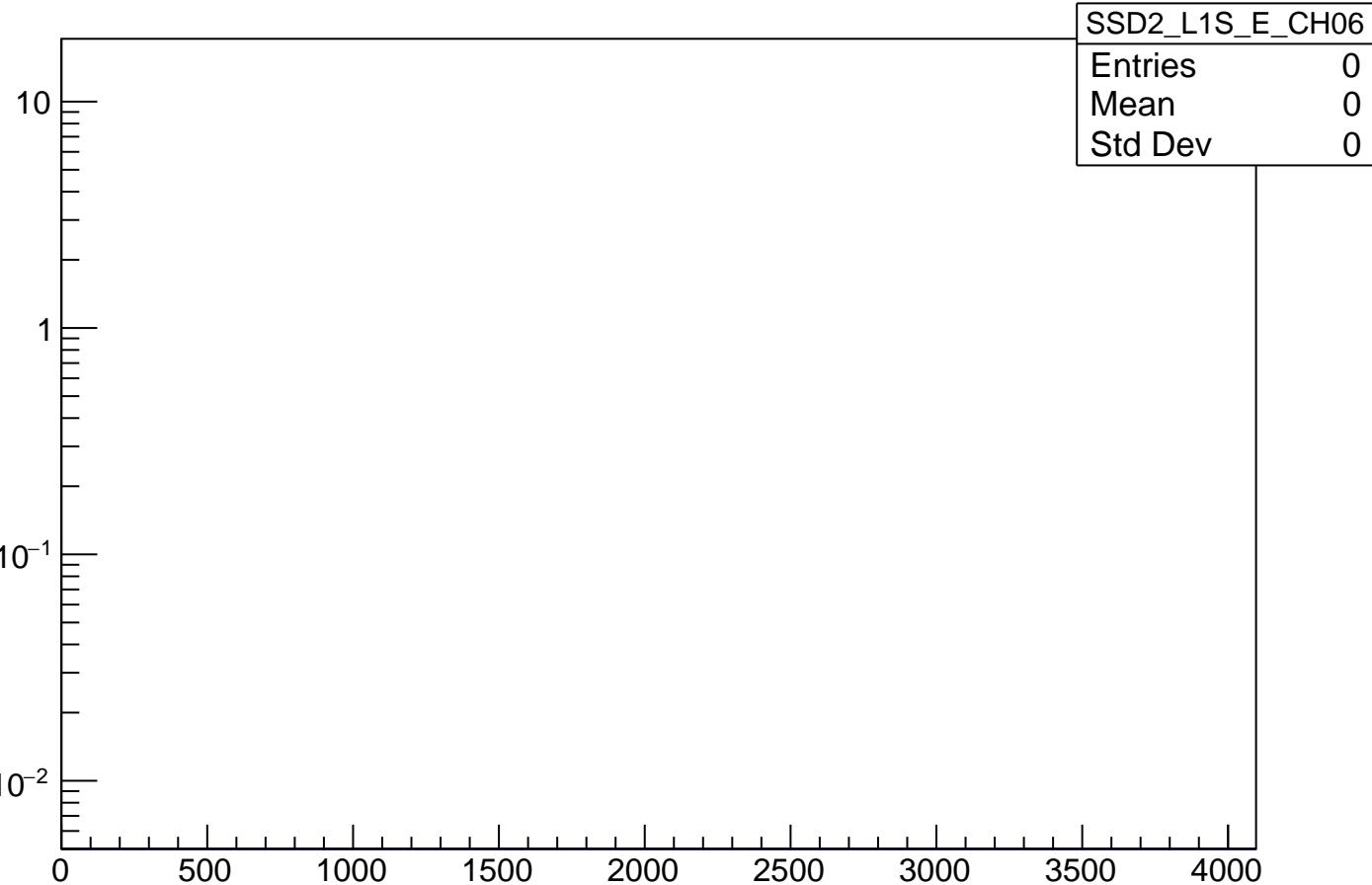
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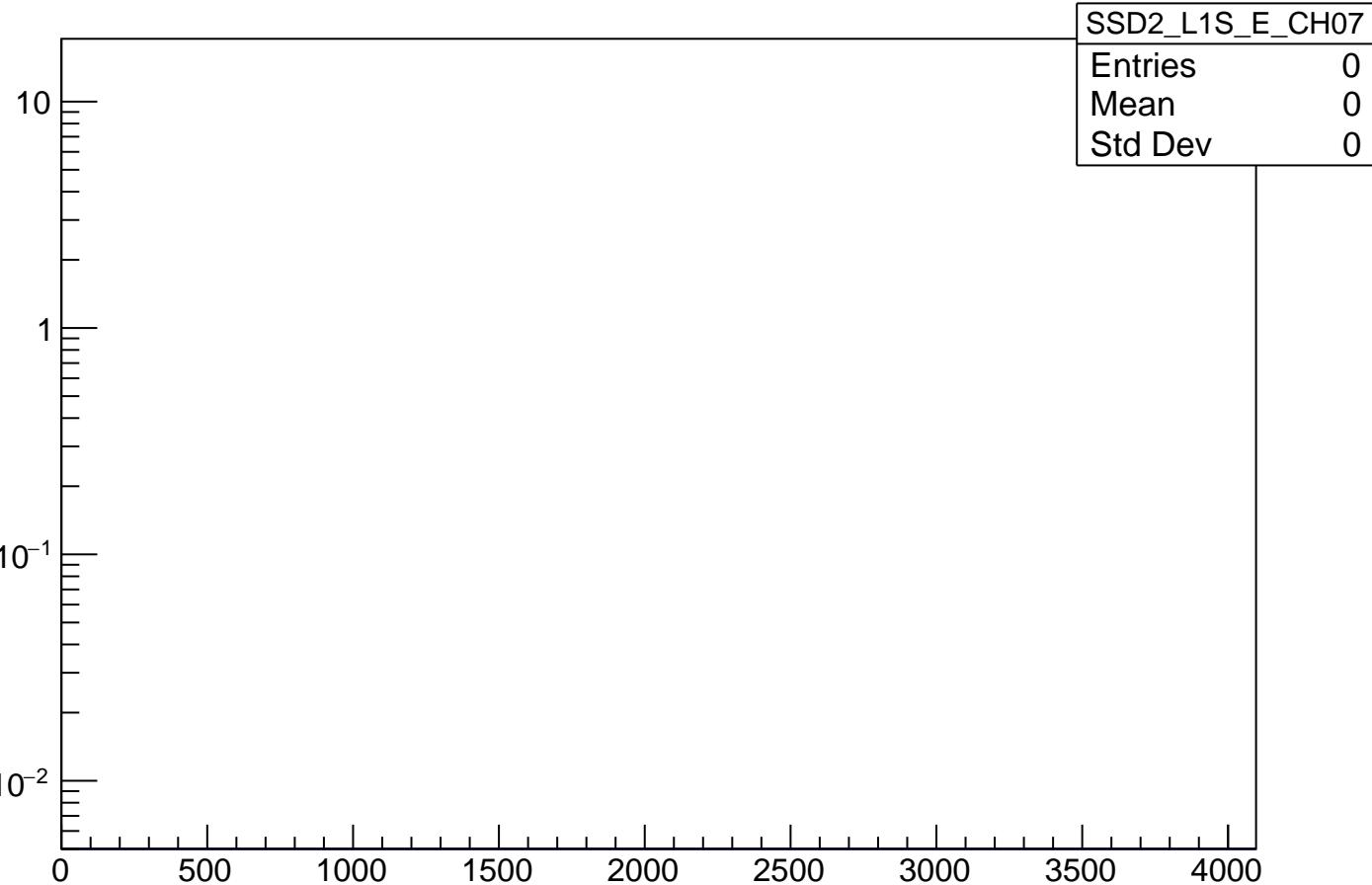
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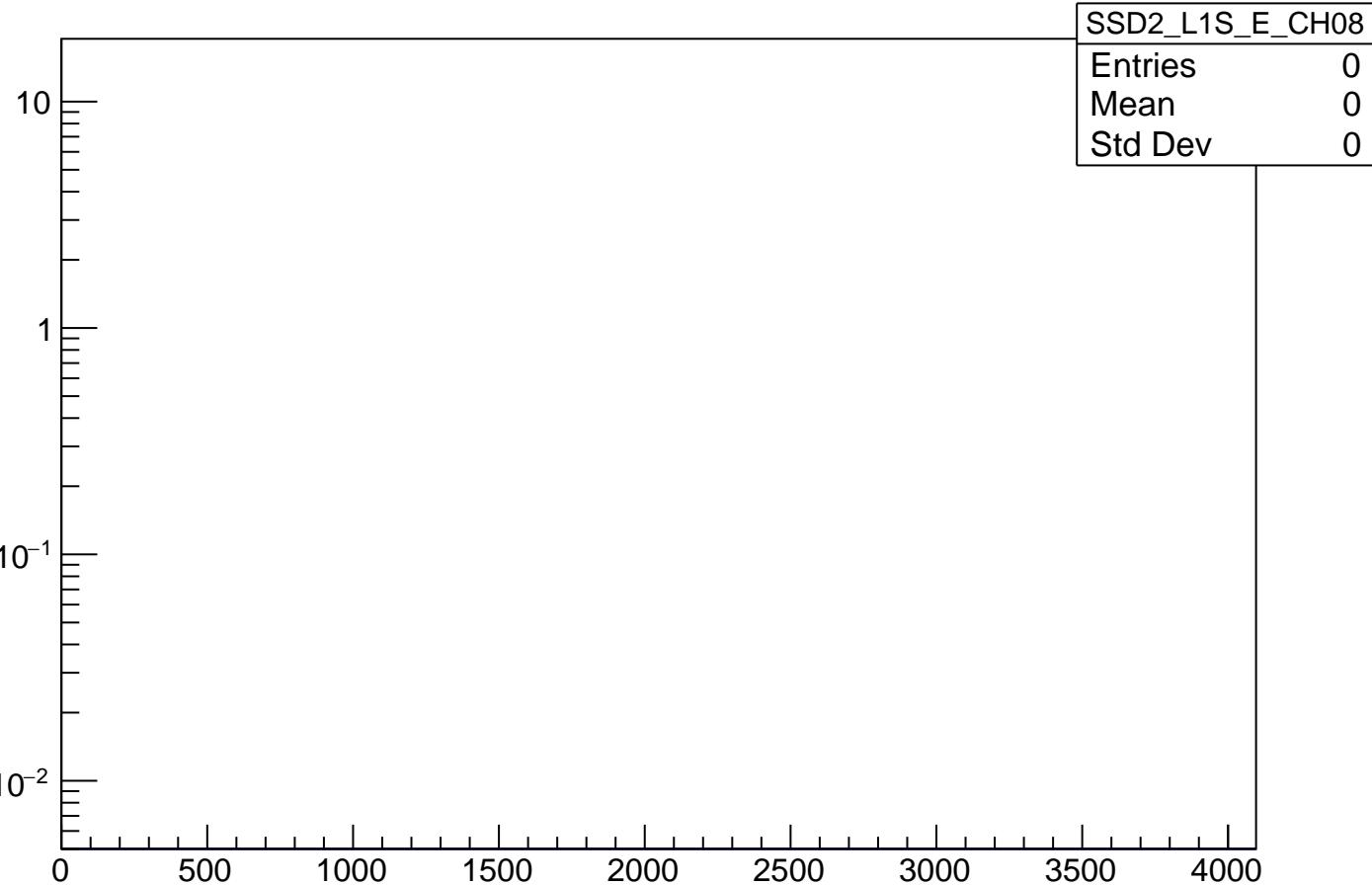
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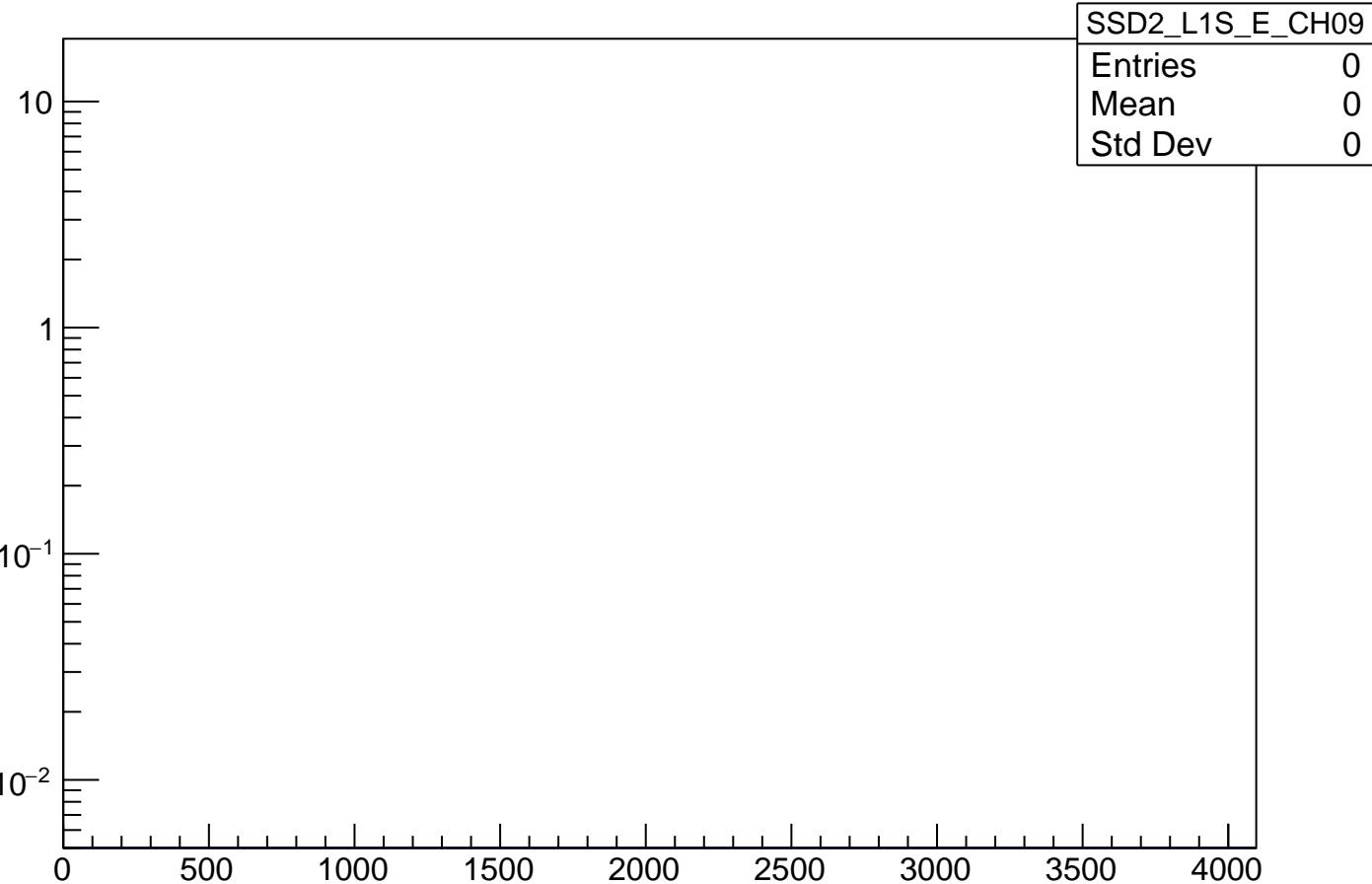
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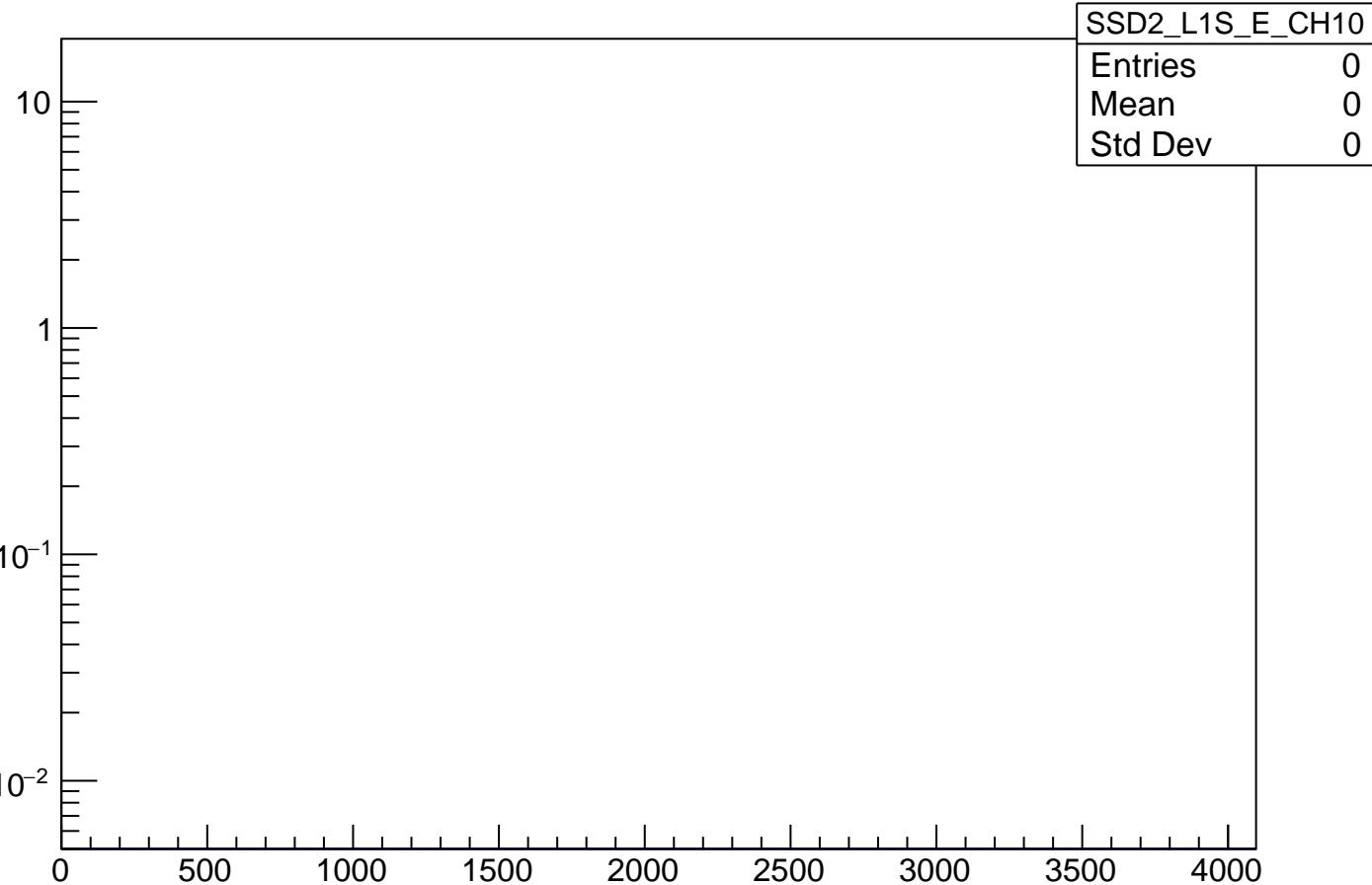
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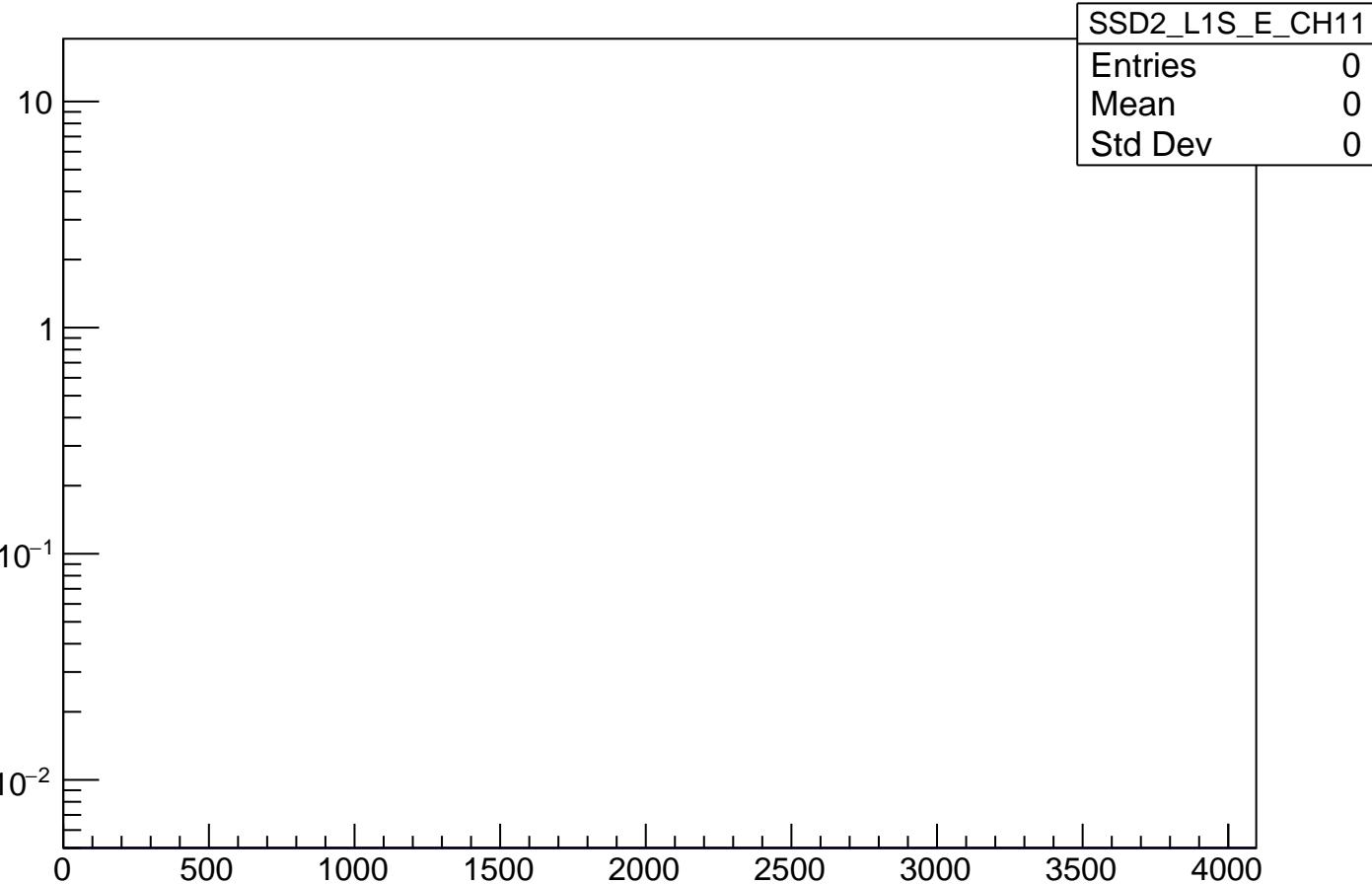
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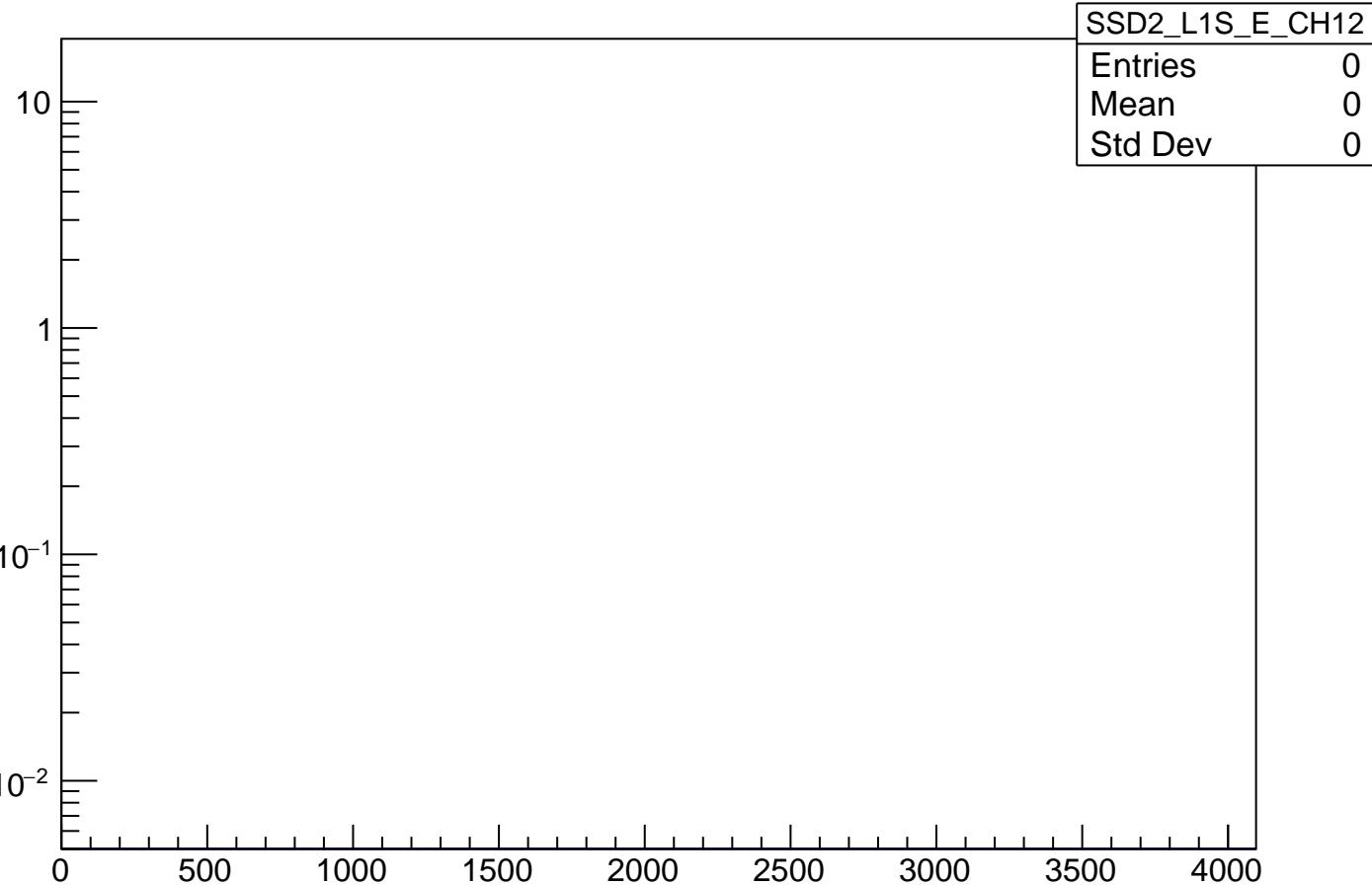
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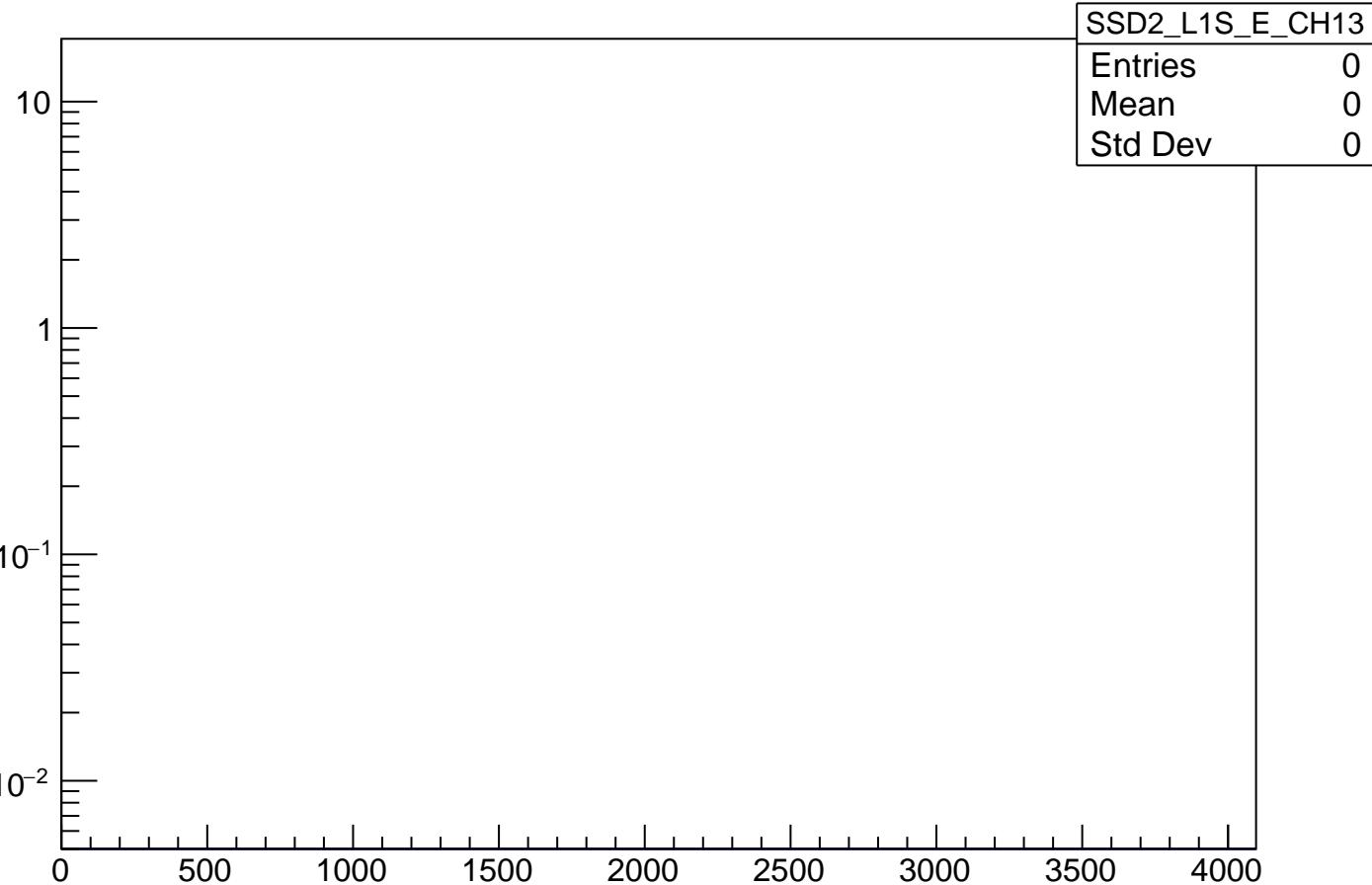
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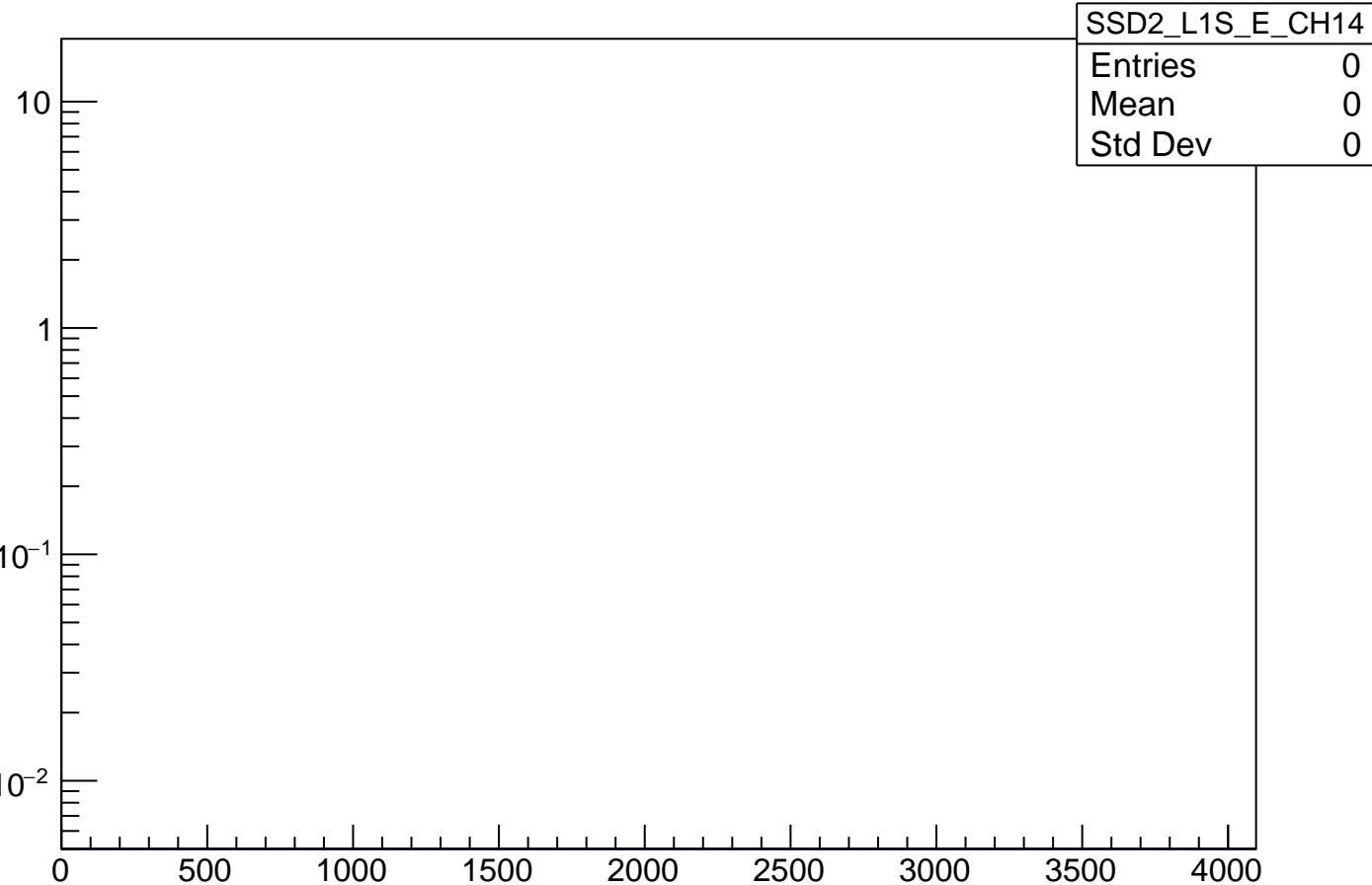
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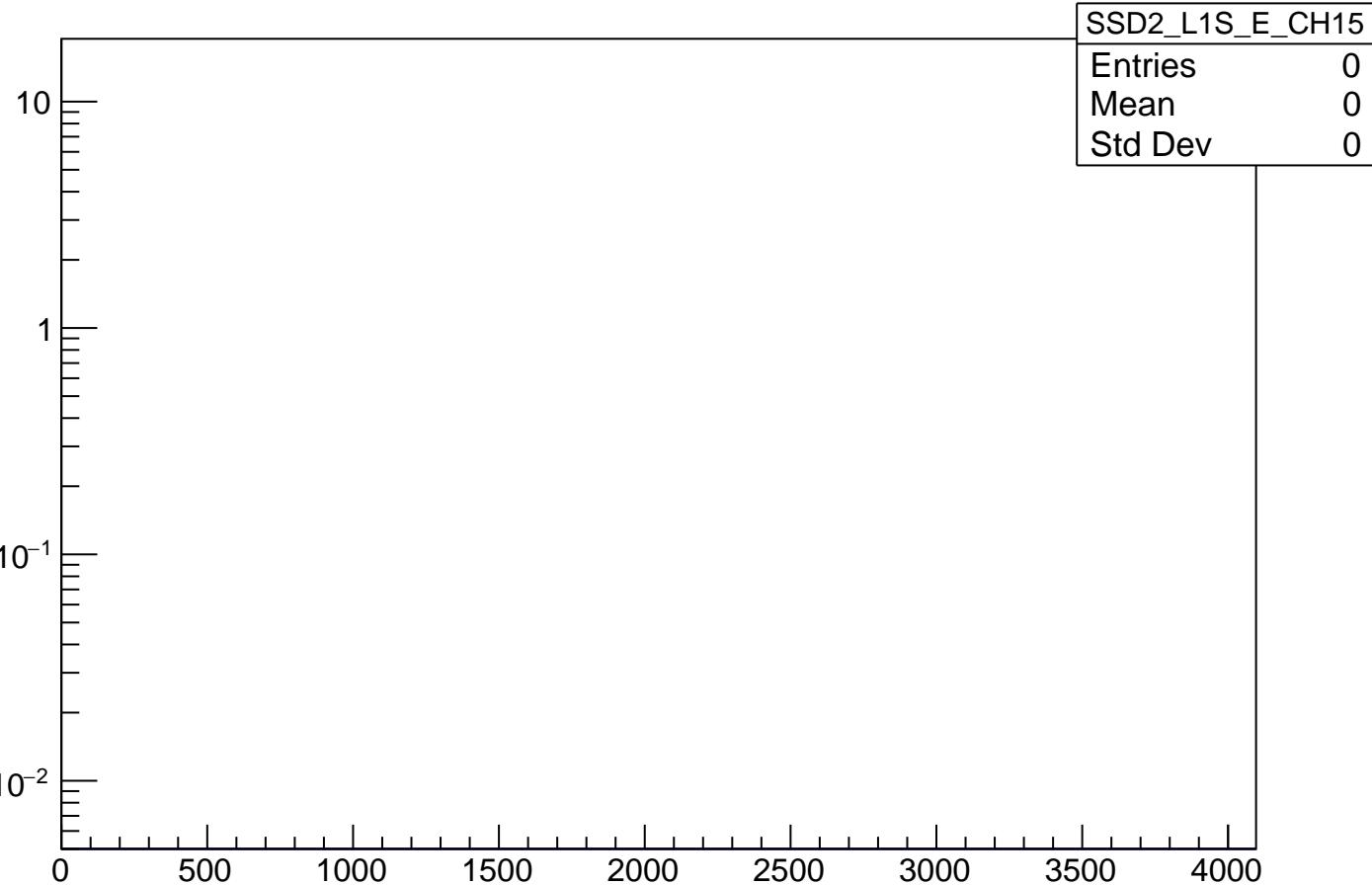
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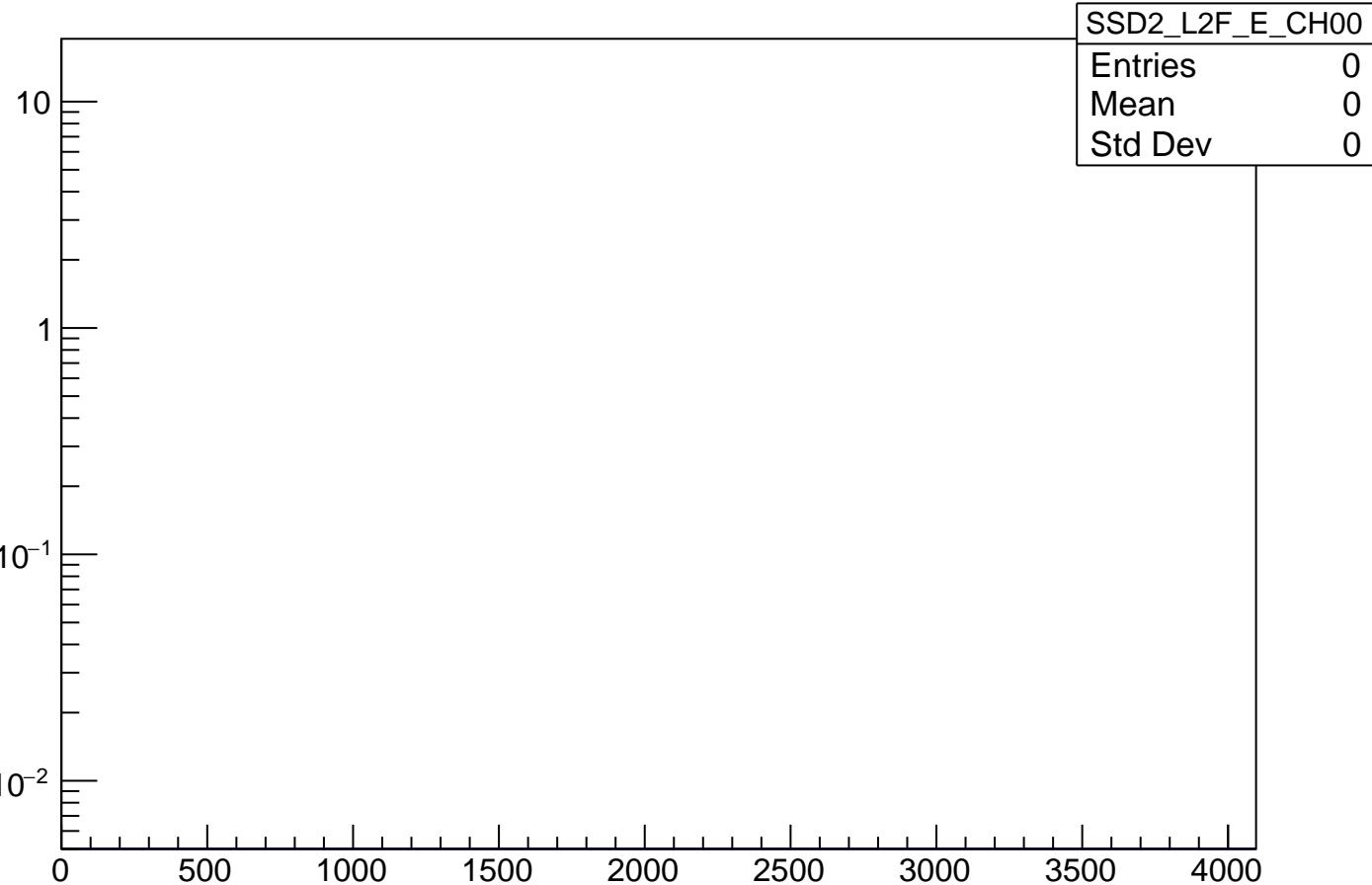
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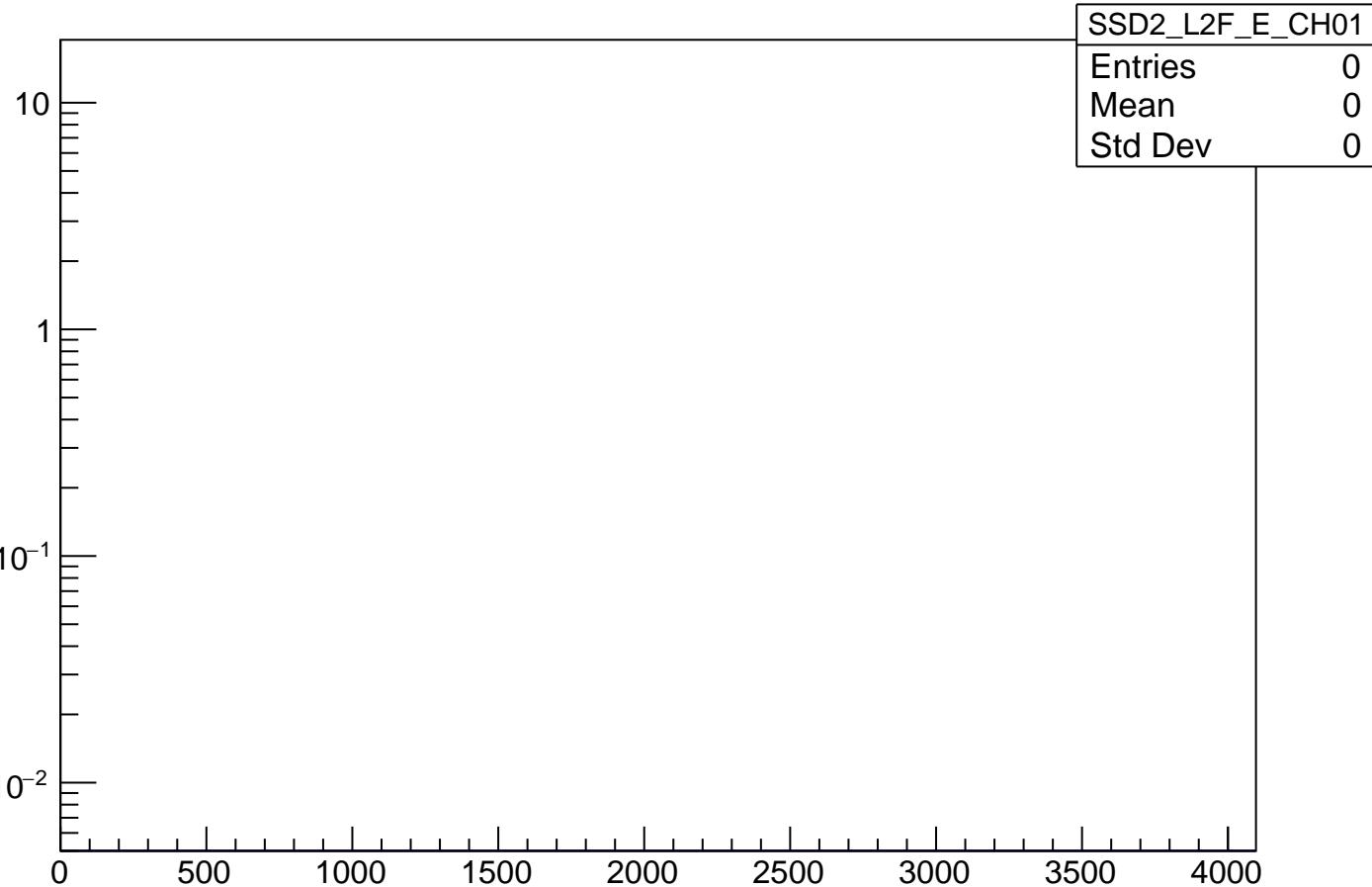
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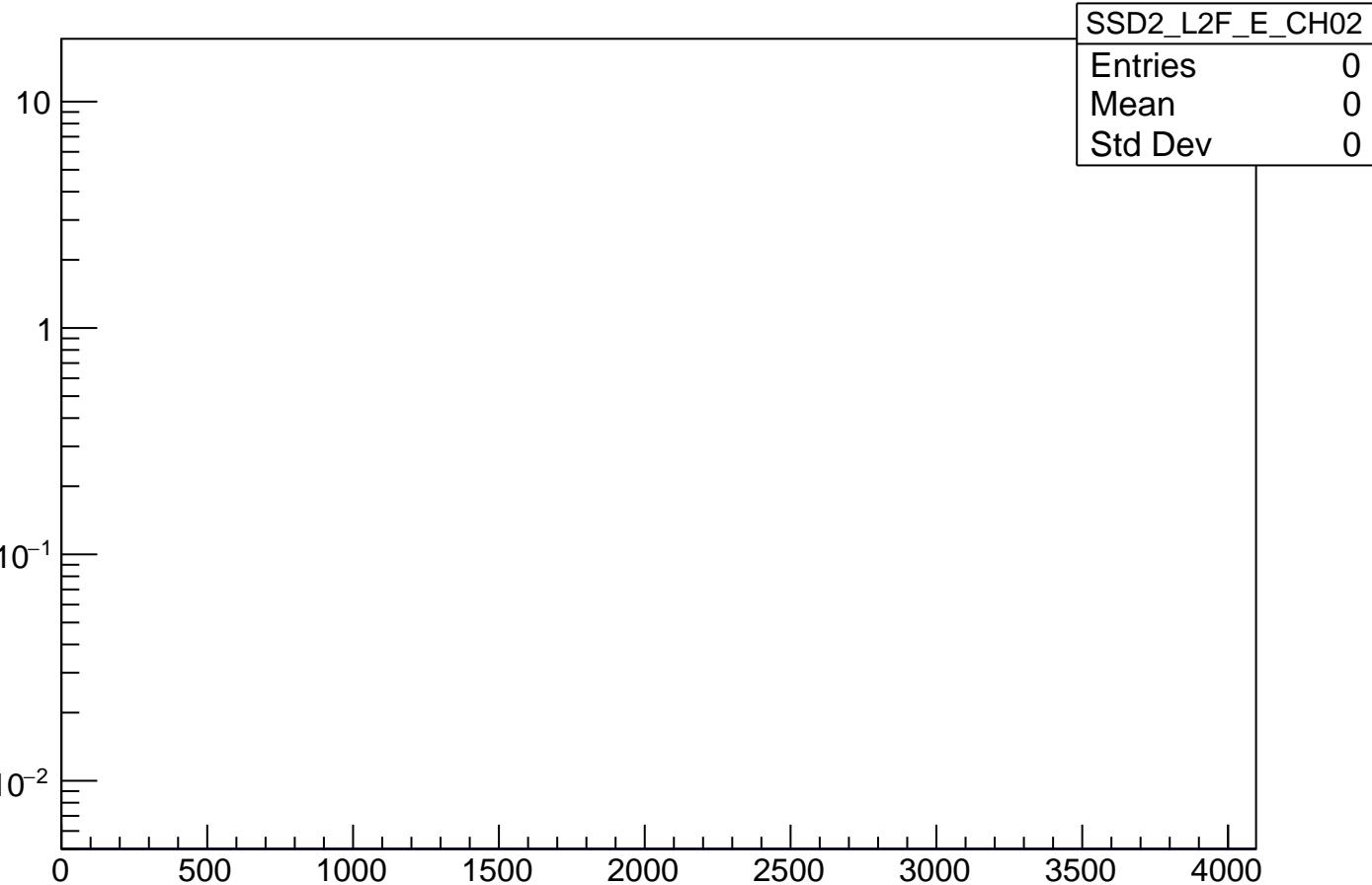
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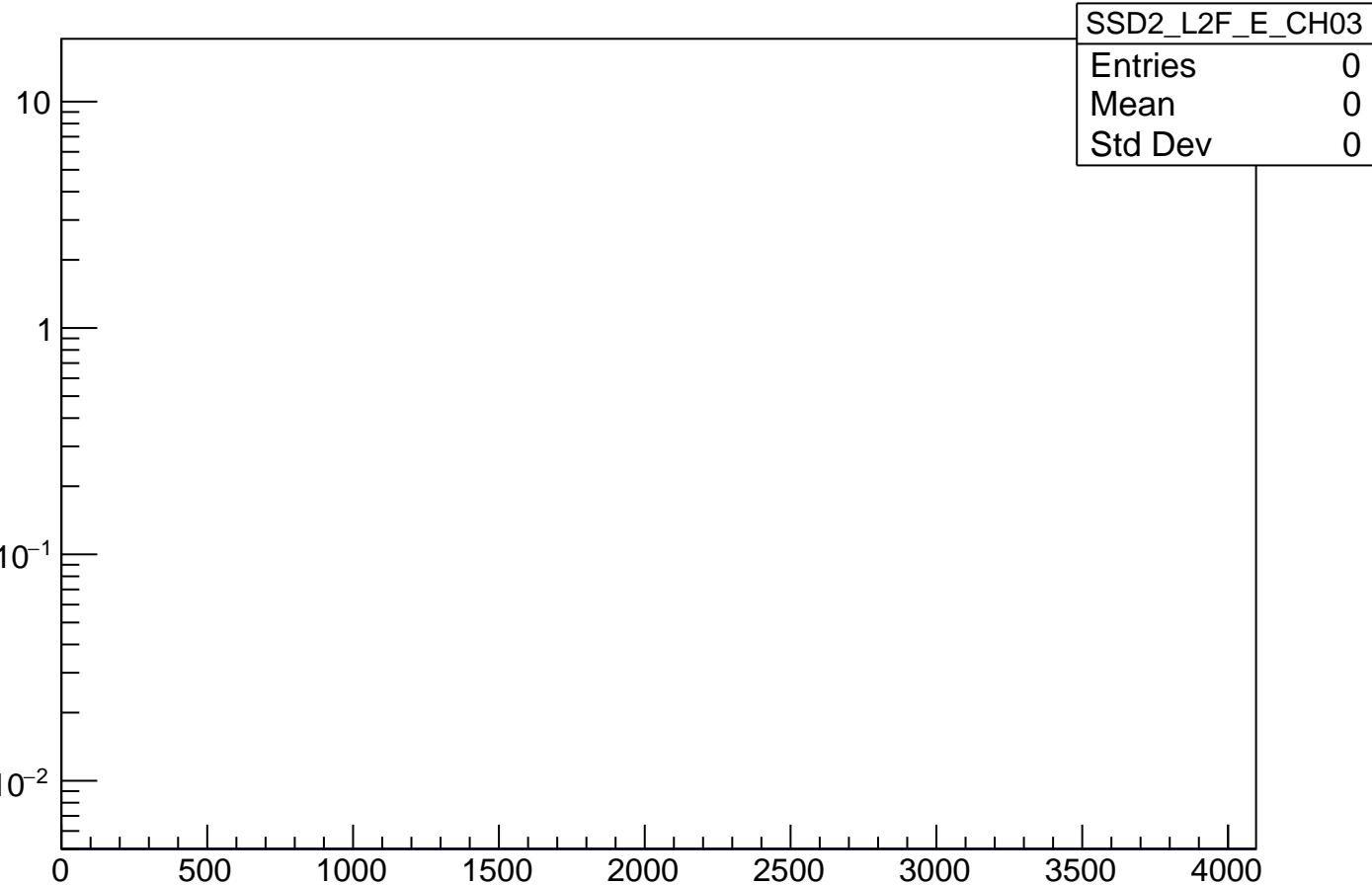
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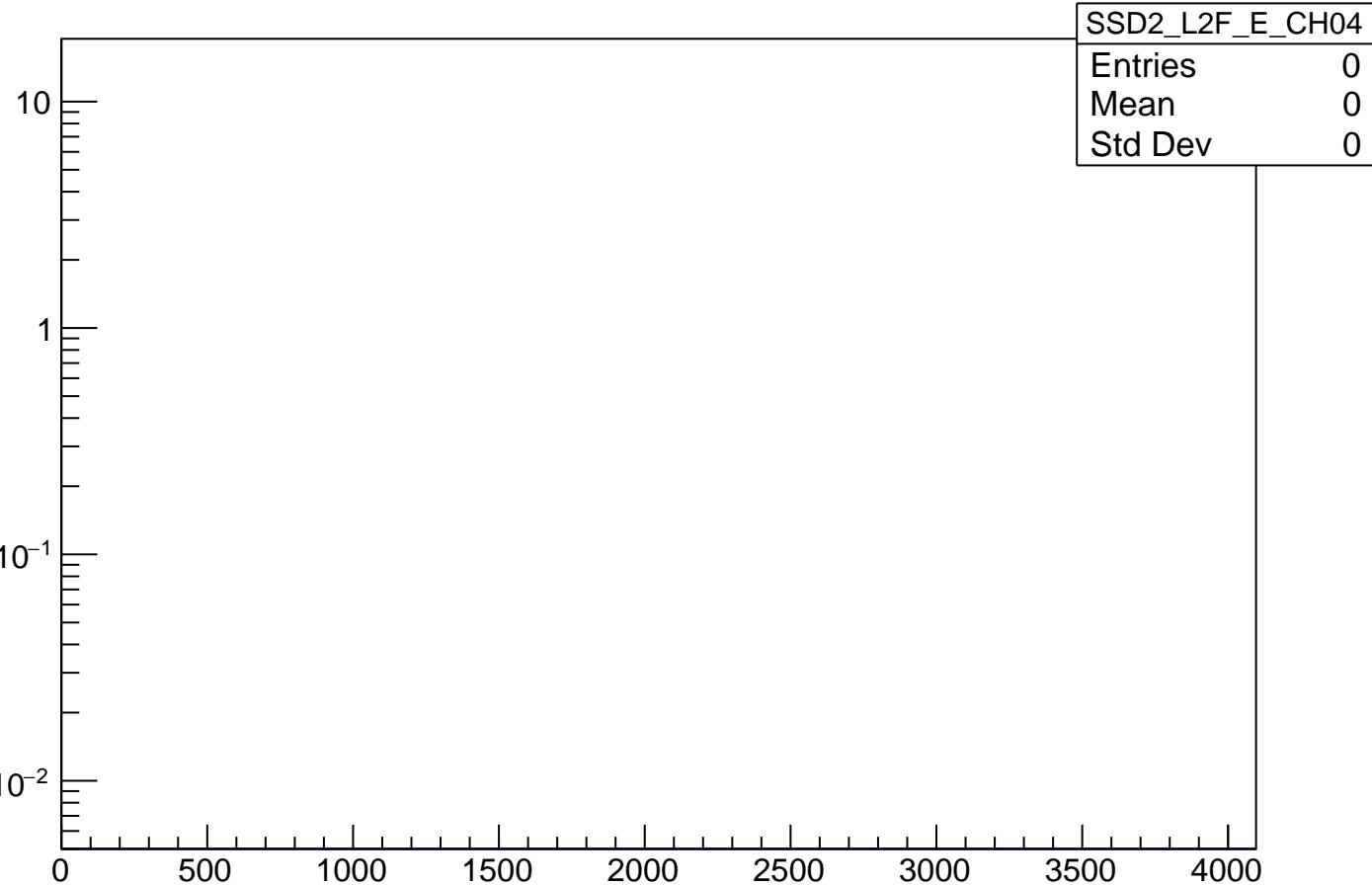
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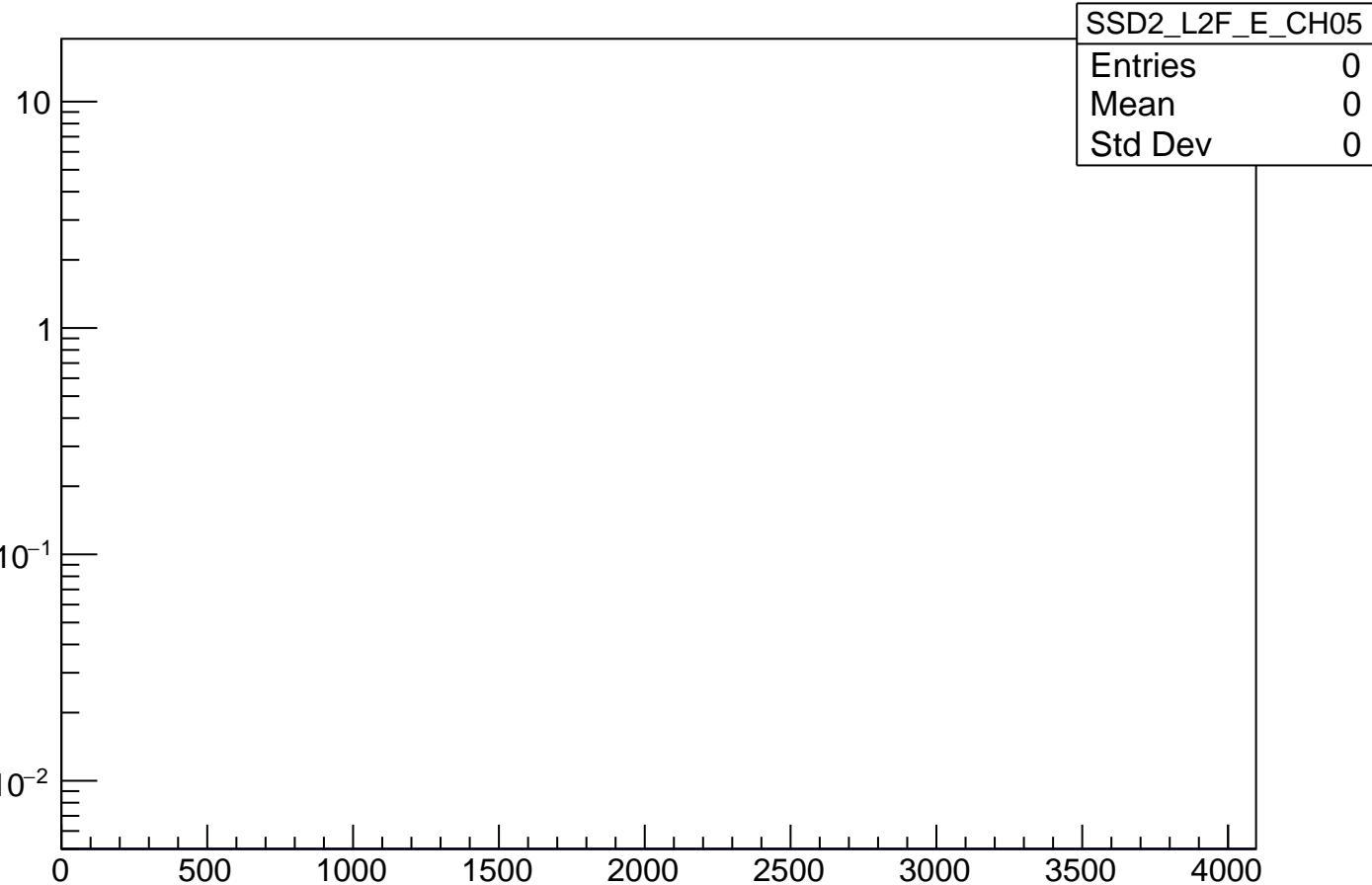
SSD2_L2F_E_CH03



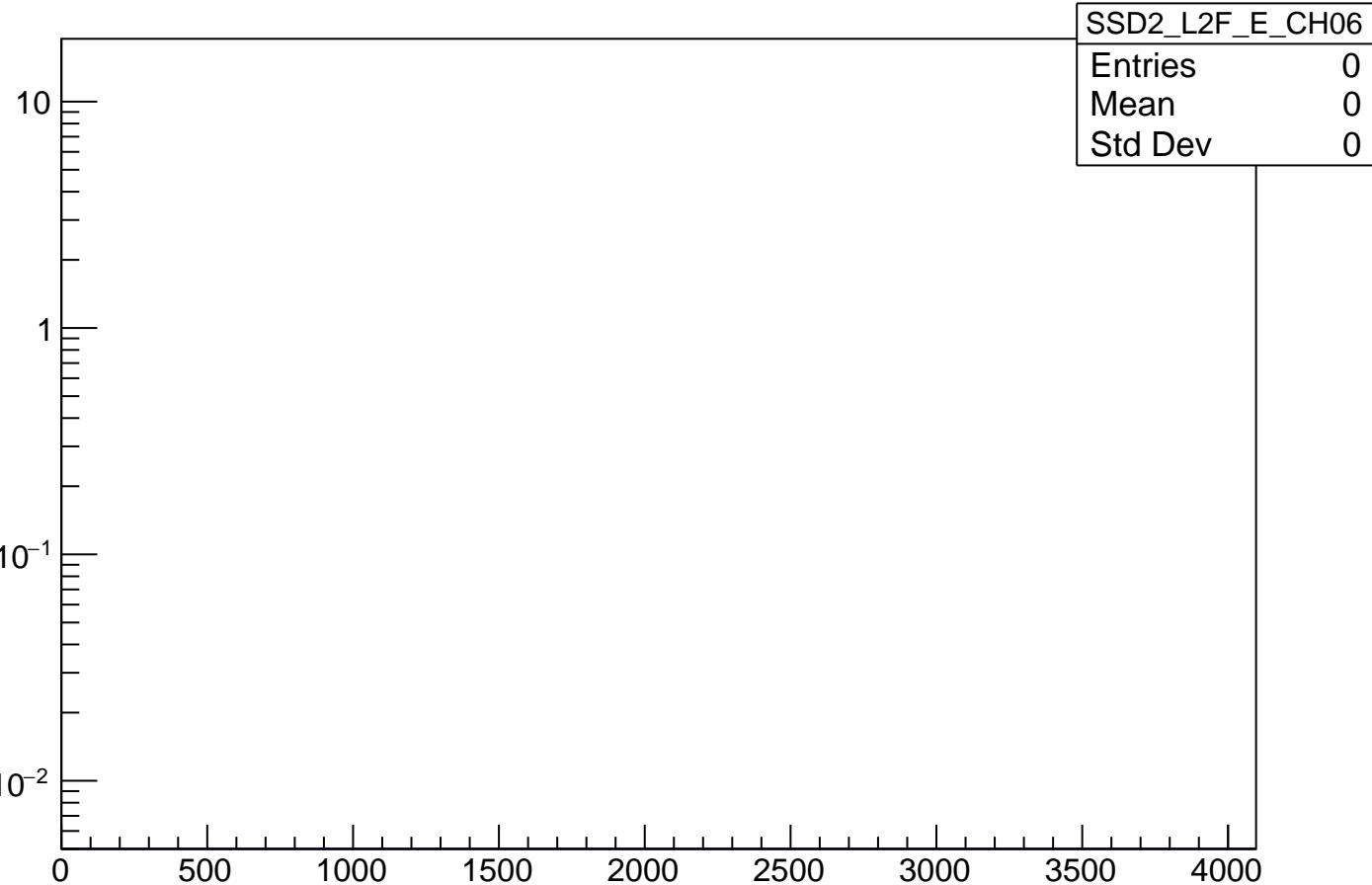
SSD2_L2F_E_CH04



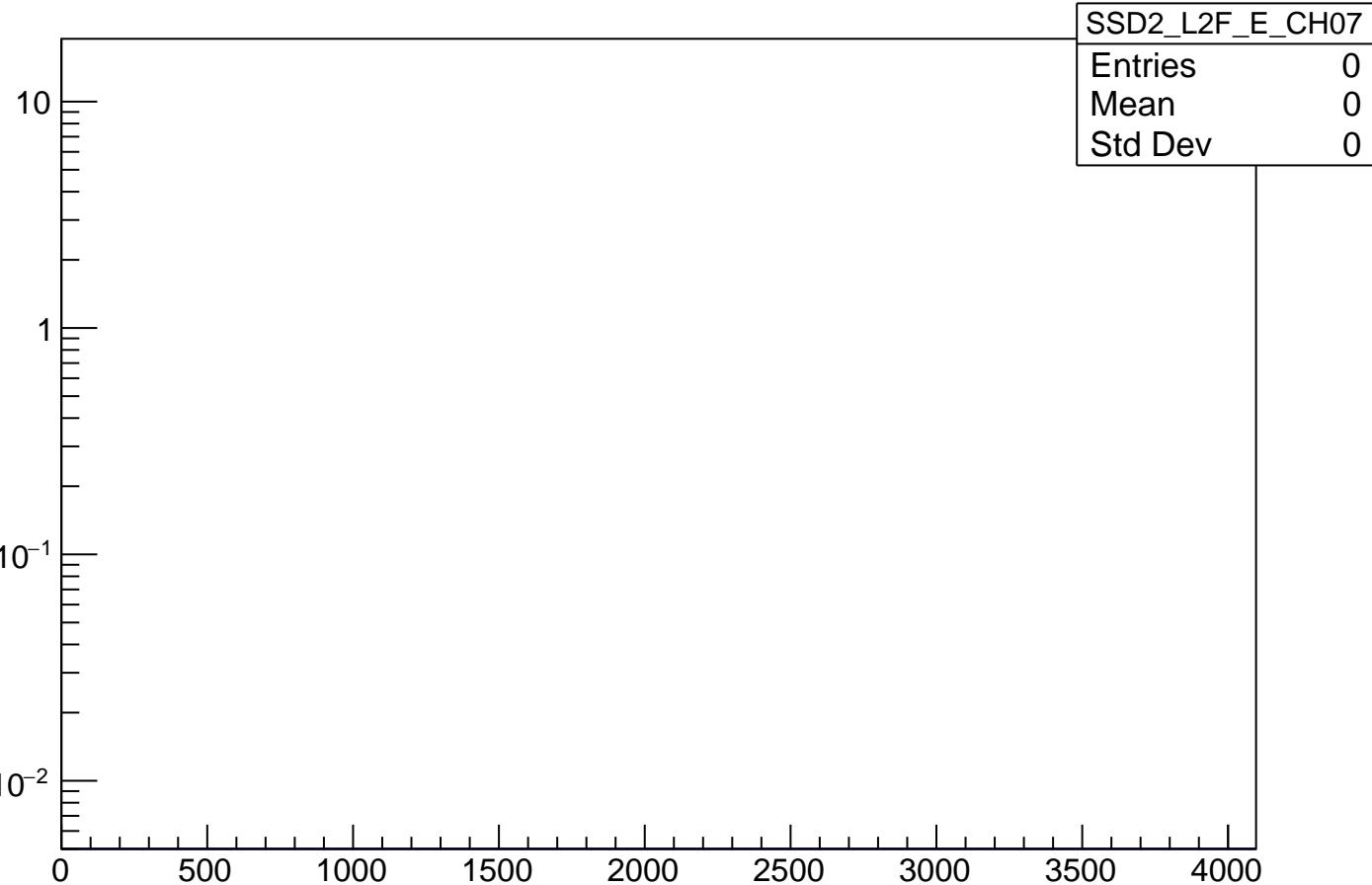
SSD2_L2F_E_CH05



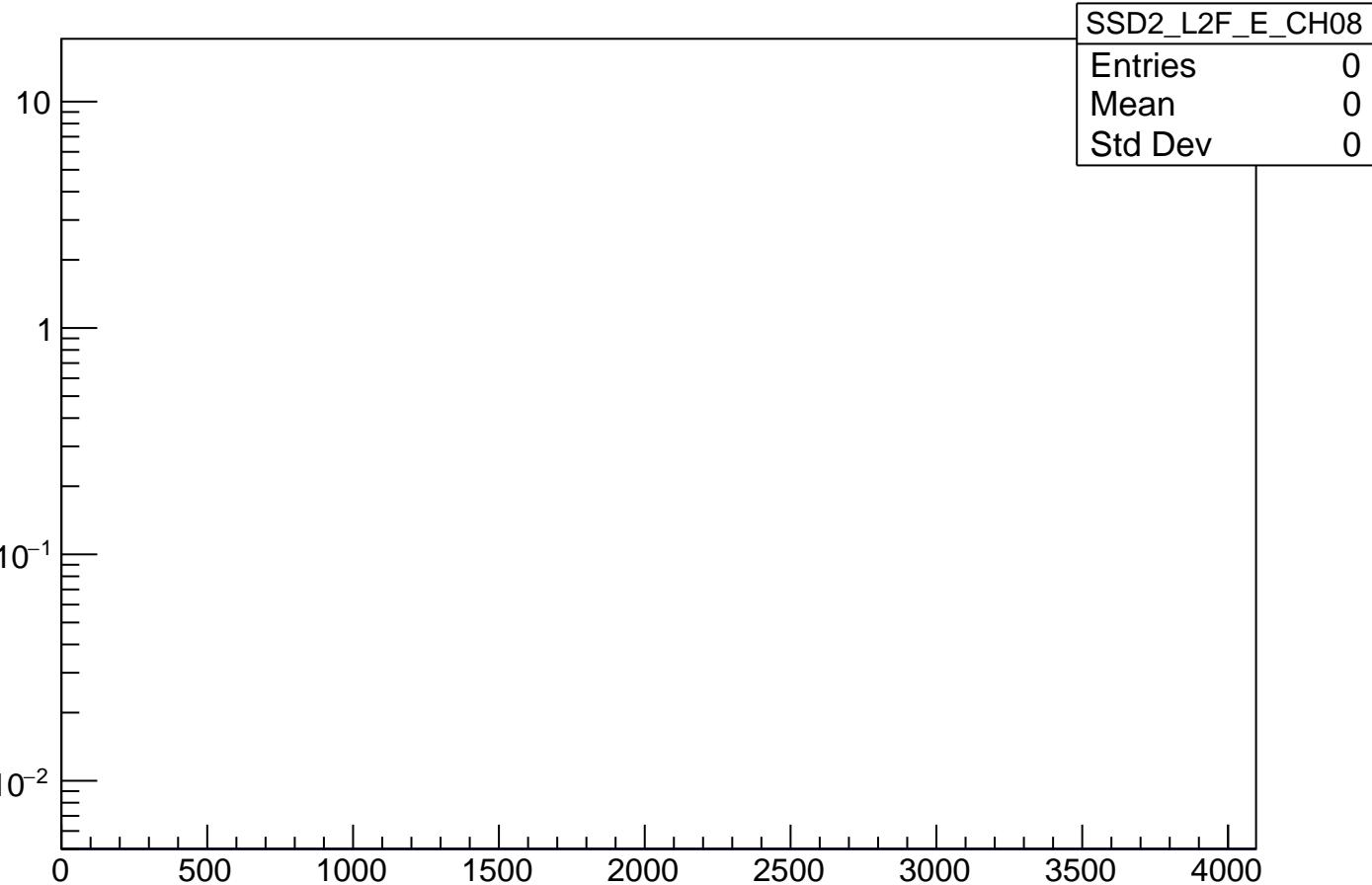
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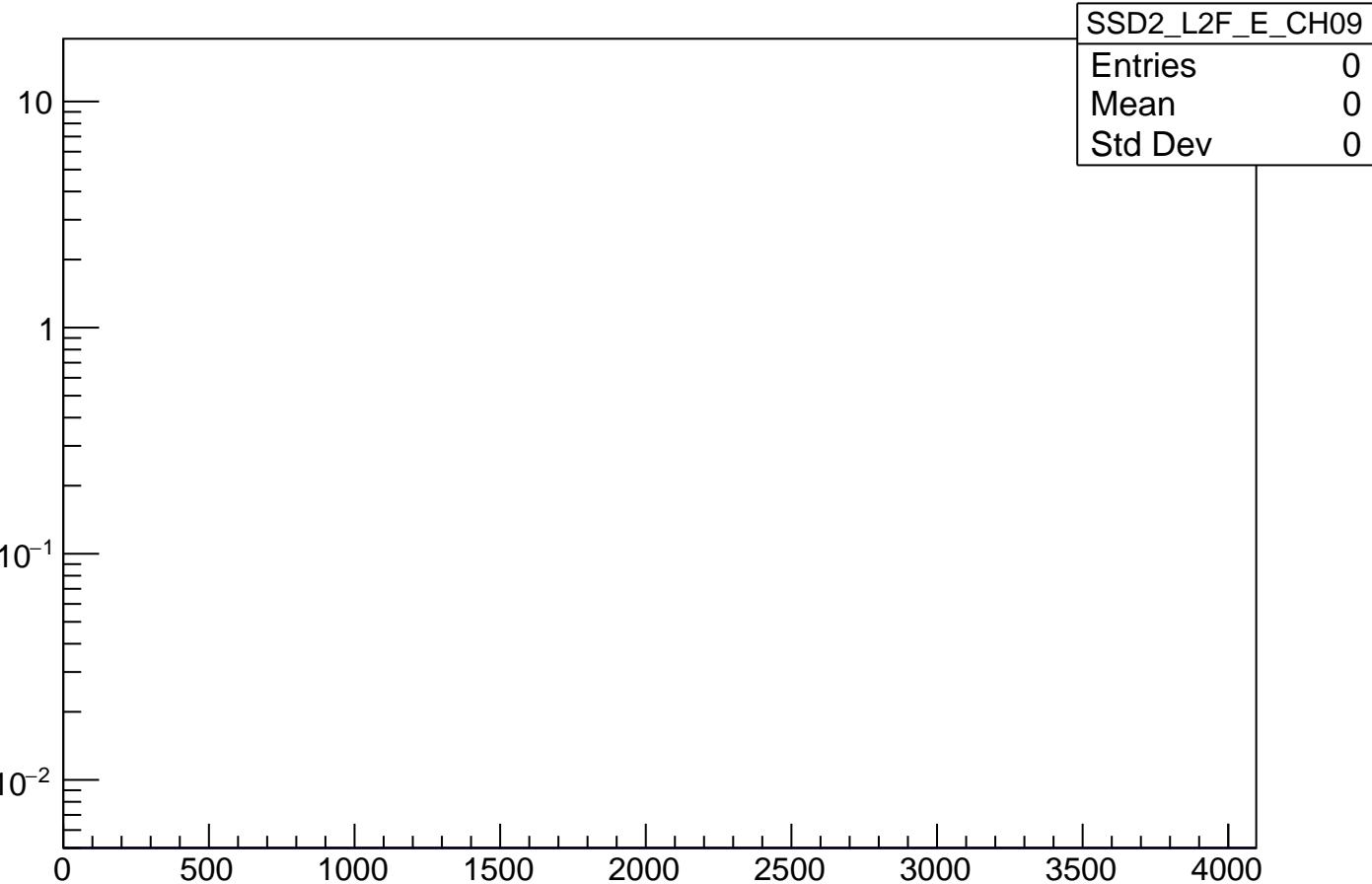
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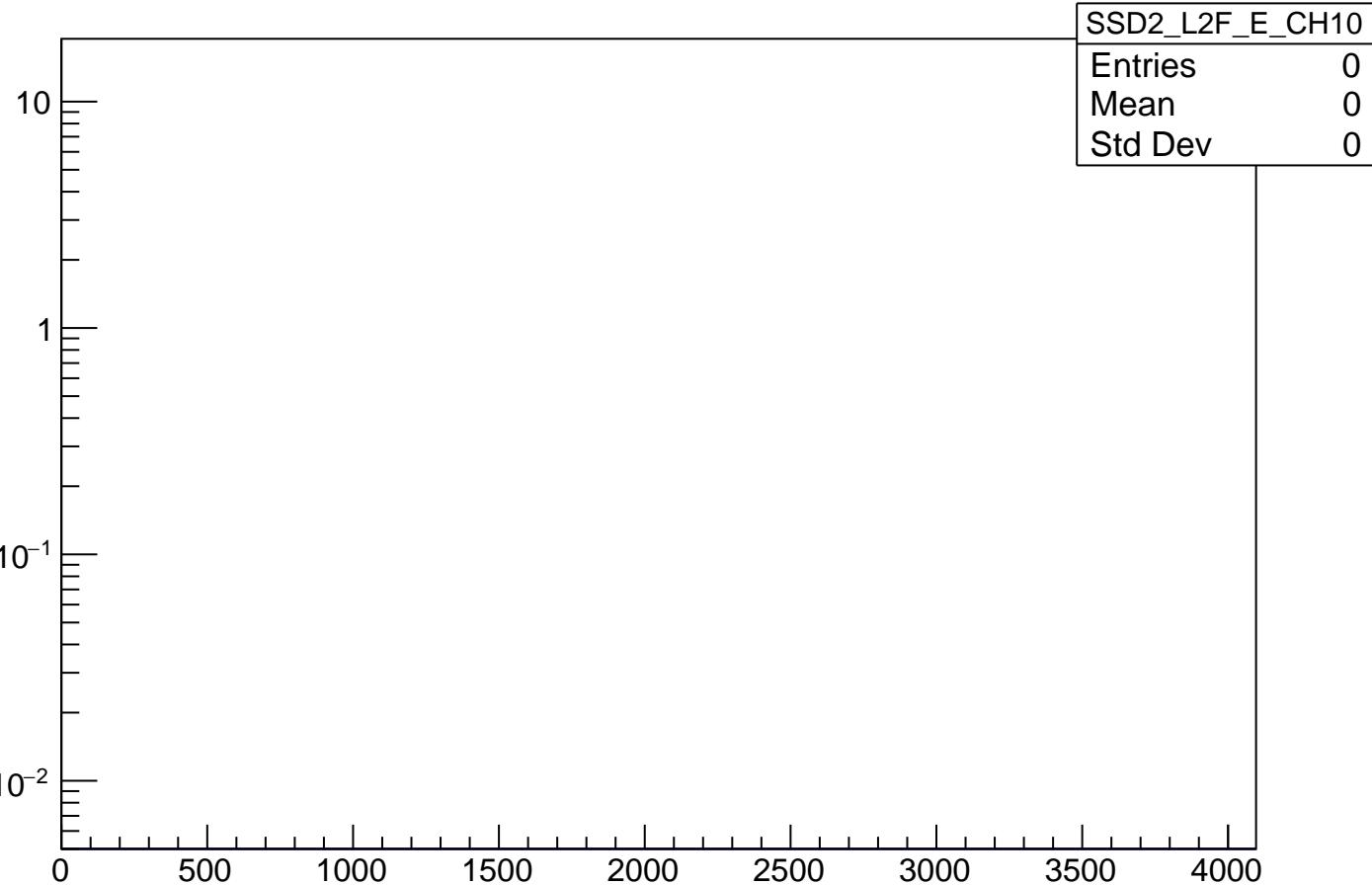
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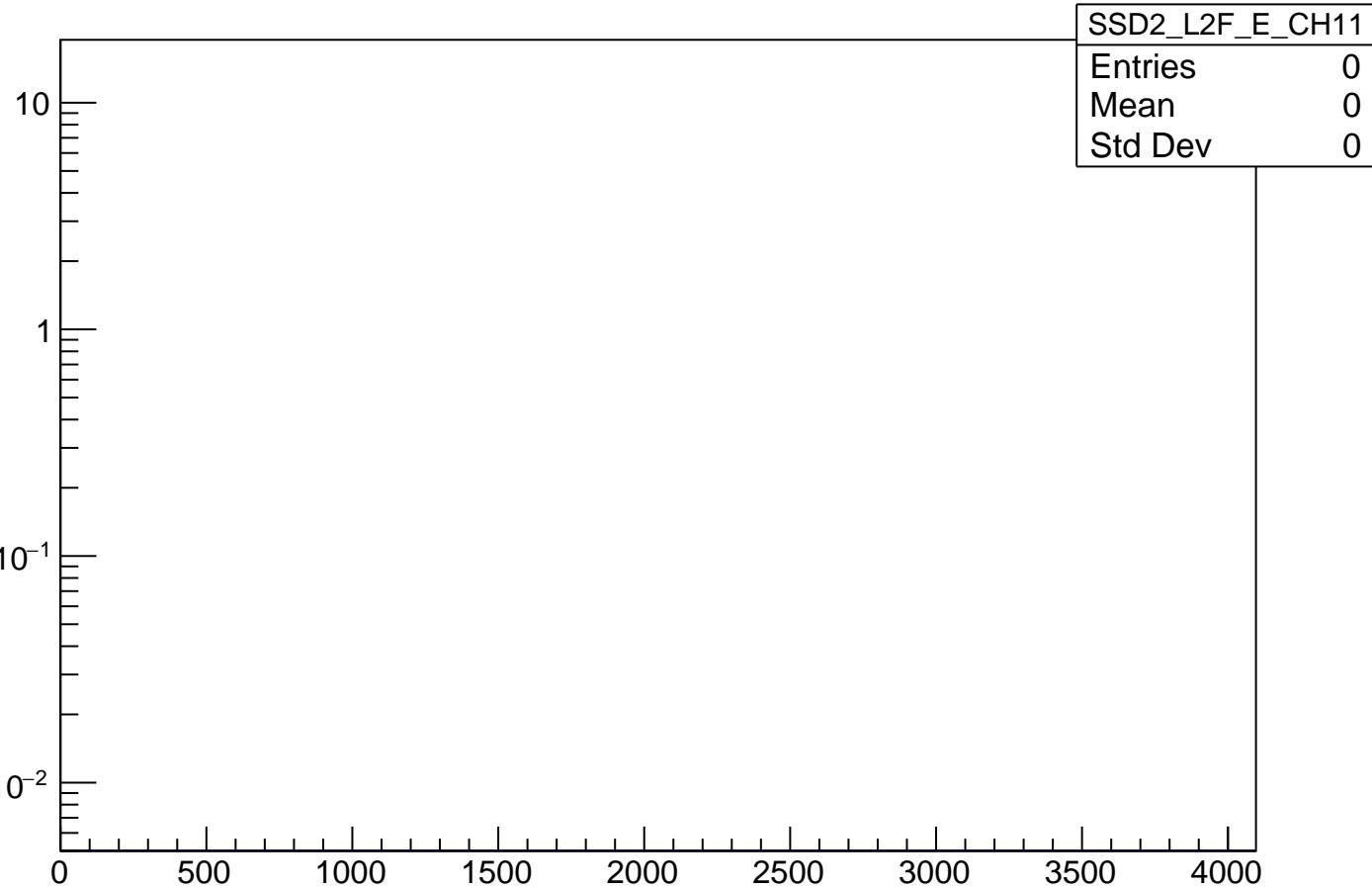
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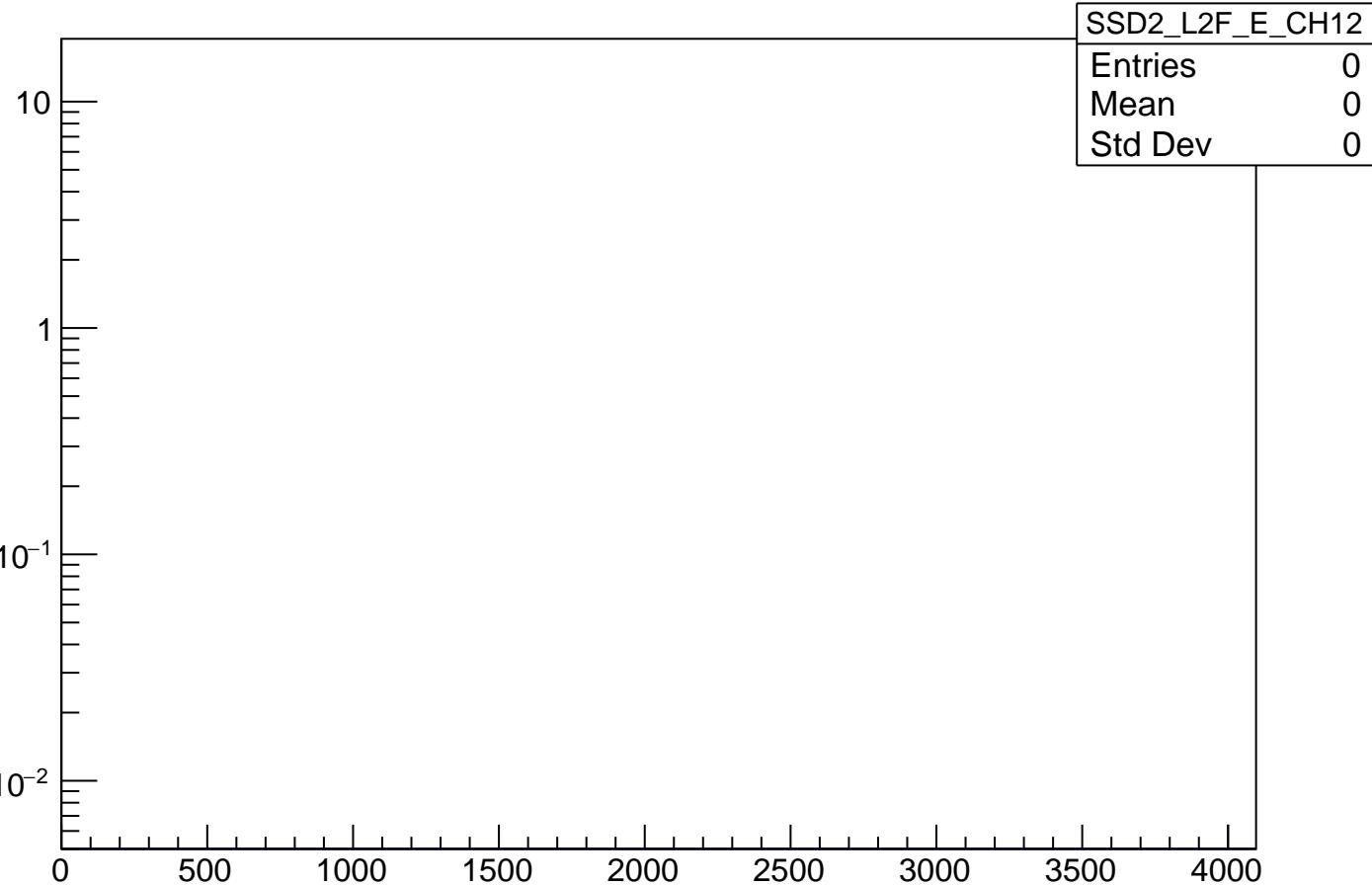
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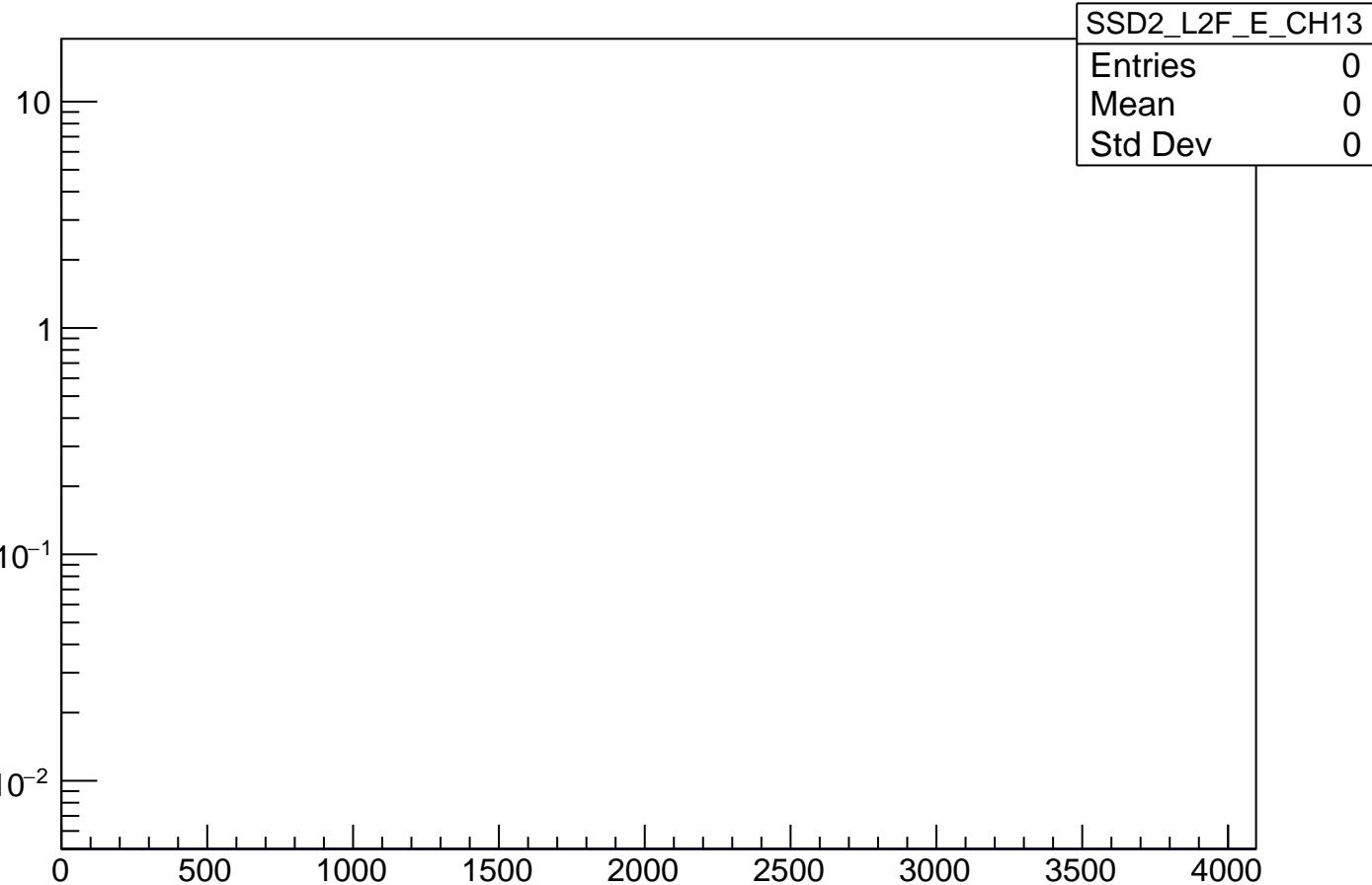
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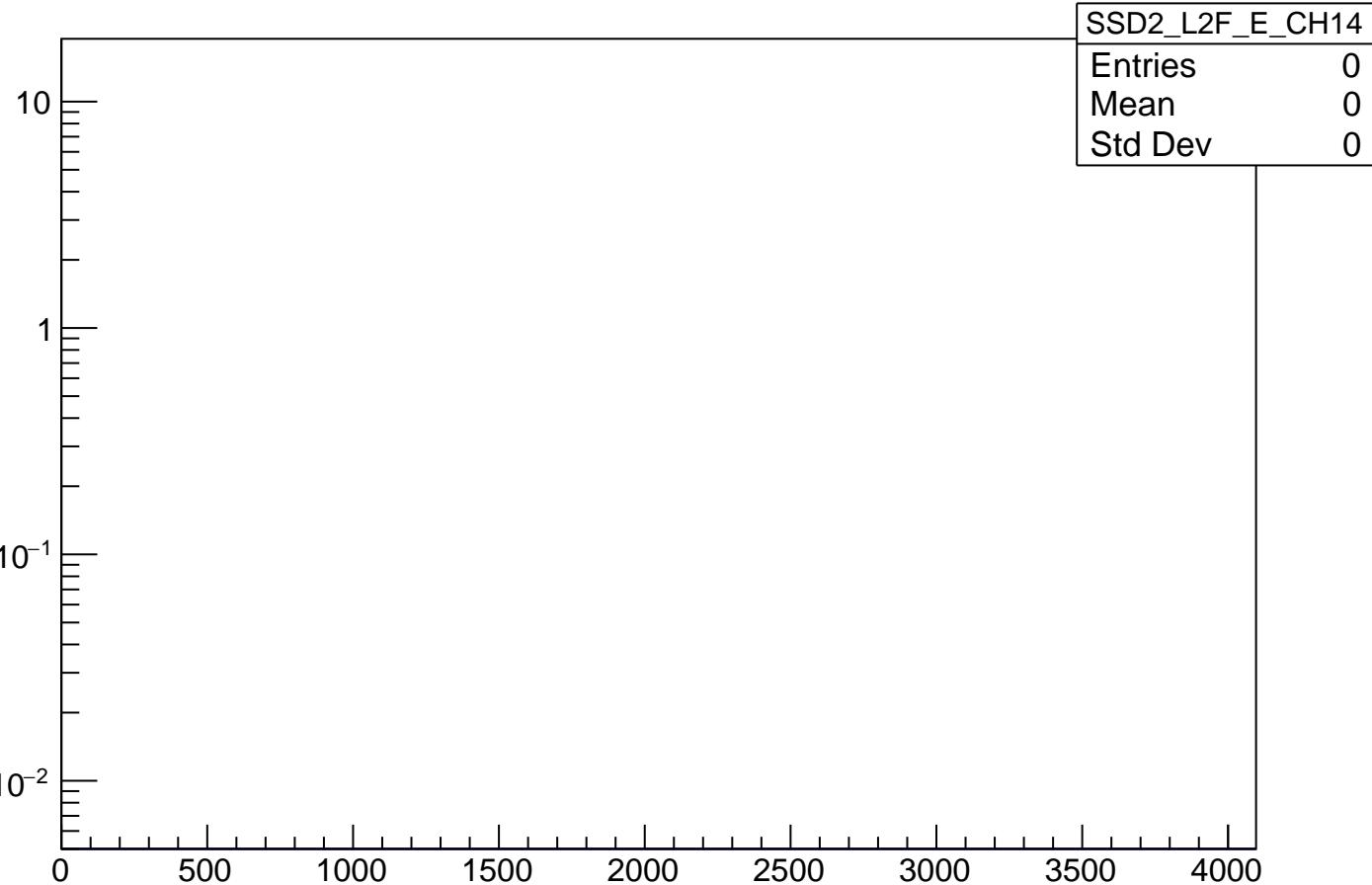
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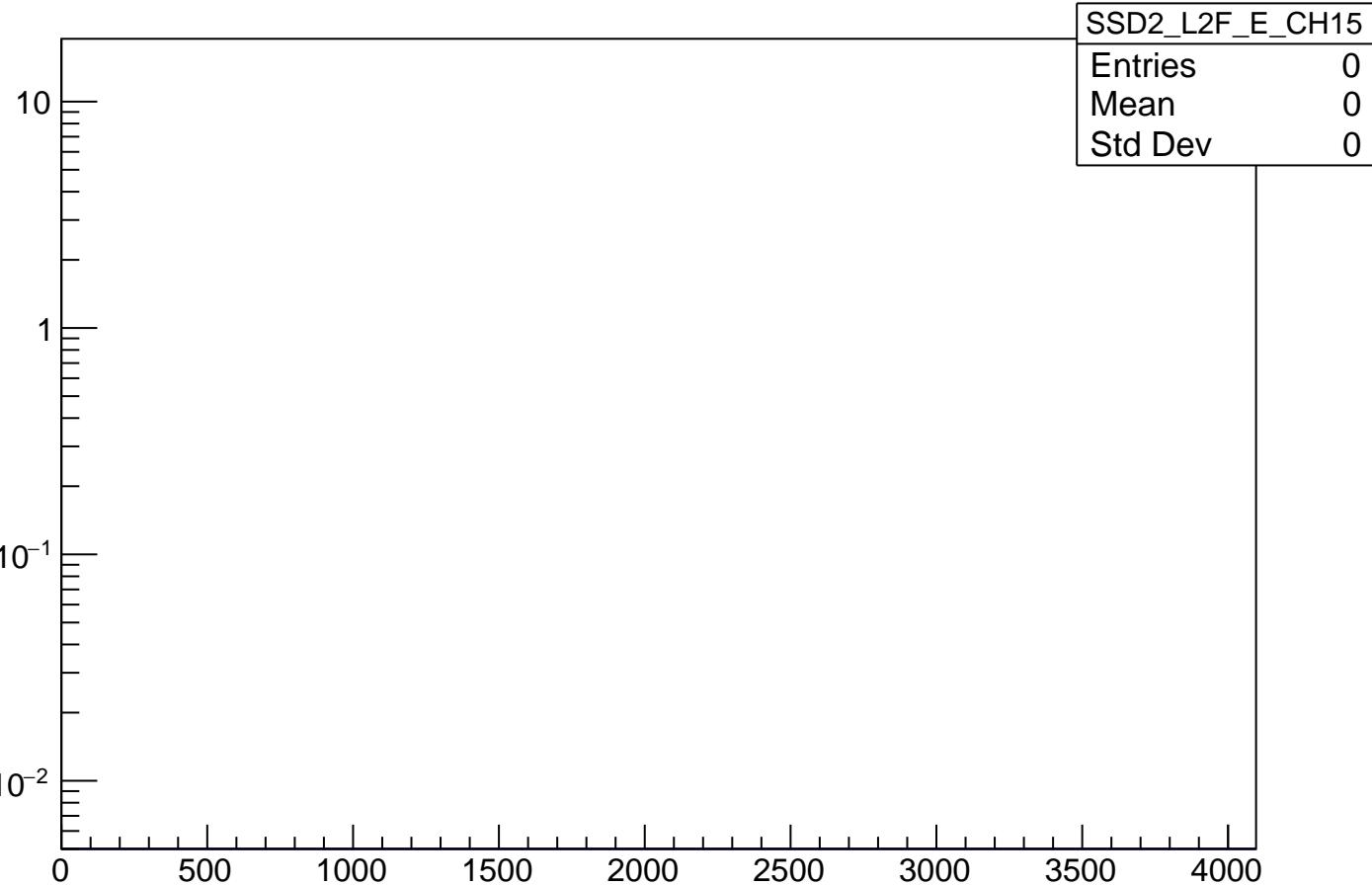
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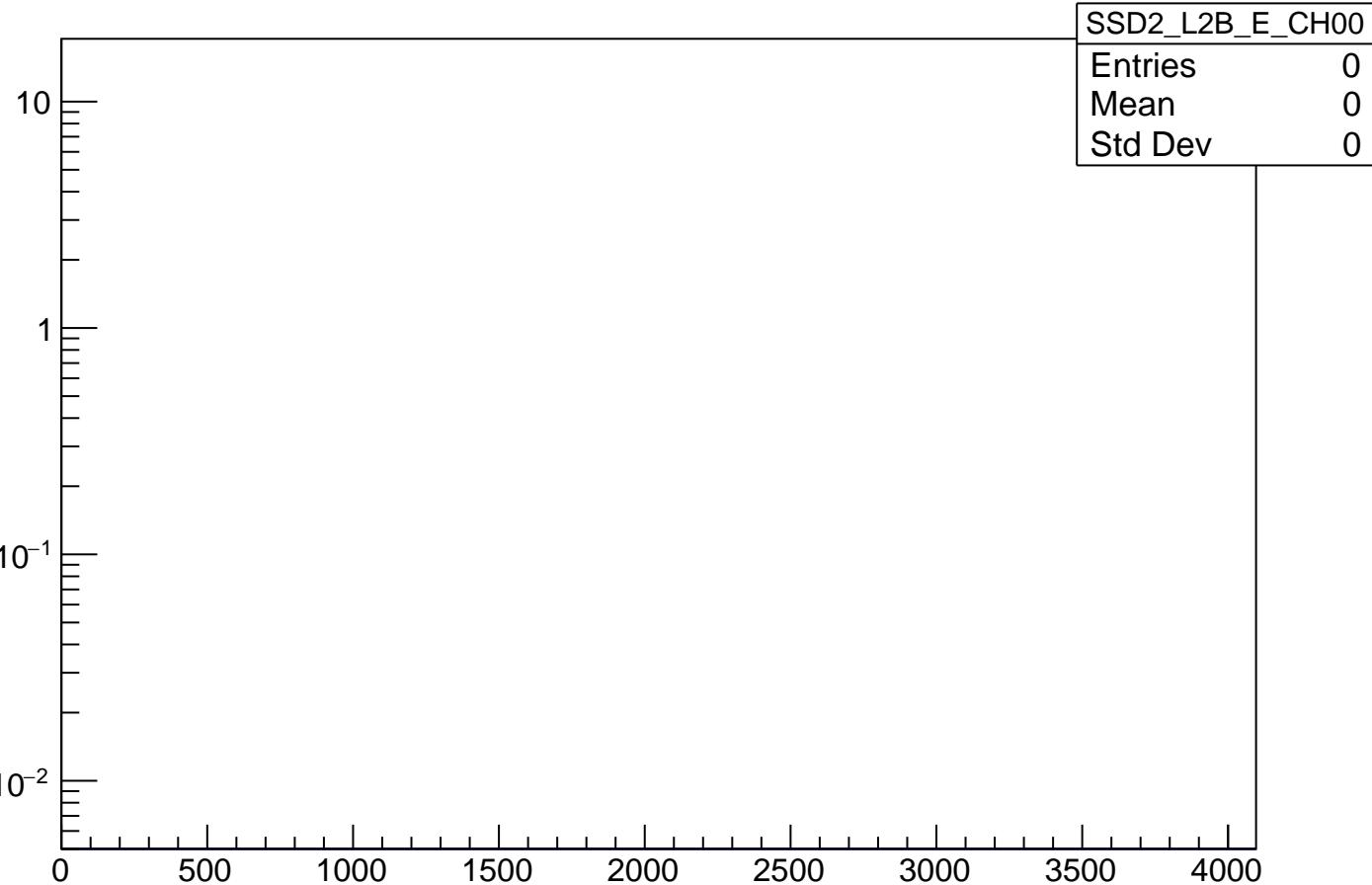
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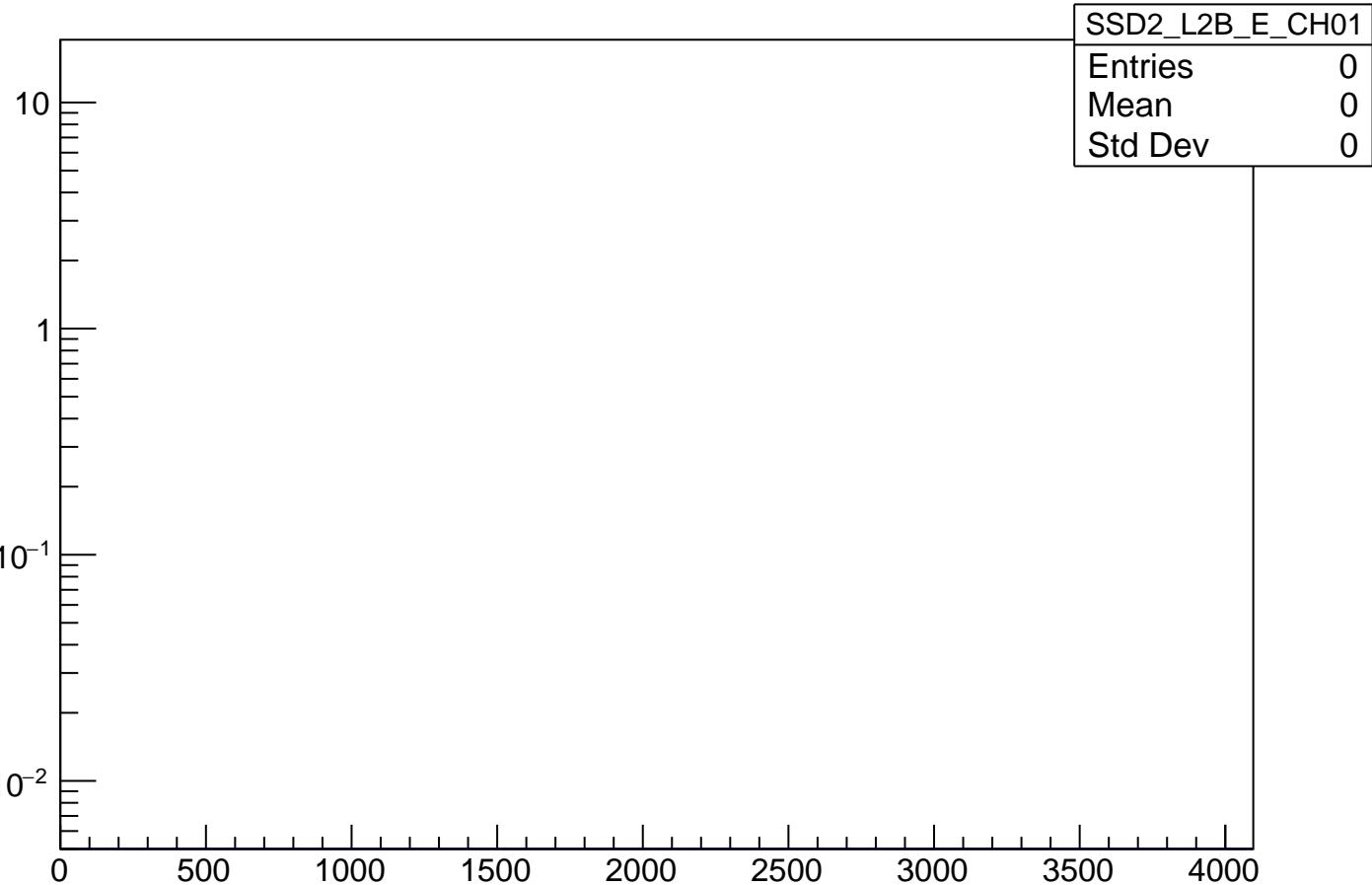
SSD2_L2F_E_CH15



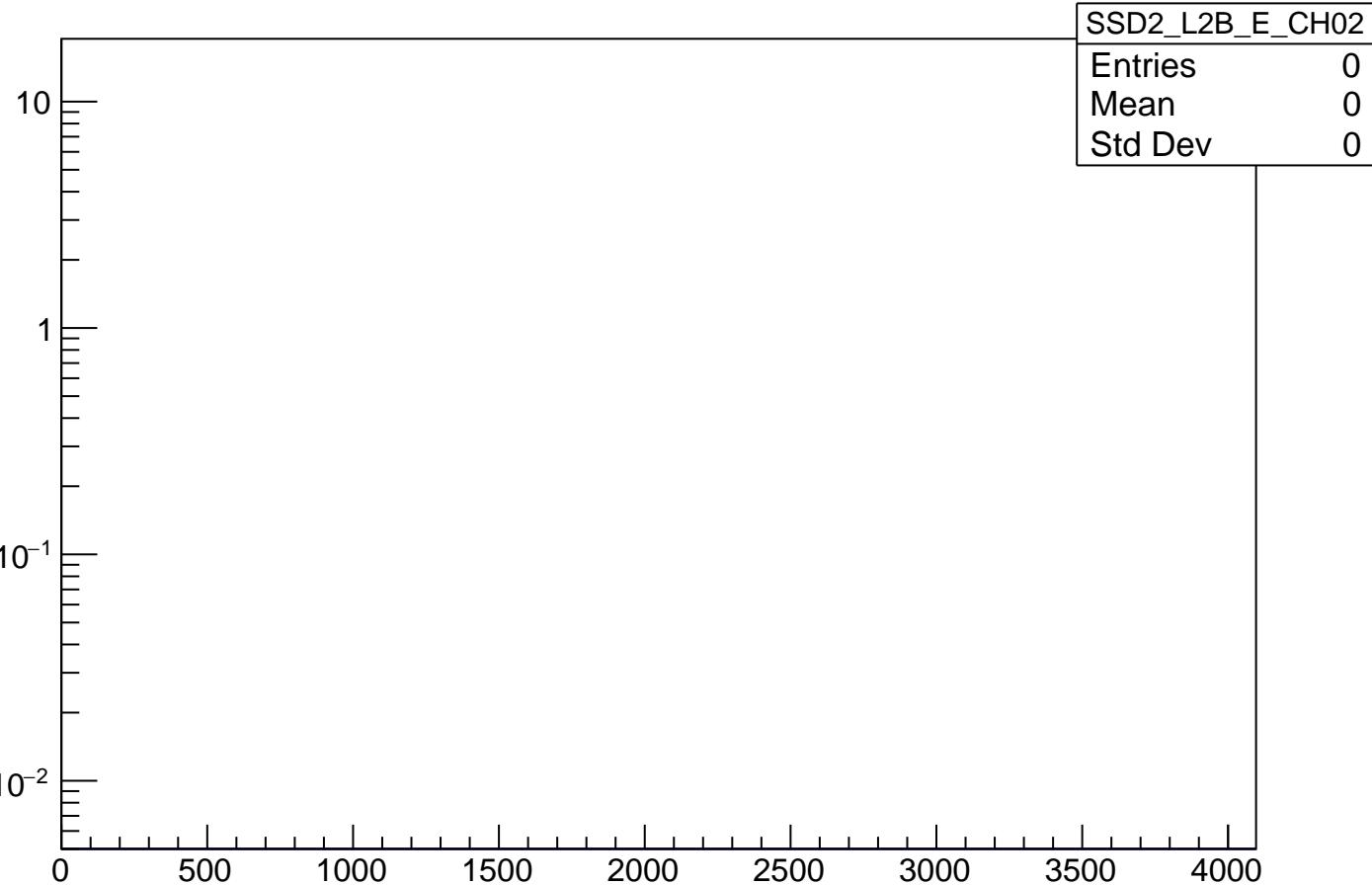
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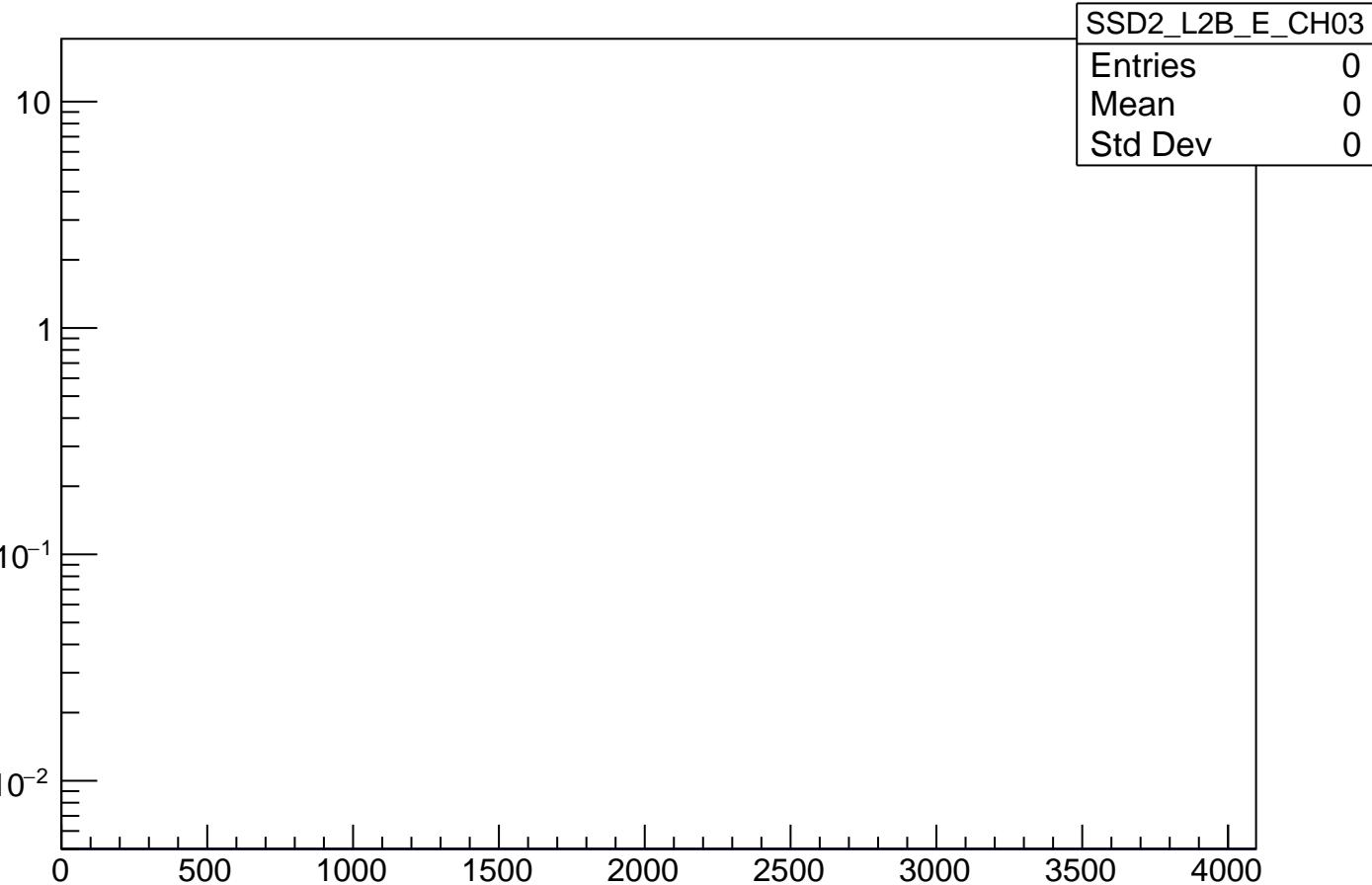
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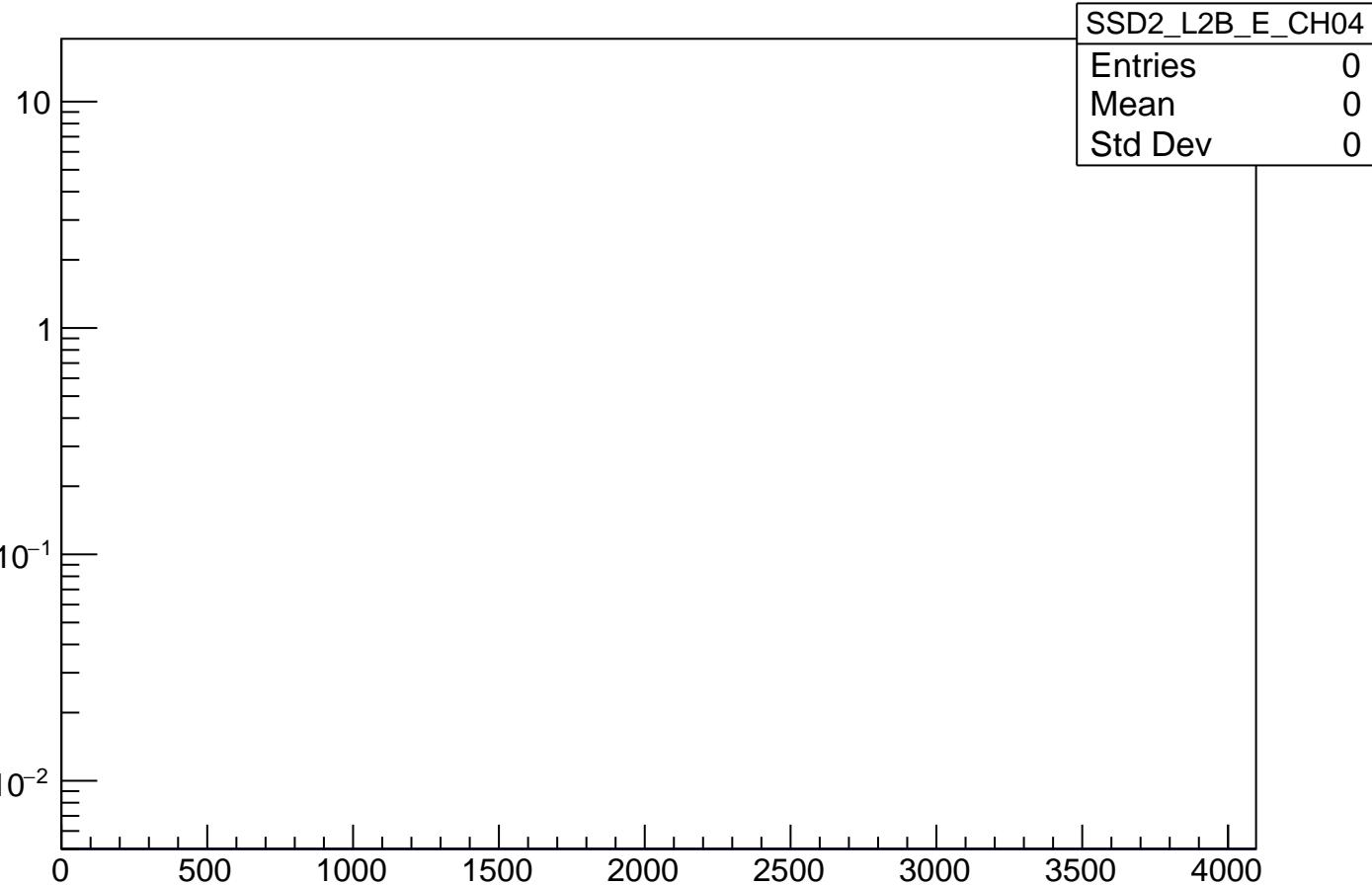
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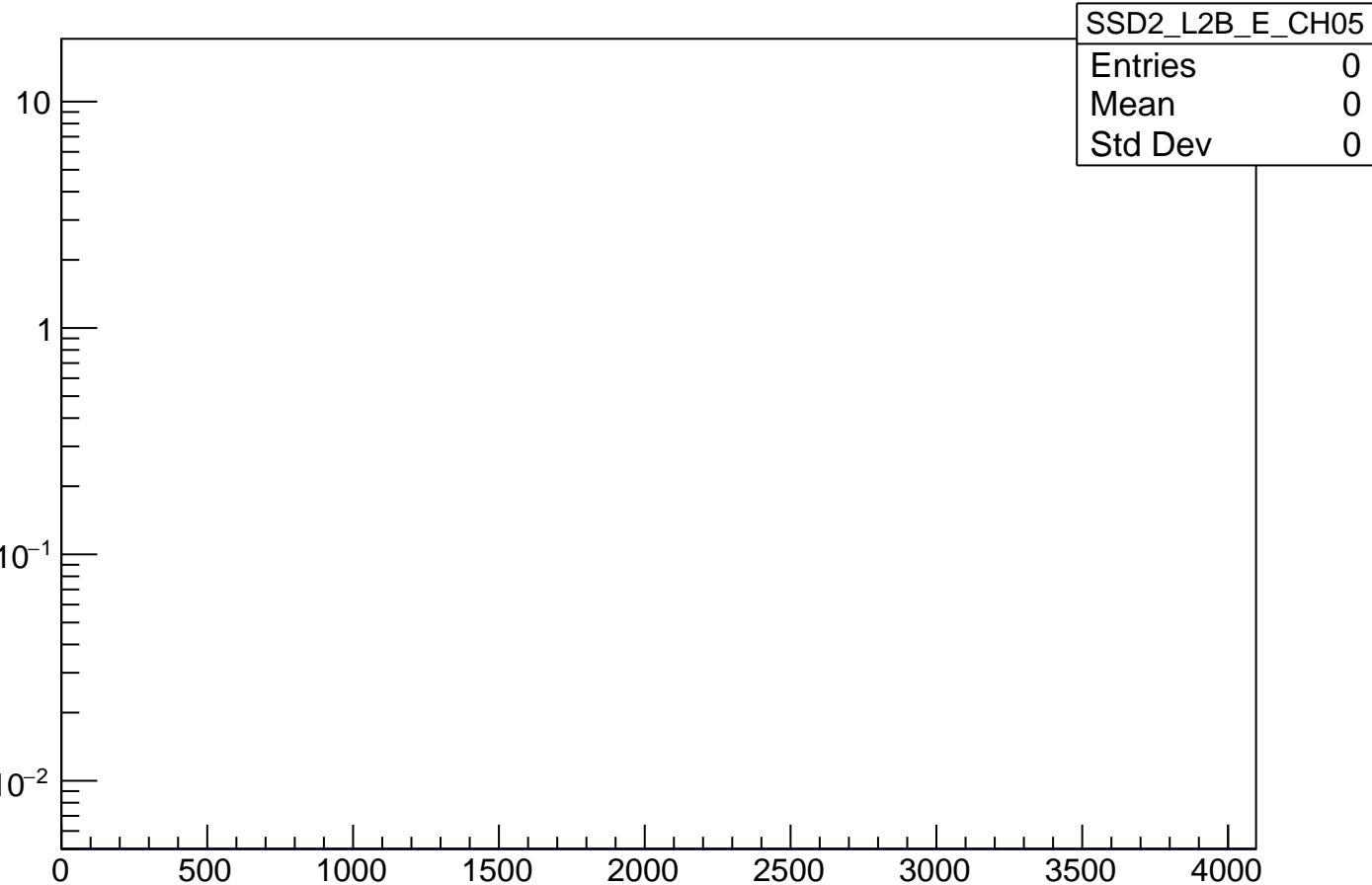
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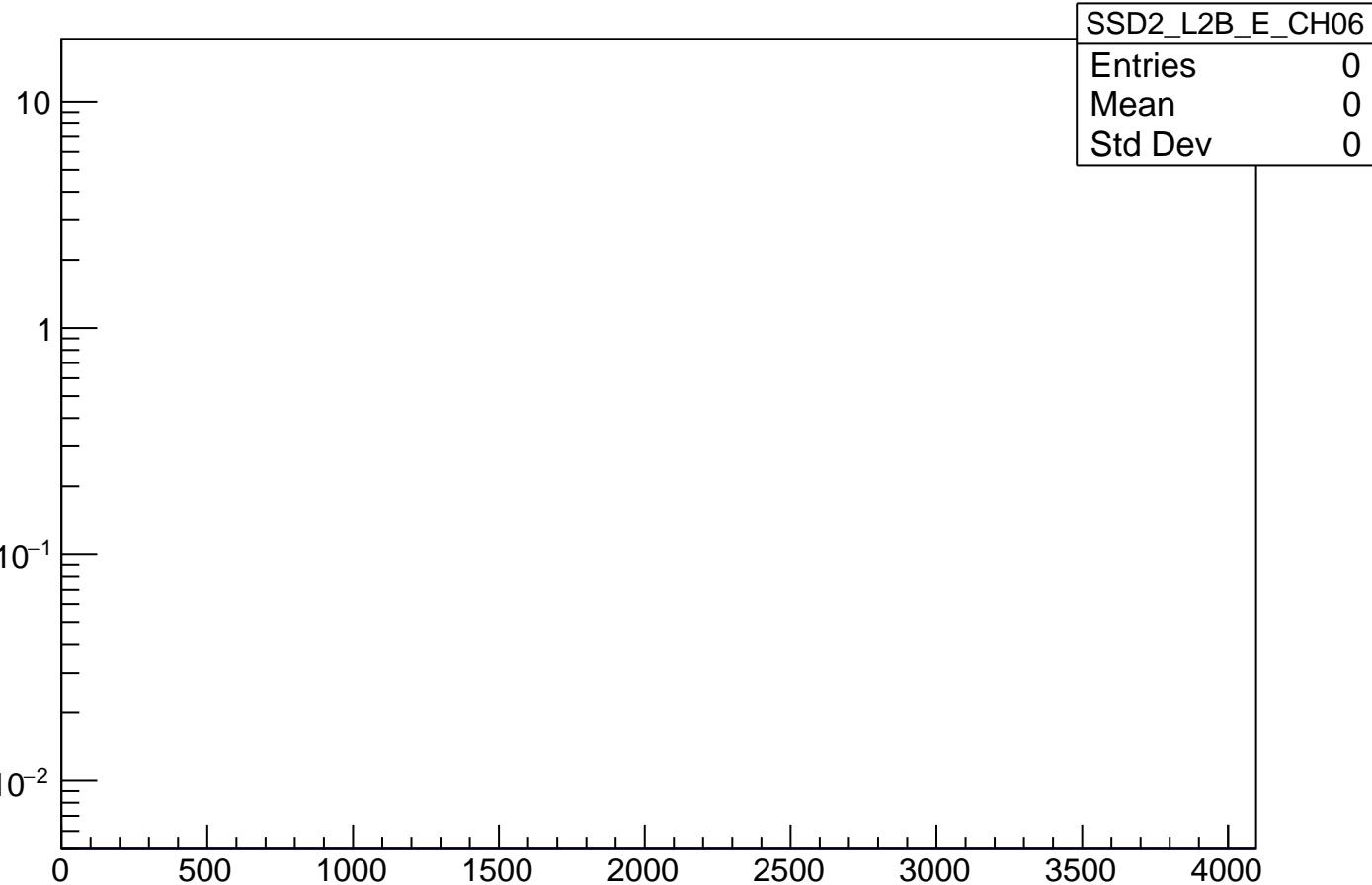
SSD2_L2B_E_CH04



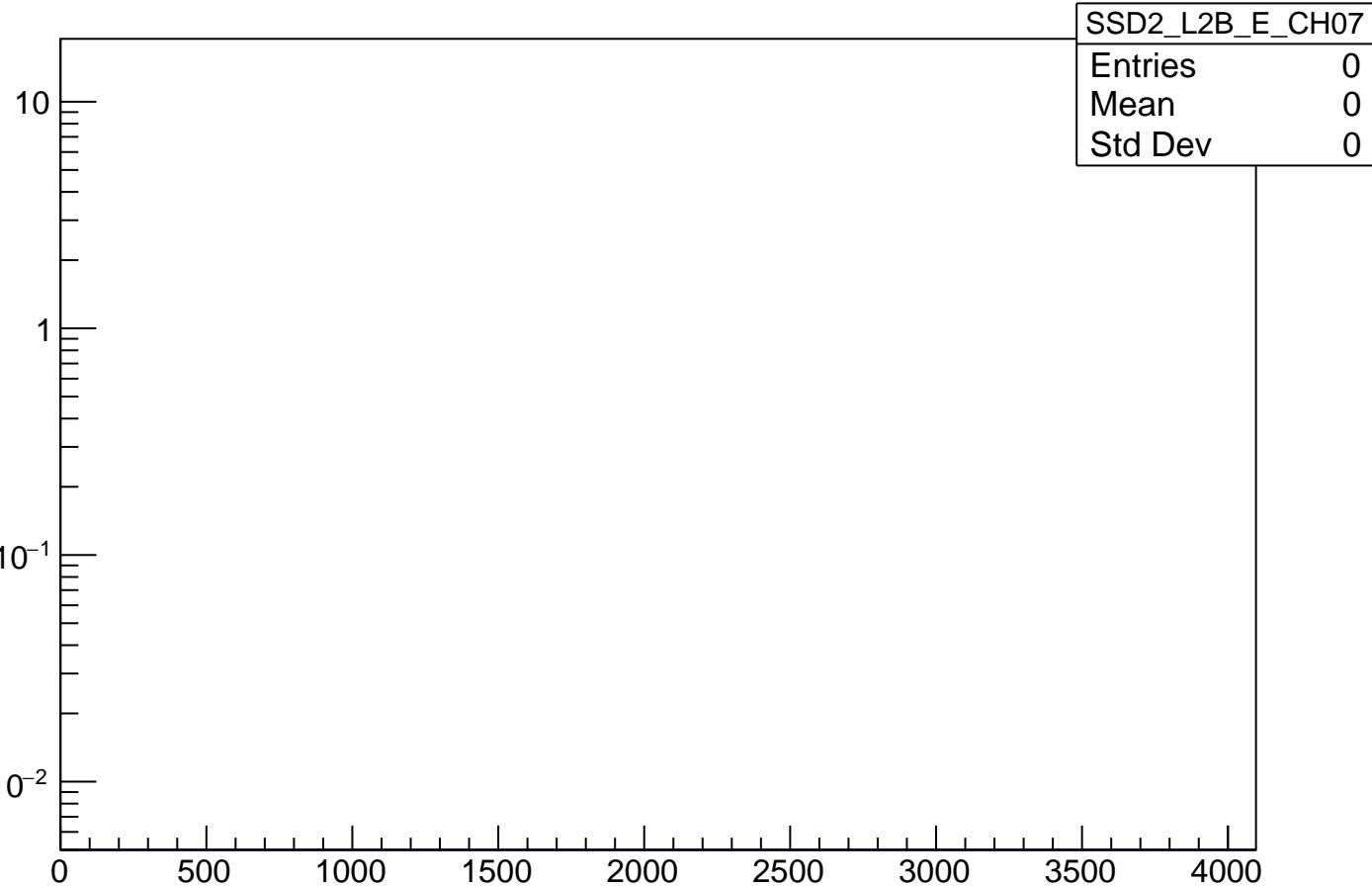
SSD2_L2B_E_CH05



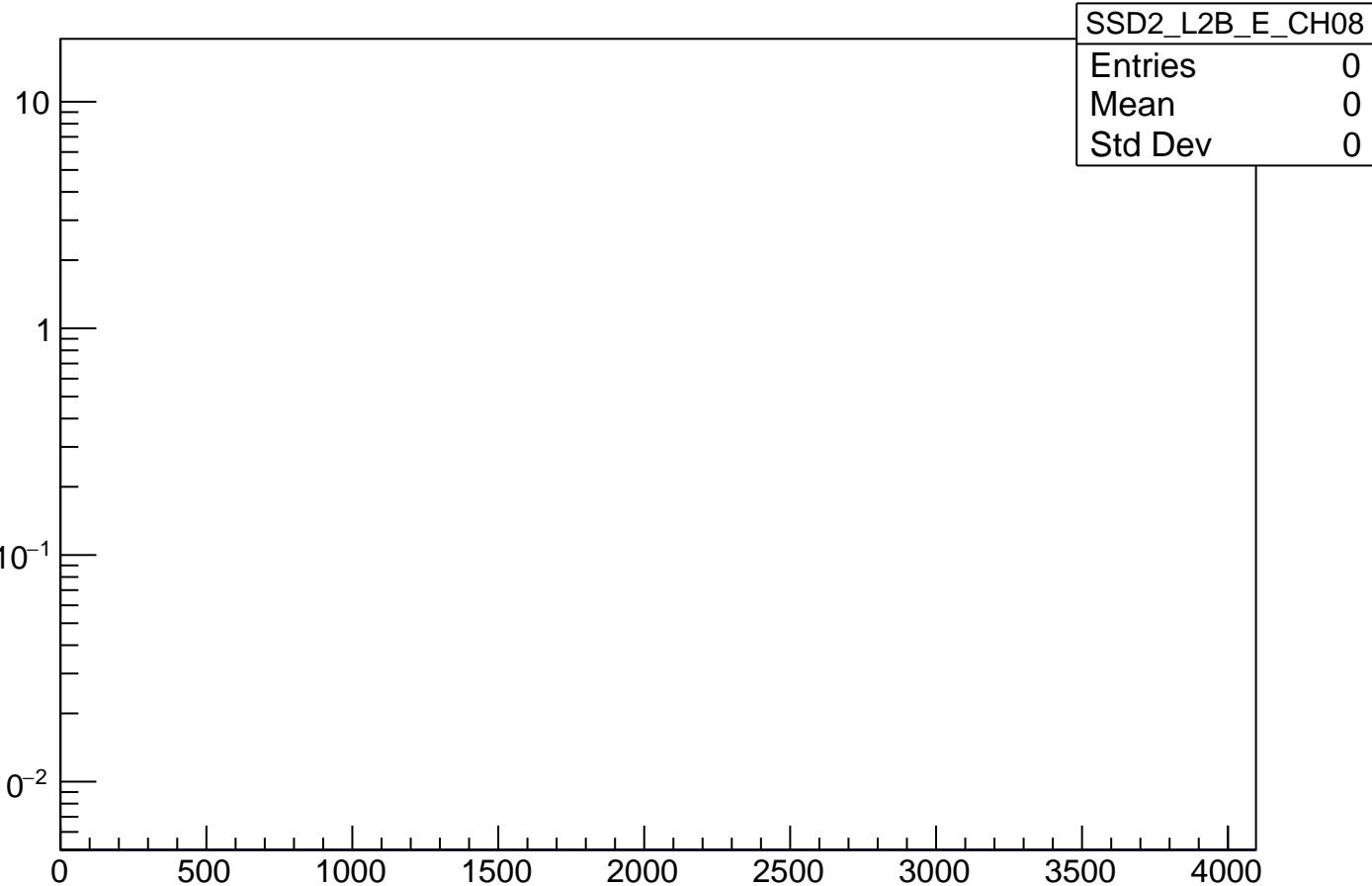
SSD2_L2B_E_CH06



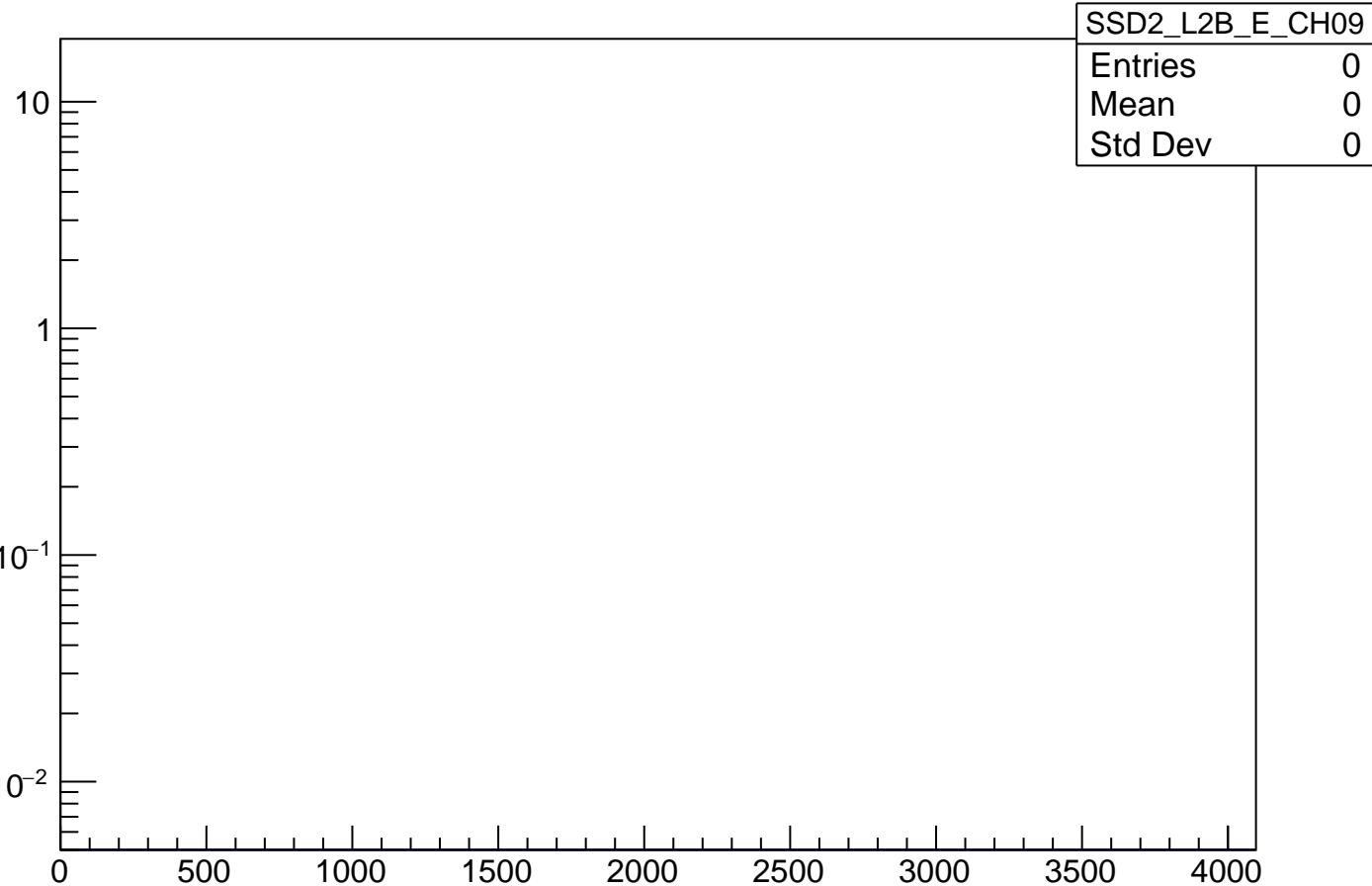
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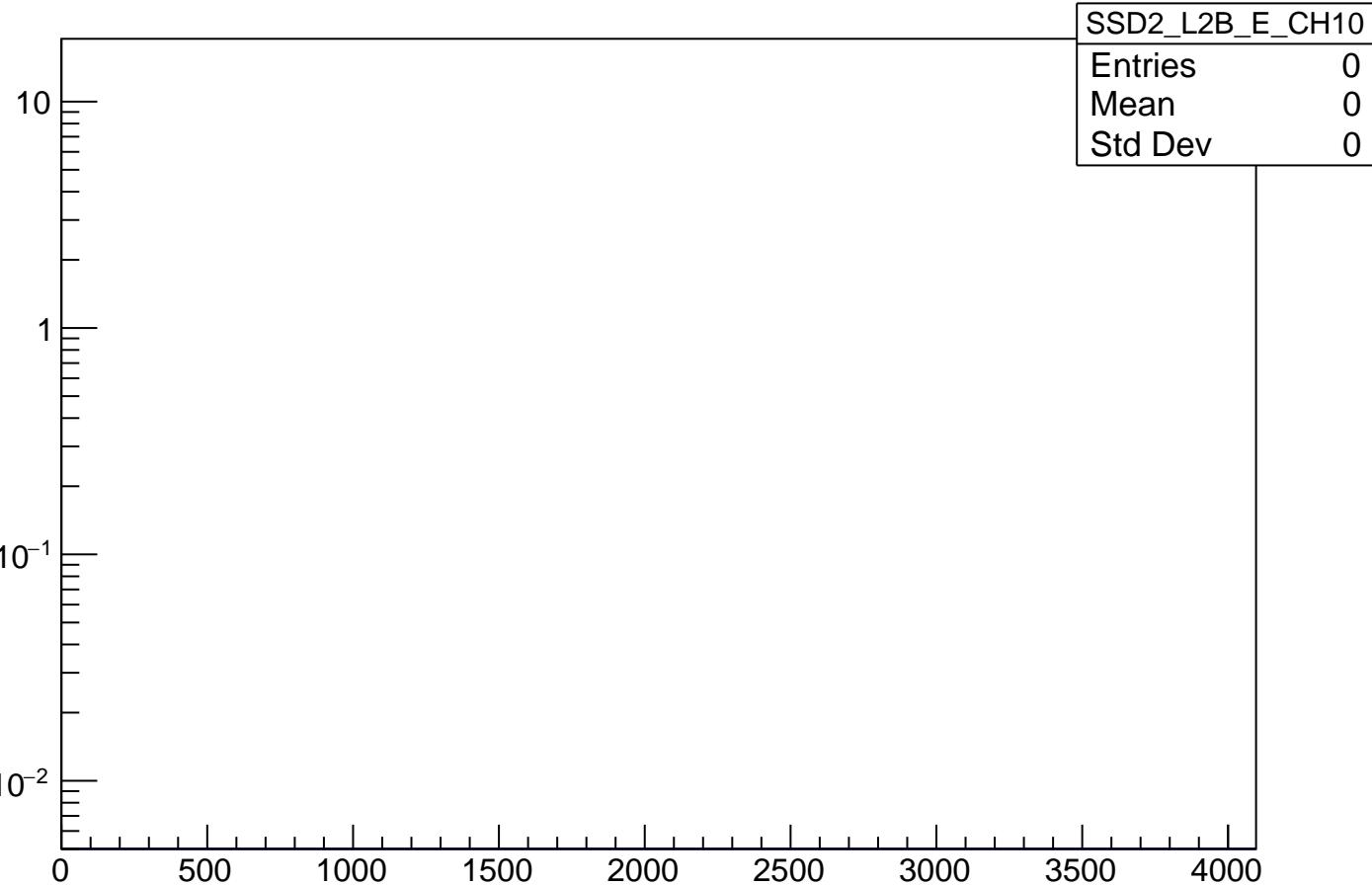
SSD2_L2B_E_CH08



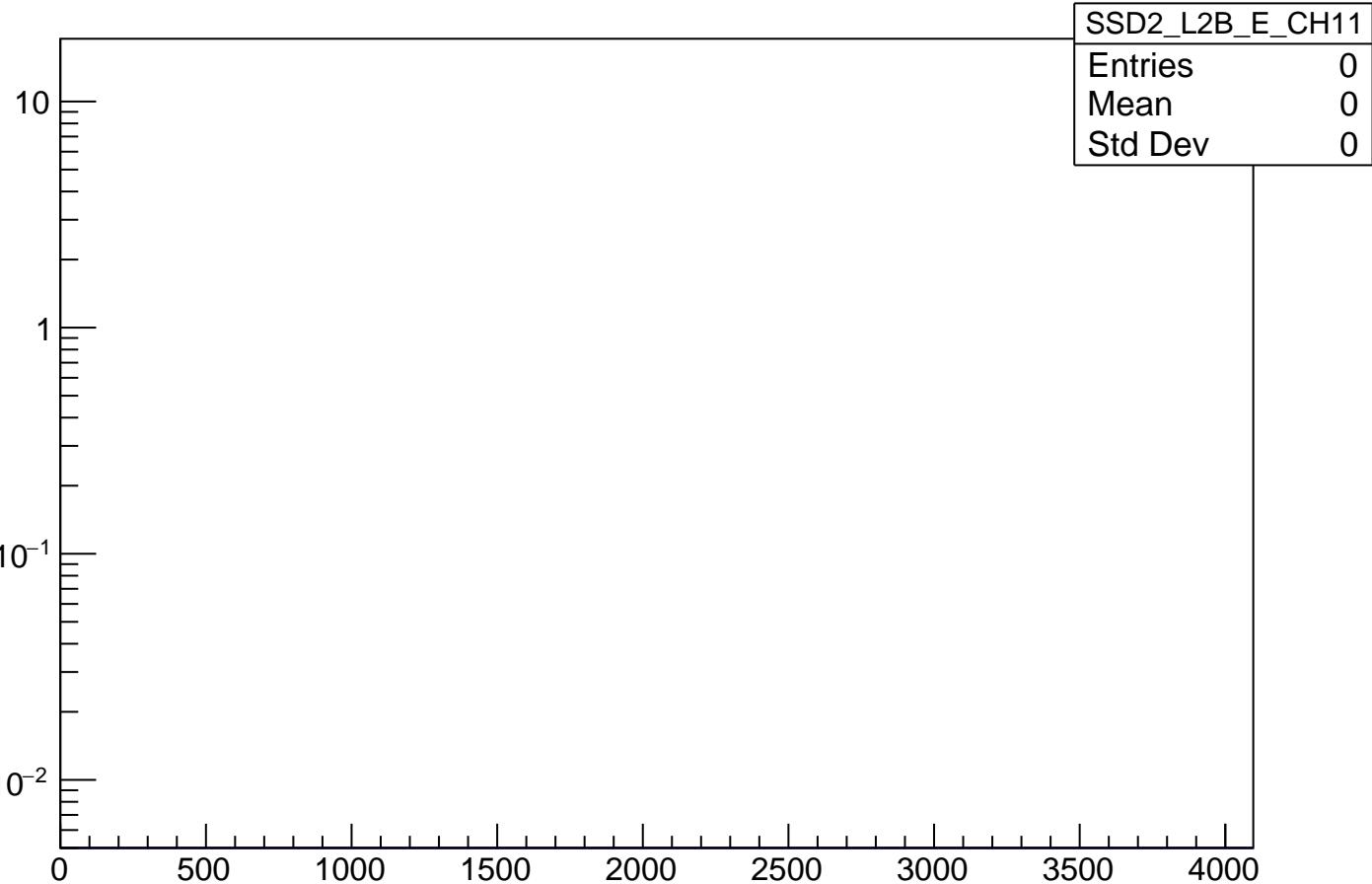
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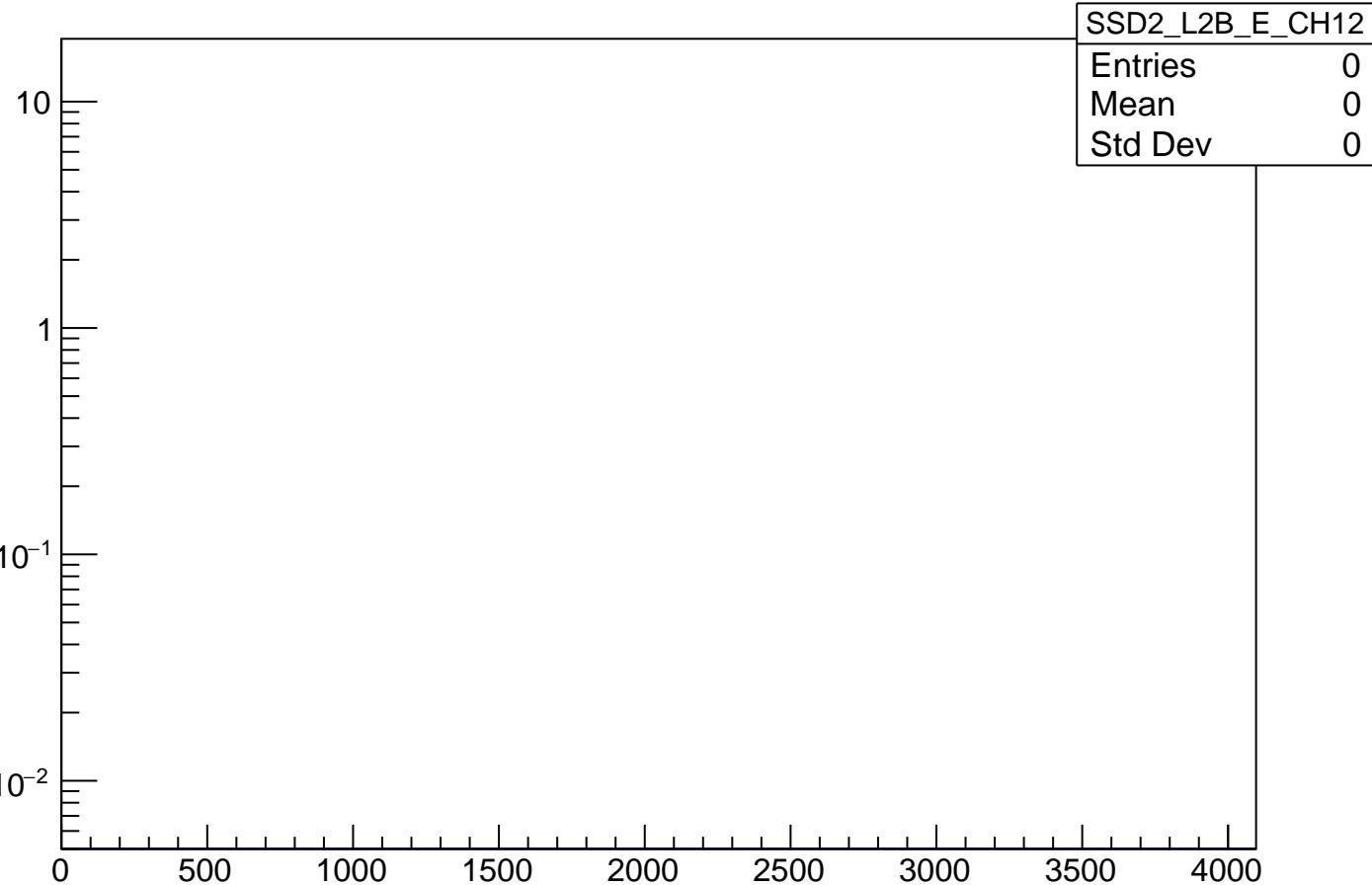
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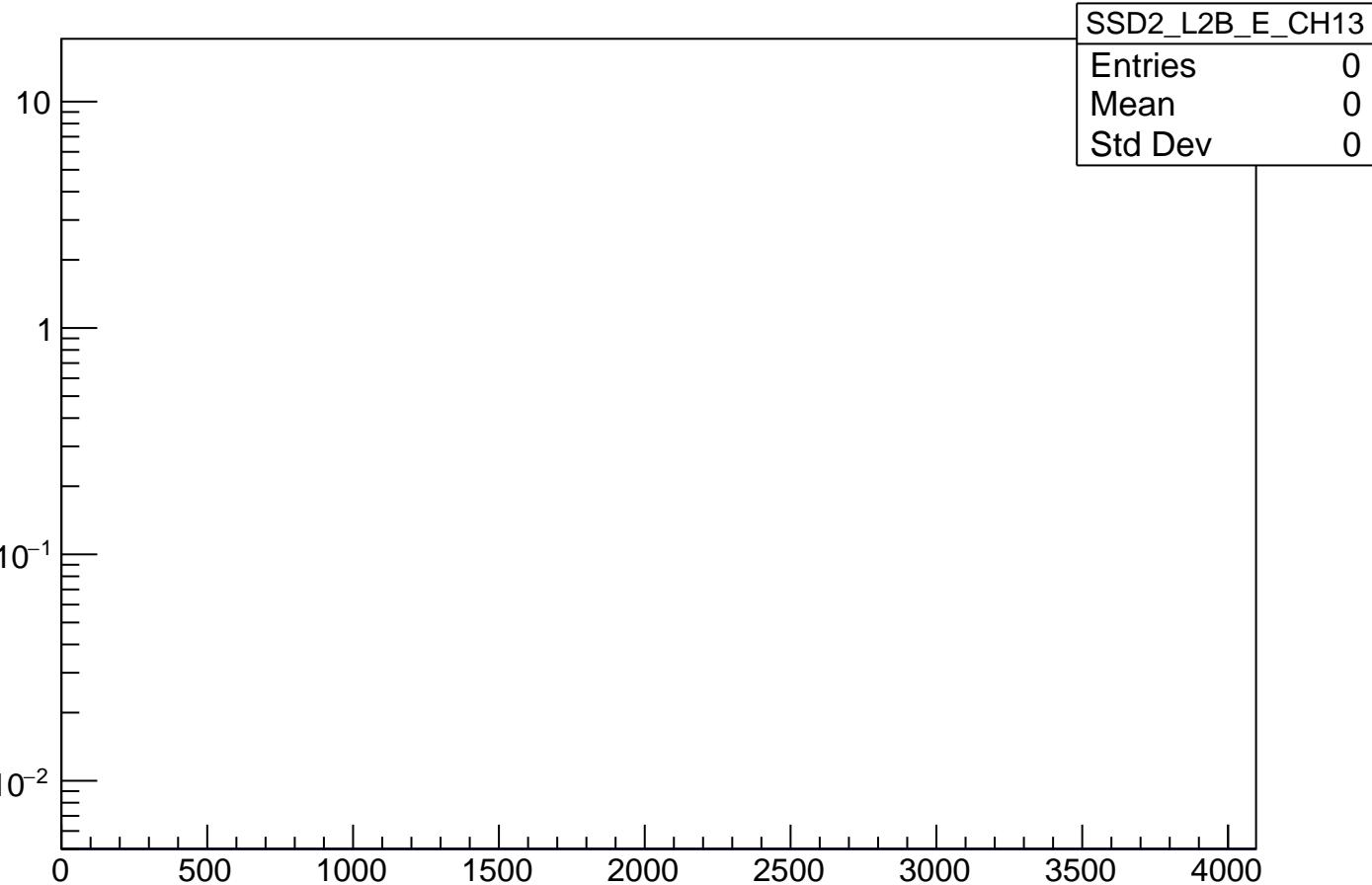
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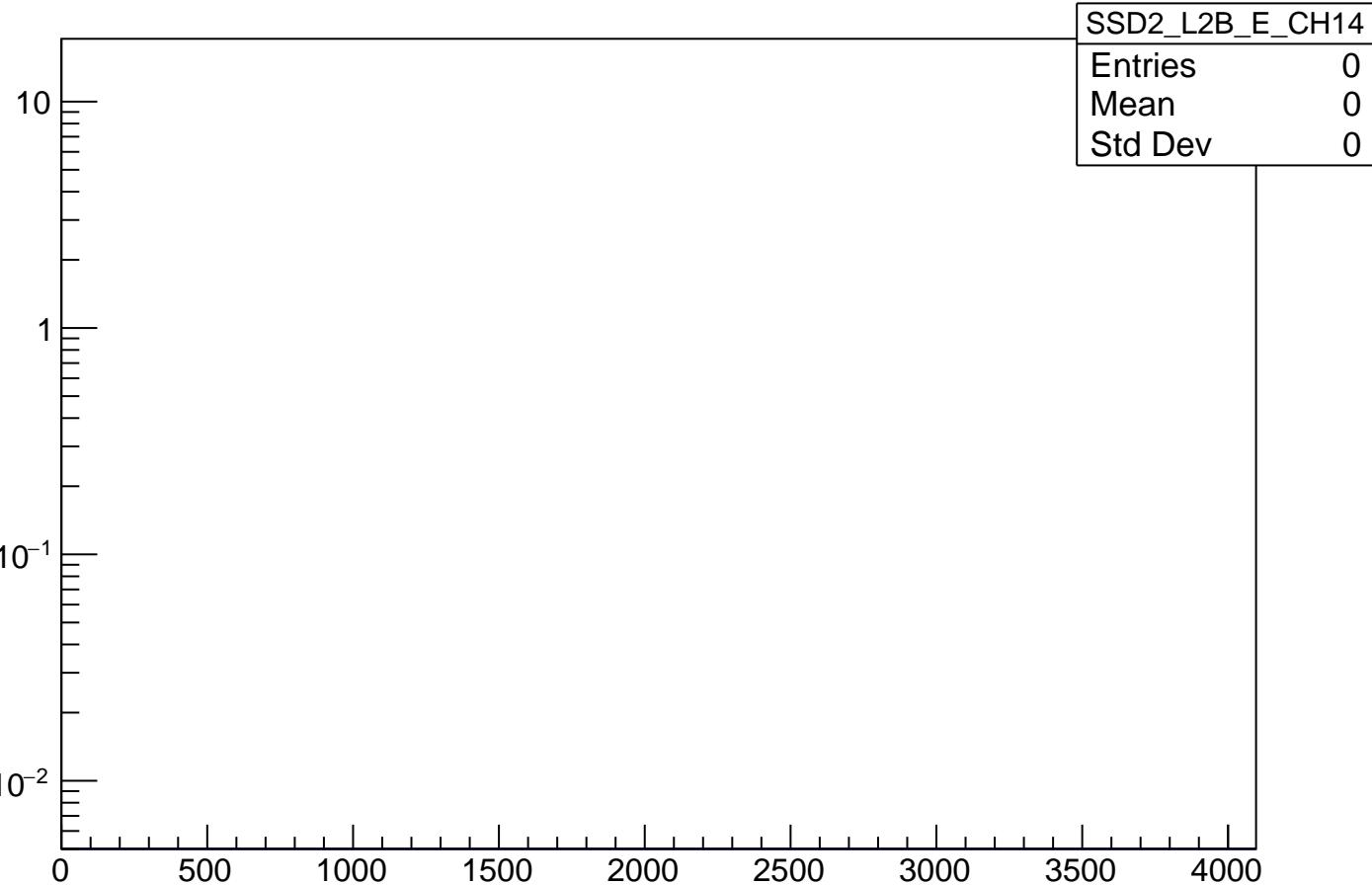
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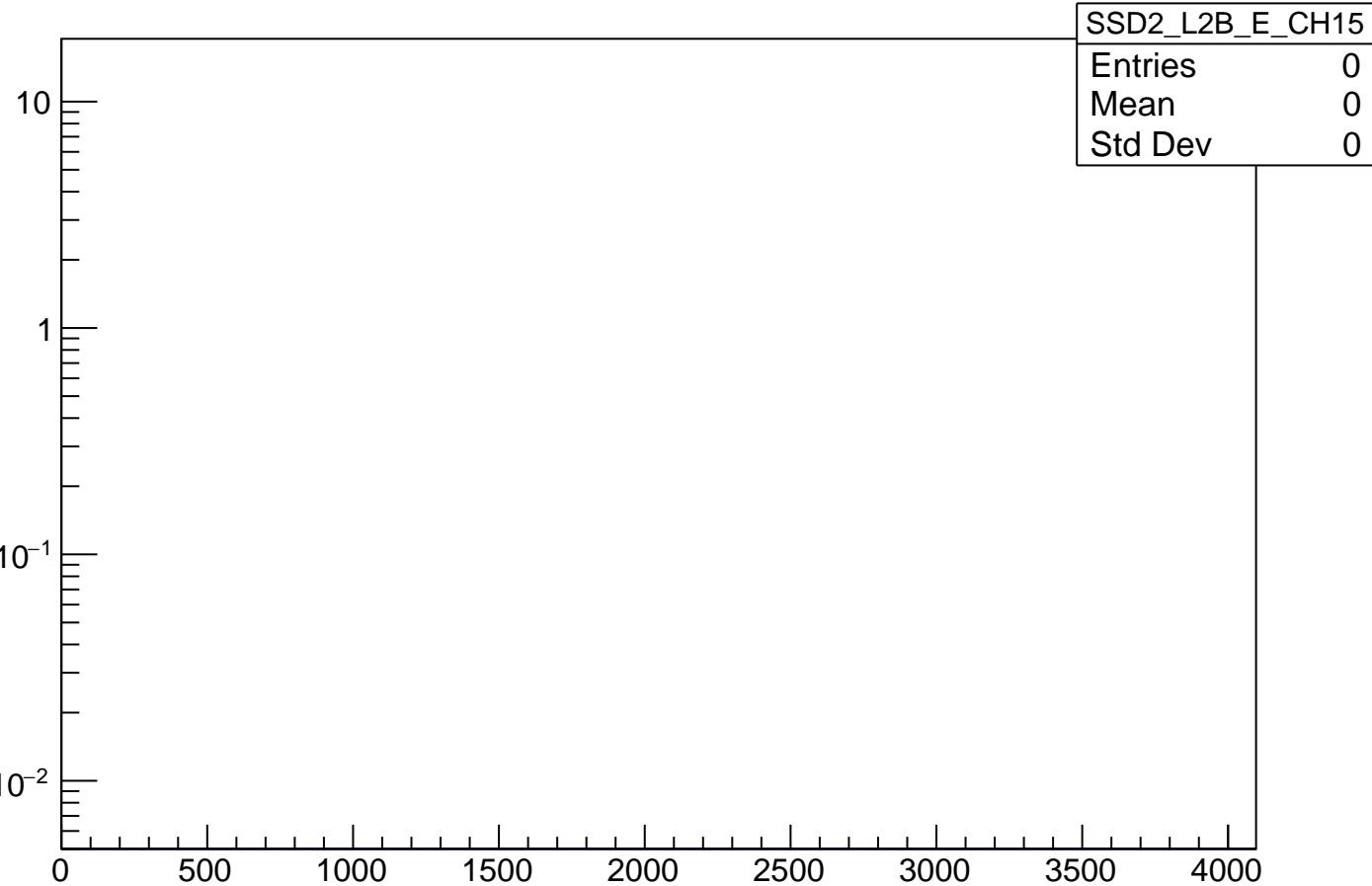
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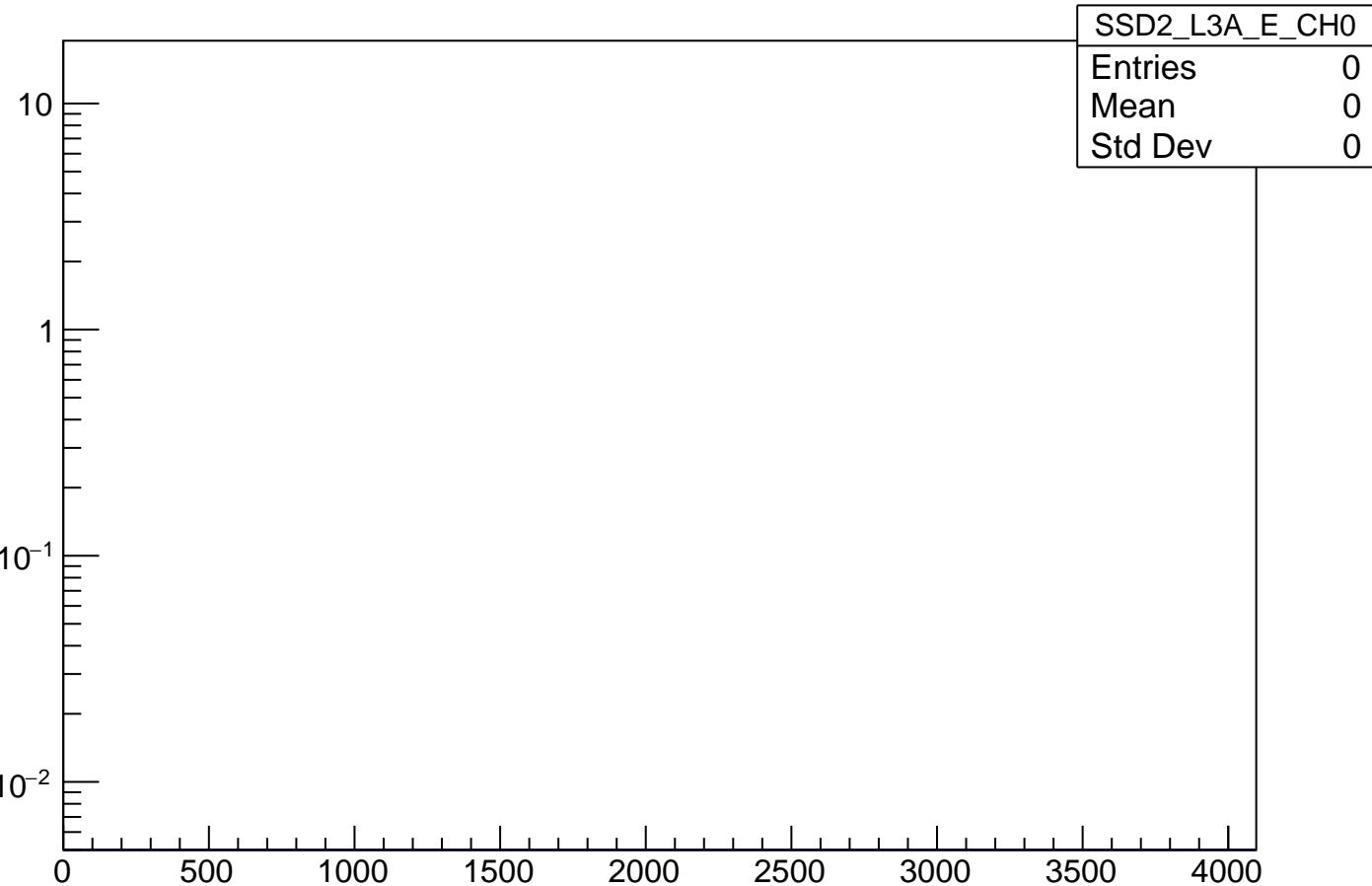
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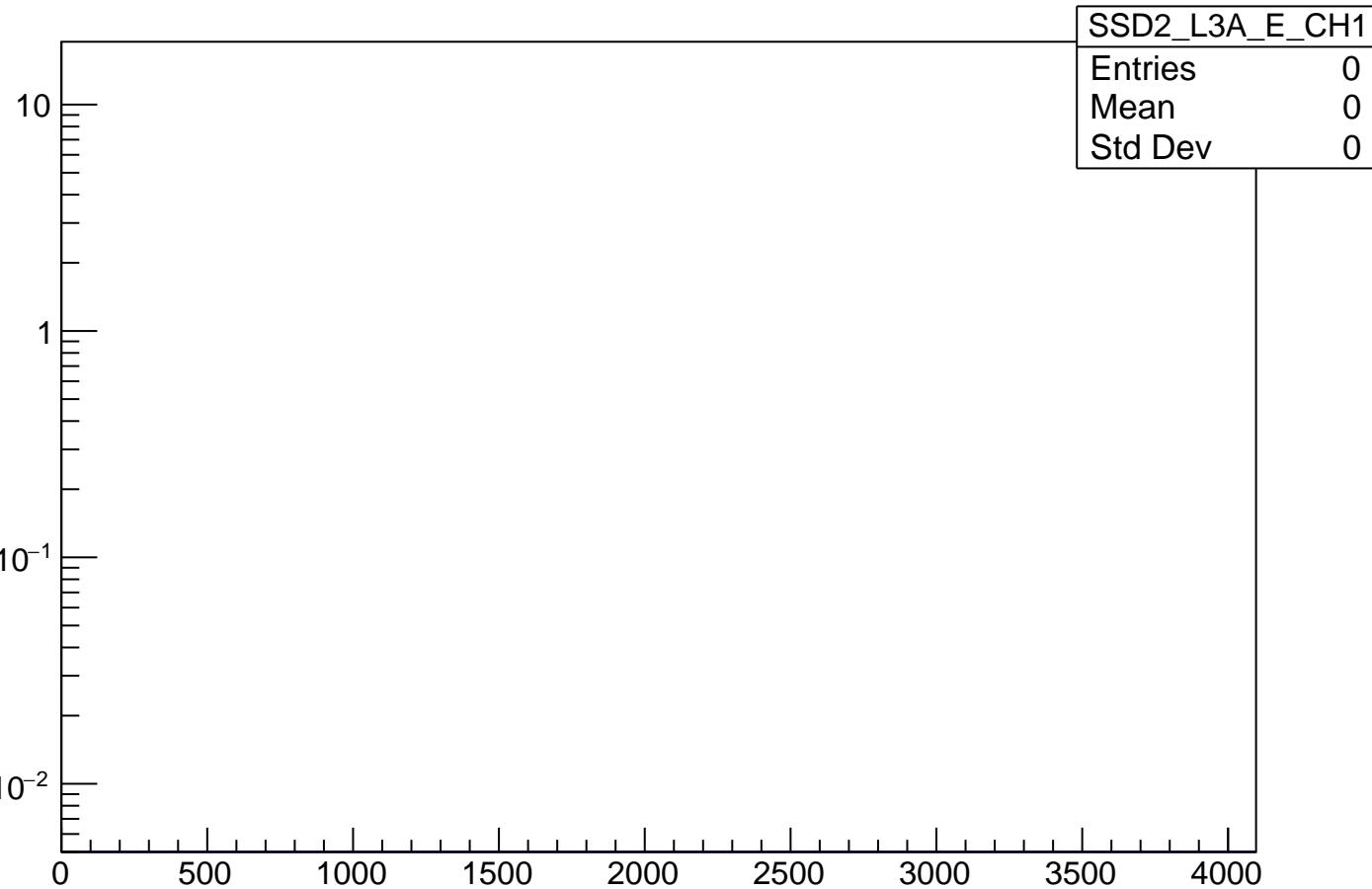
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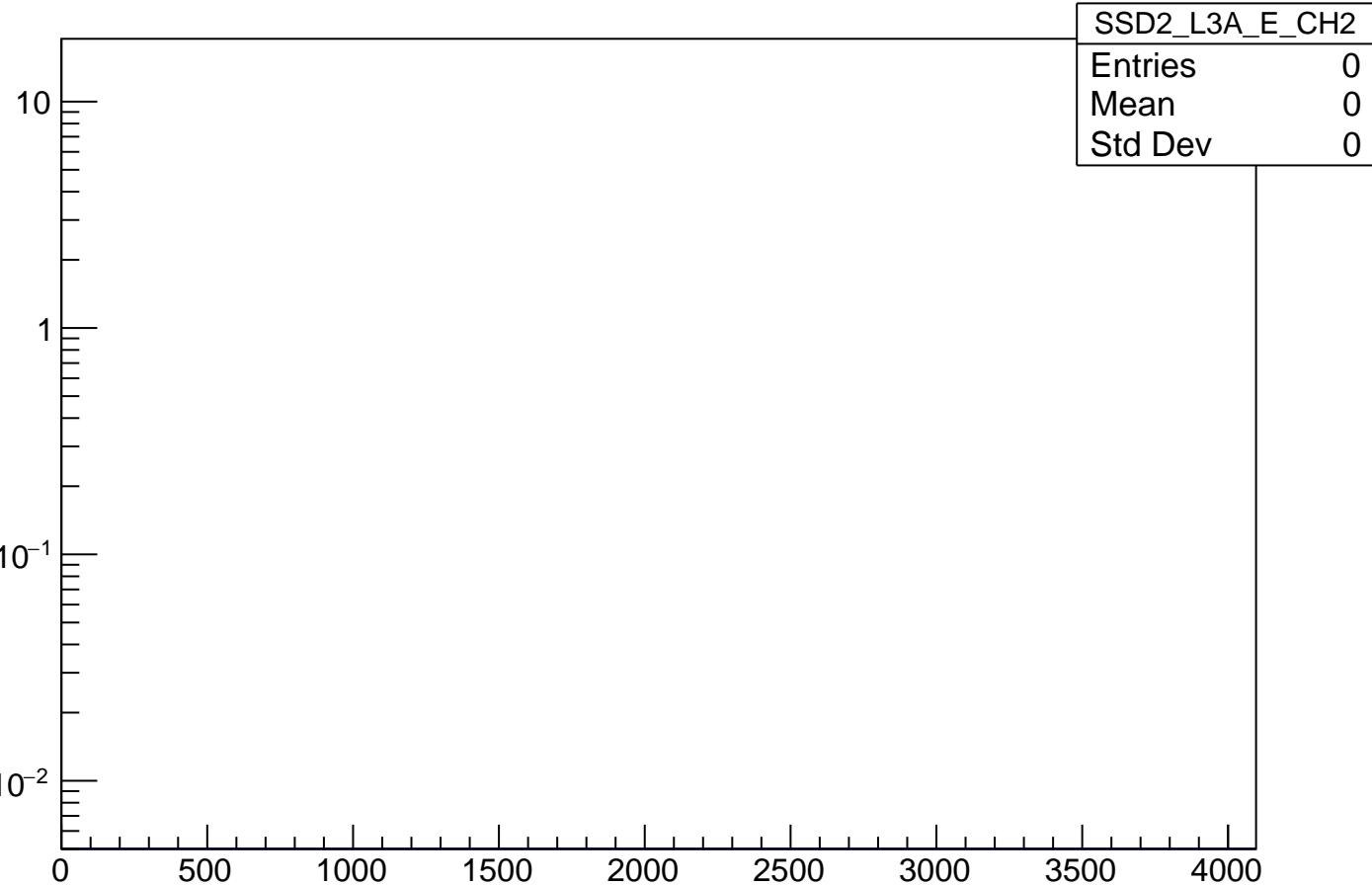
SSD2_L3A_E_CH0



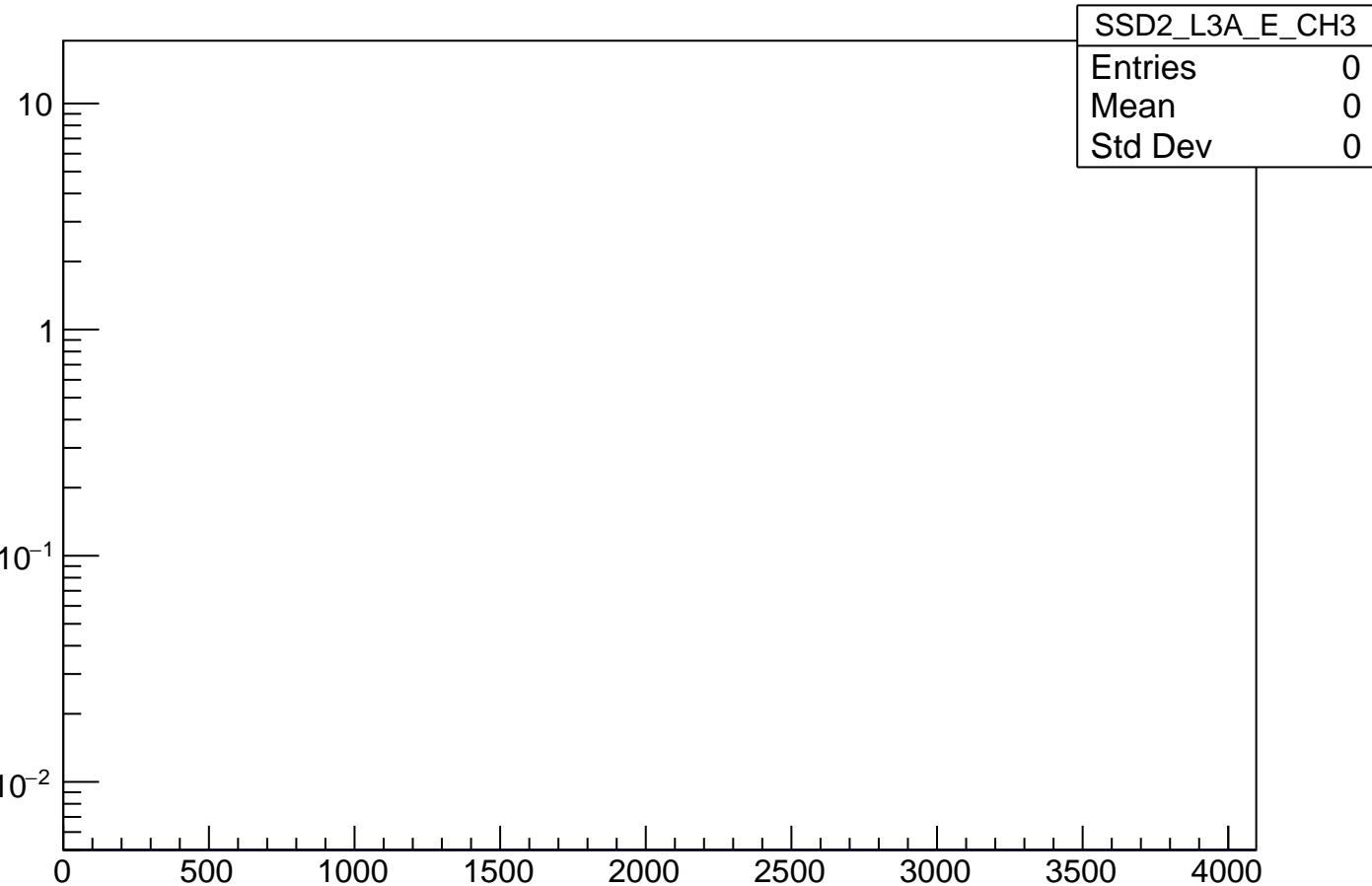
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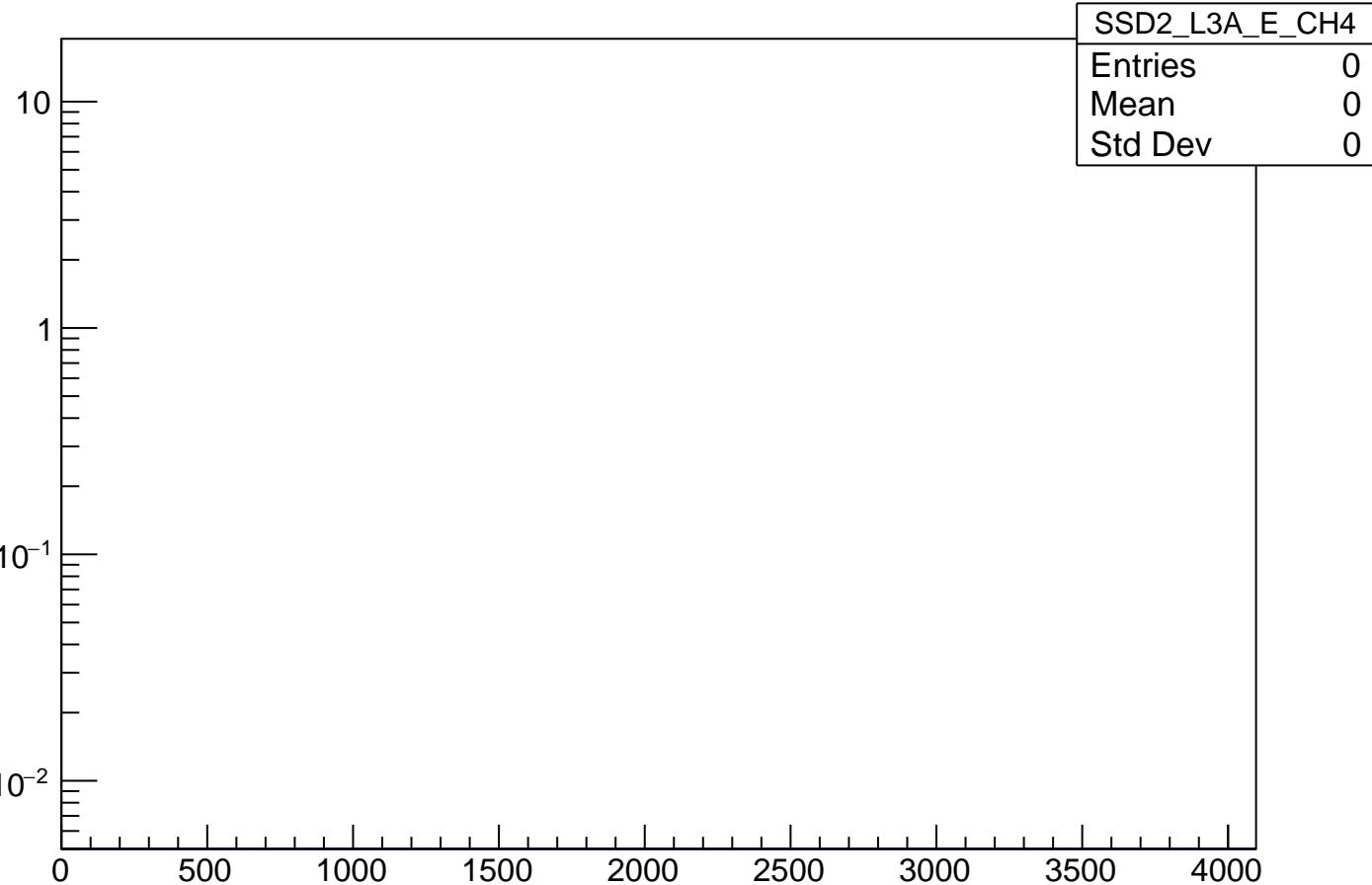
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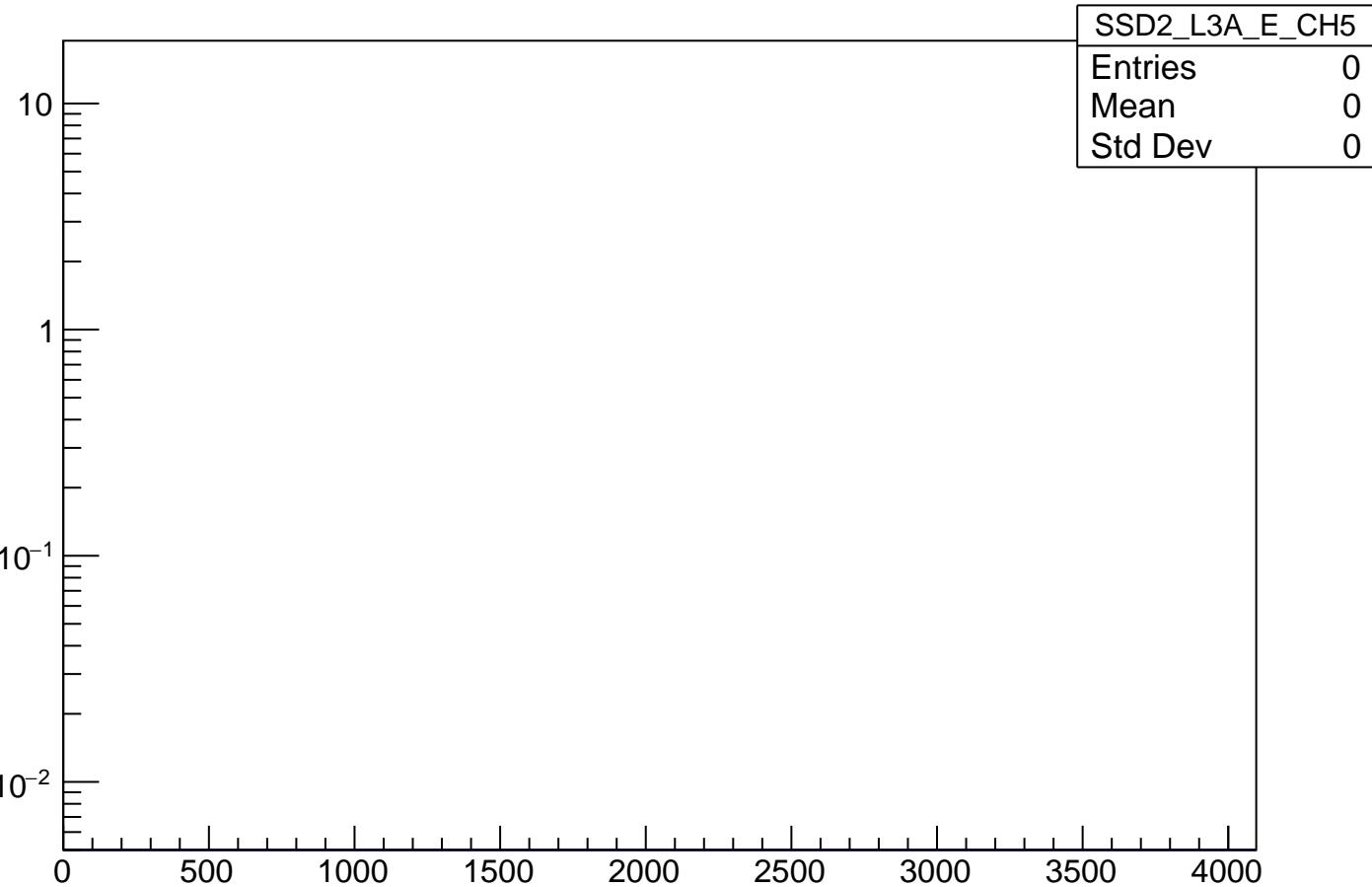
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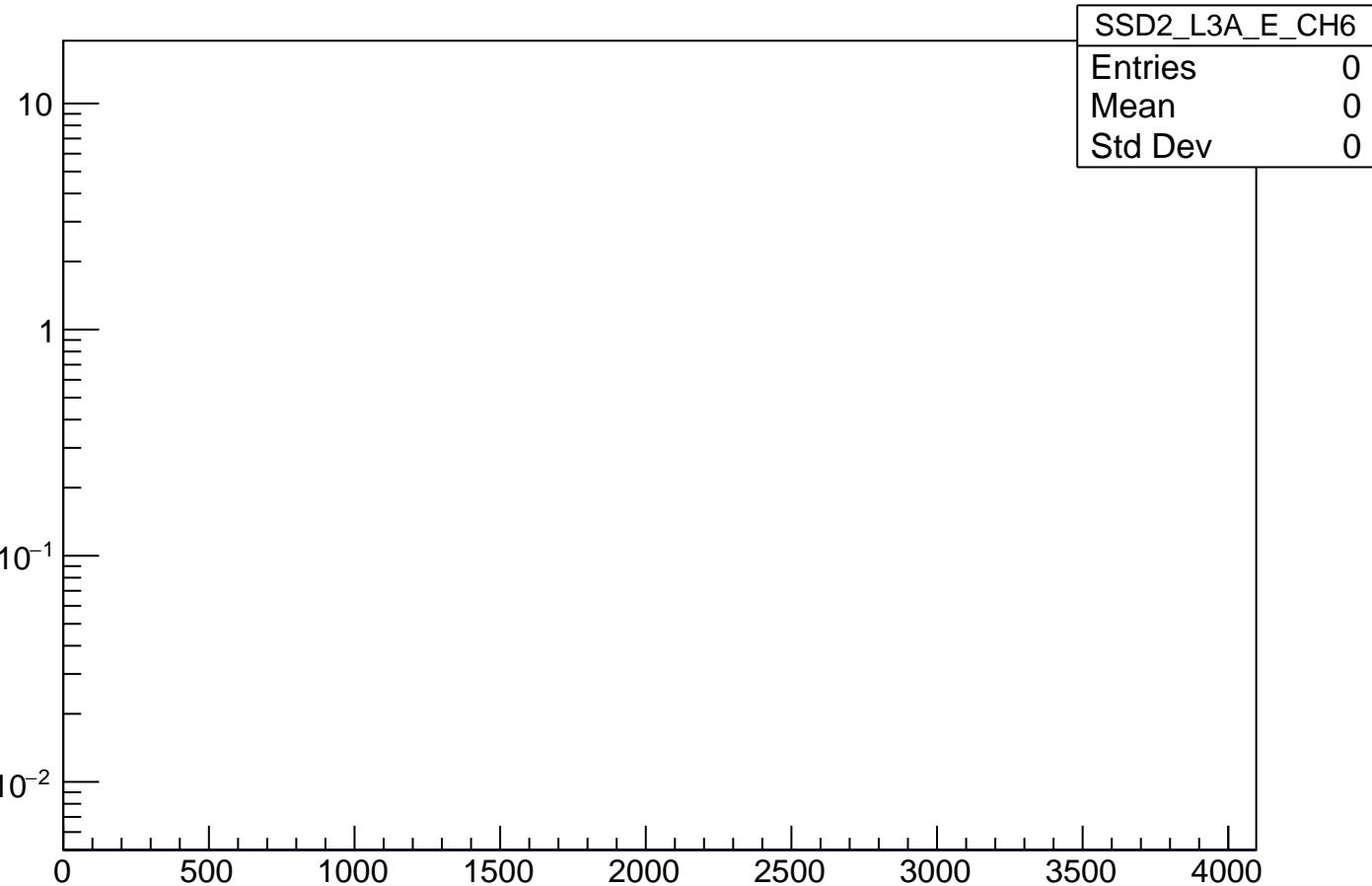
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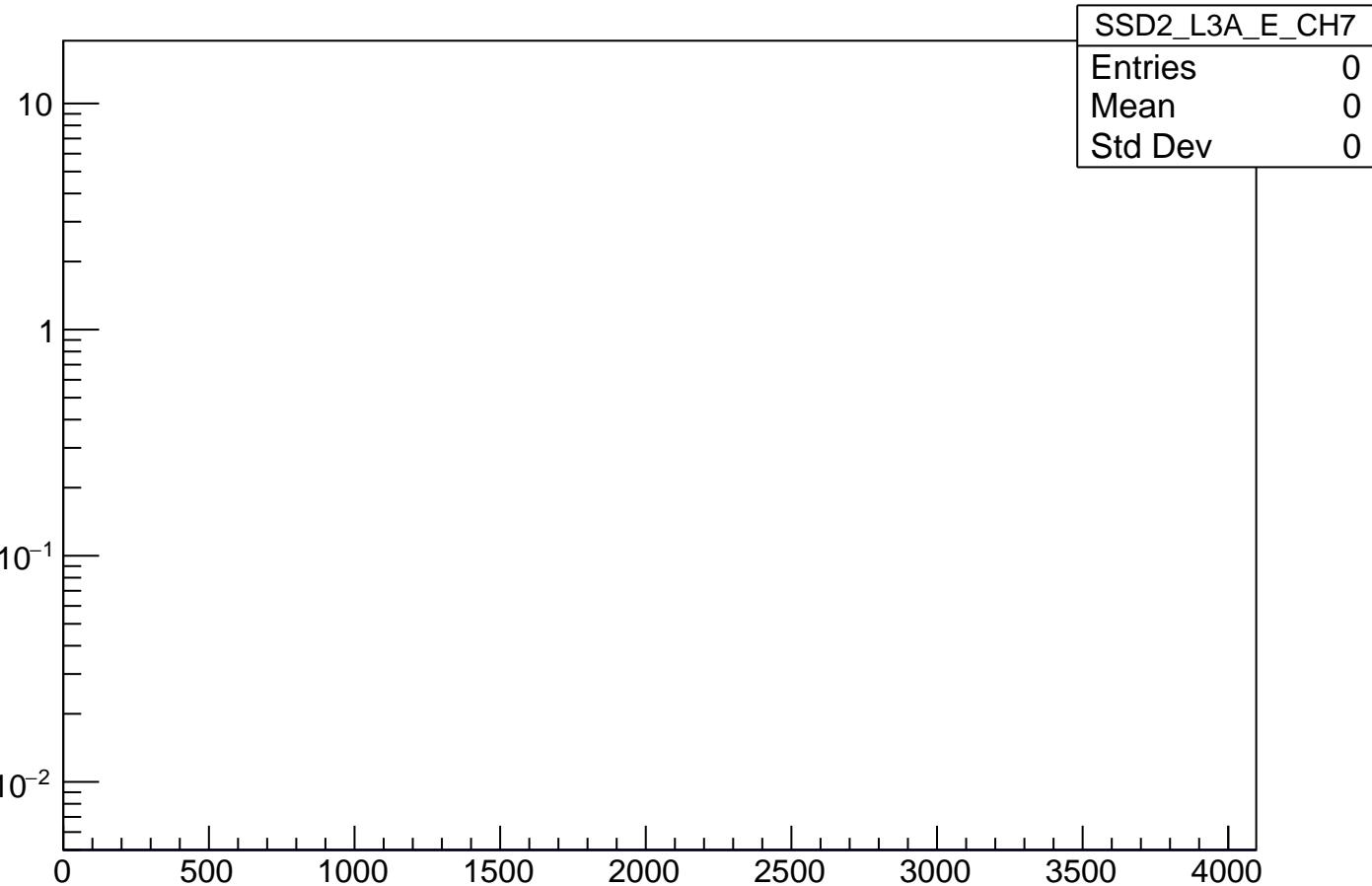
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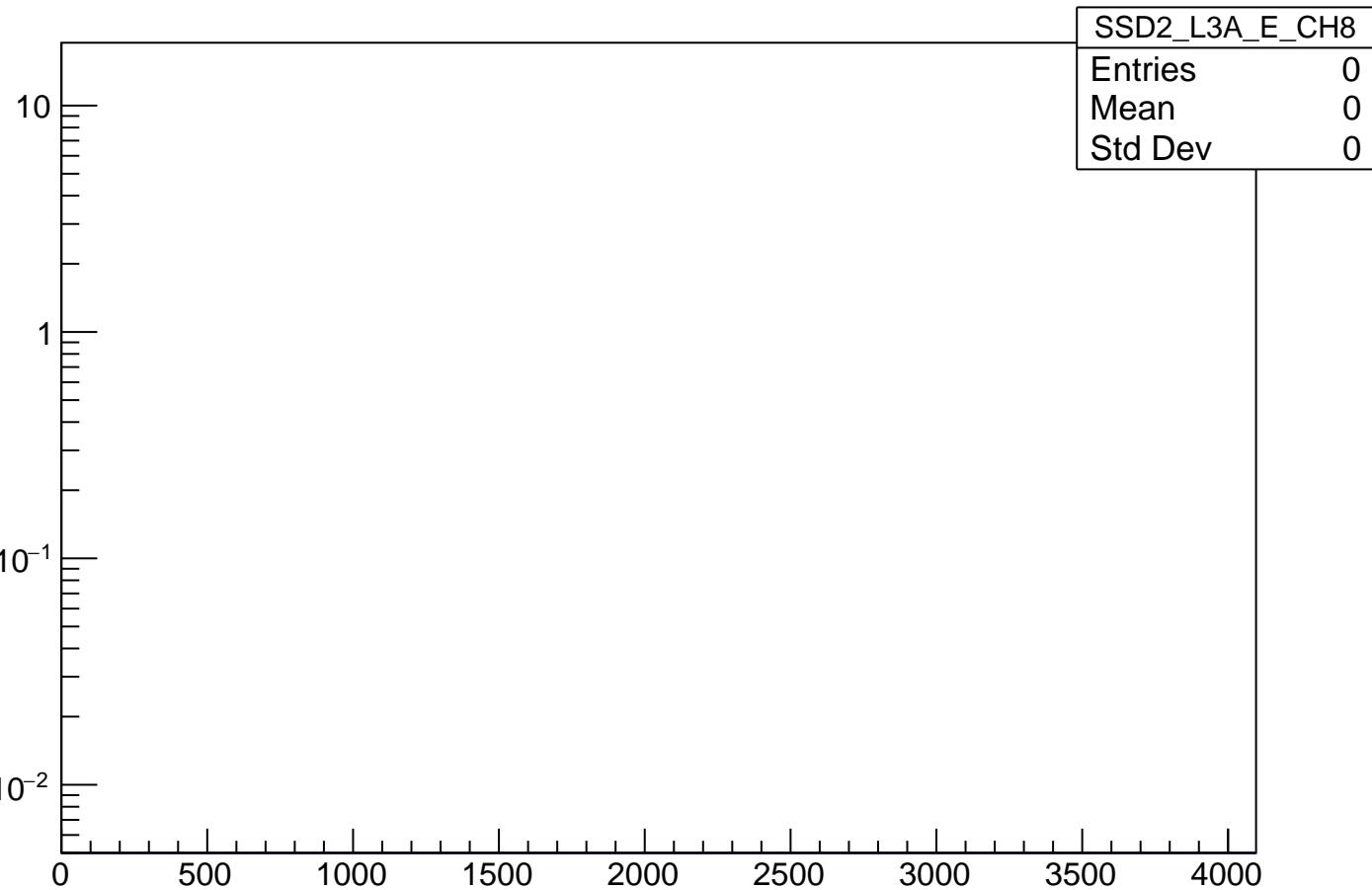
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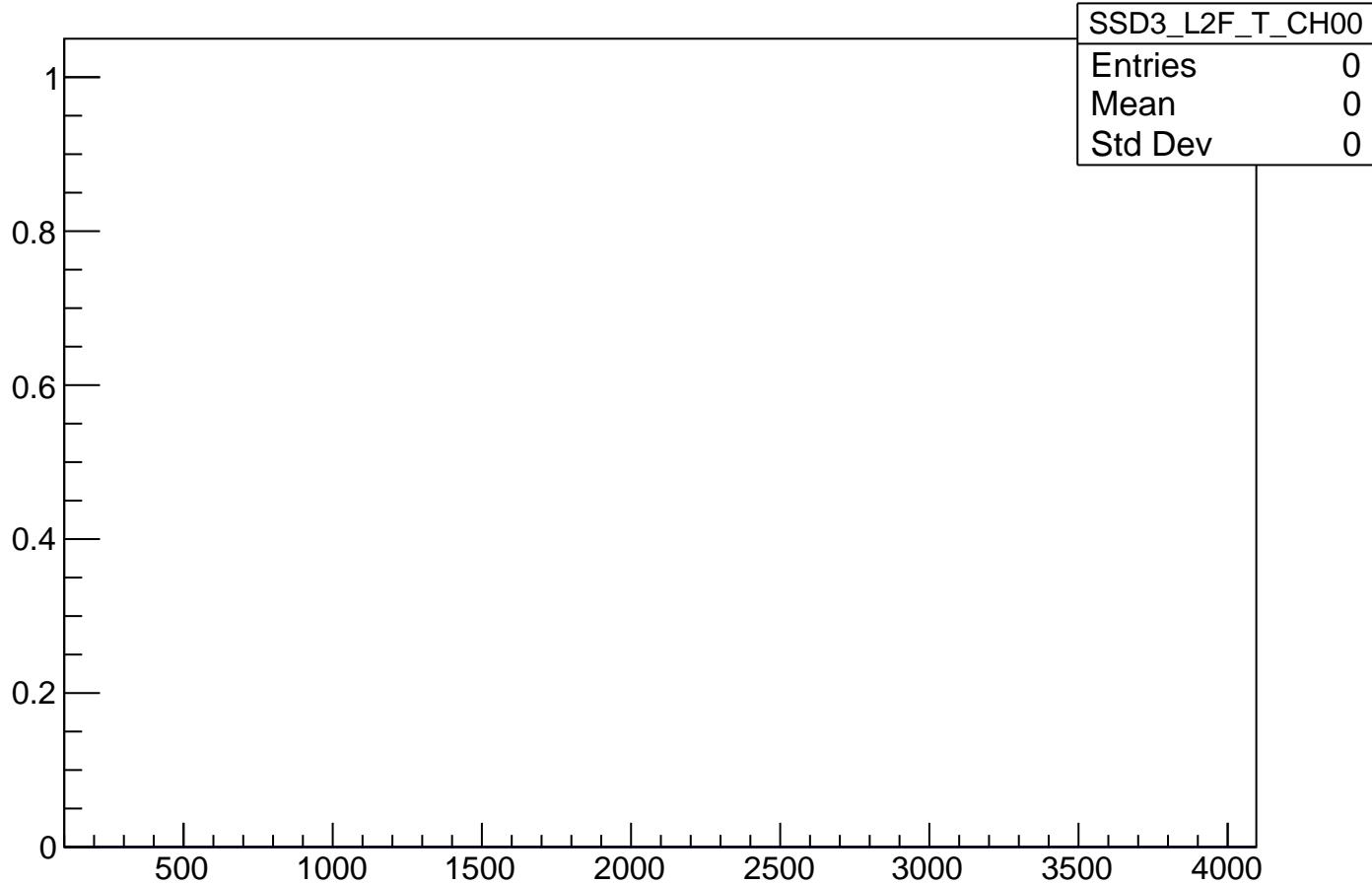
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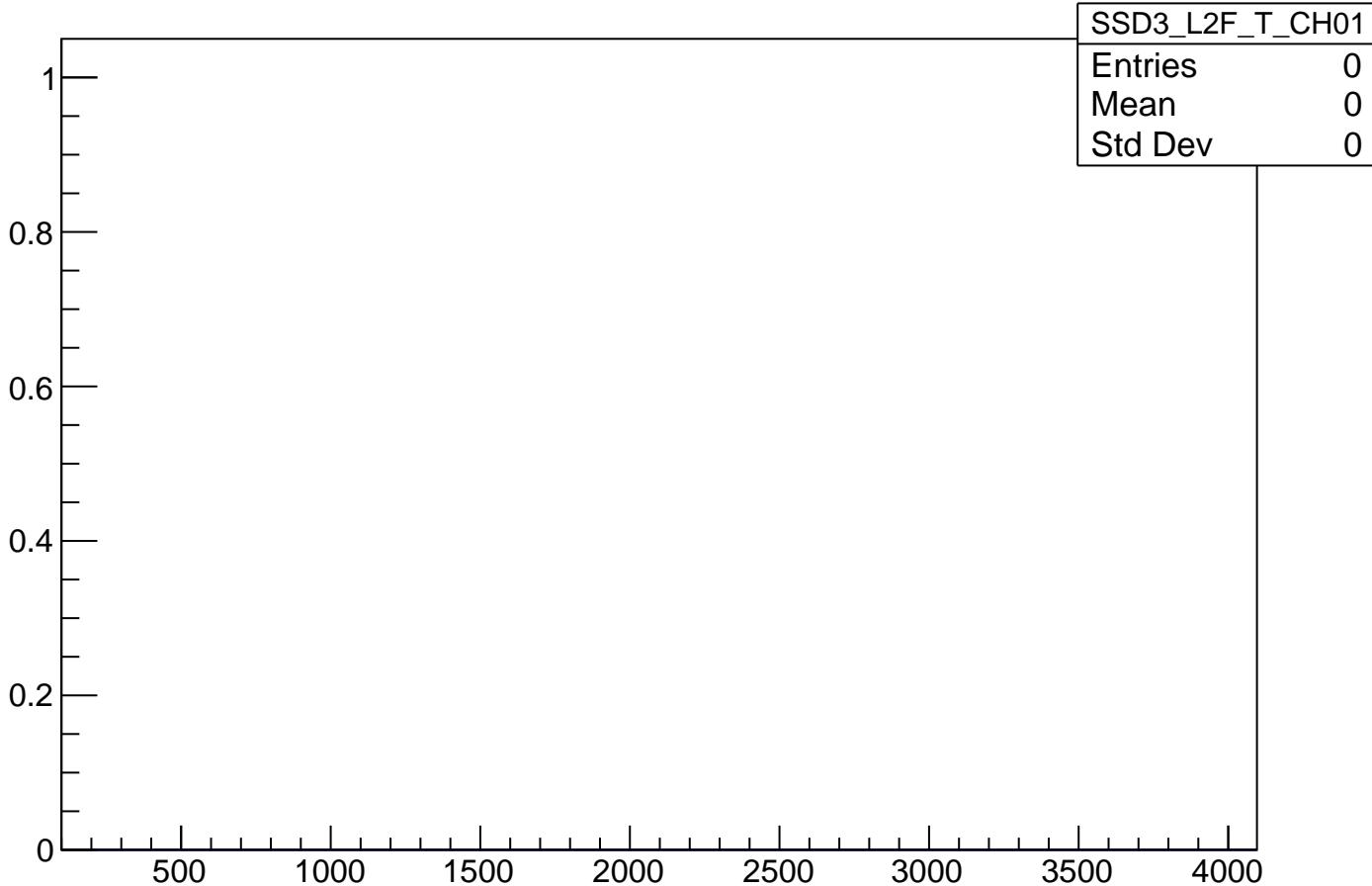
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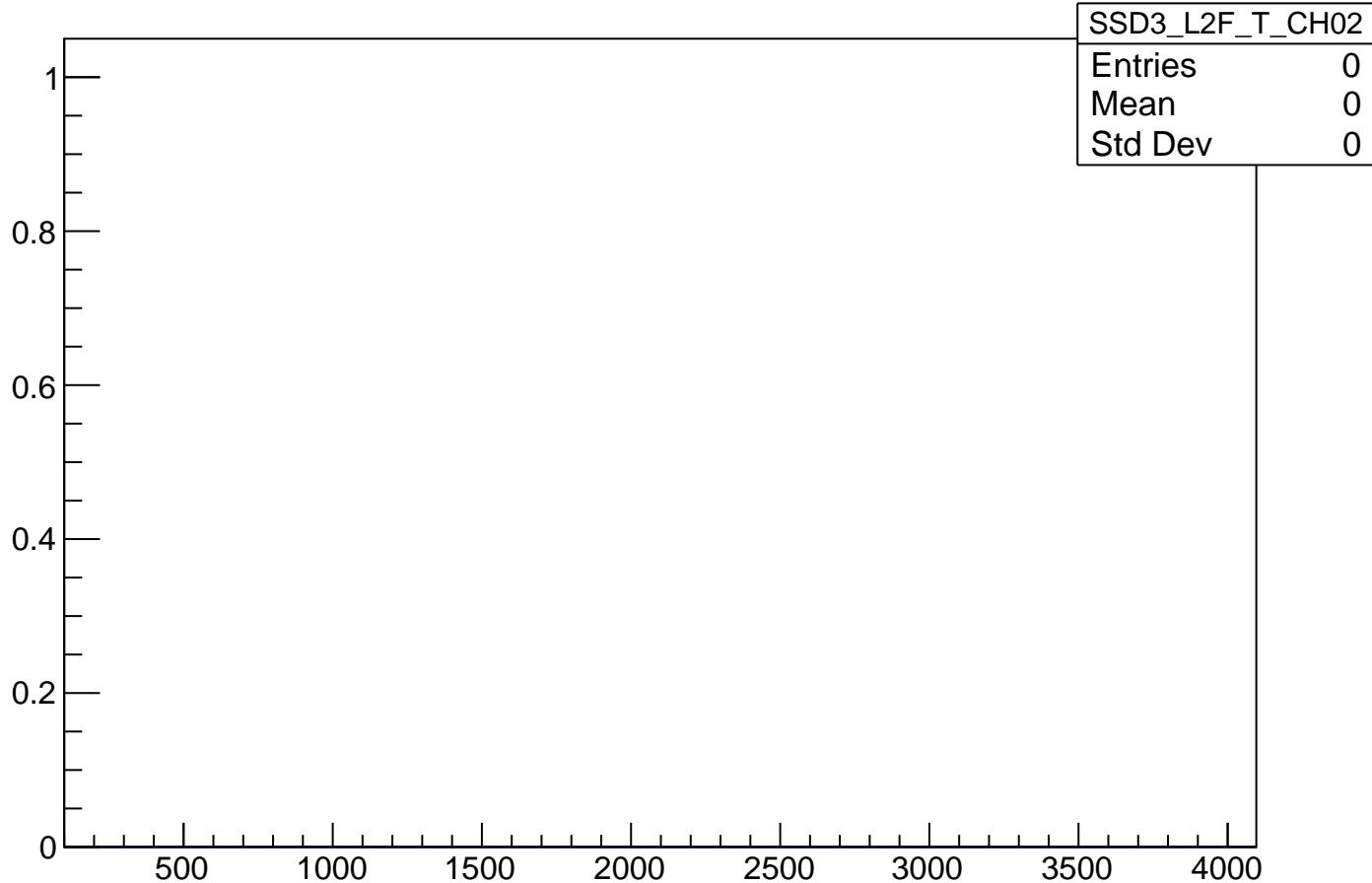
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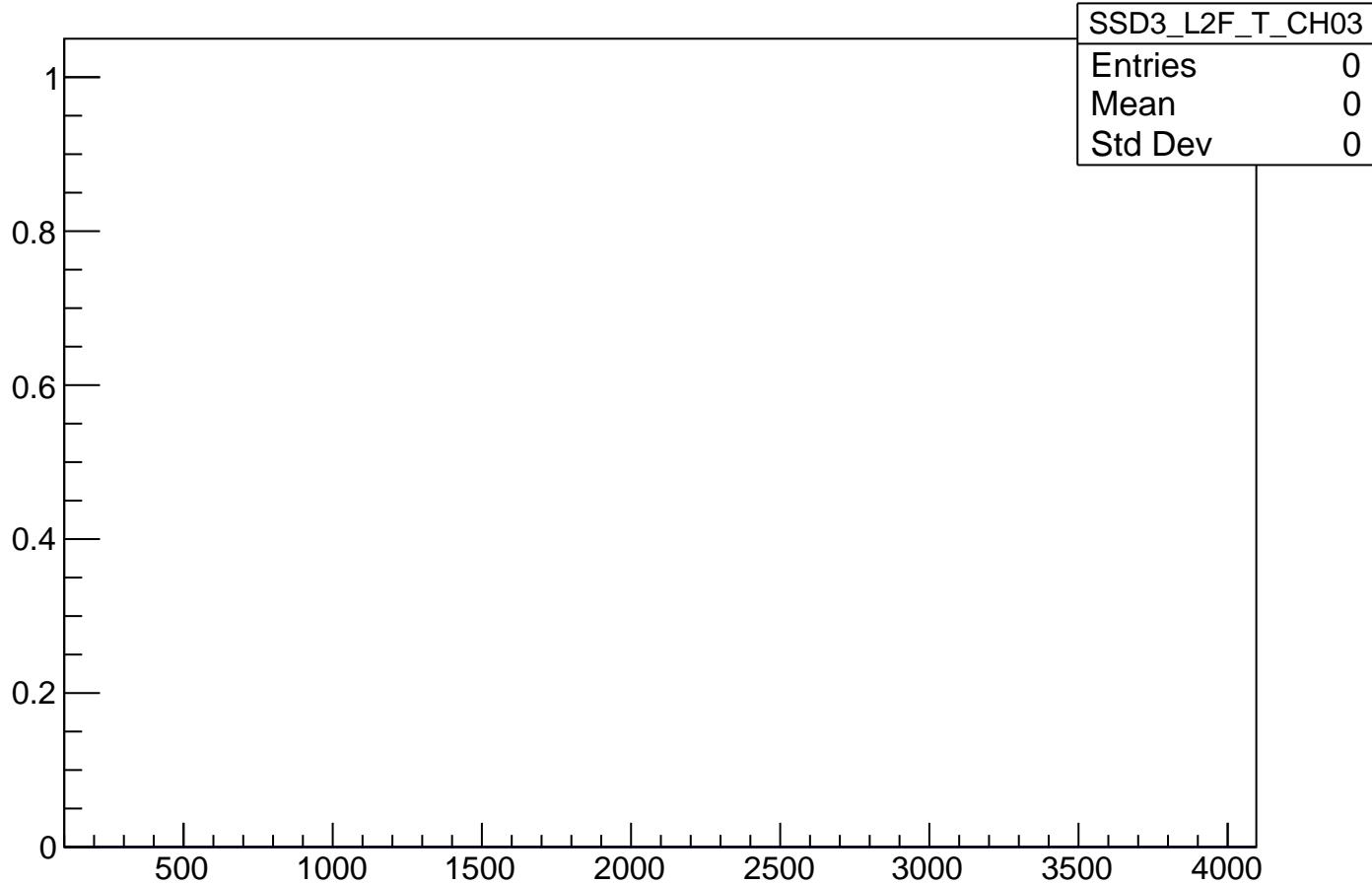
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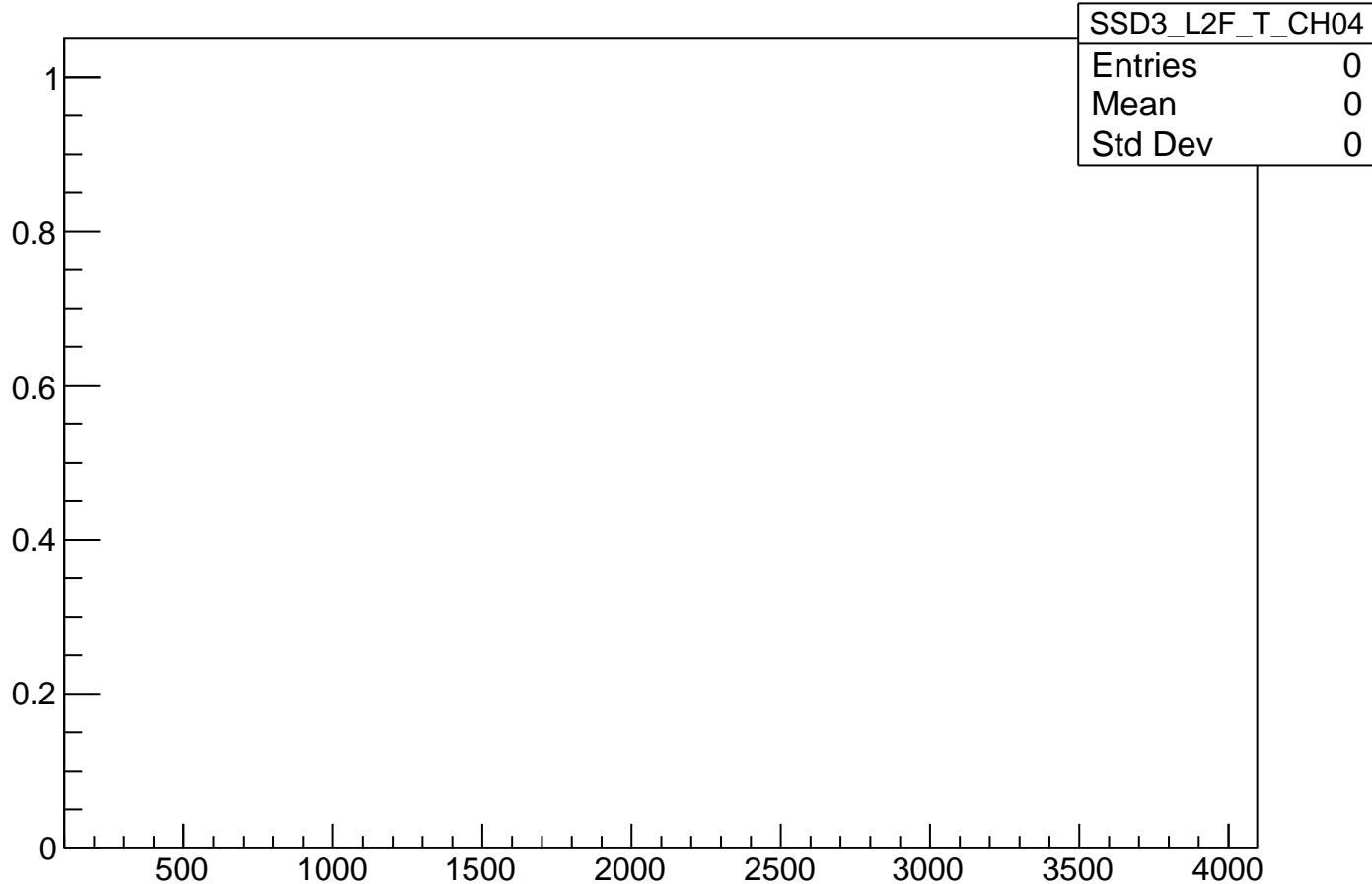
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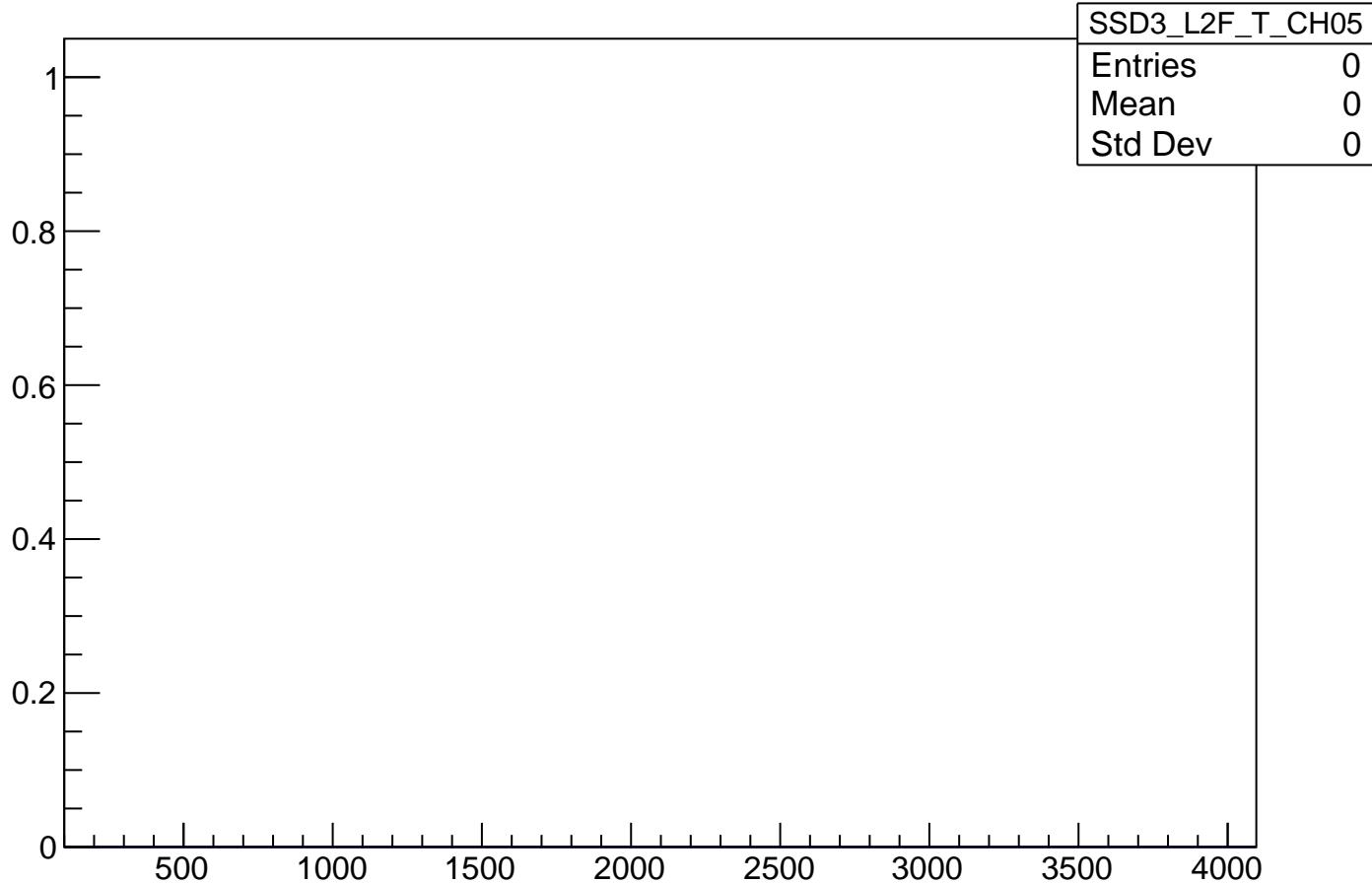
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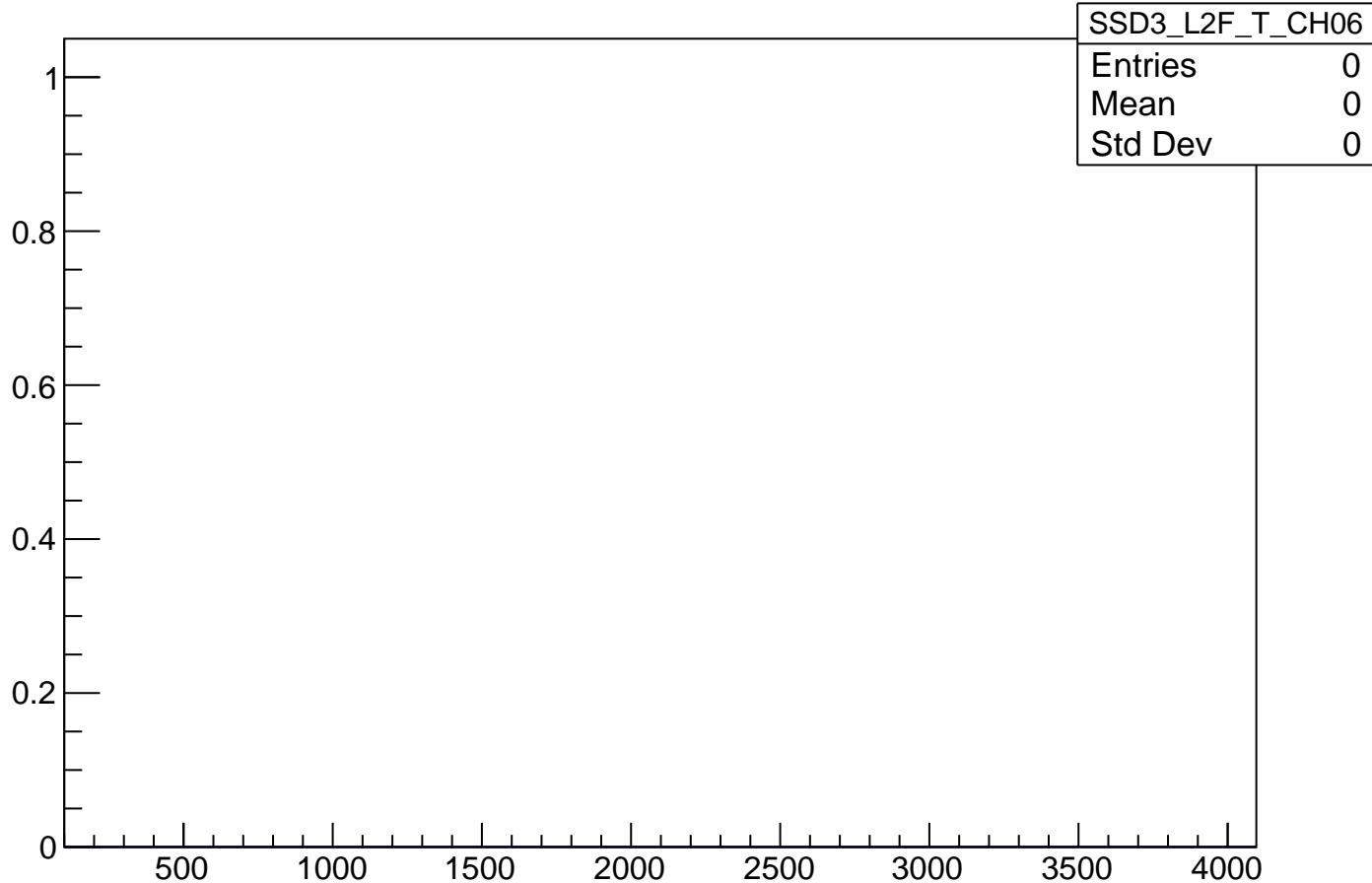
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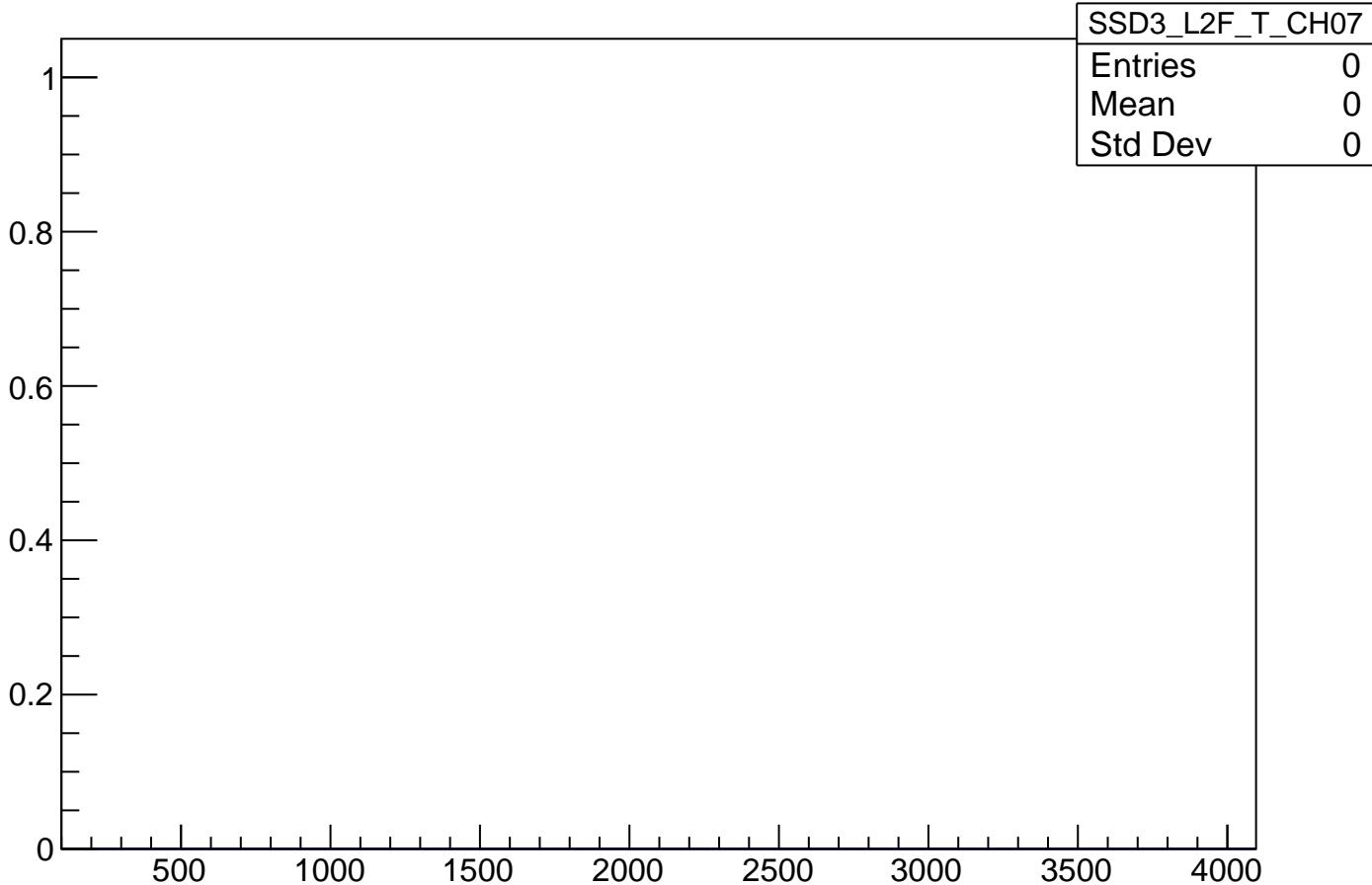
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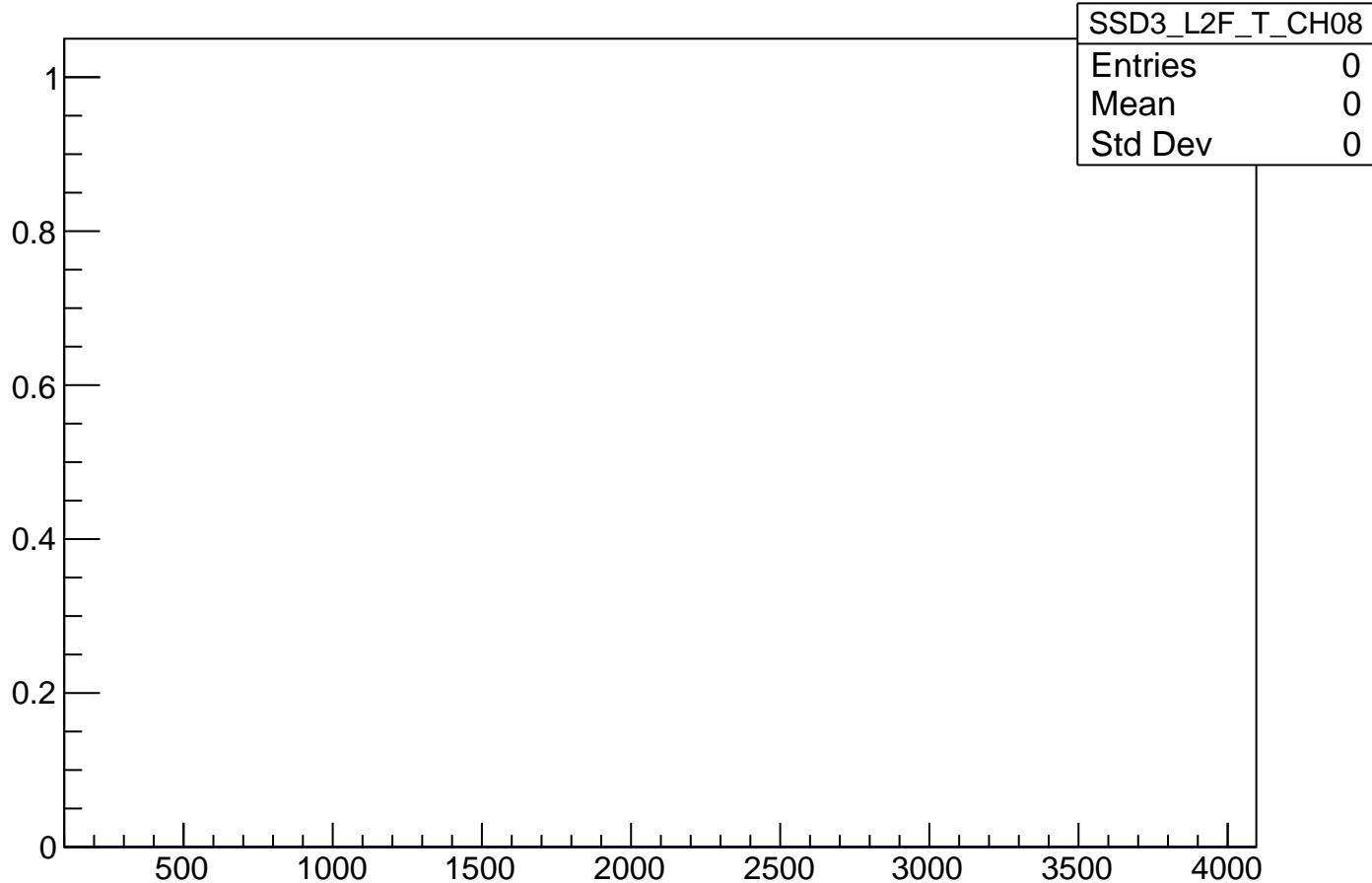
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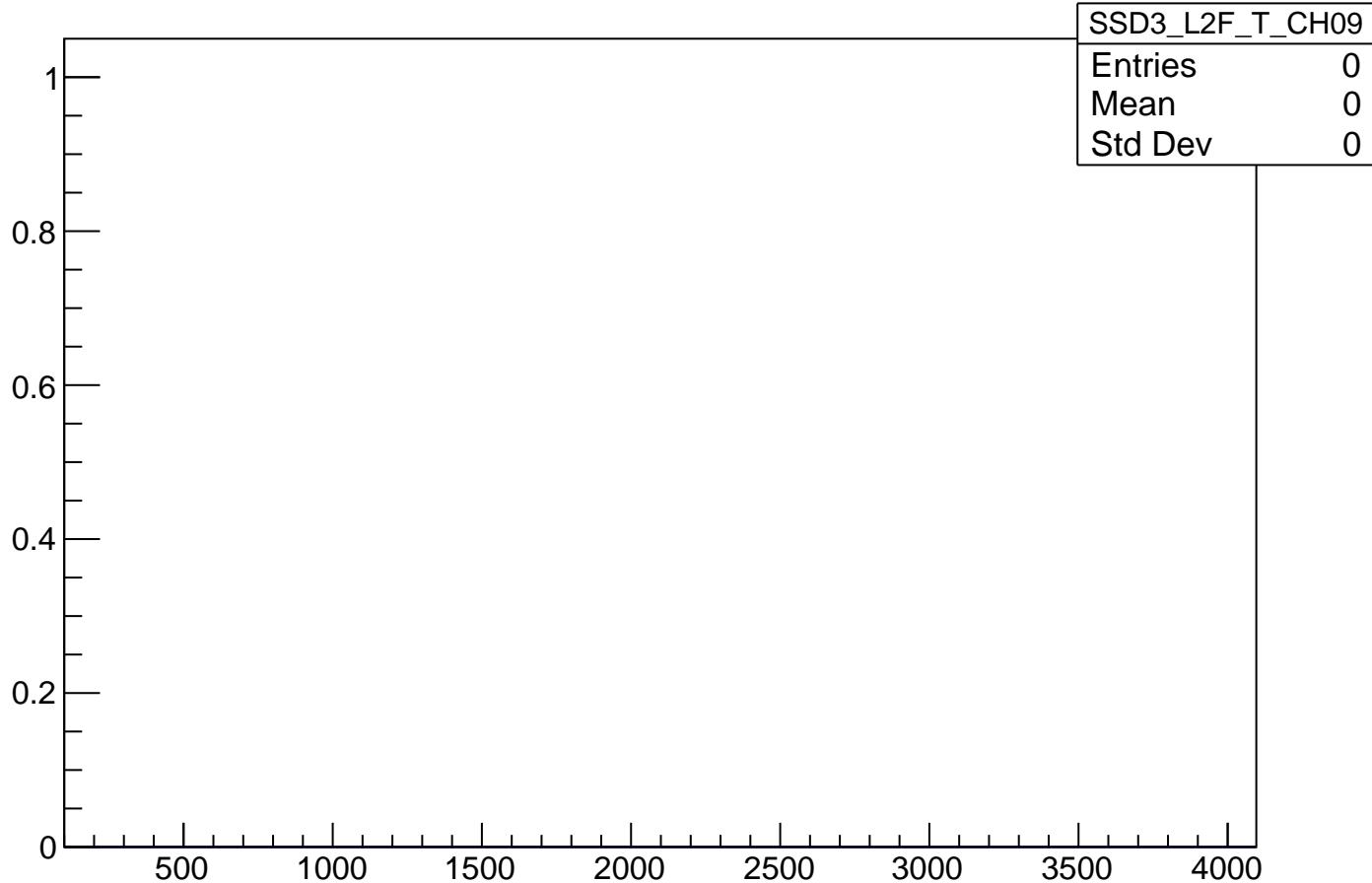
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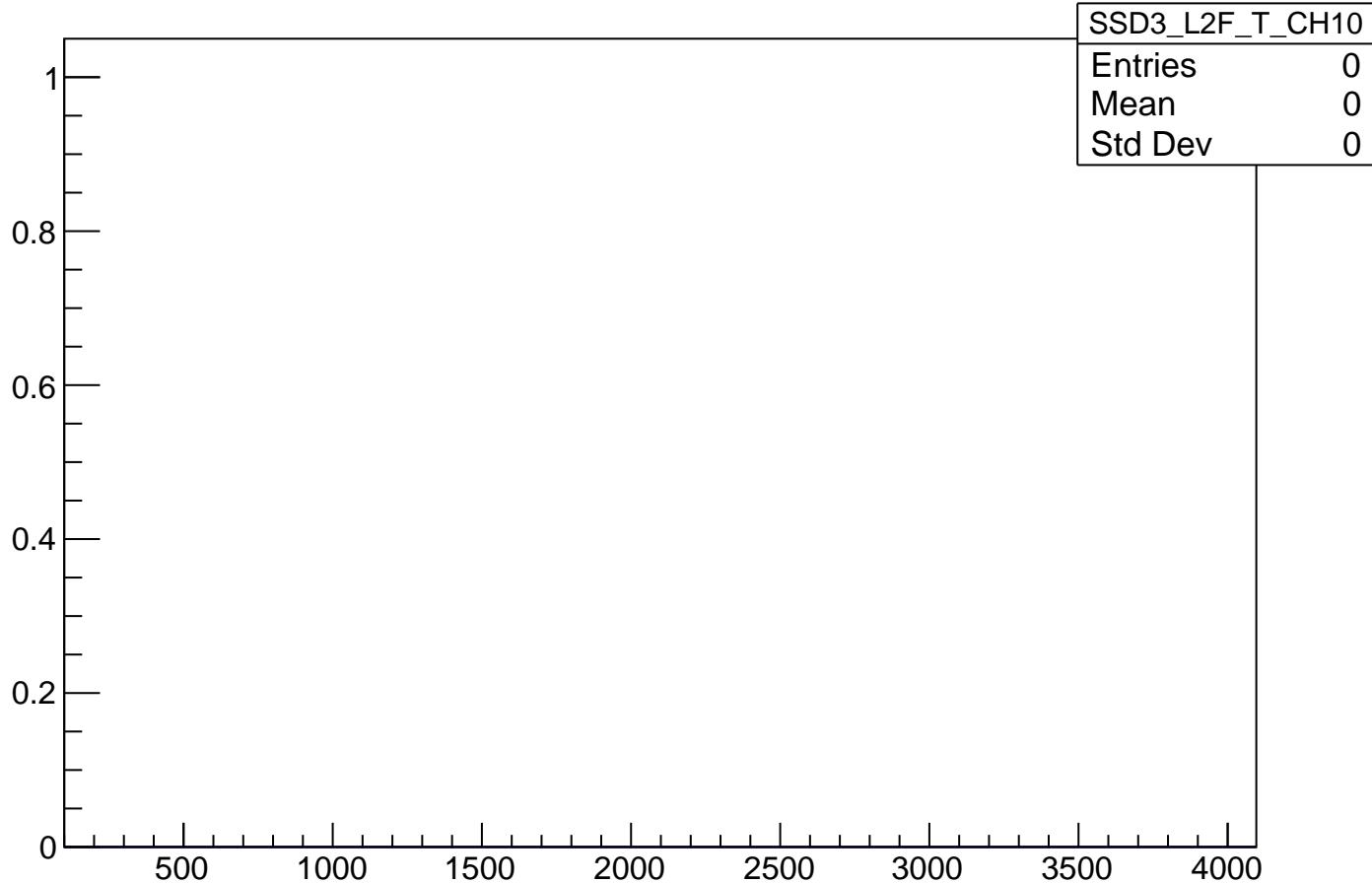
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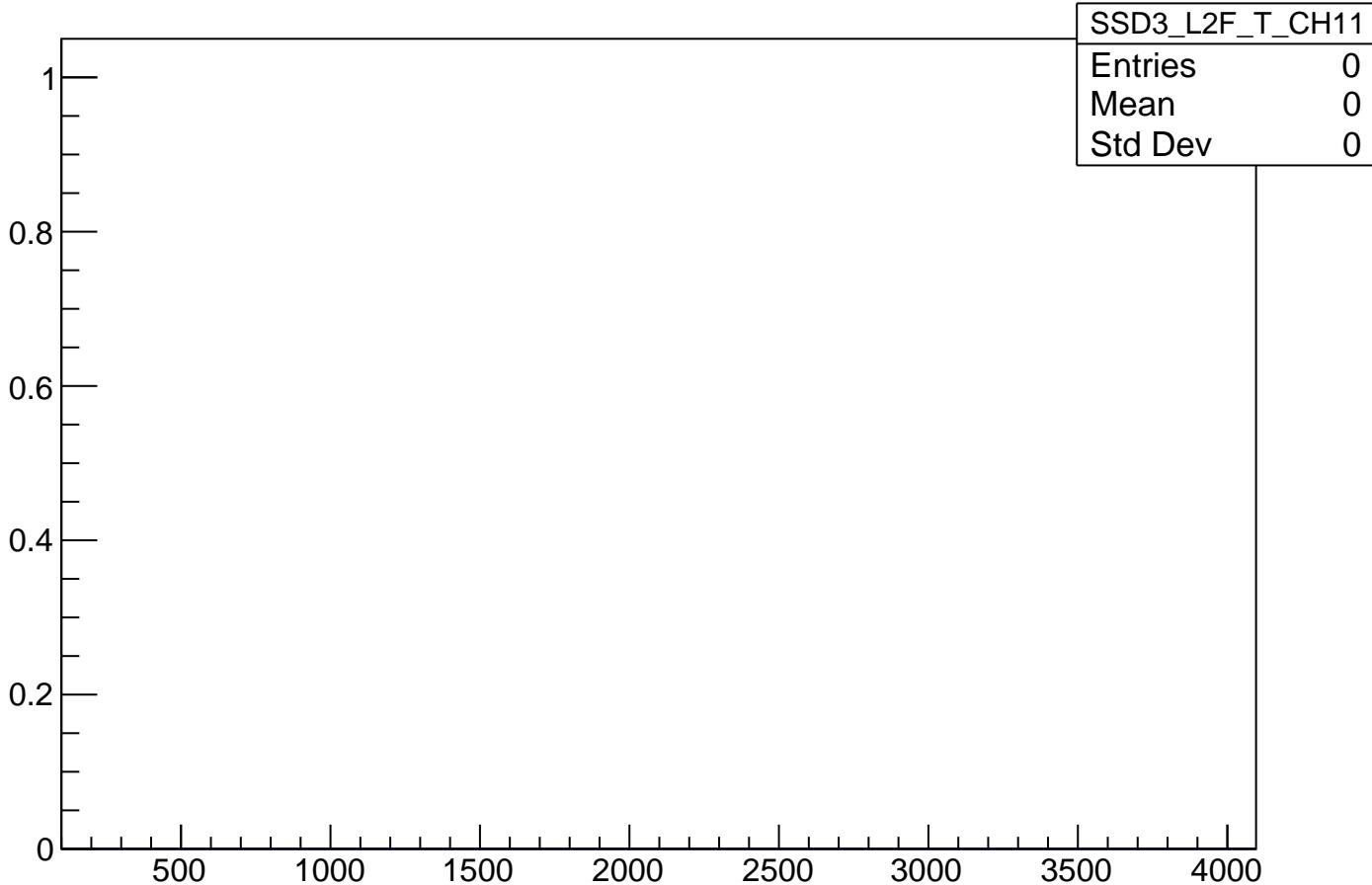
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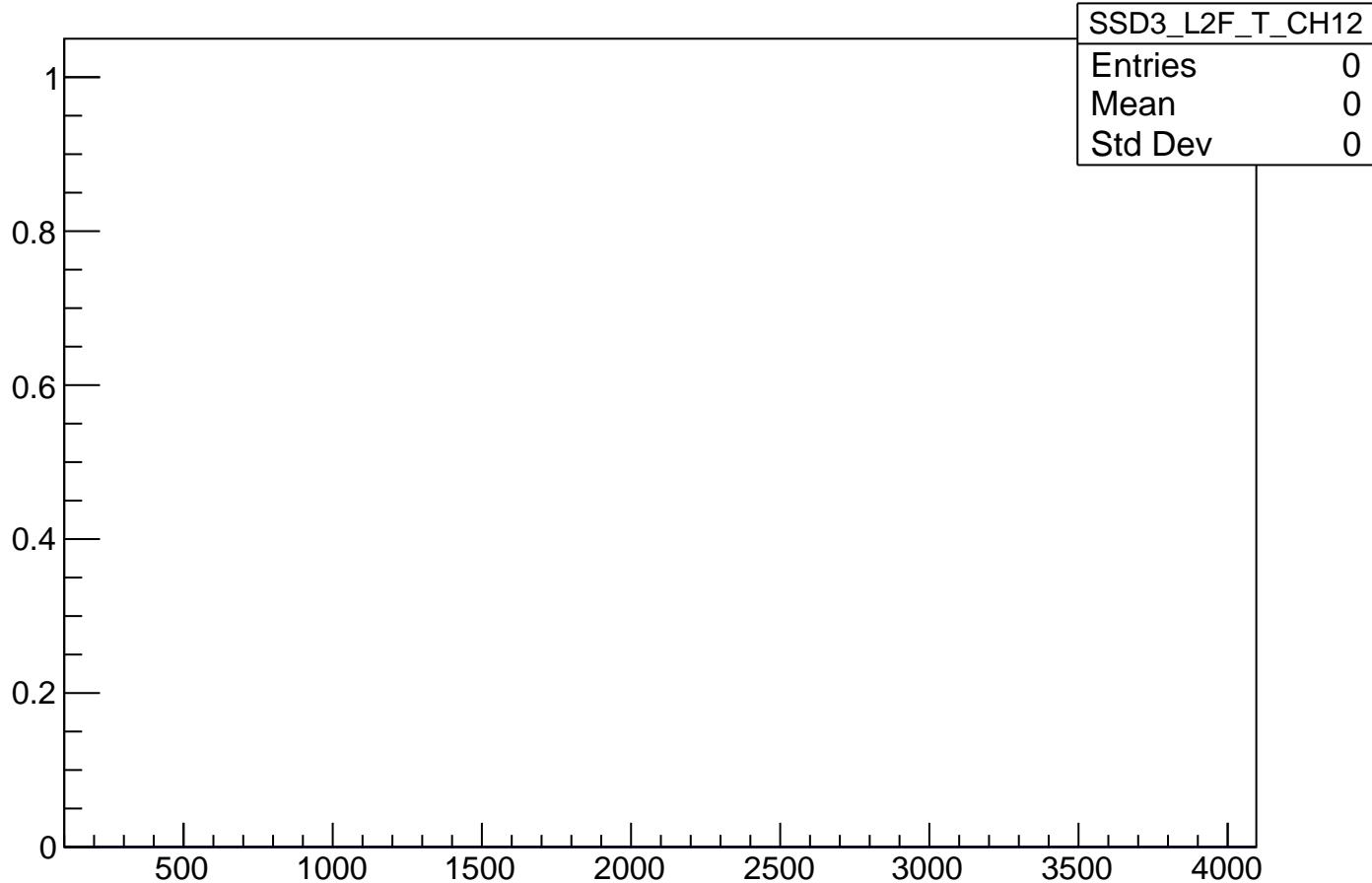
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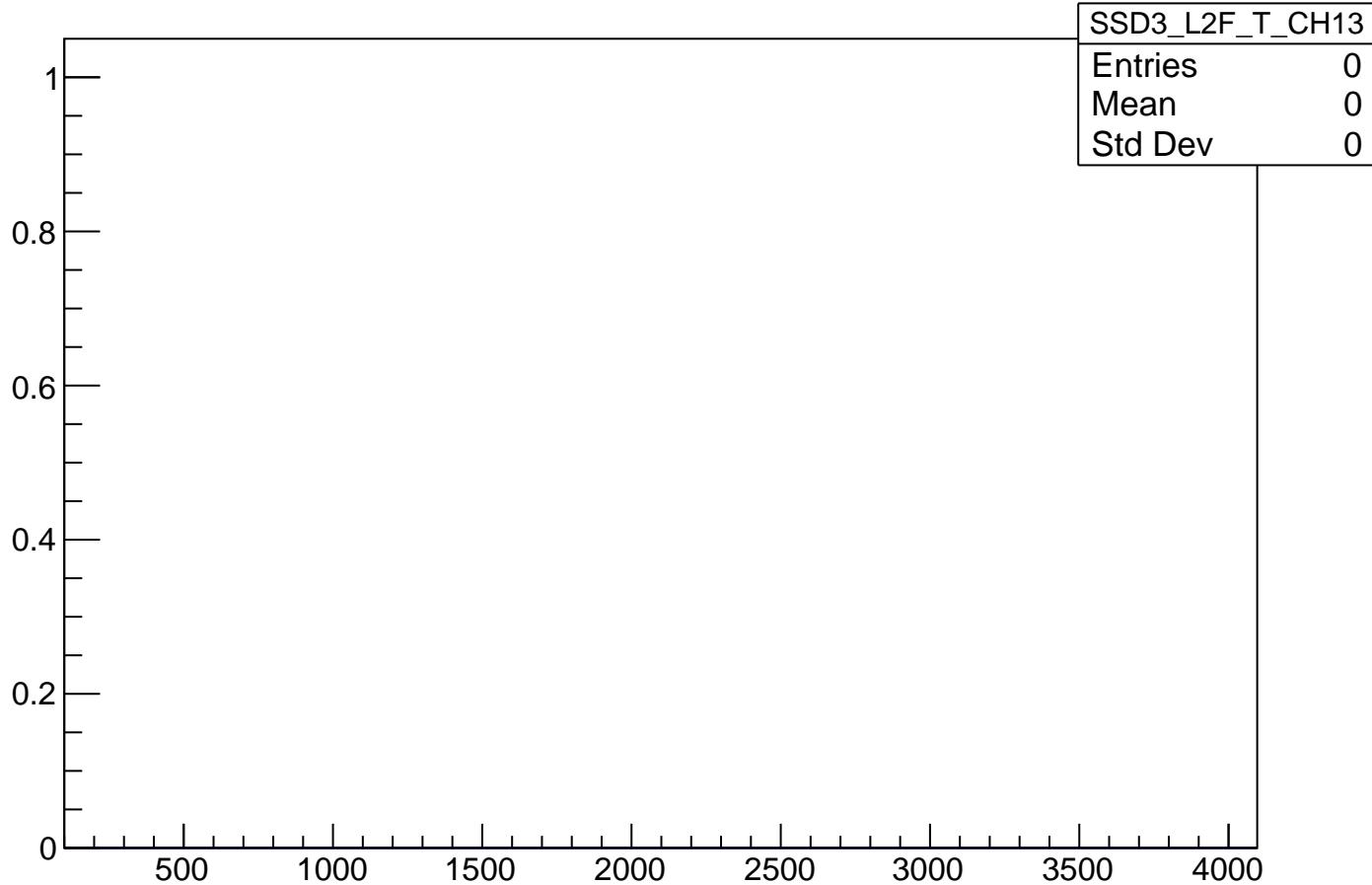
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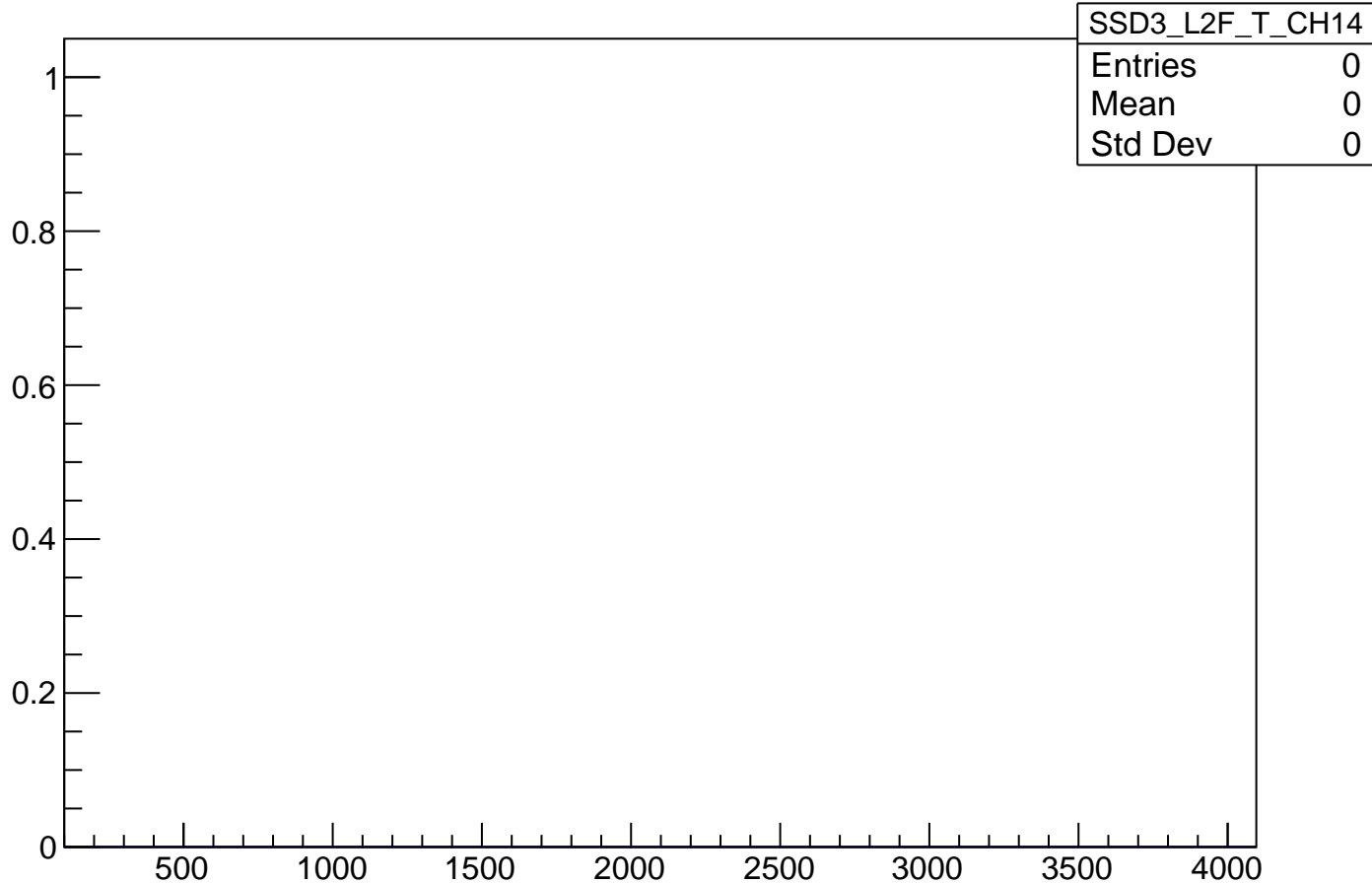
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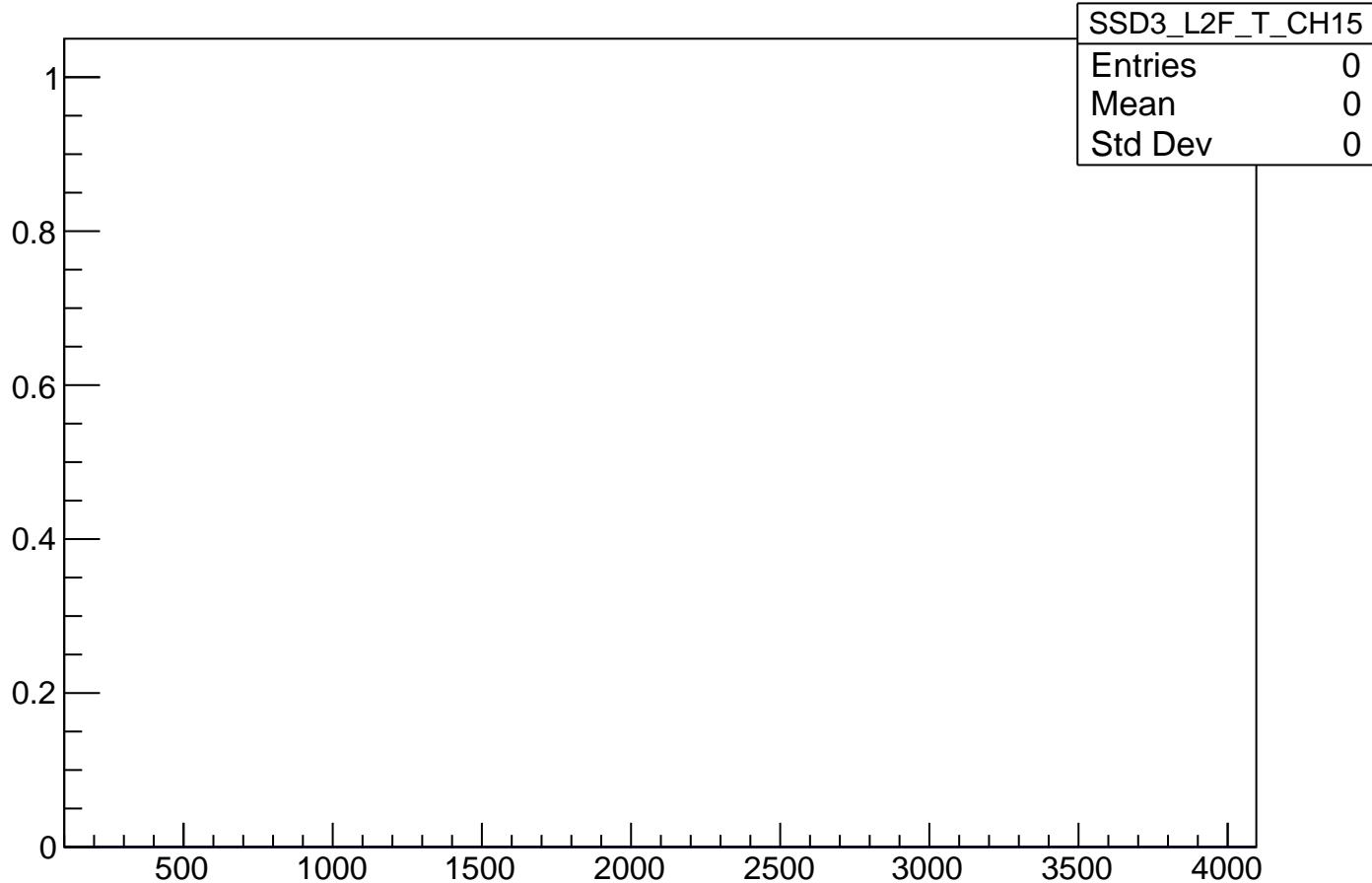
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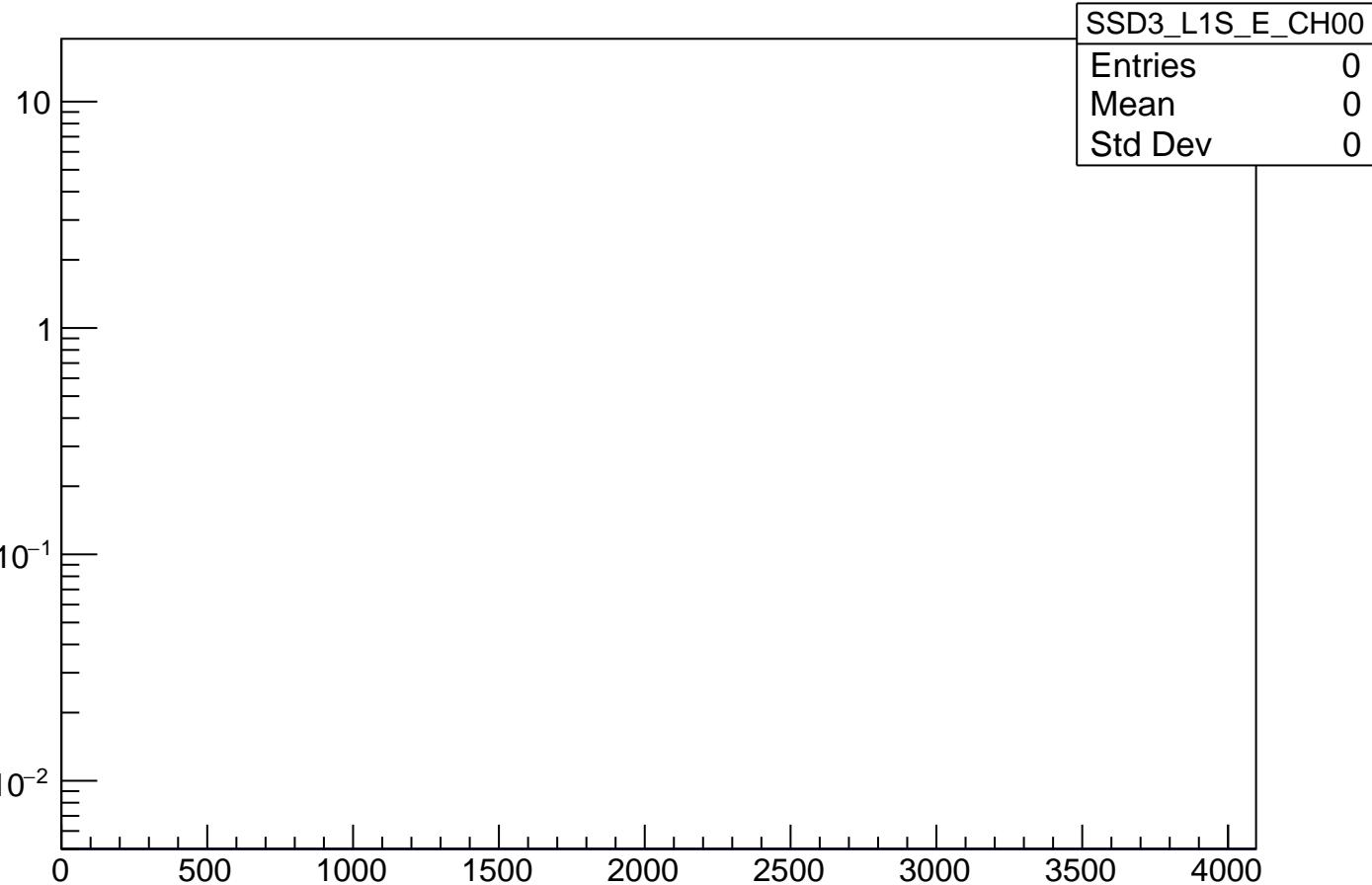
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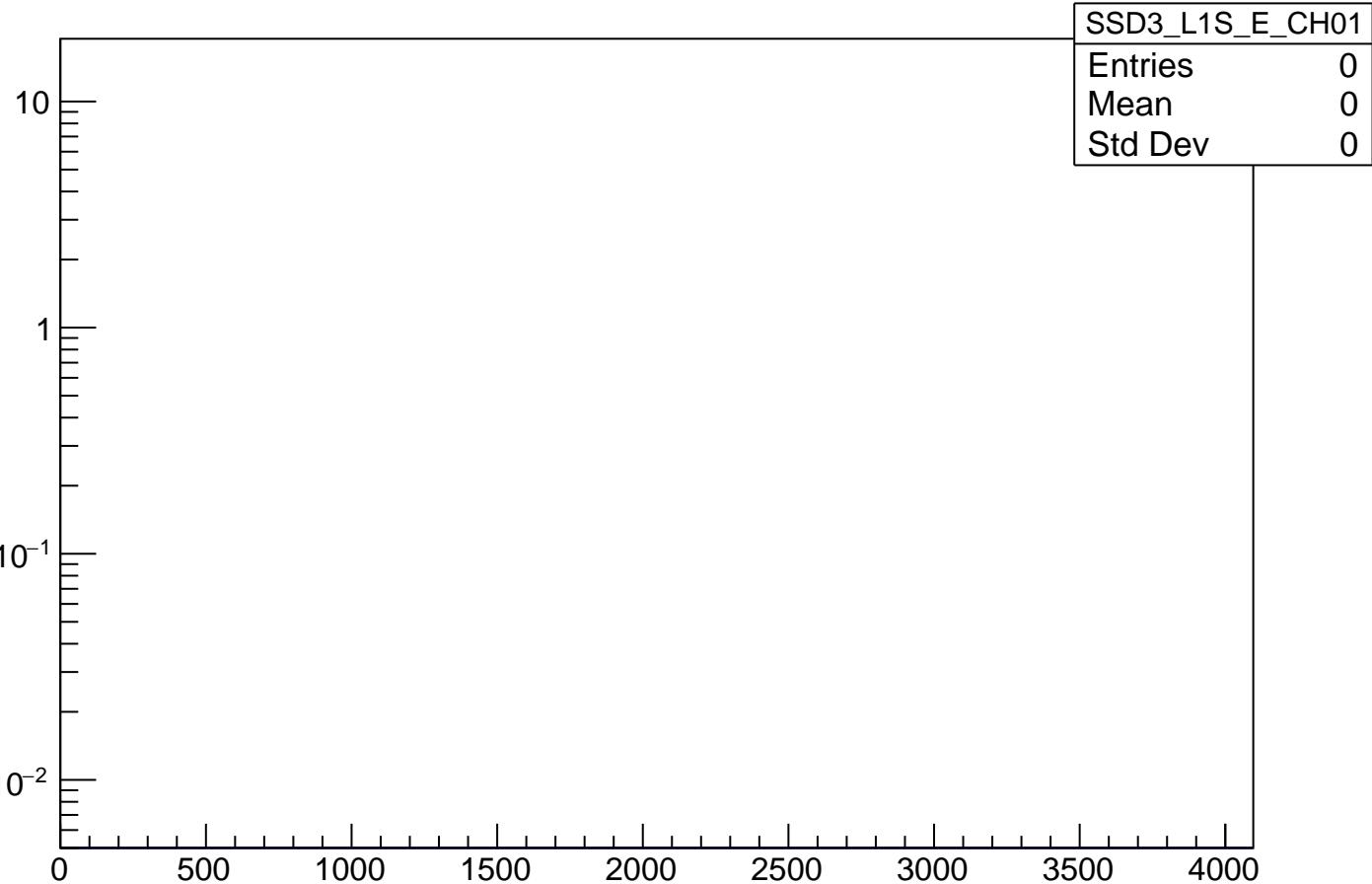
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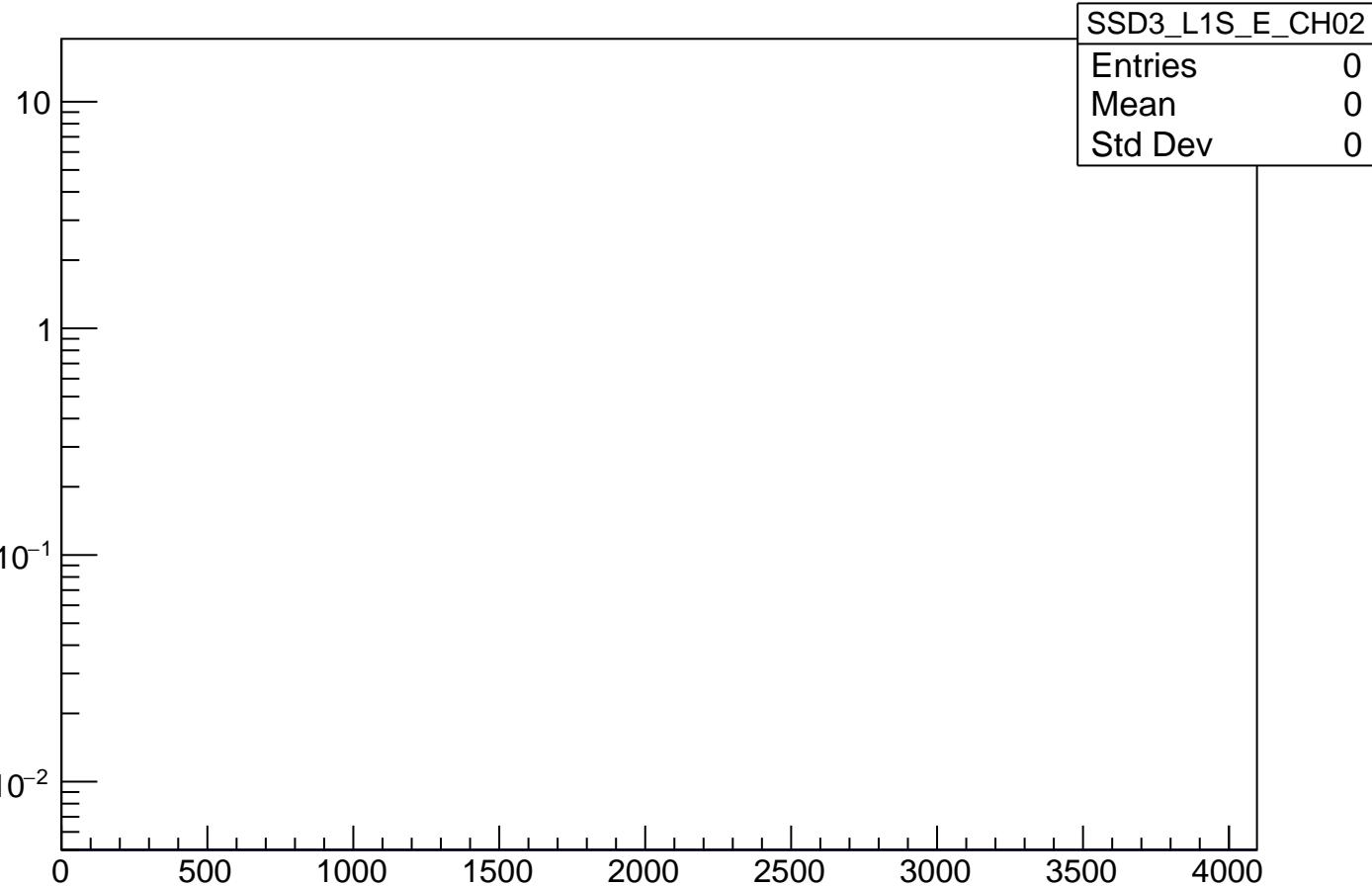
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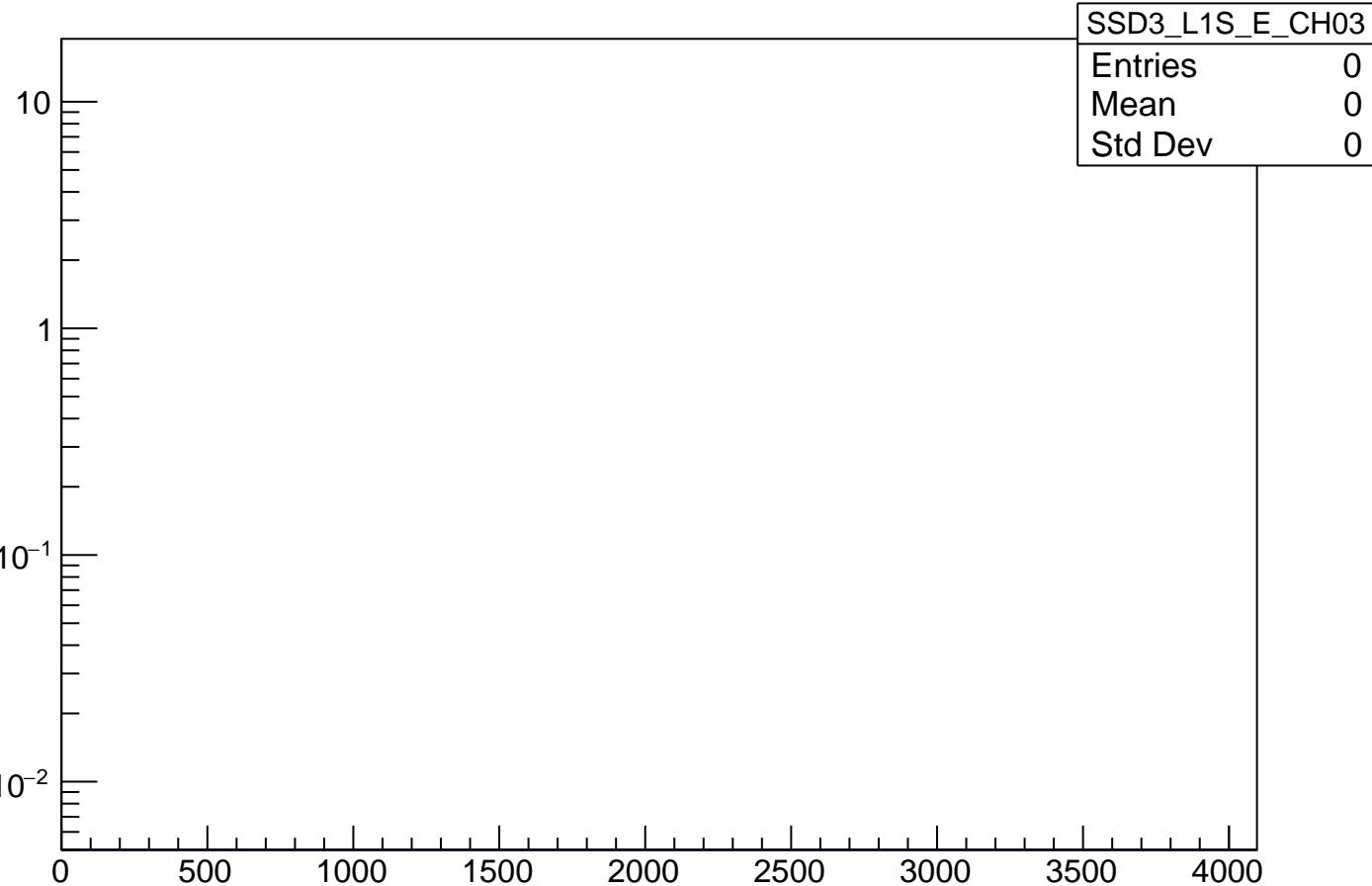
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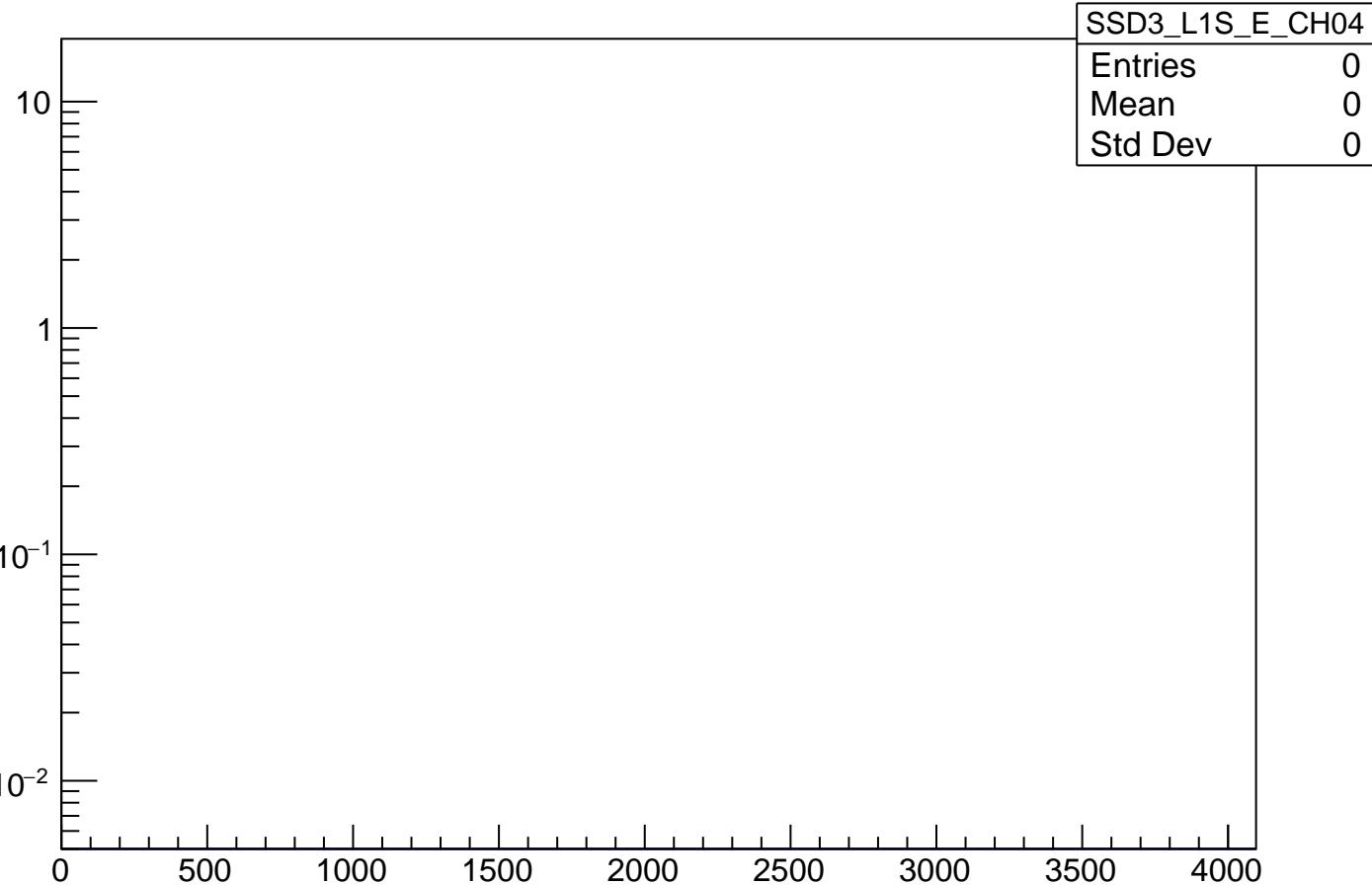
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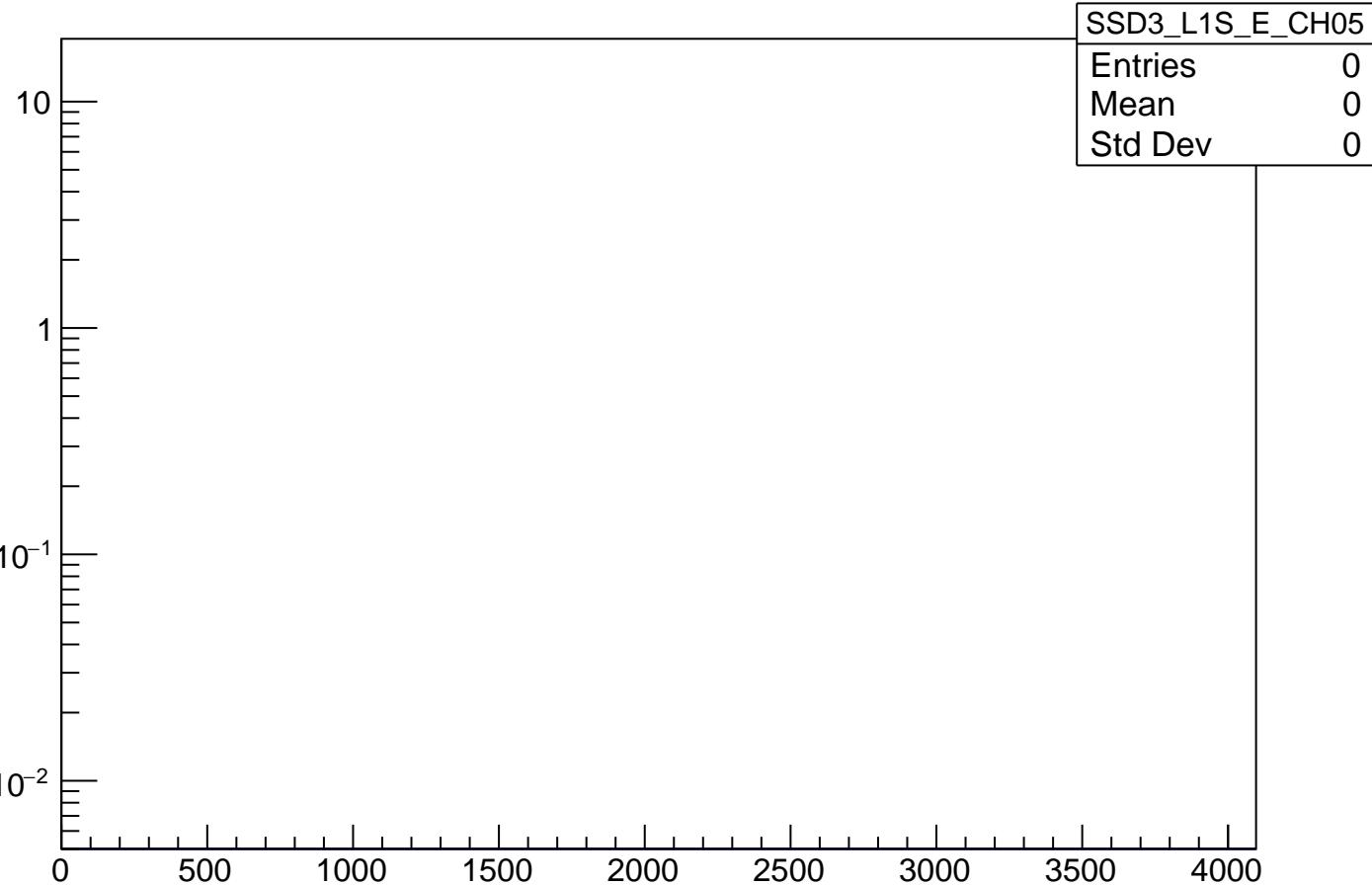
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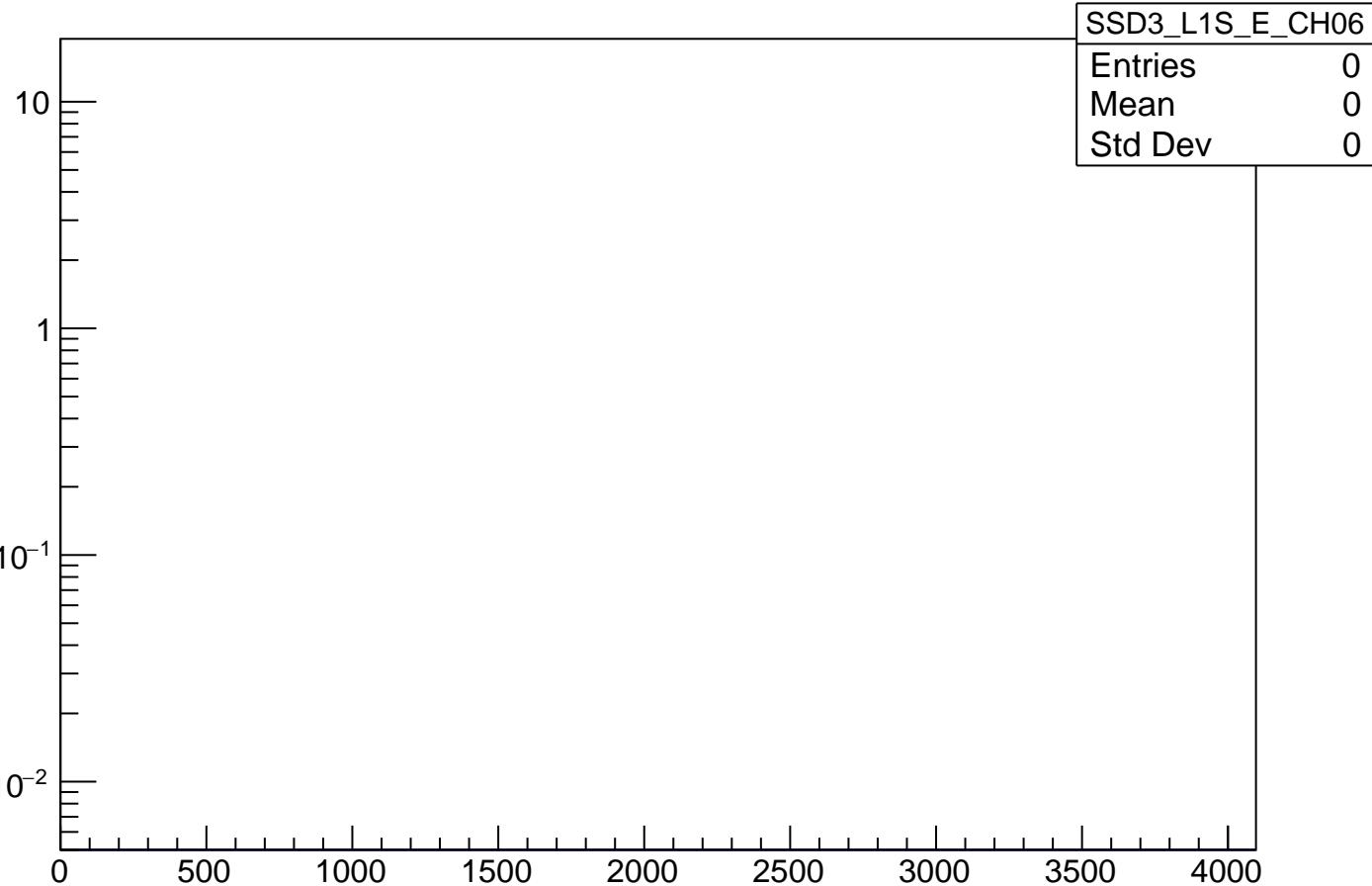
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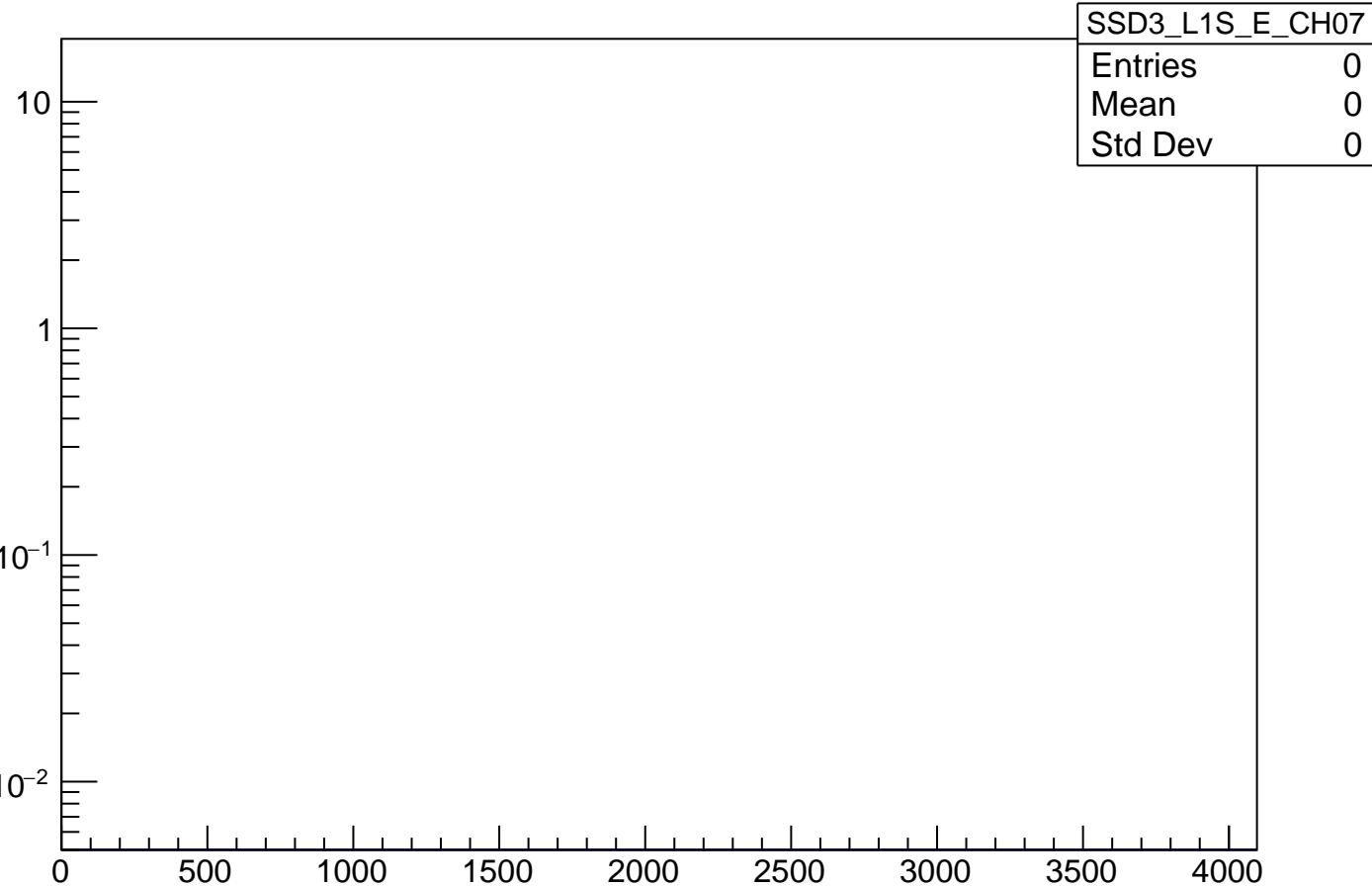
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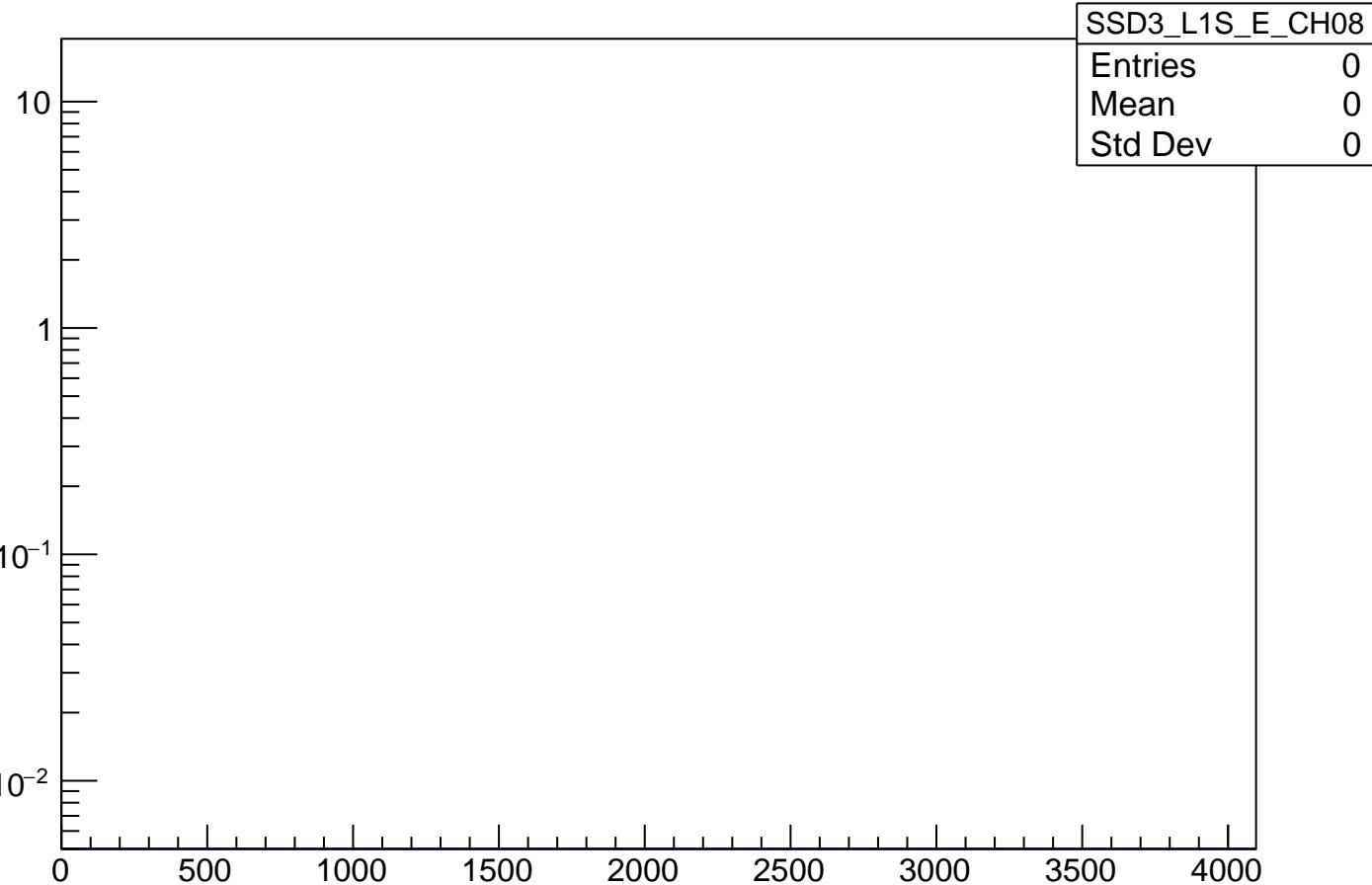
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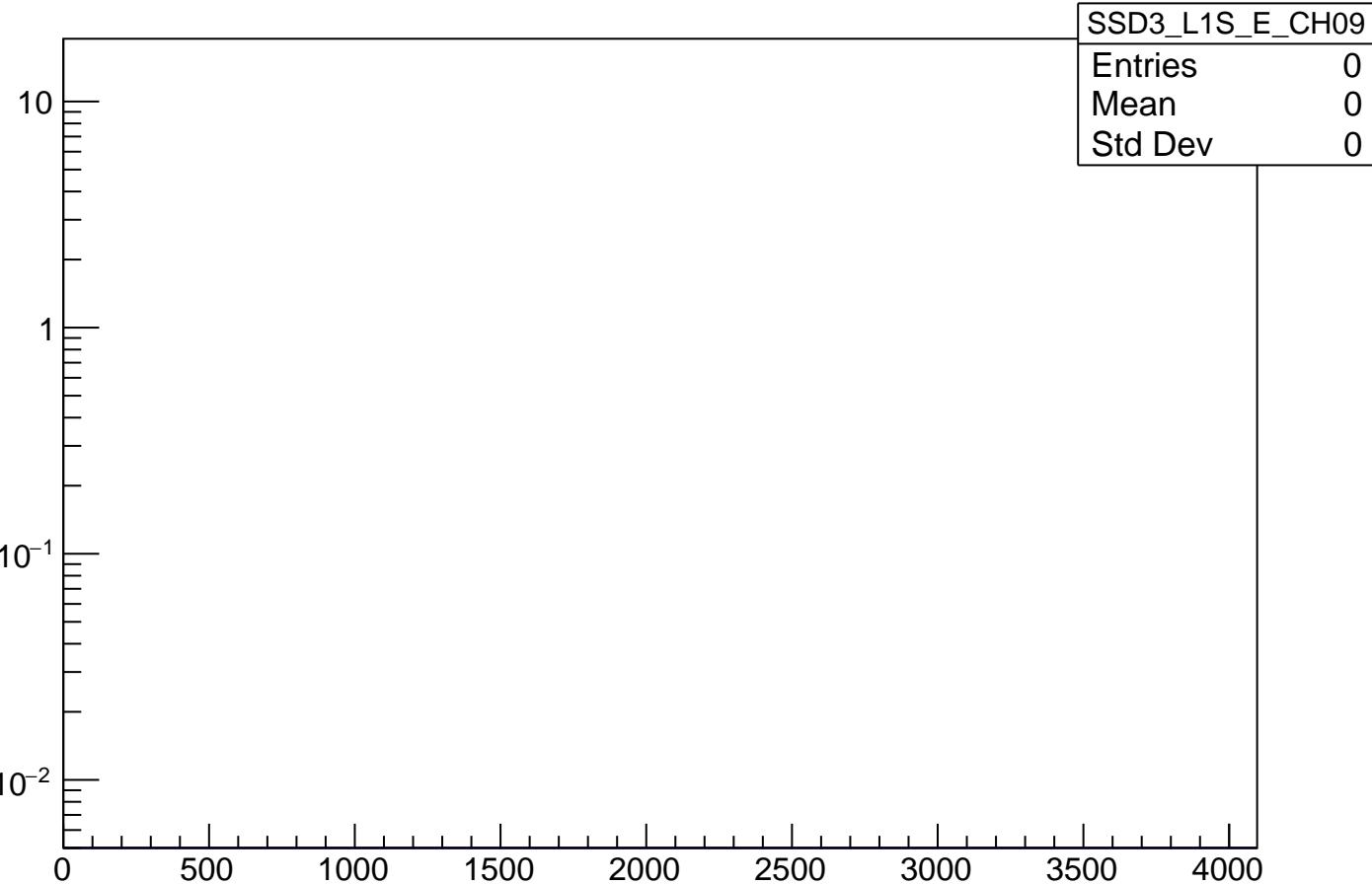
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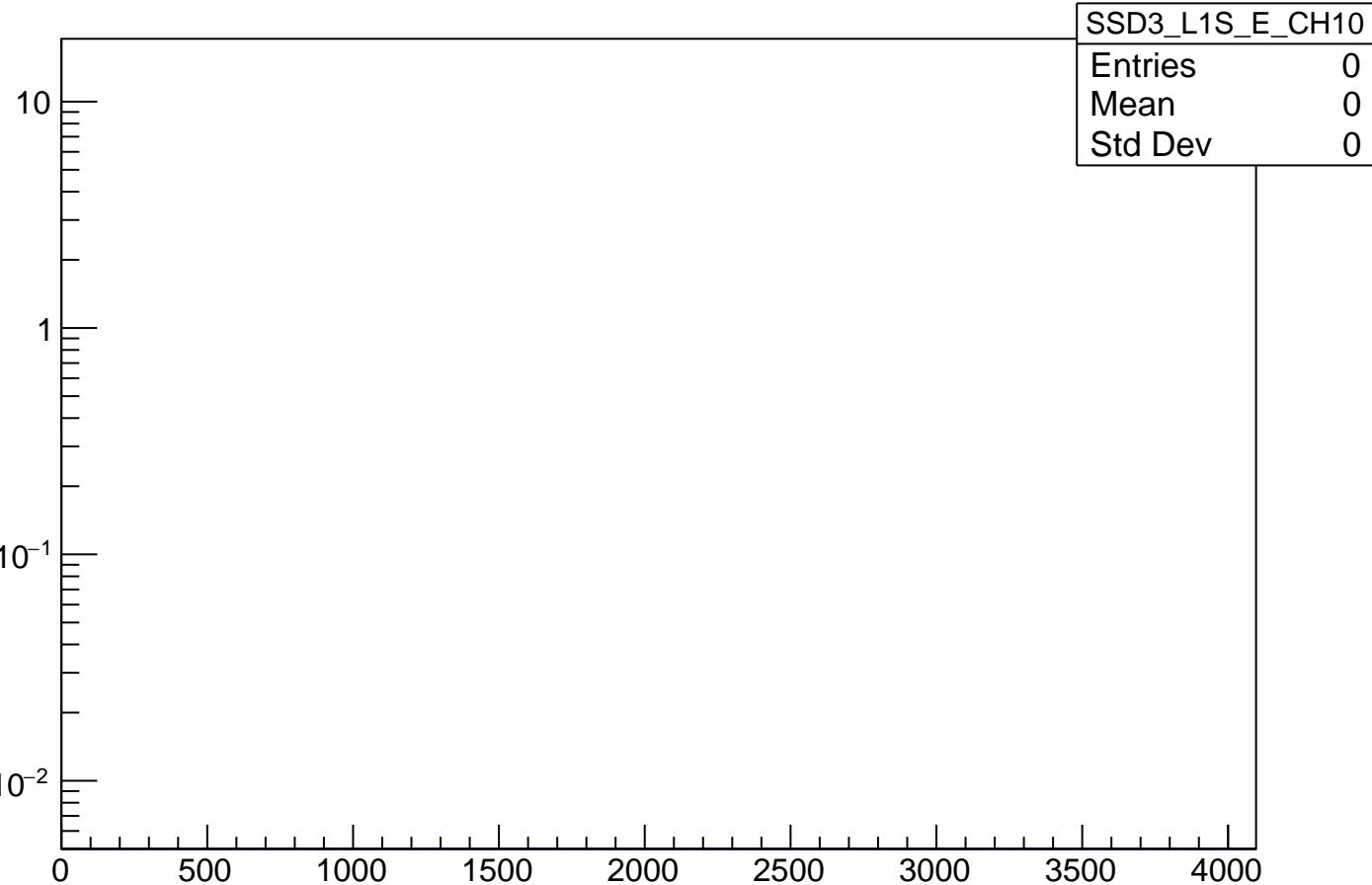
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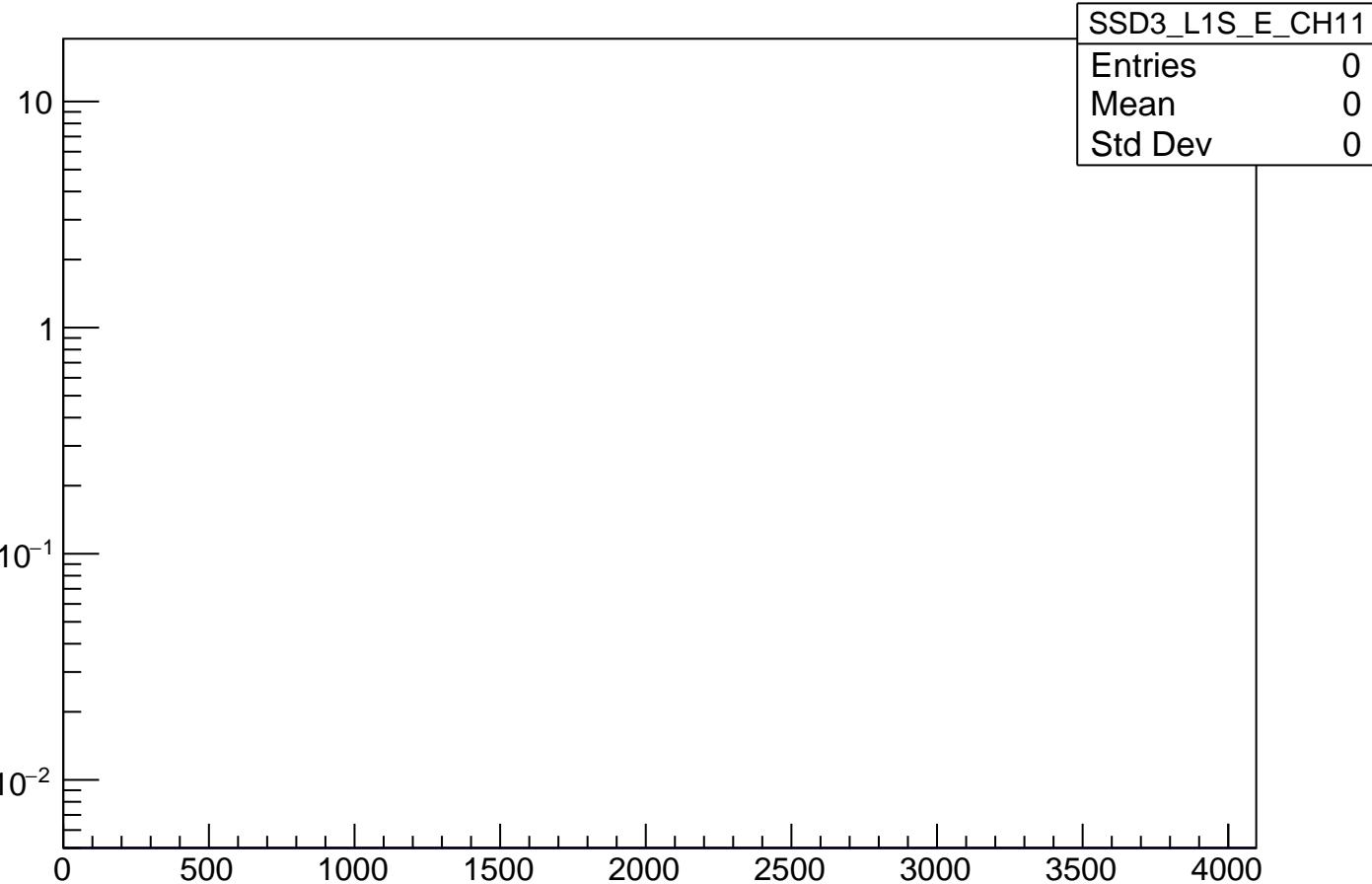
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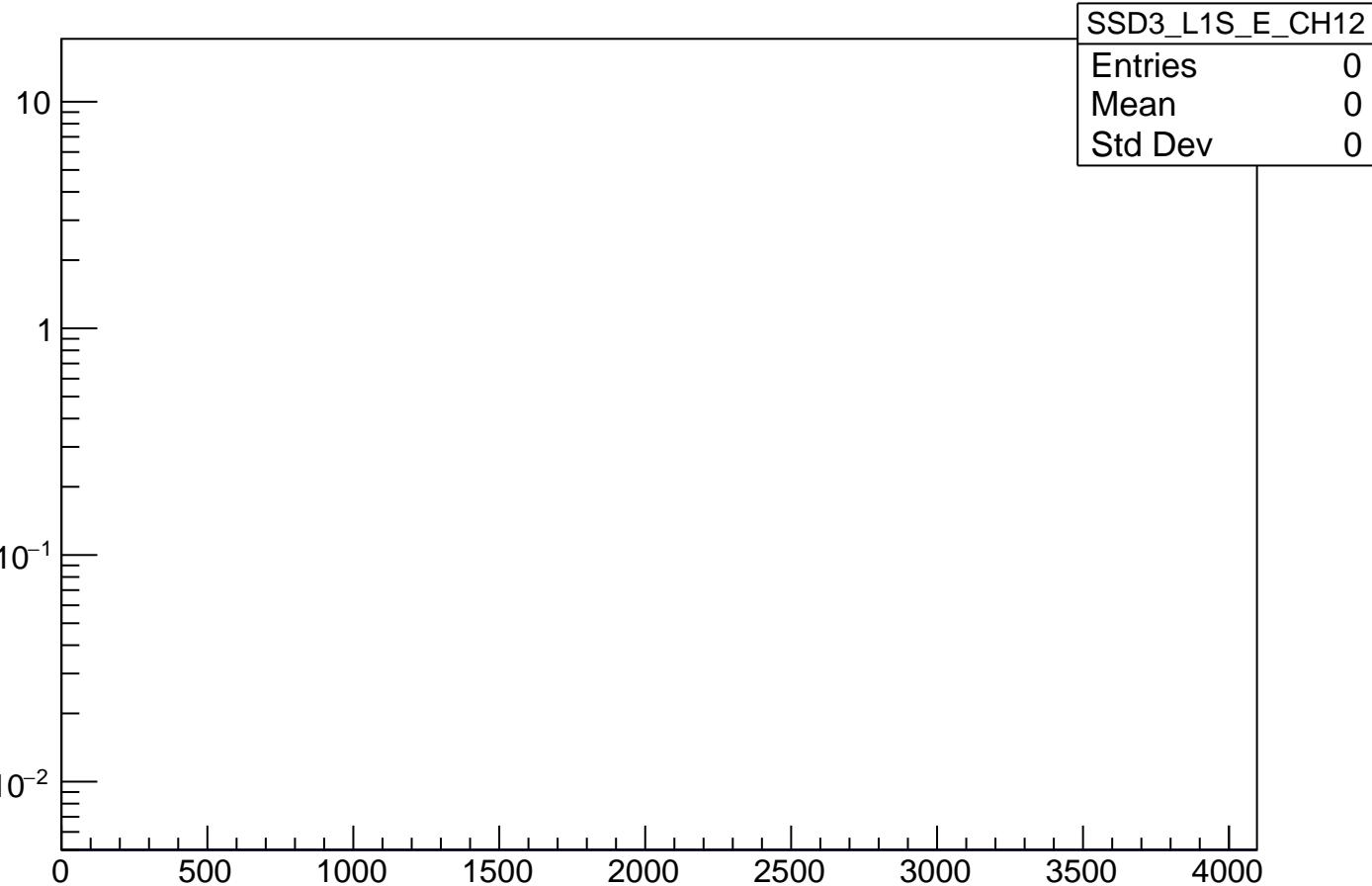
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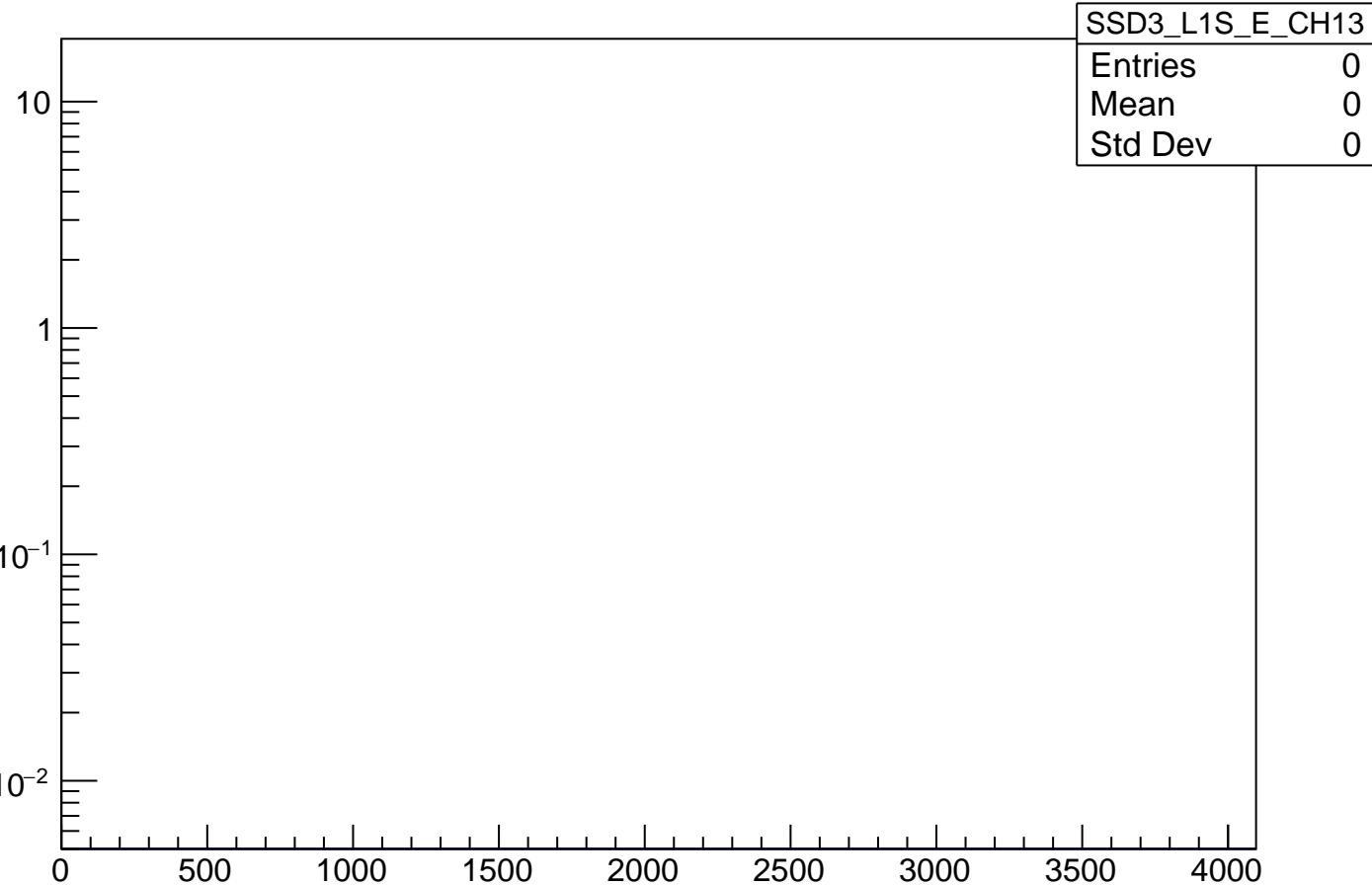
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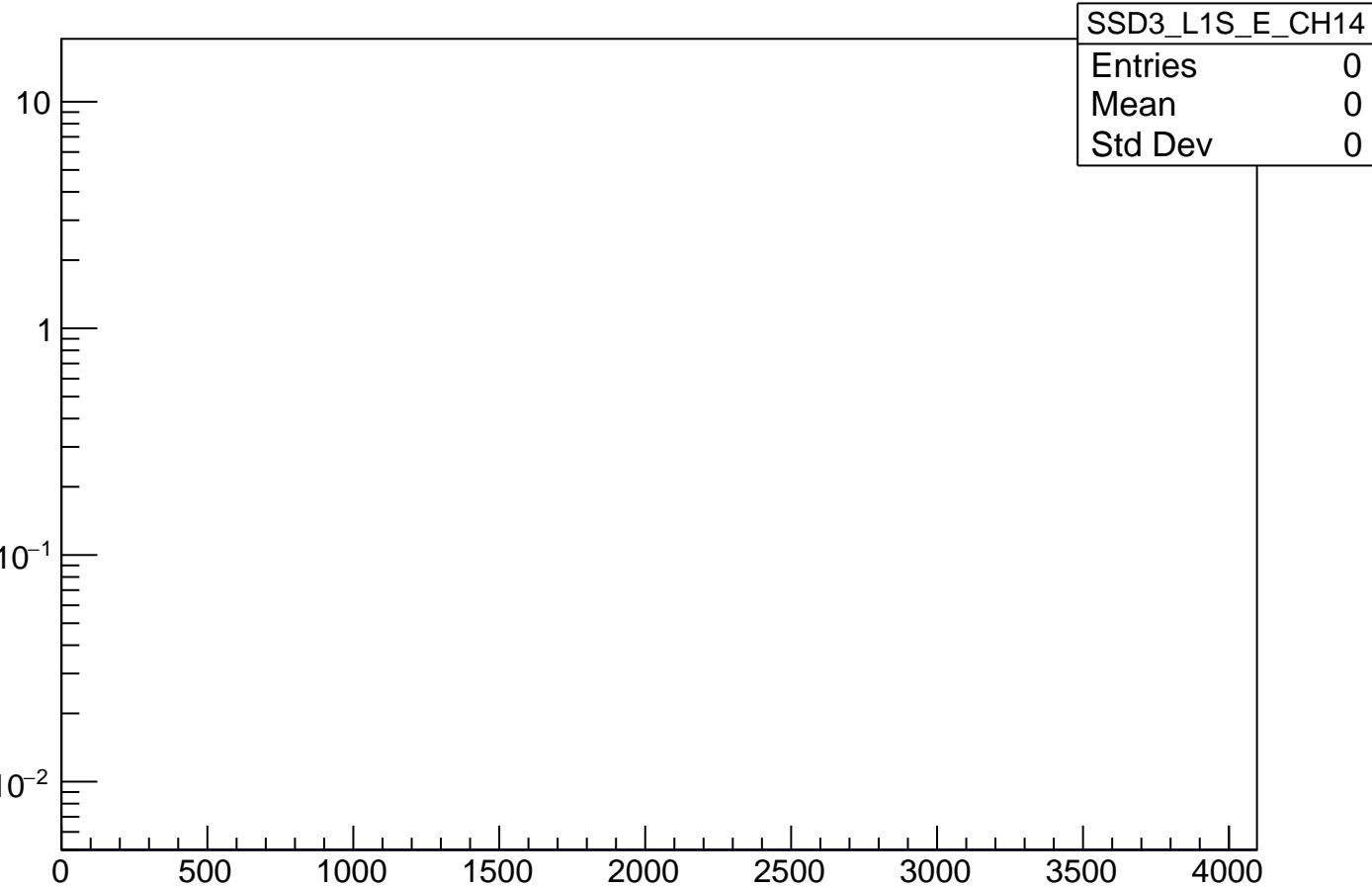
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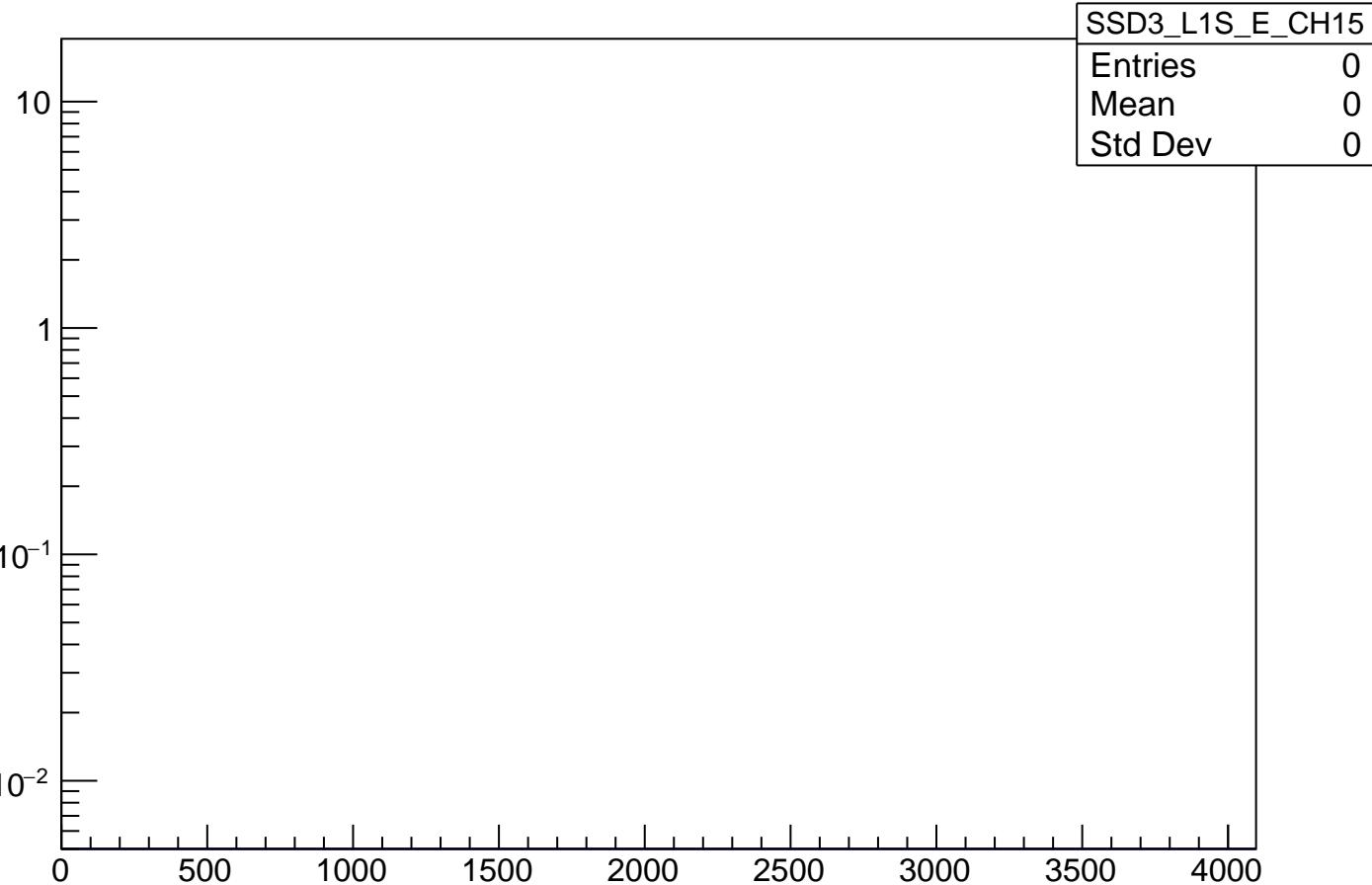
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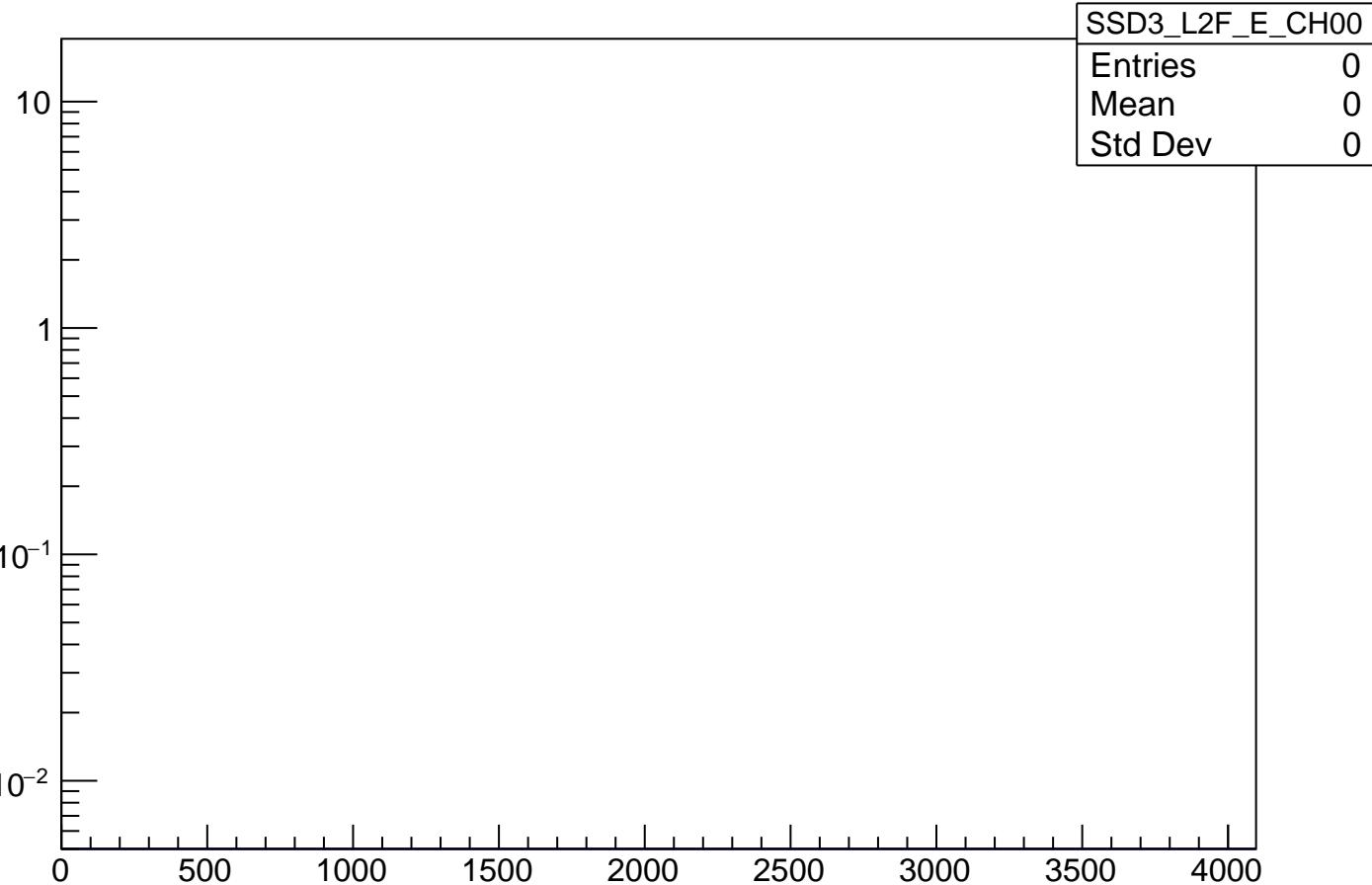
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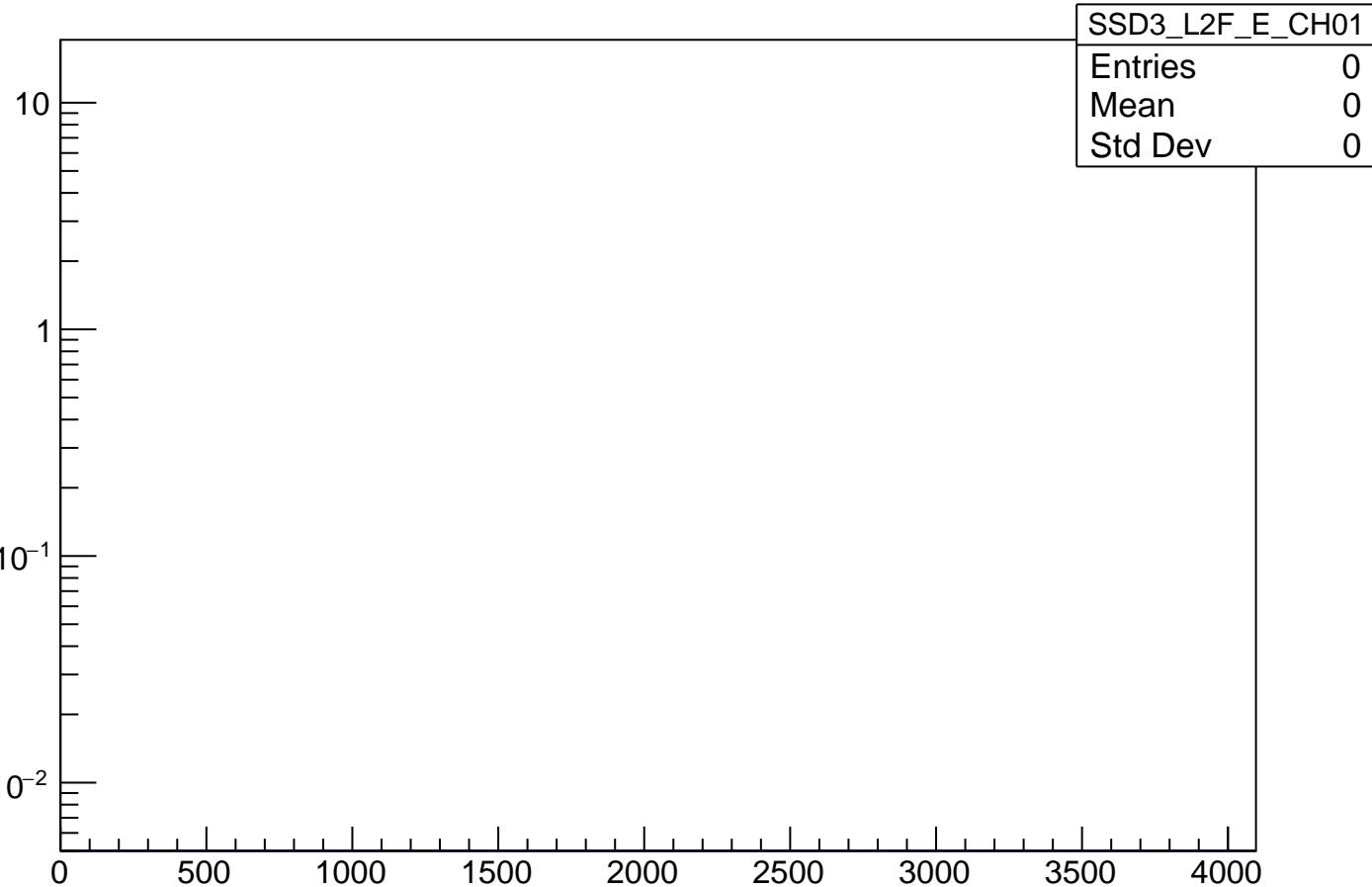
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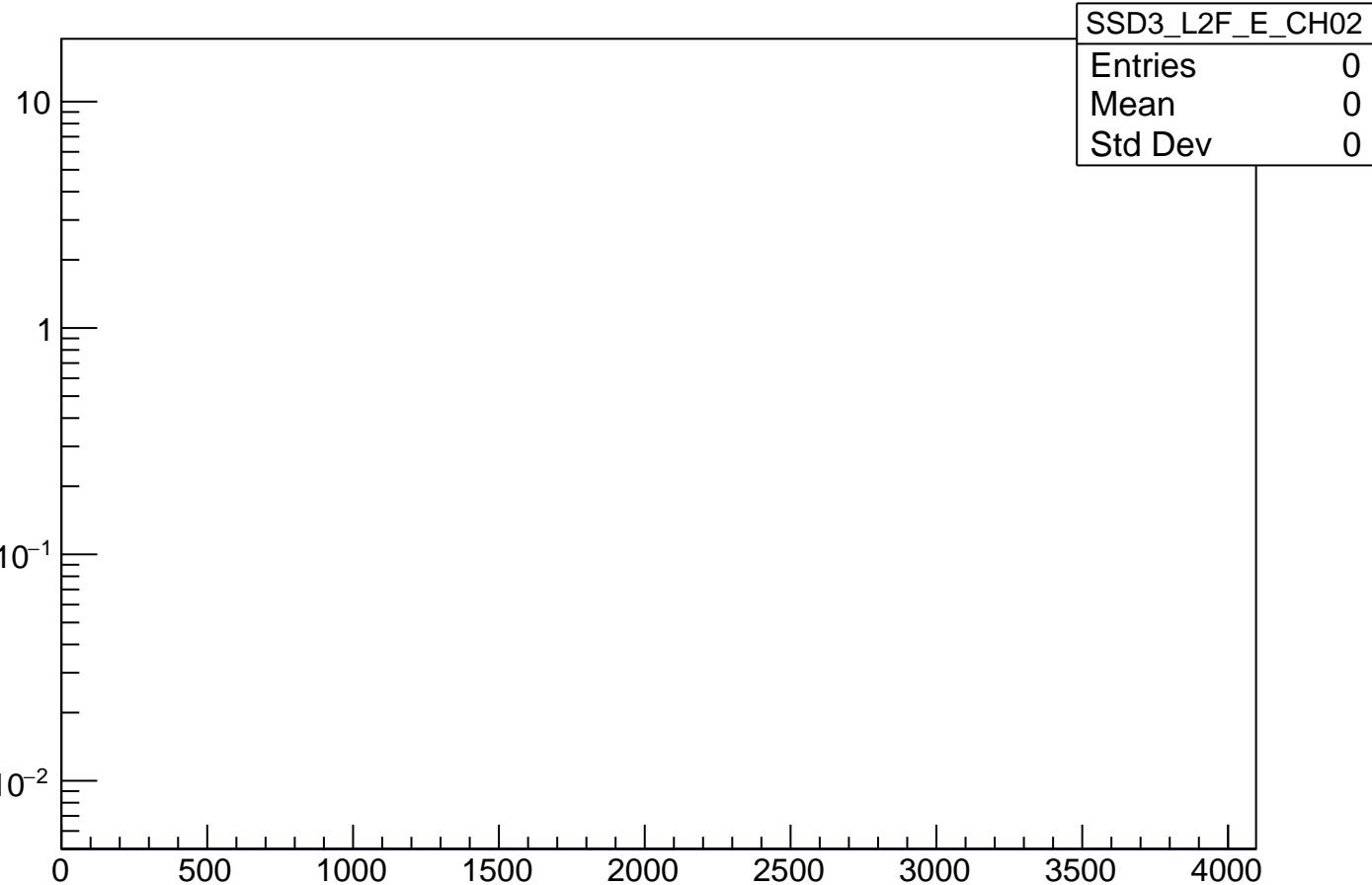
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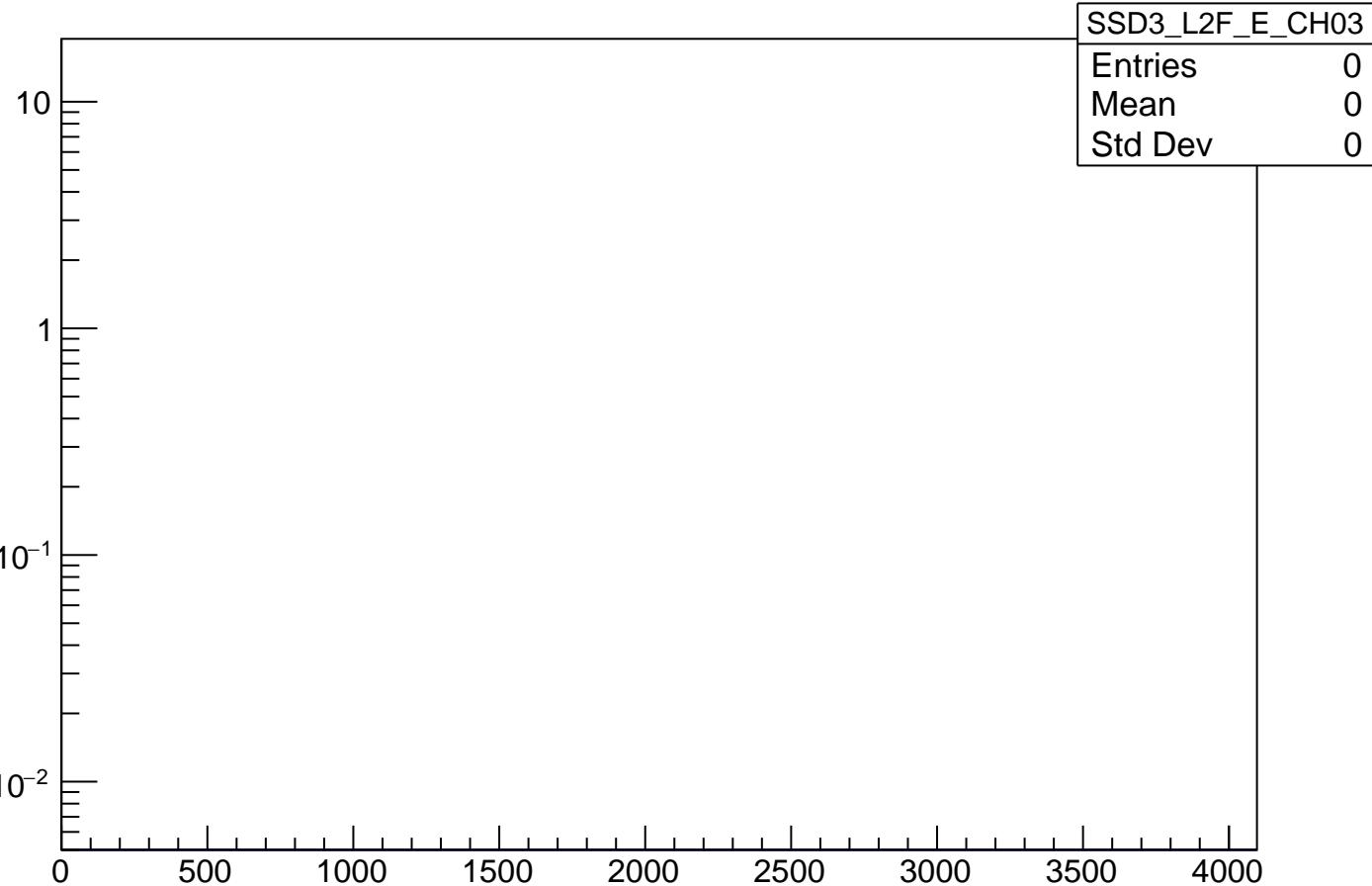
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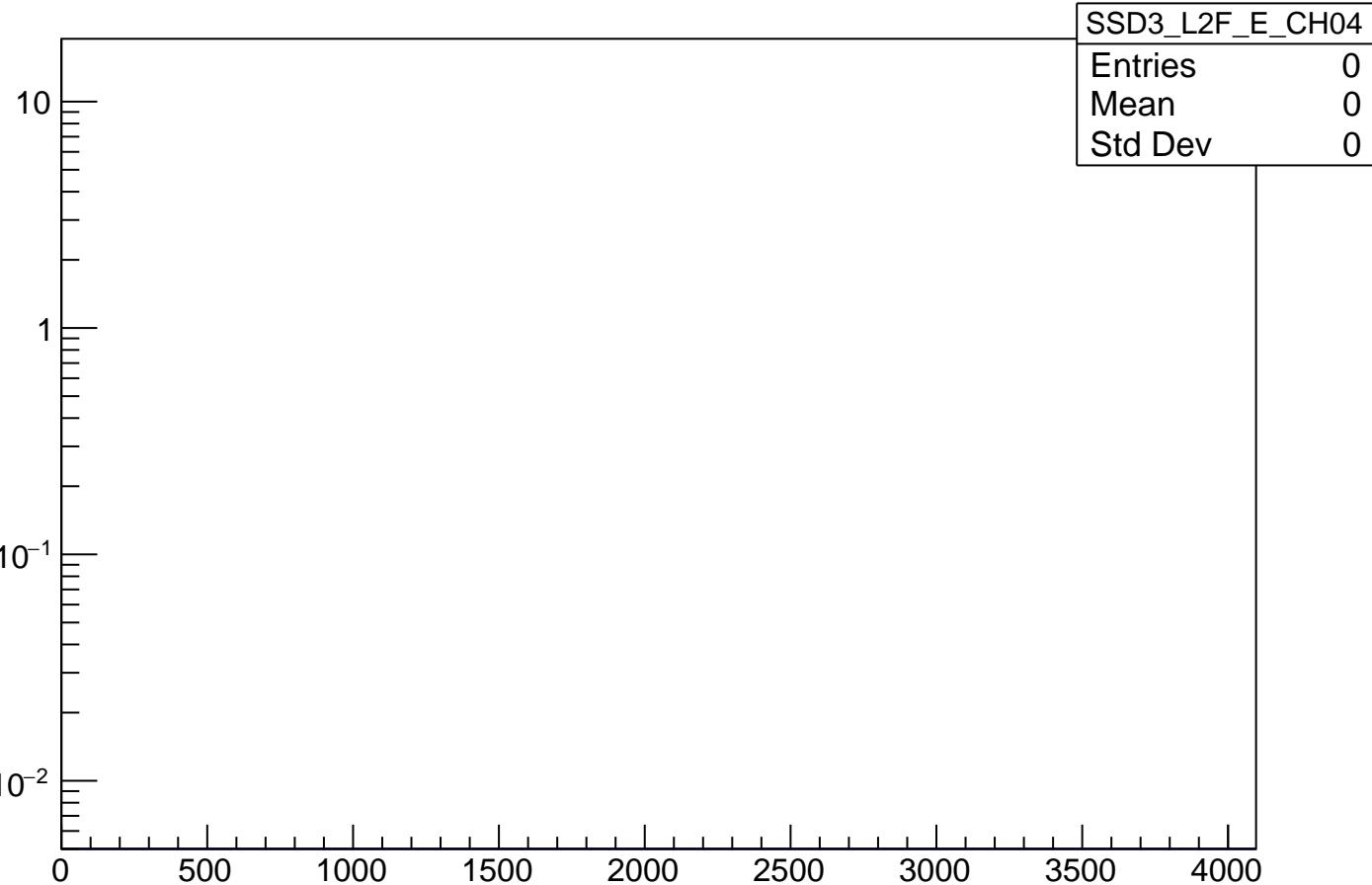
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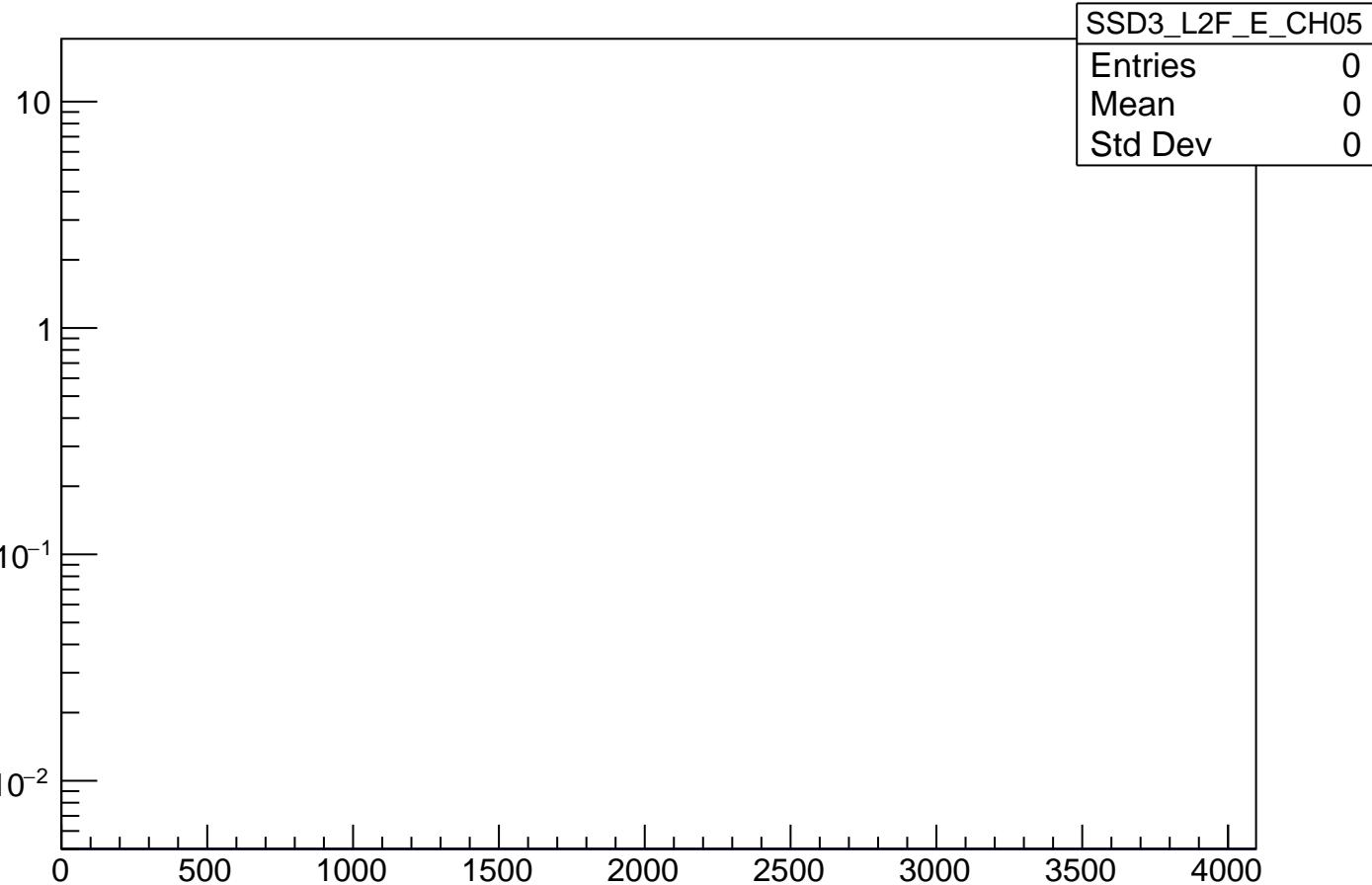
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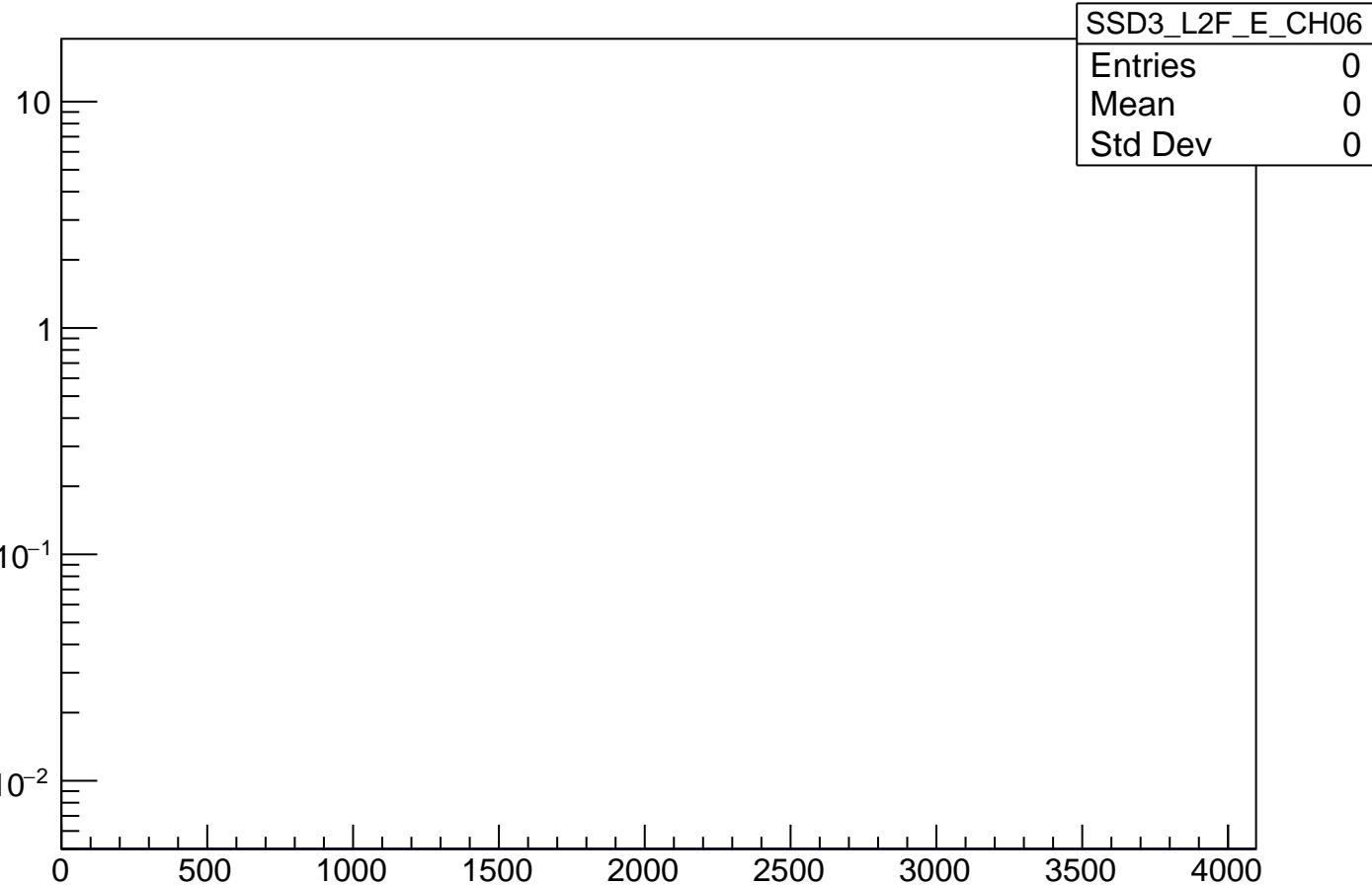
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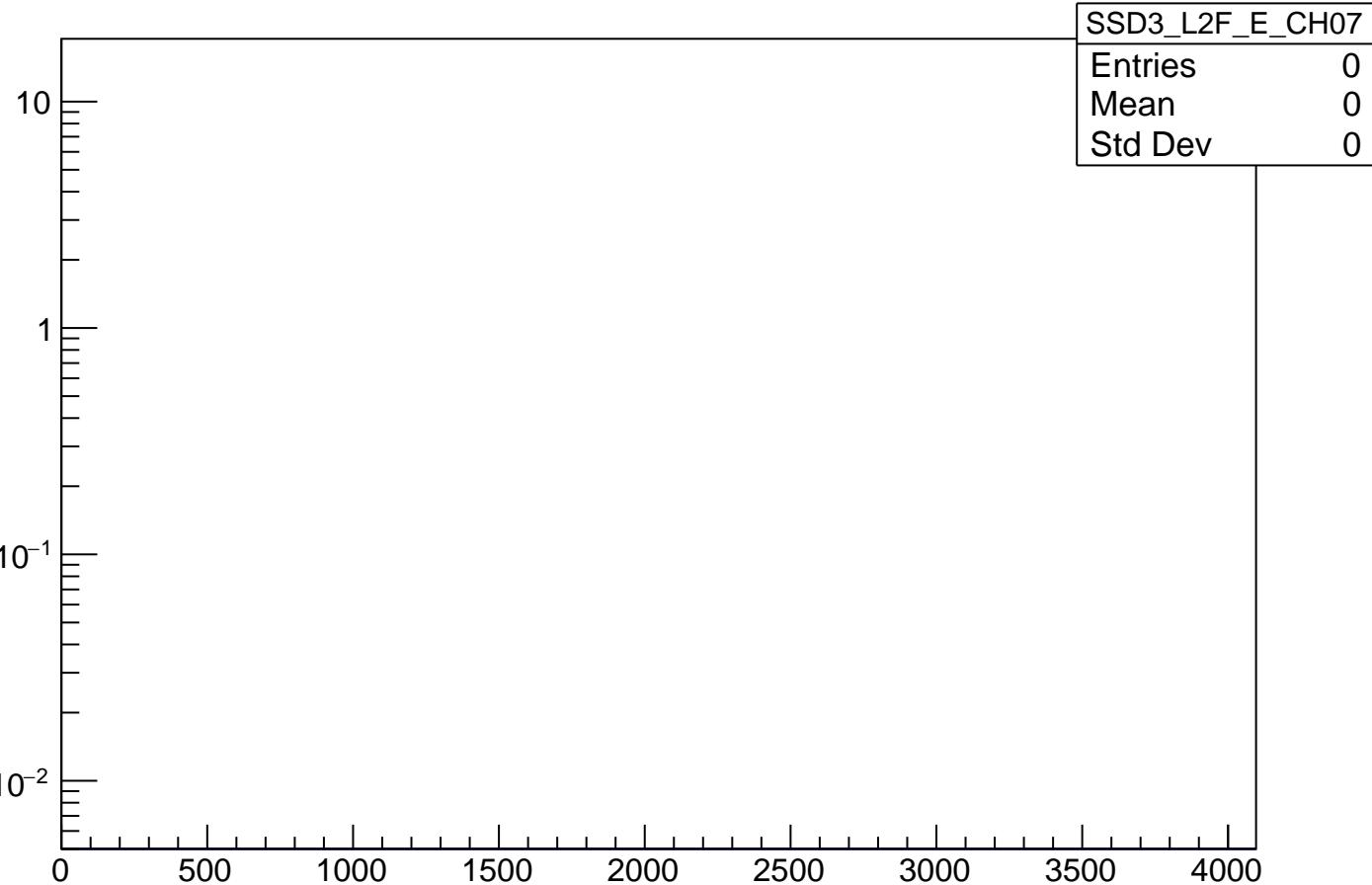
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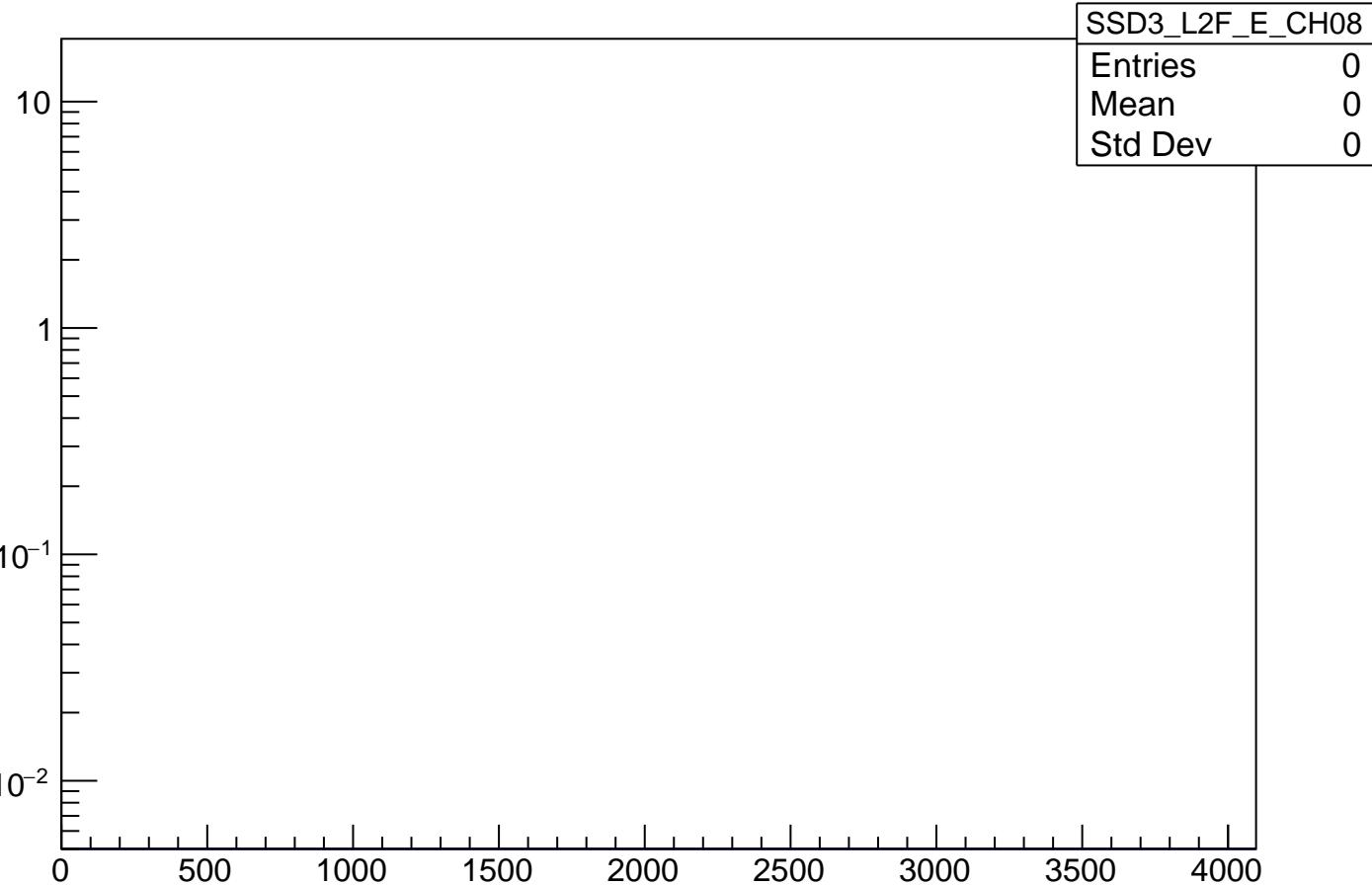
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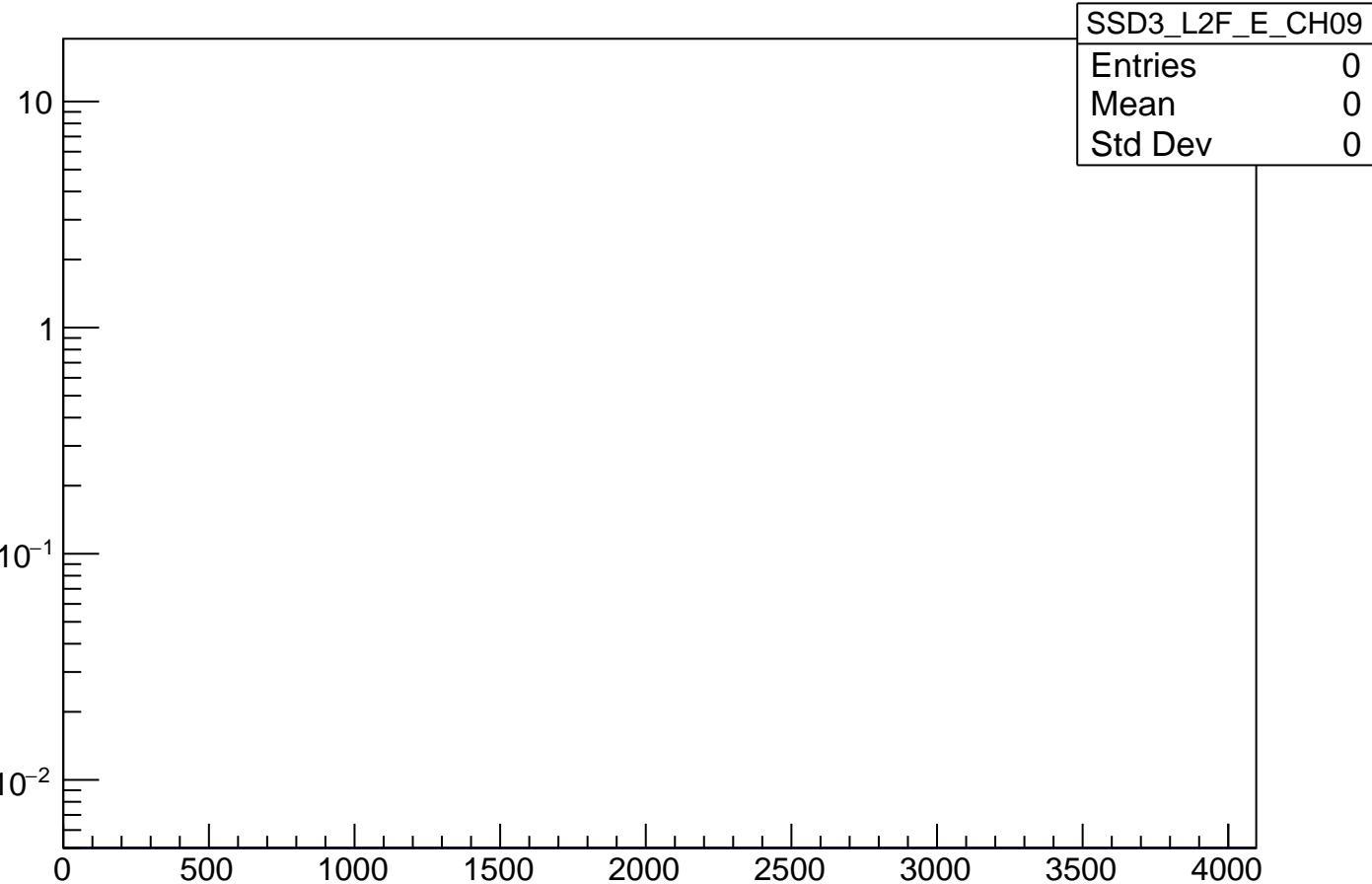
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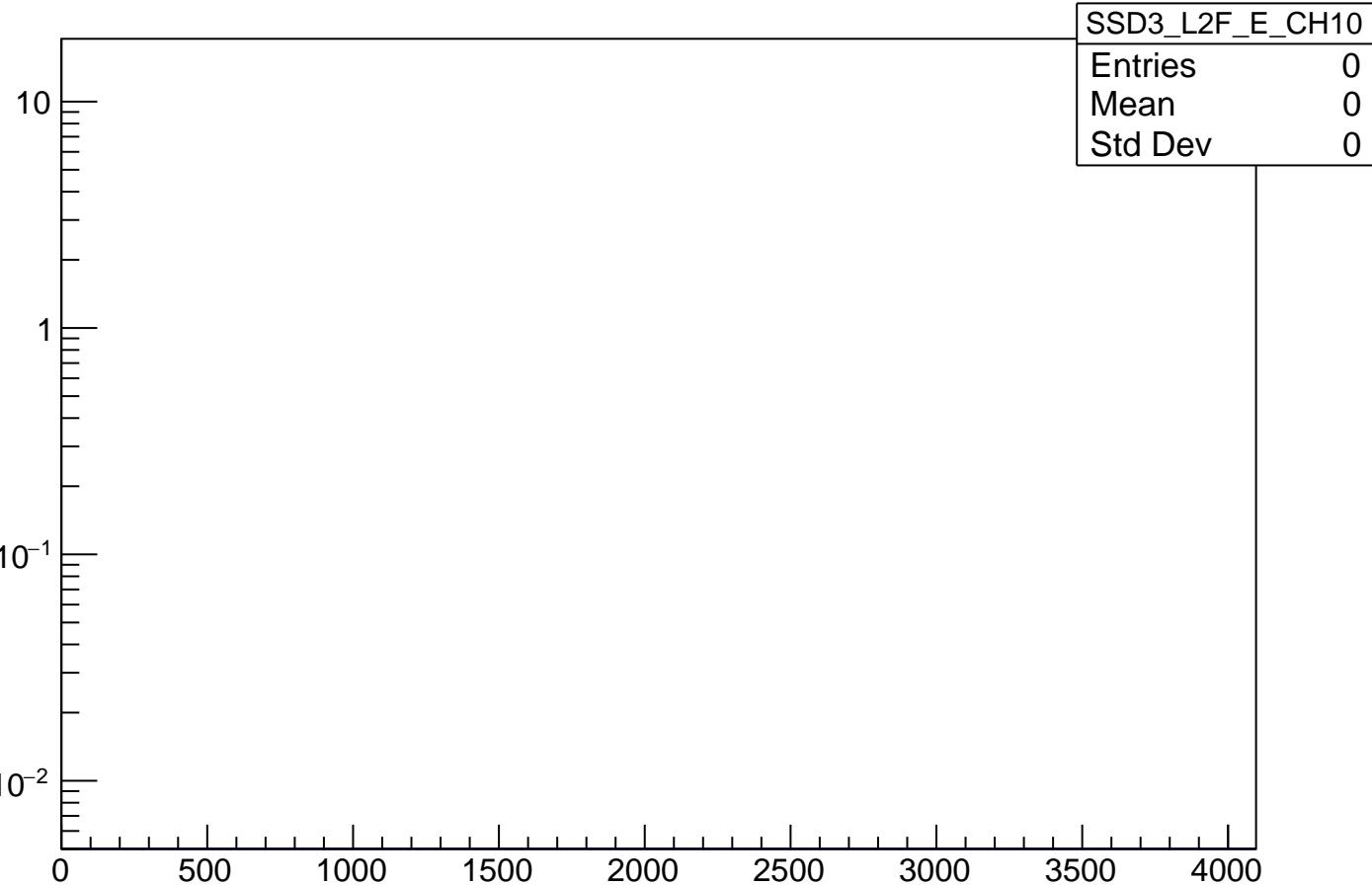
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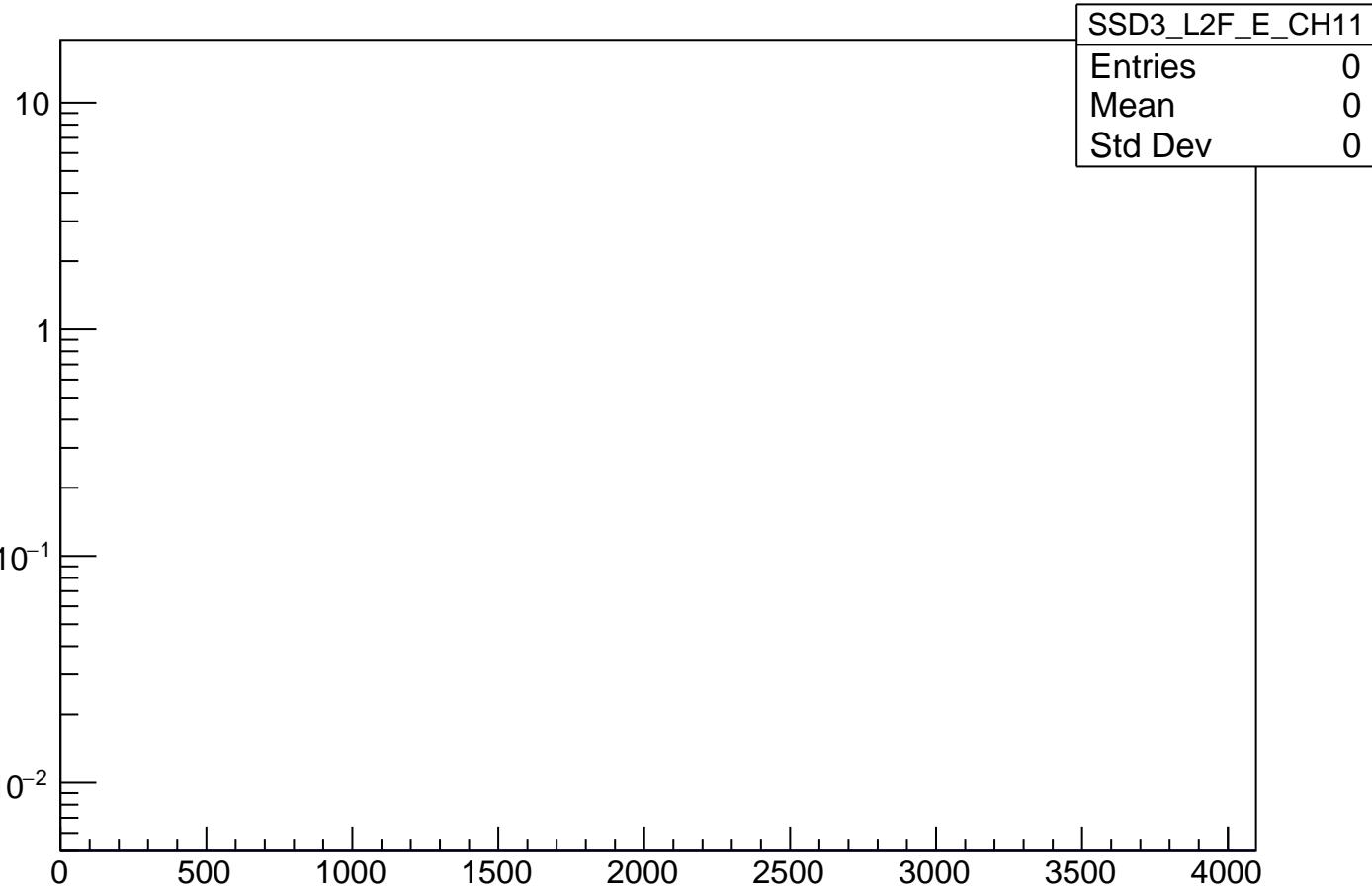
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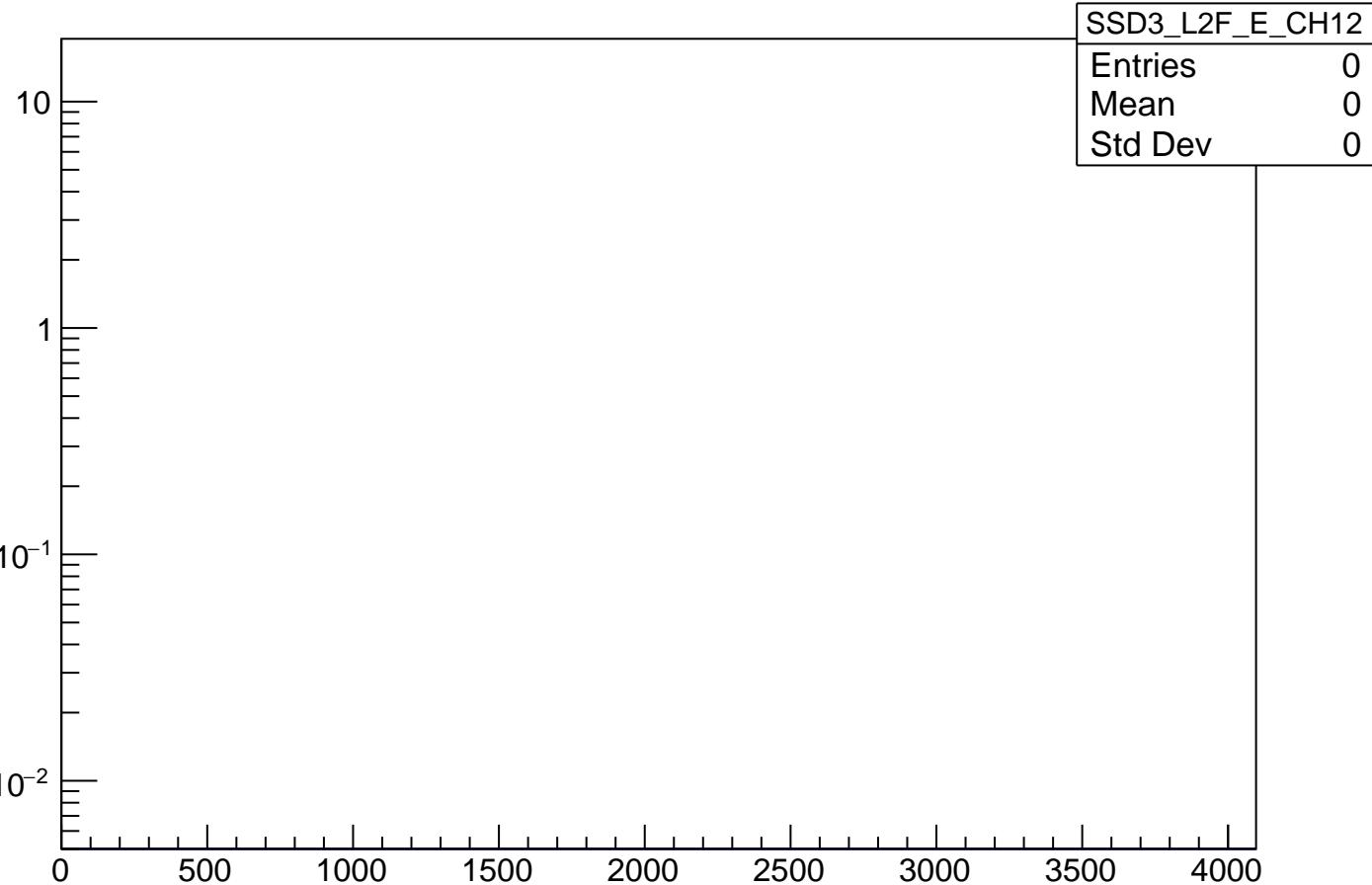
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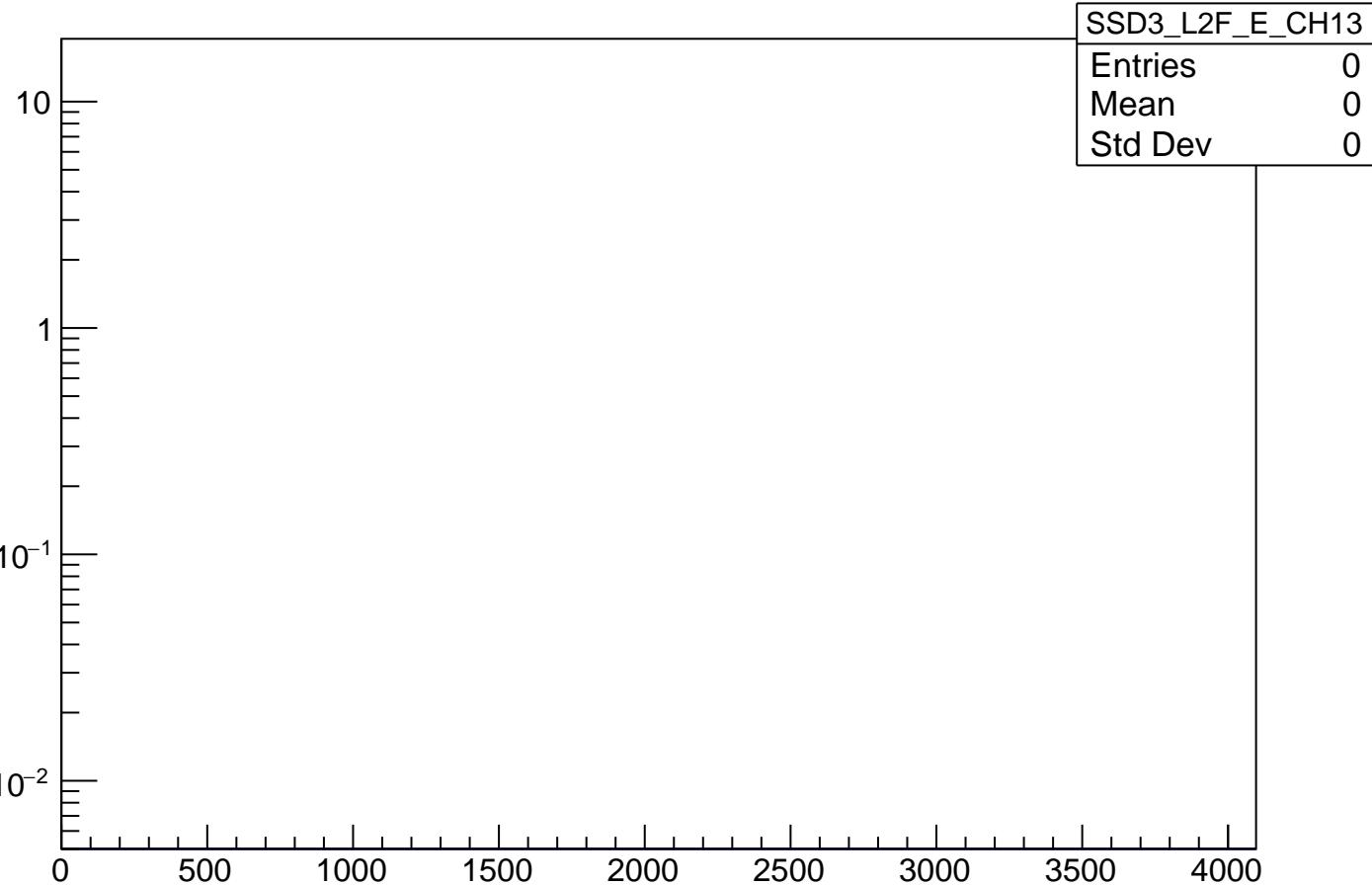
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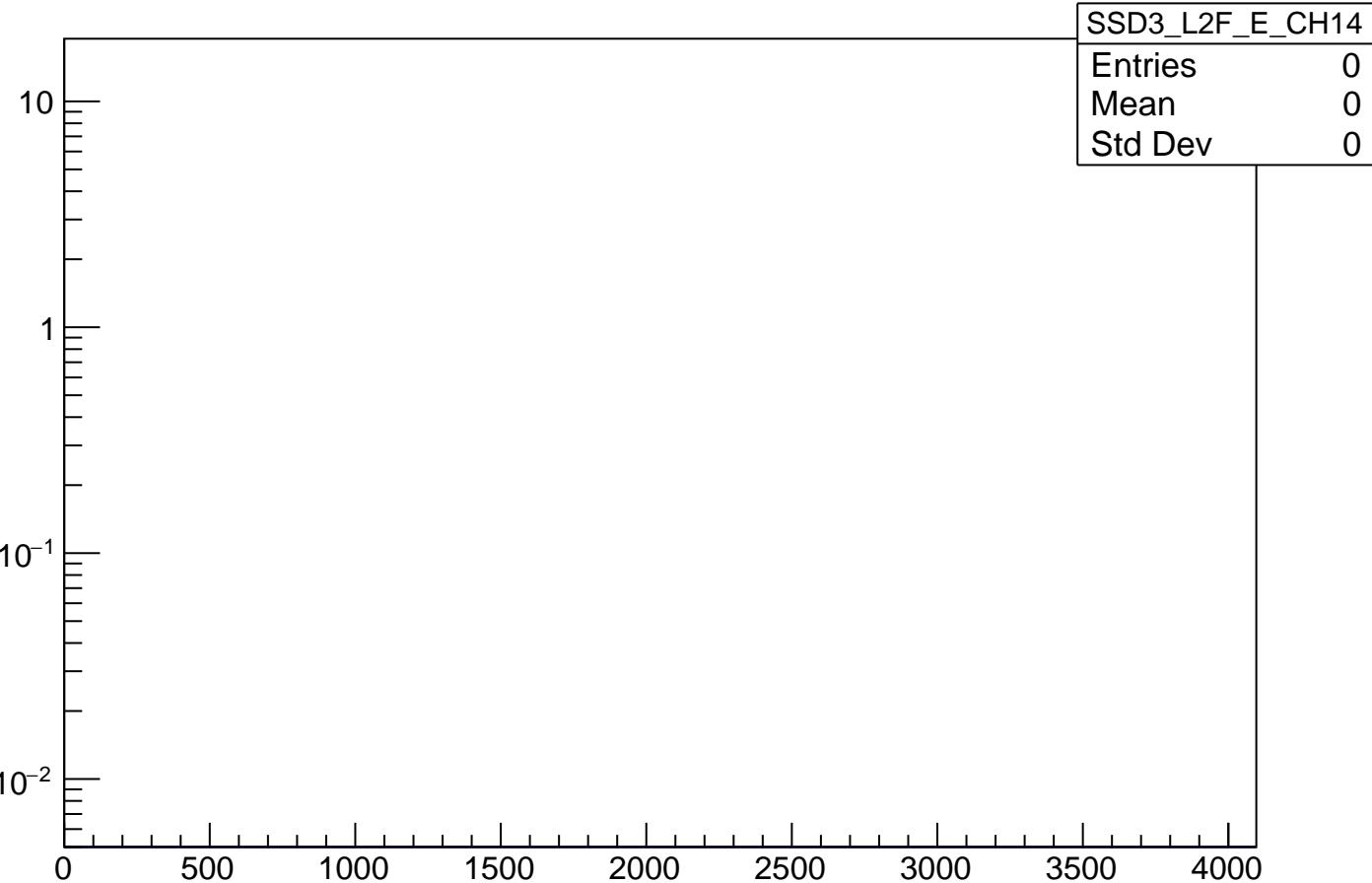
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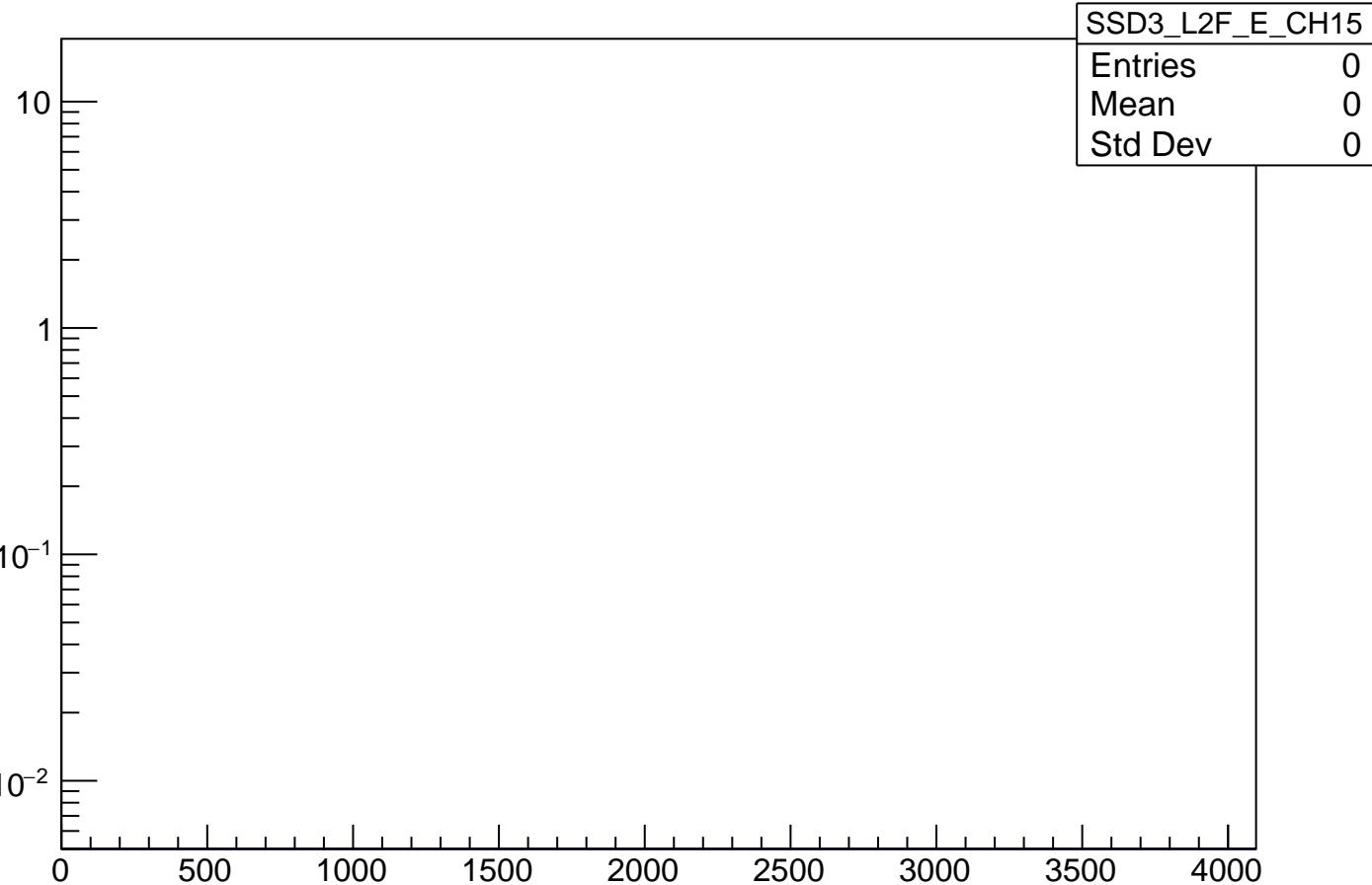
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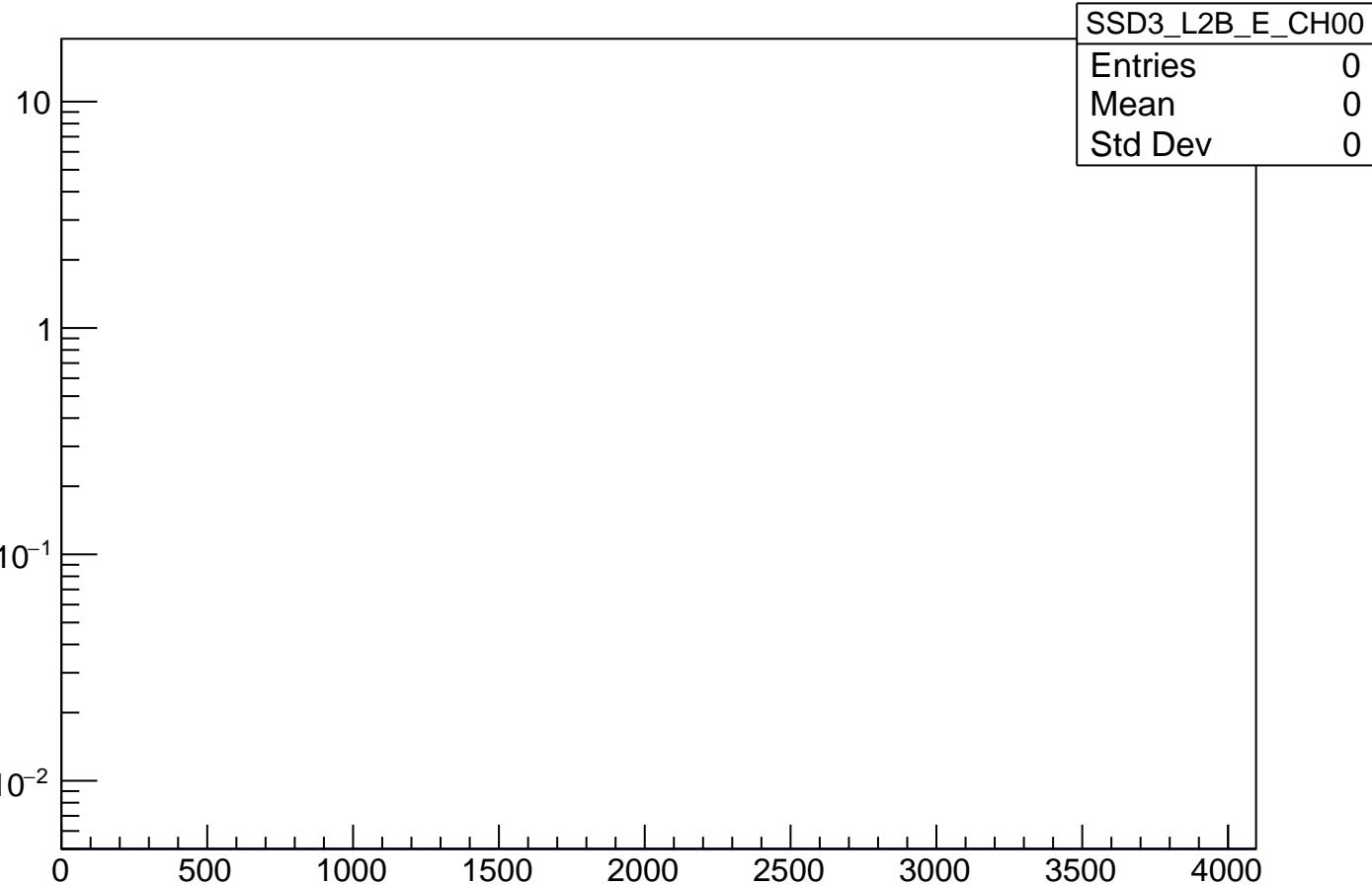
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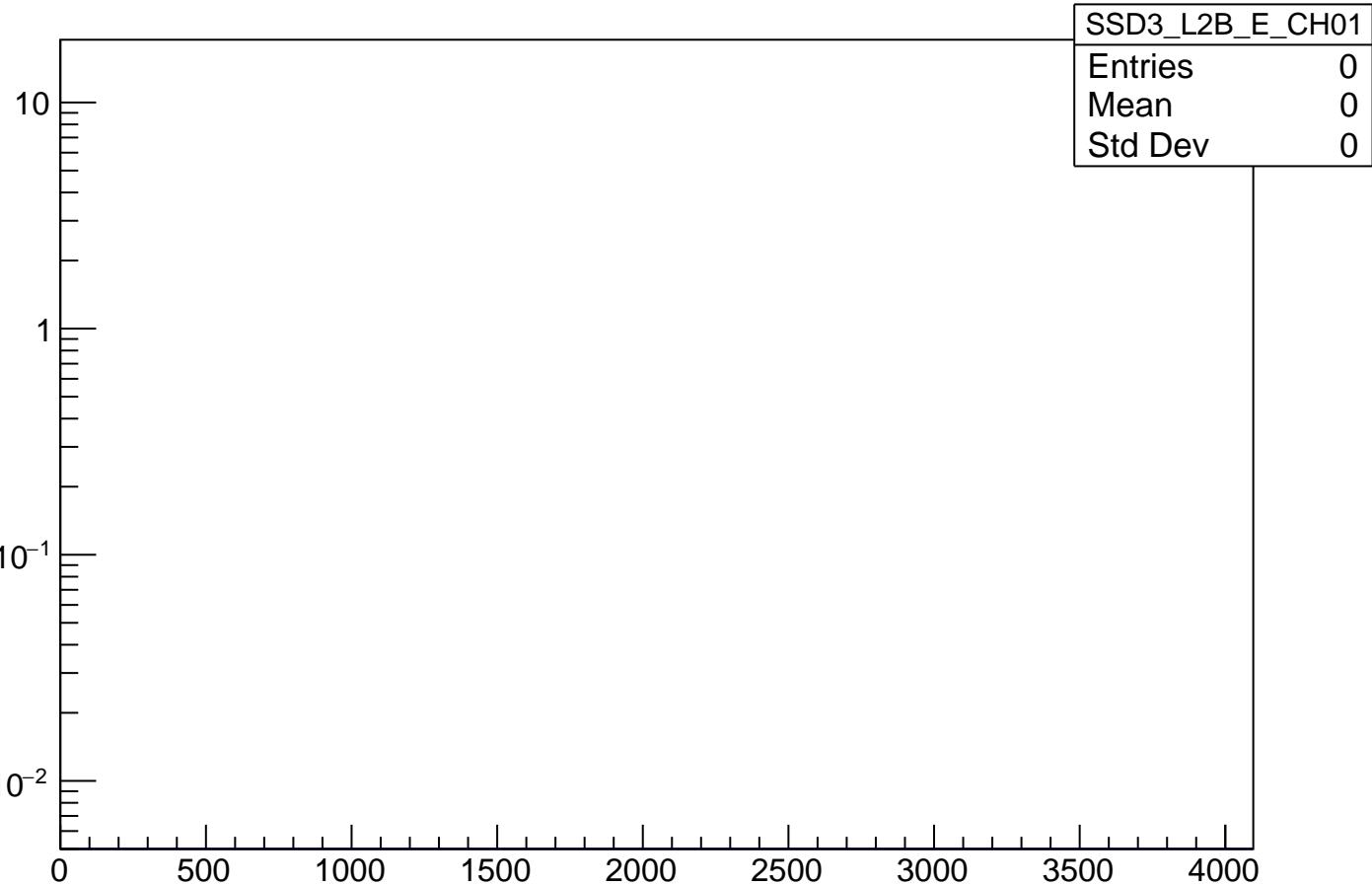
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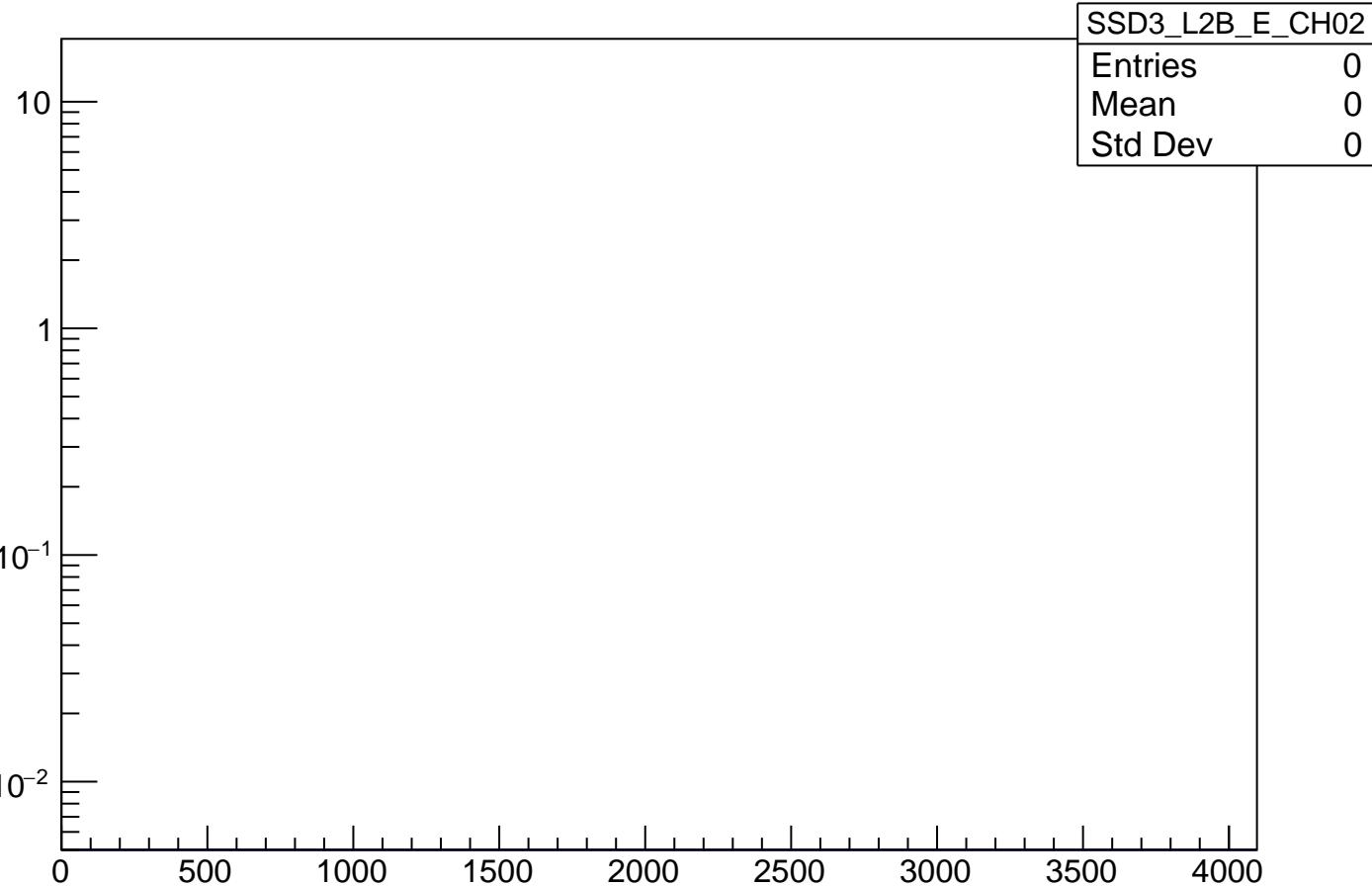
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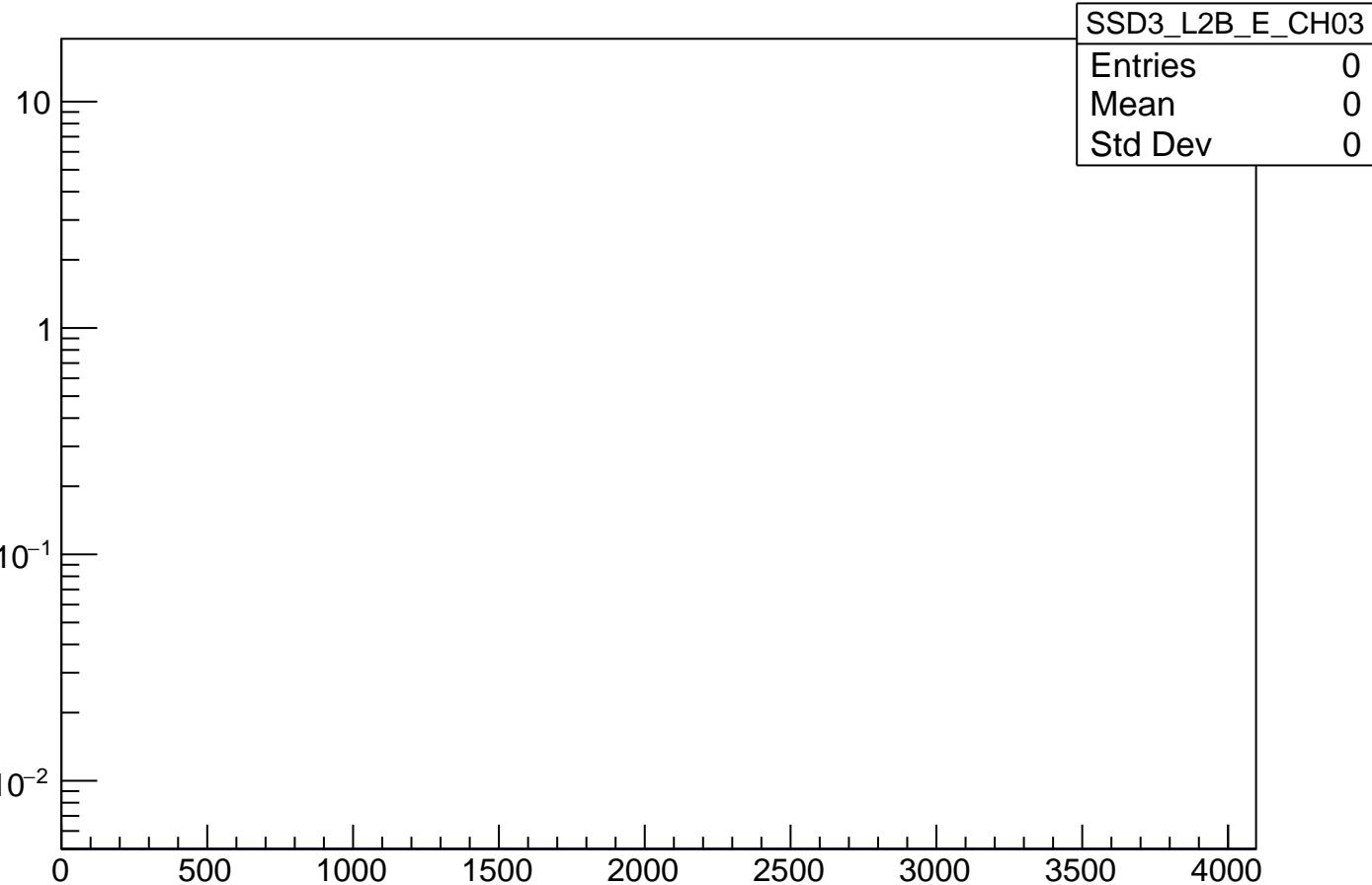
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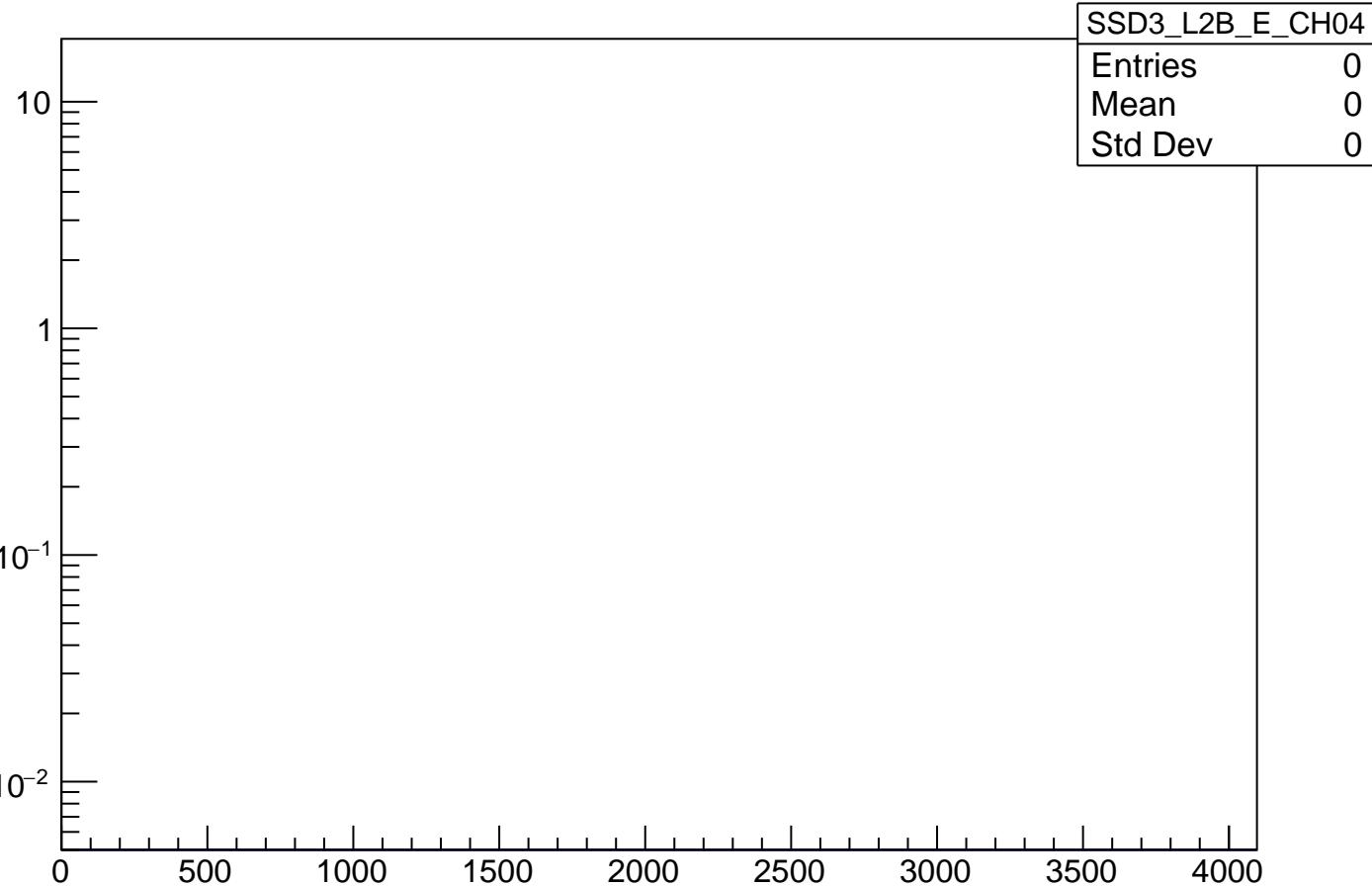
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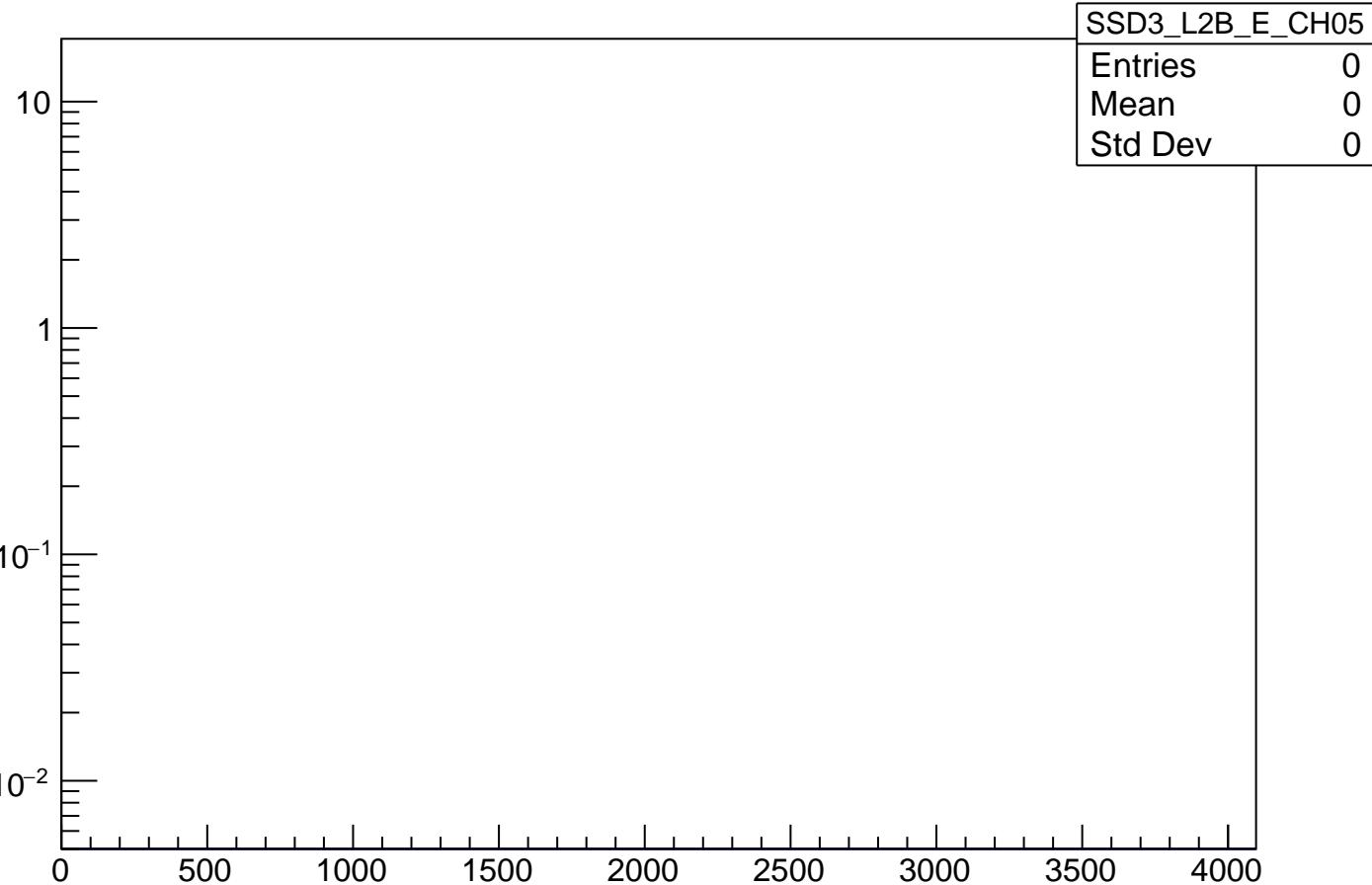
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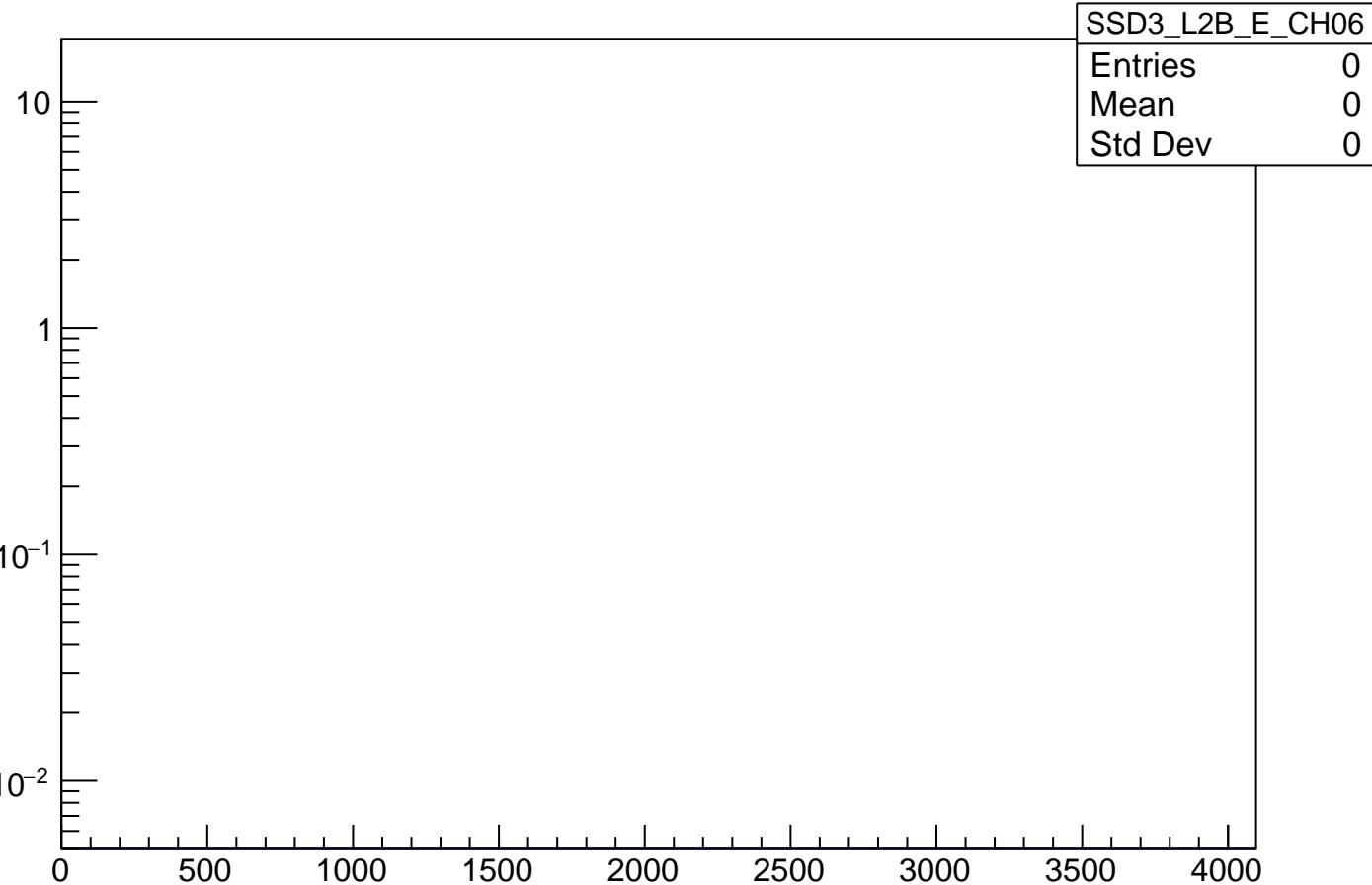
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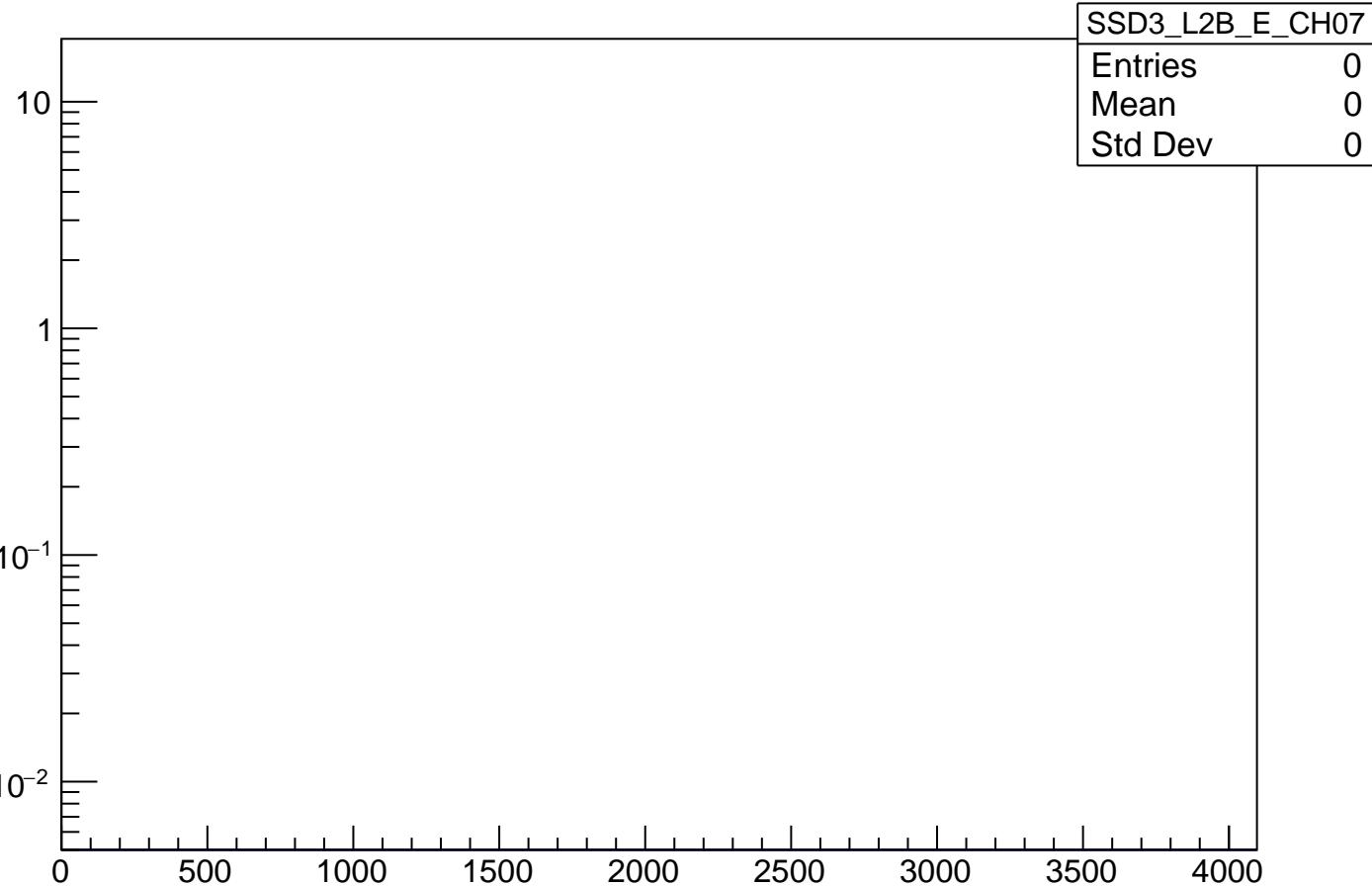
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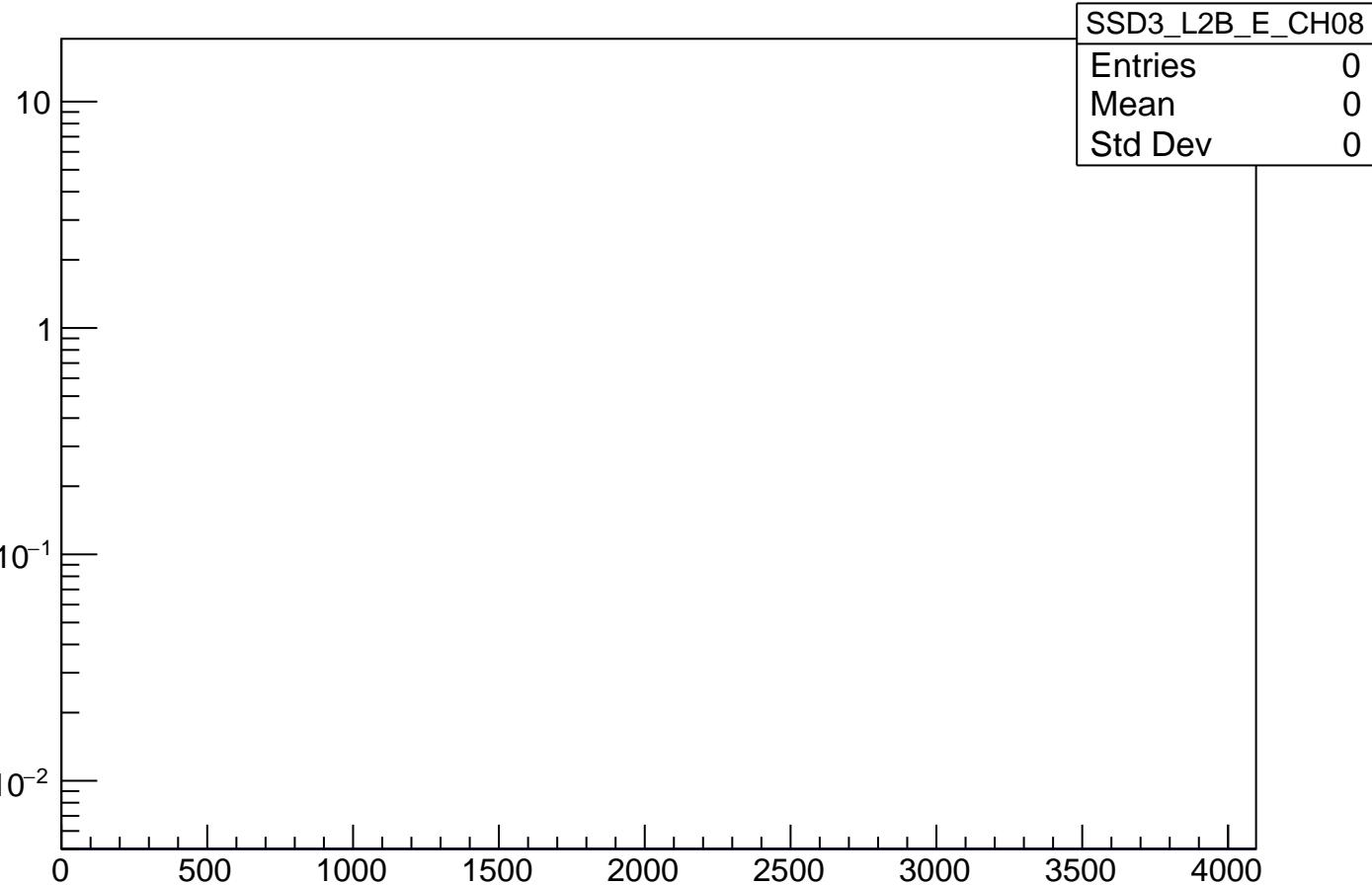
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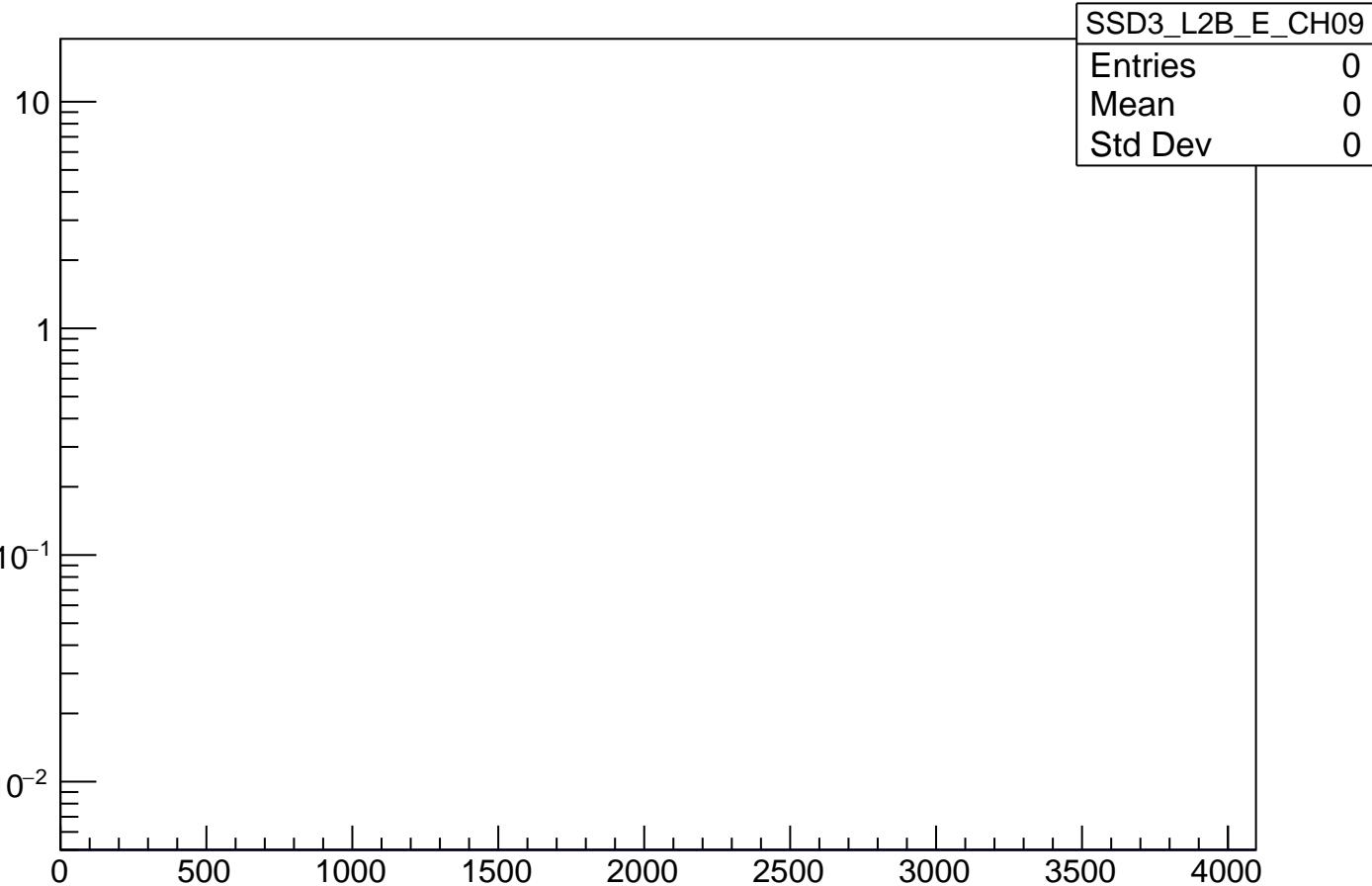
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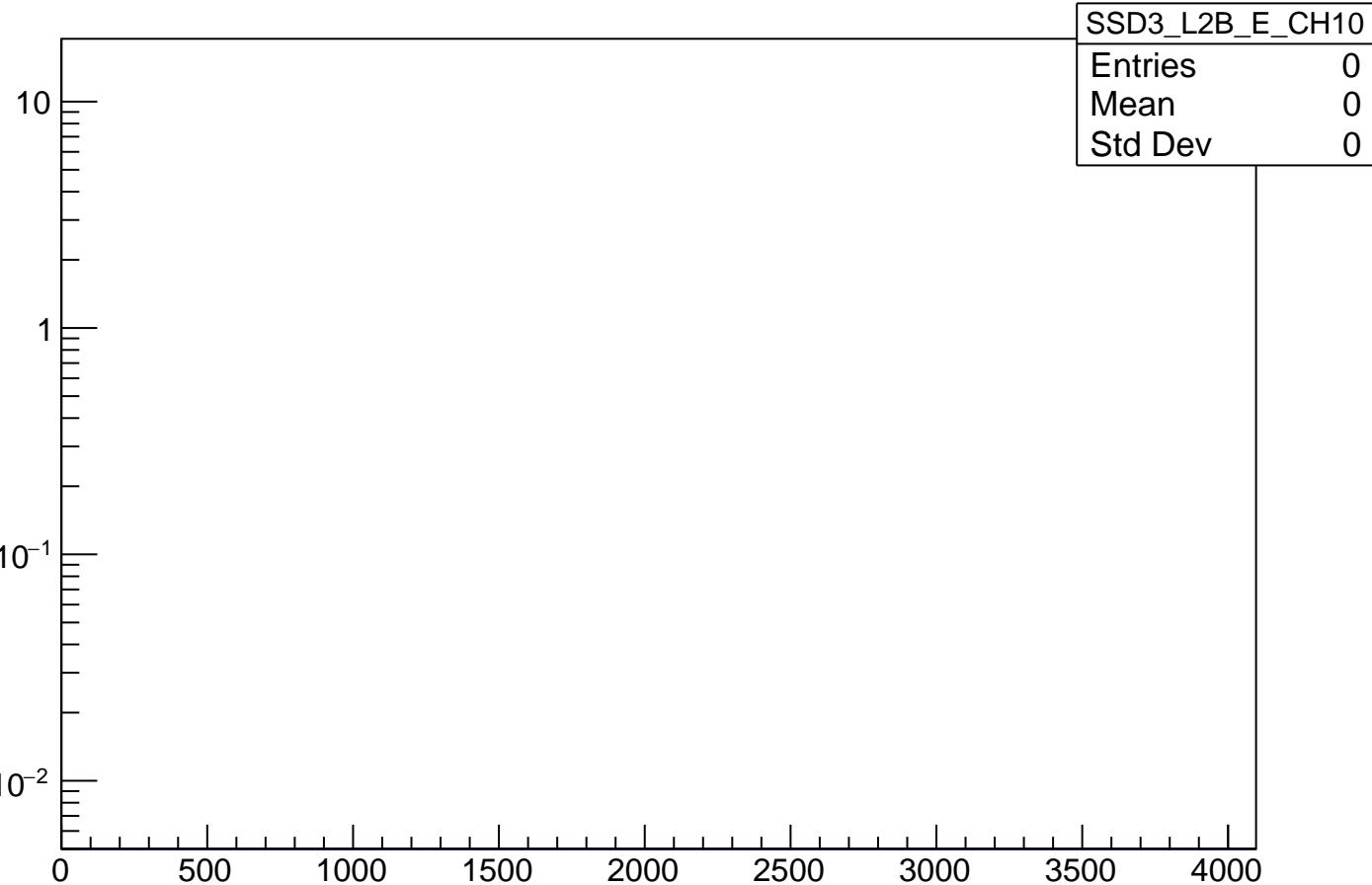
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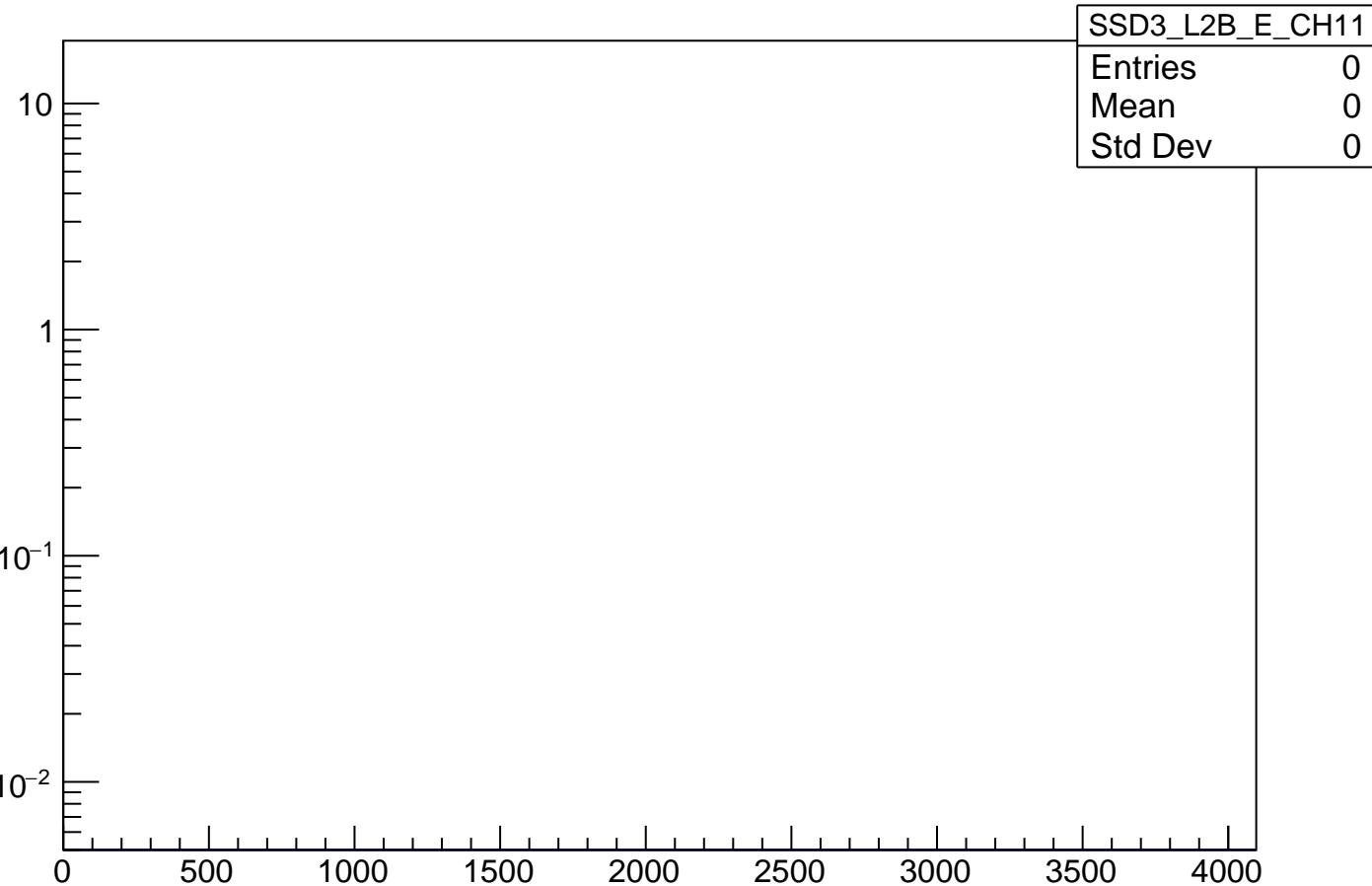
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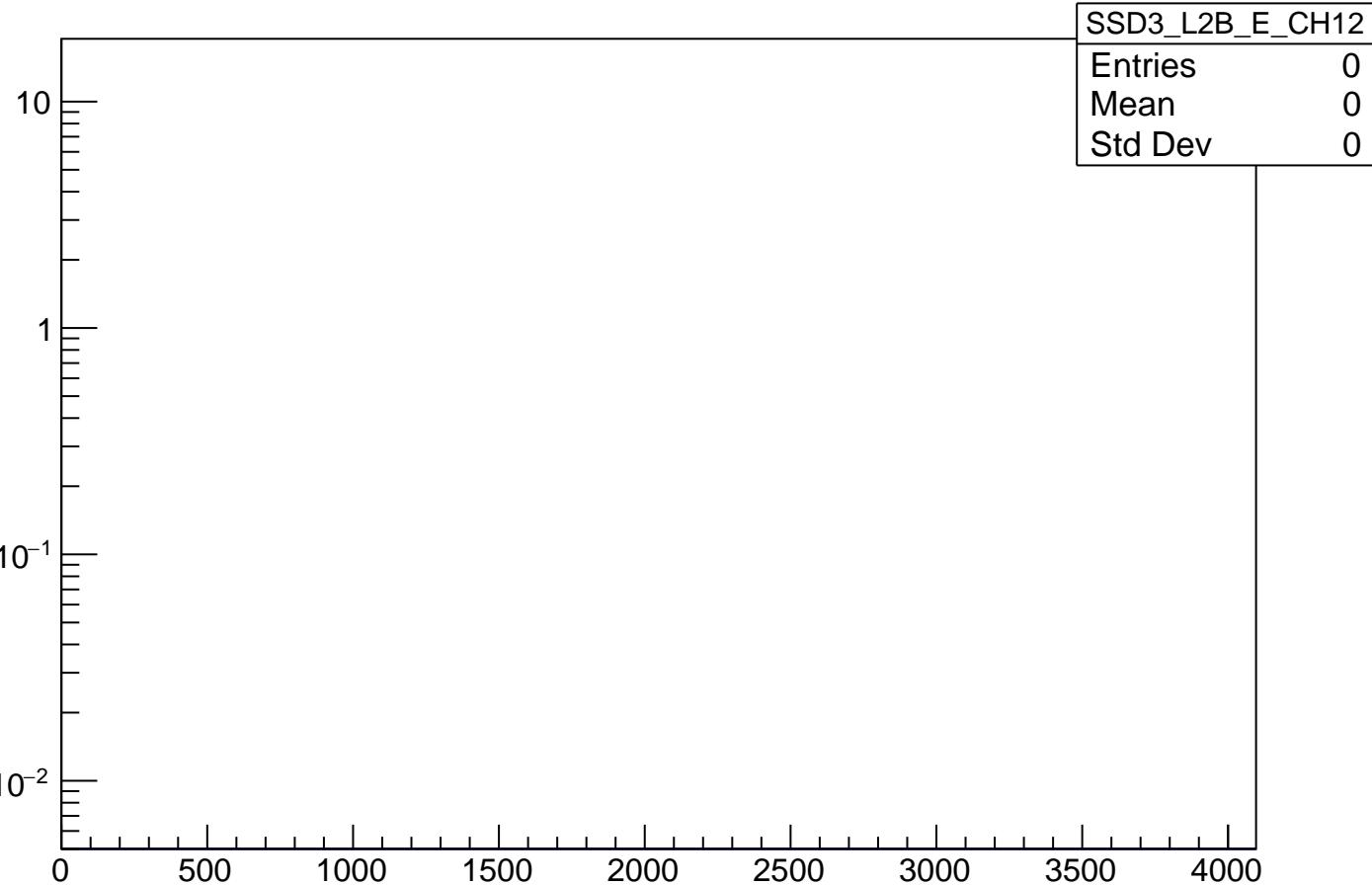
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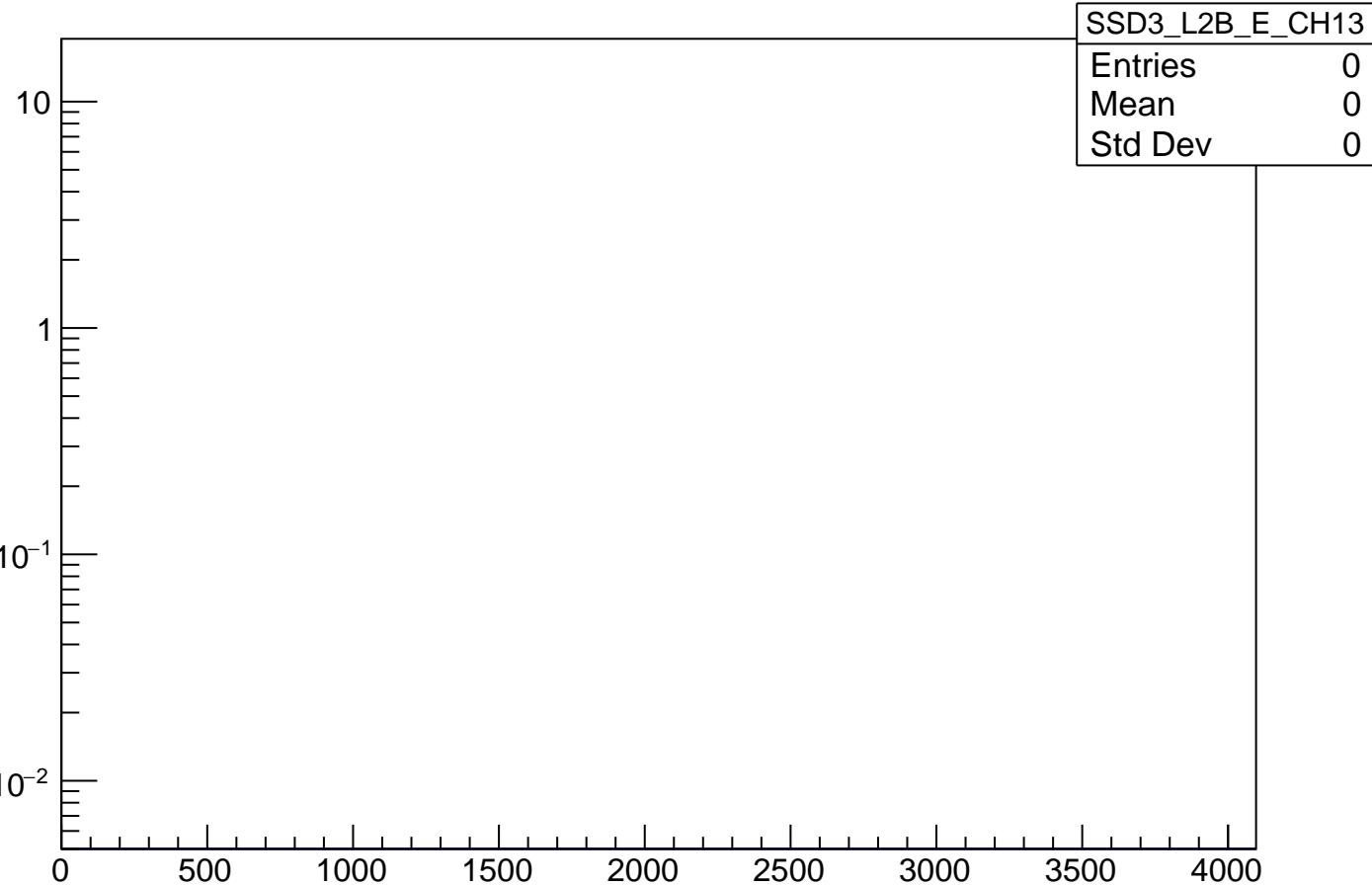
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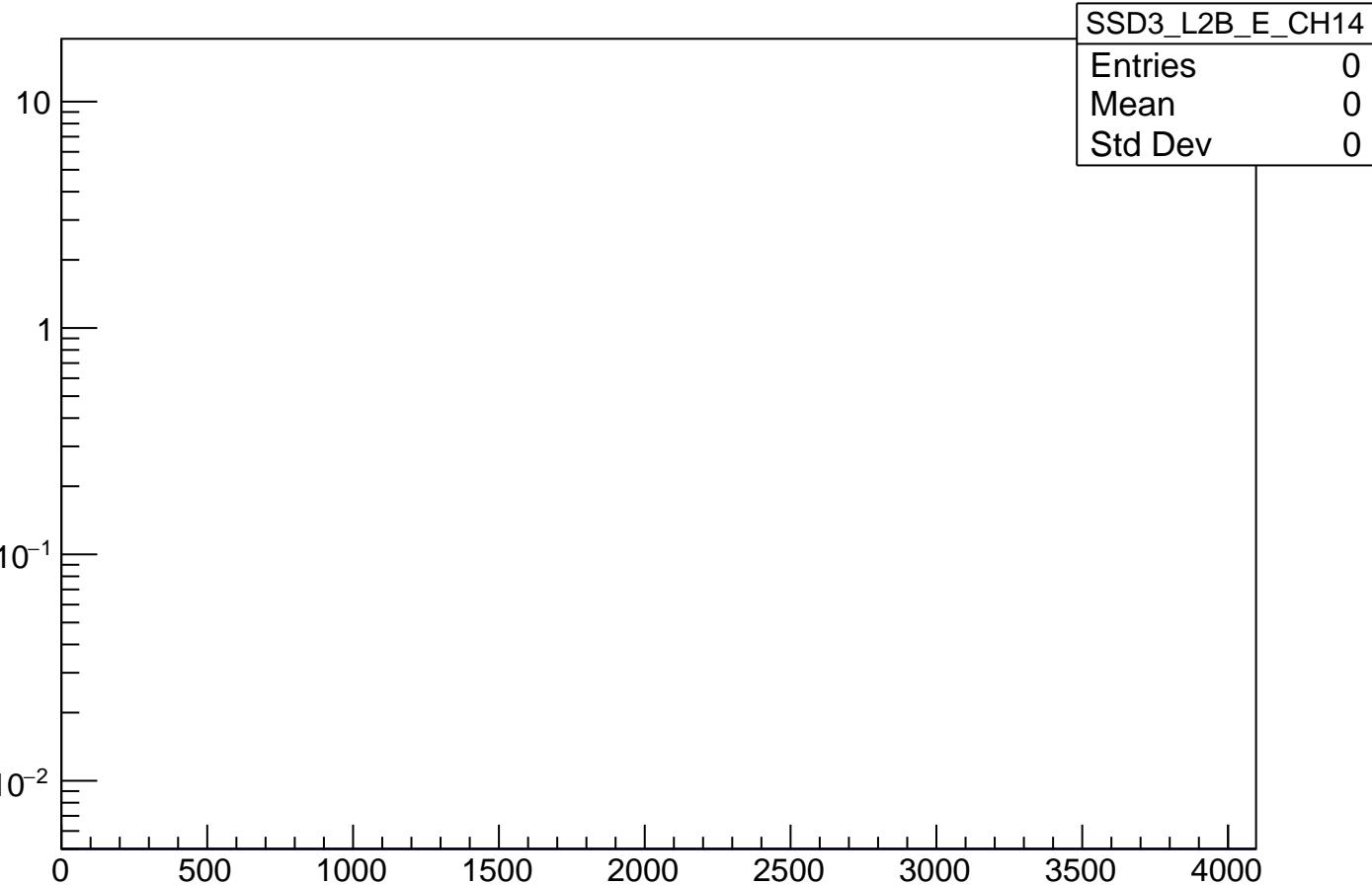
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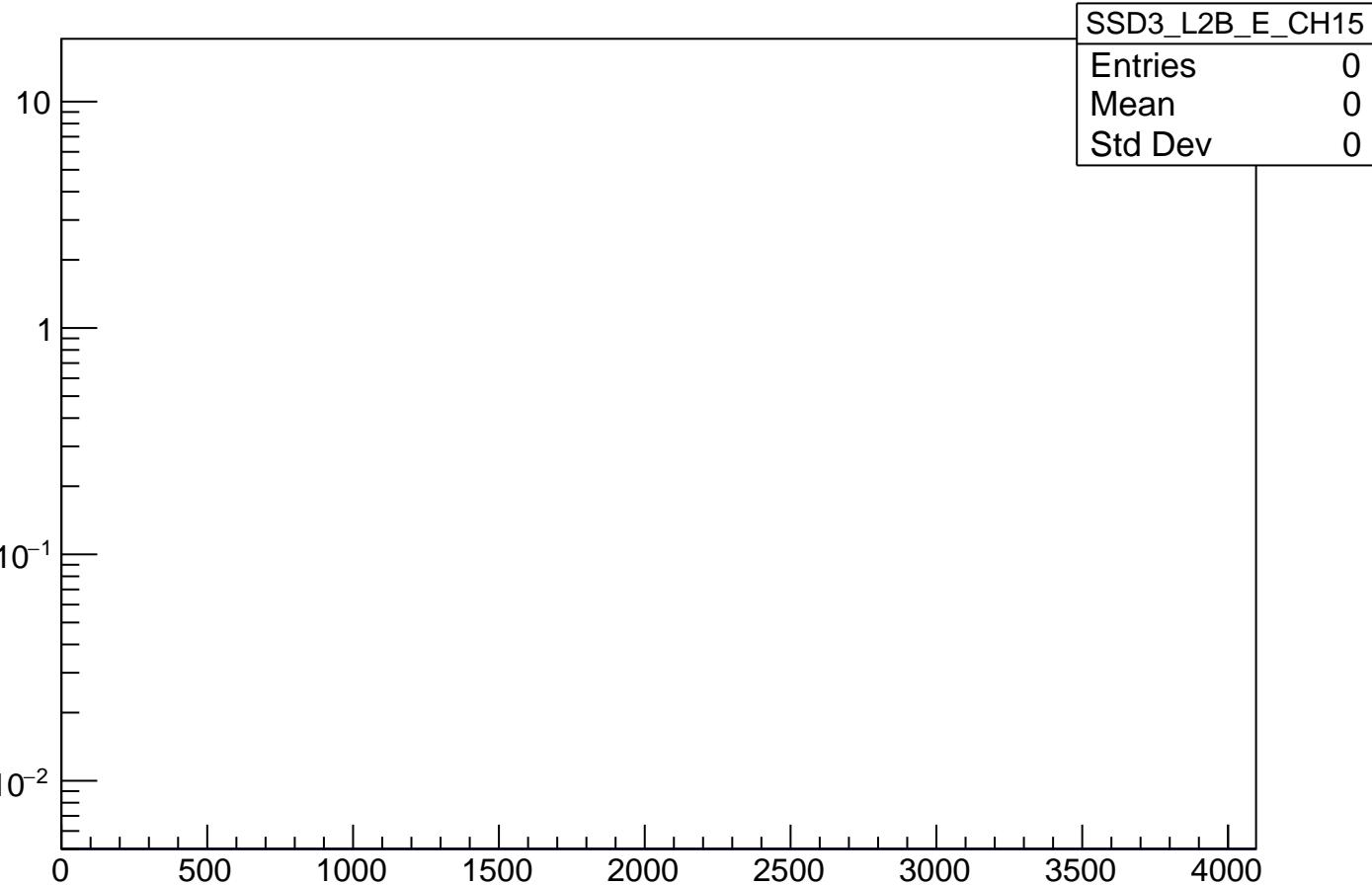
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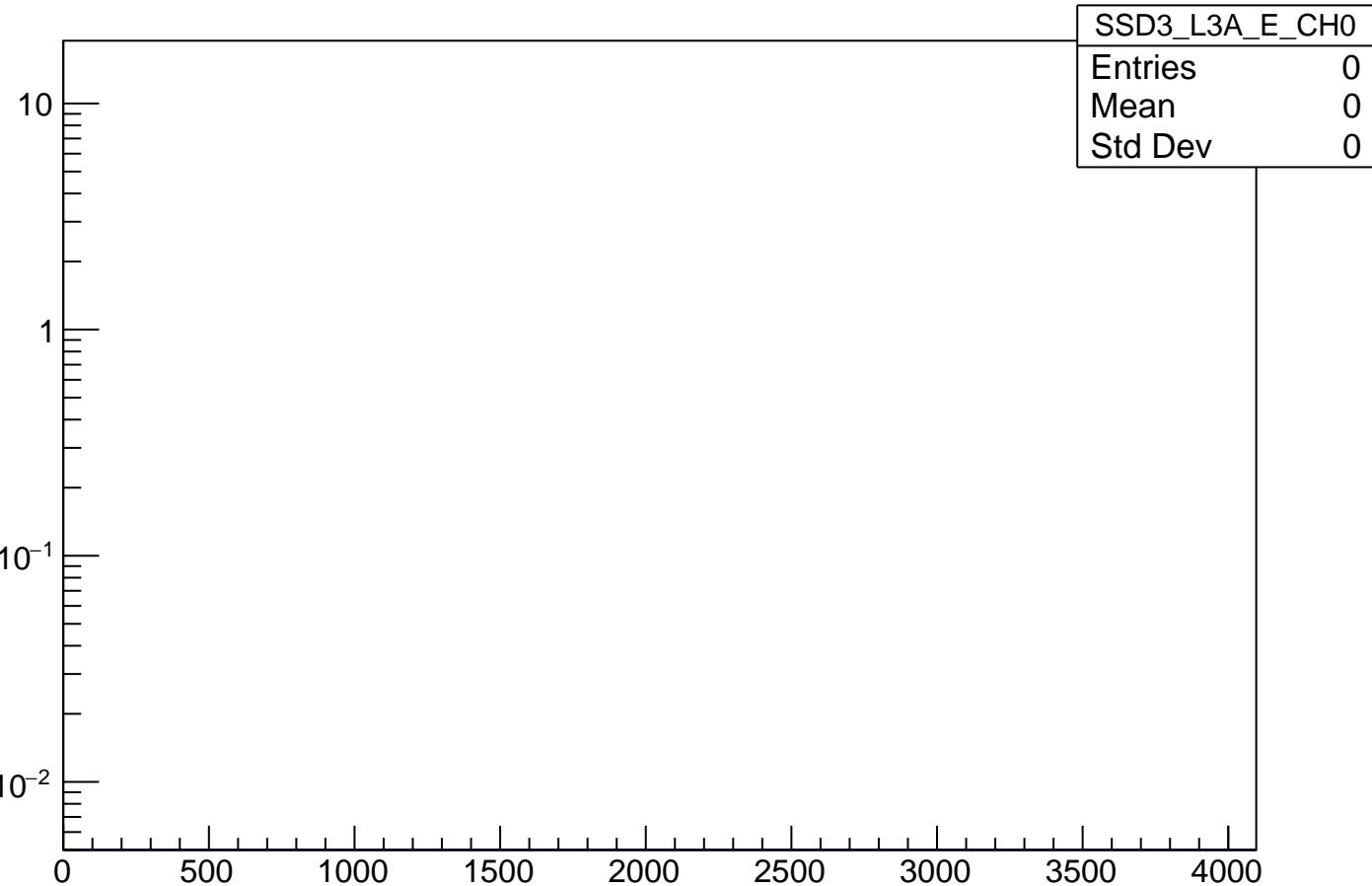
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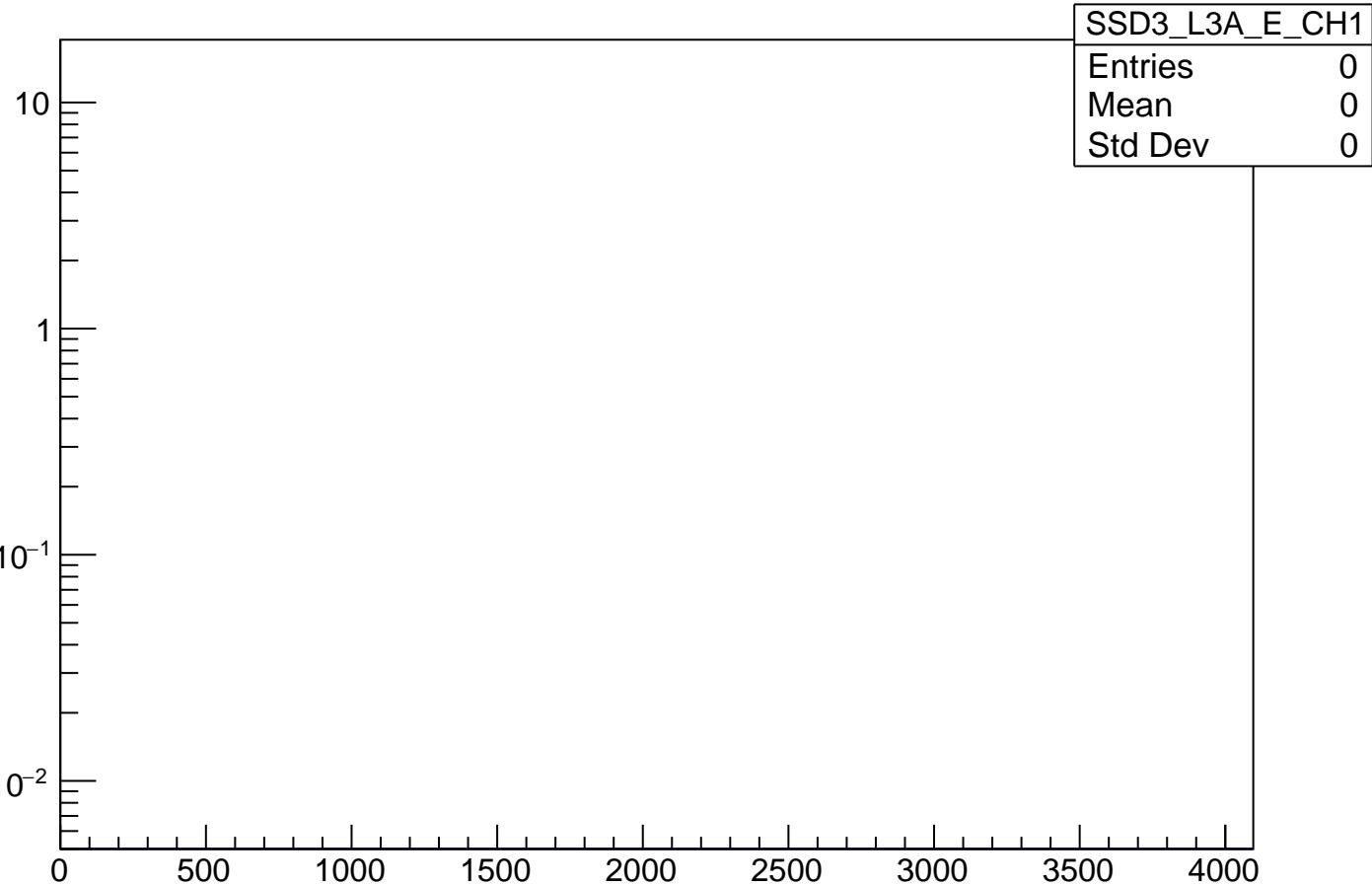
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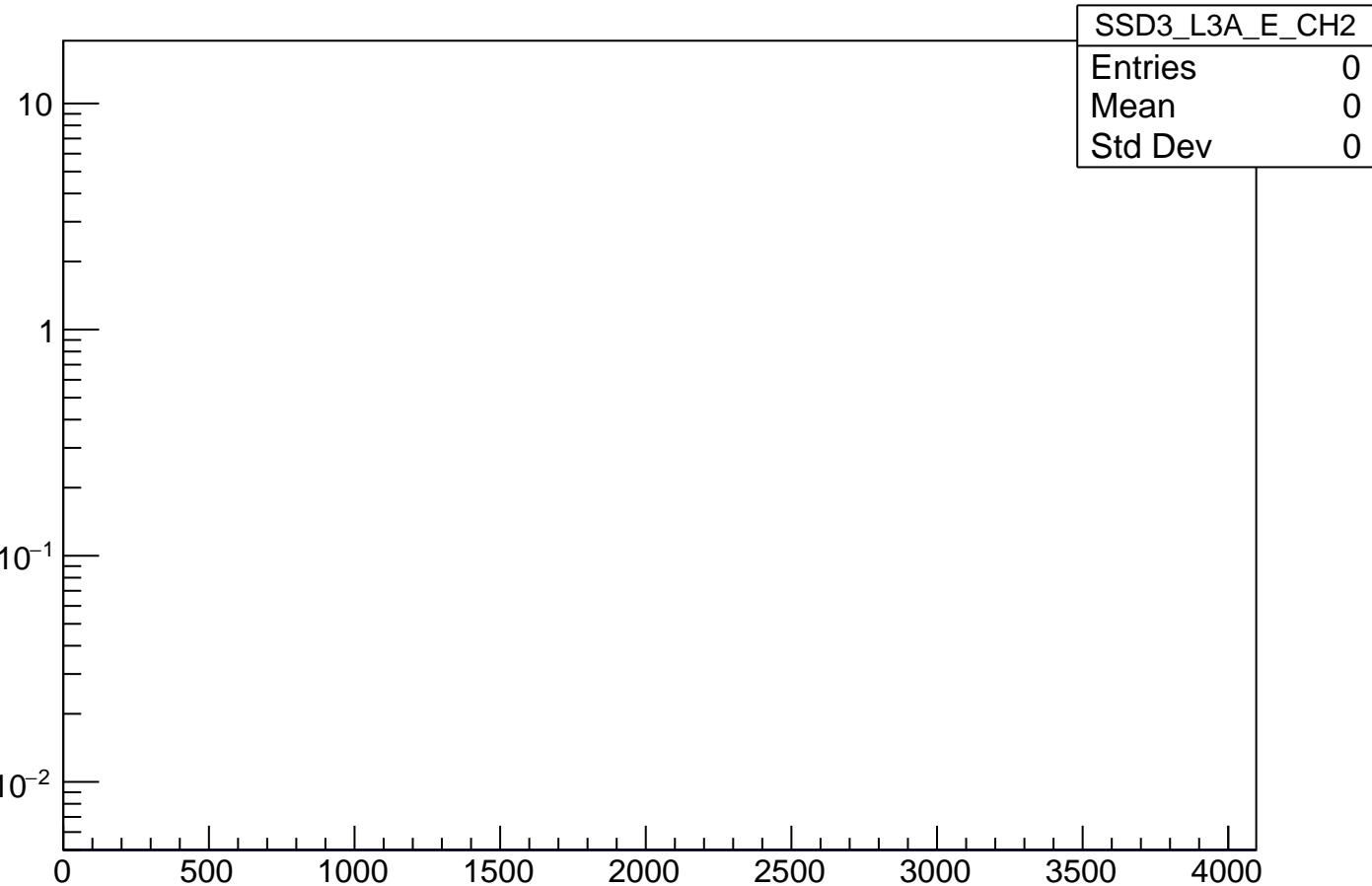
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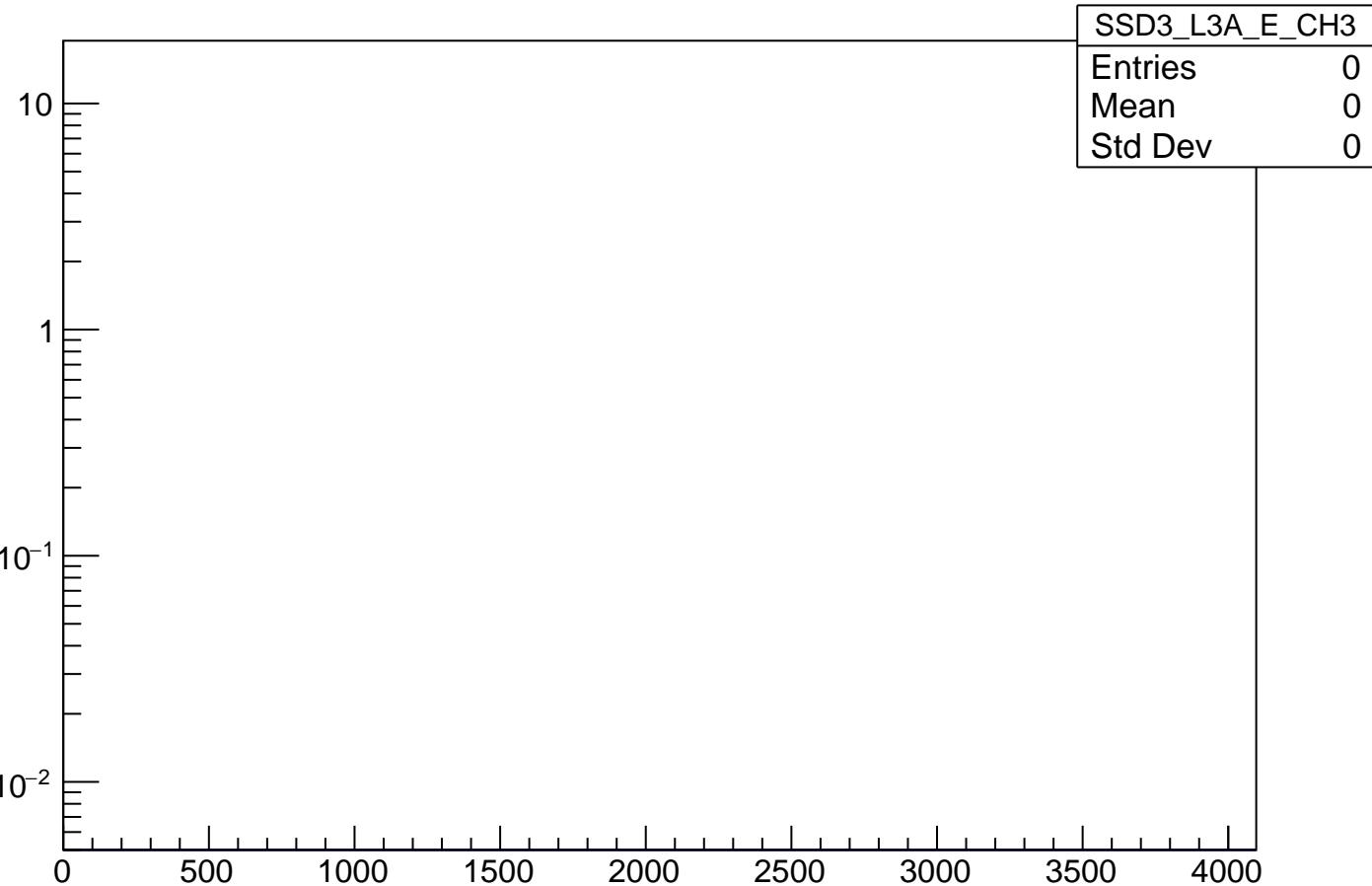
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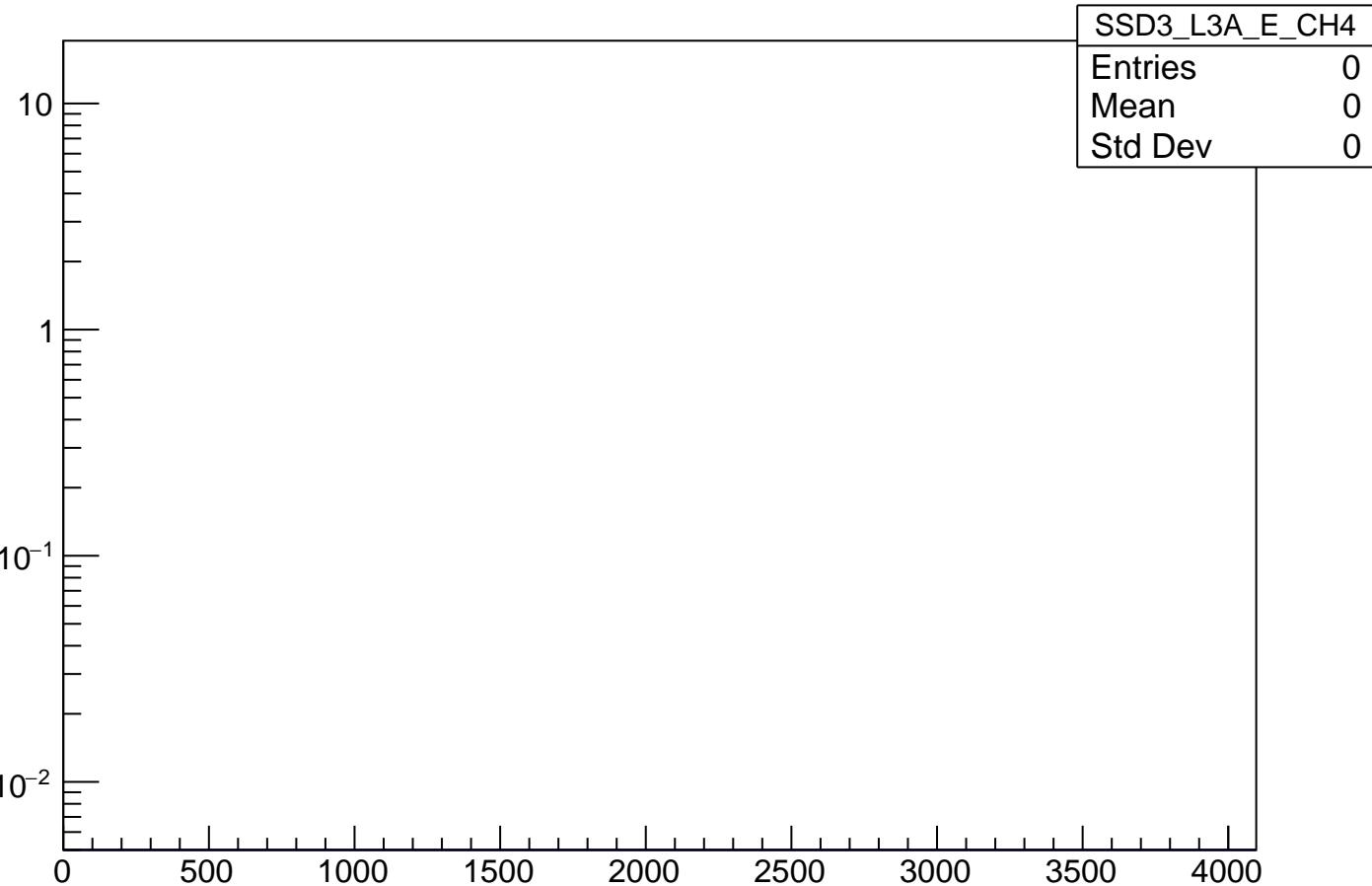
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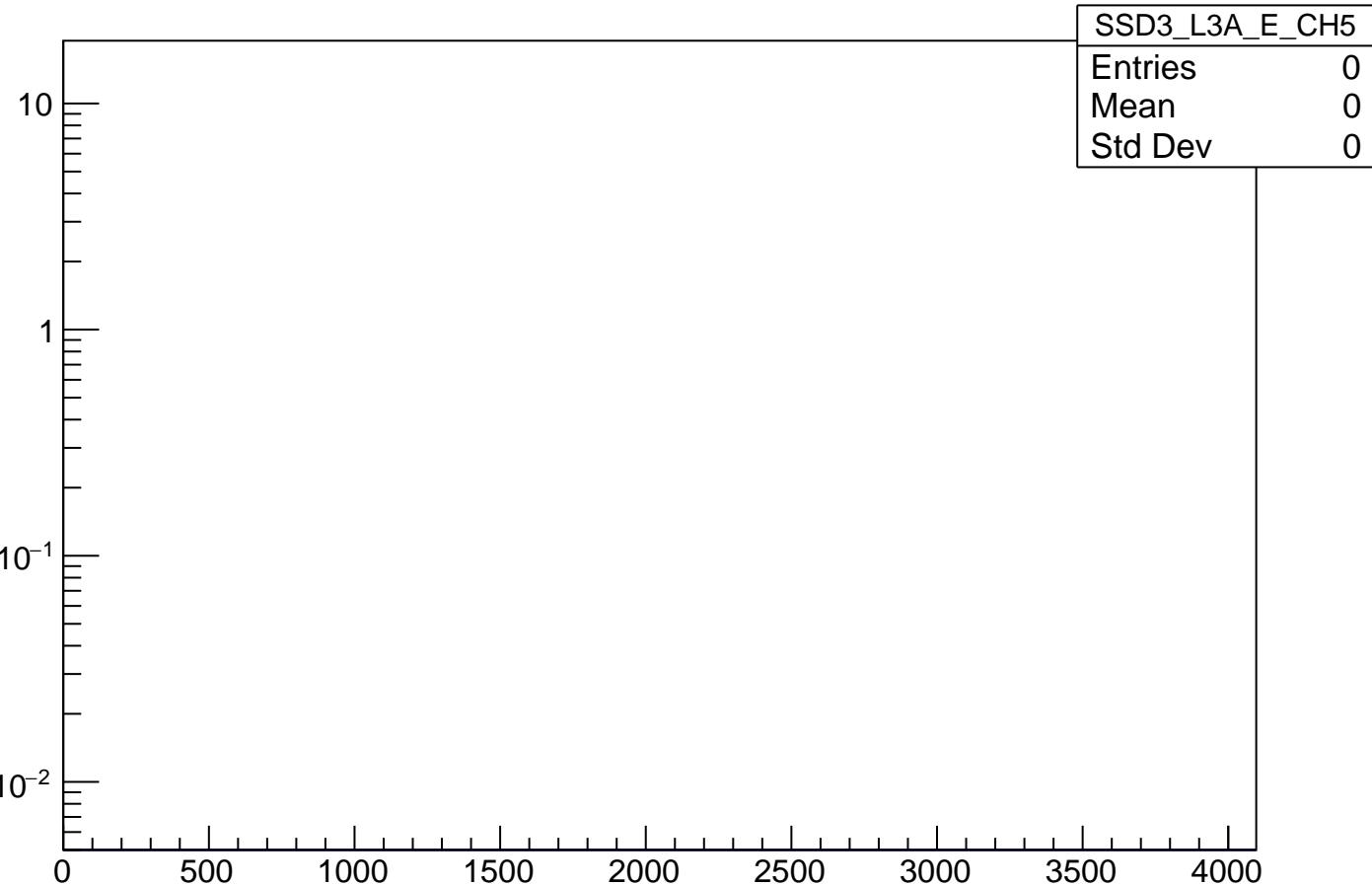
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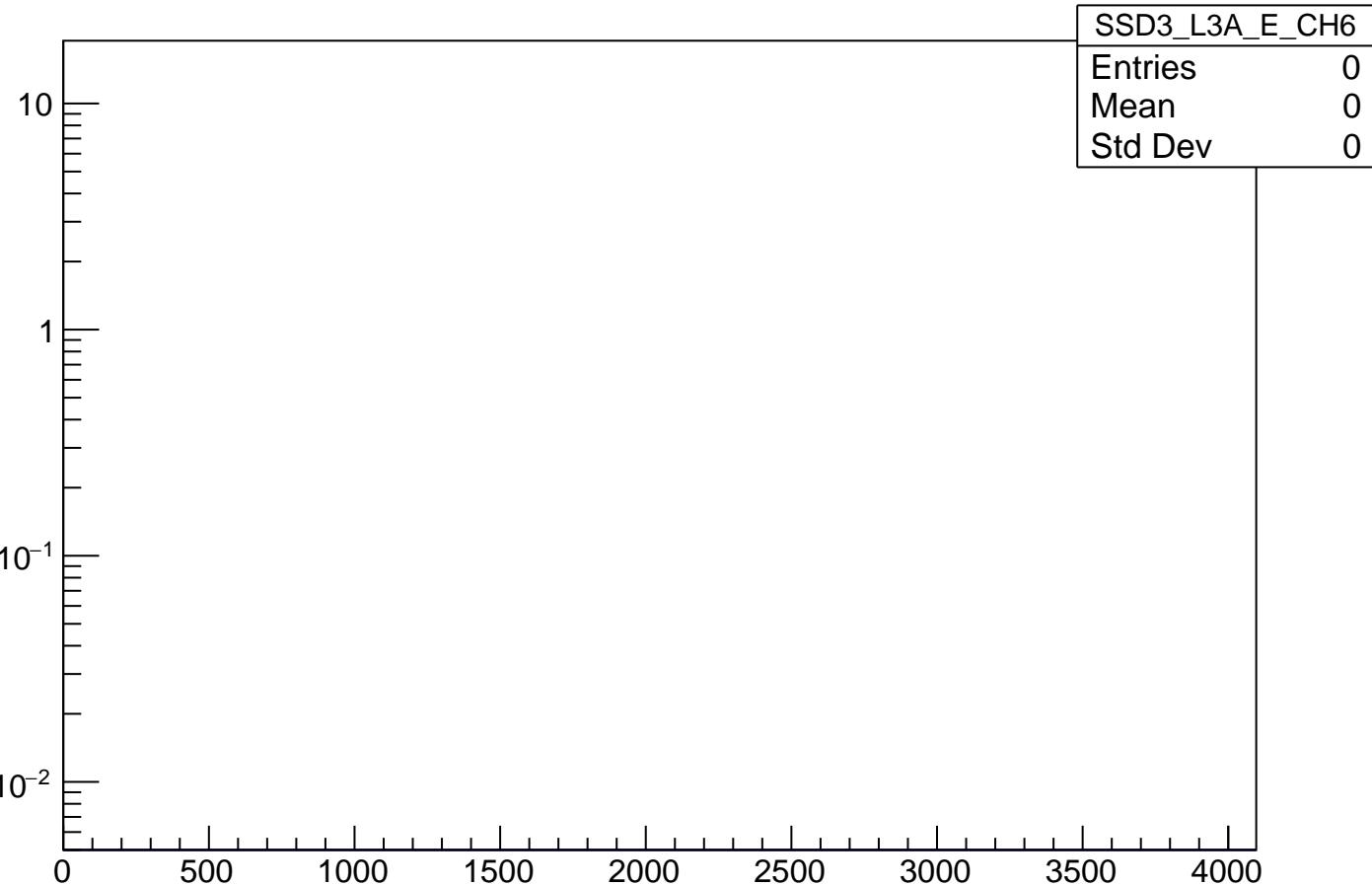
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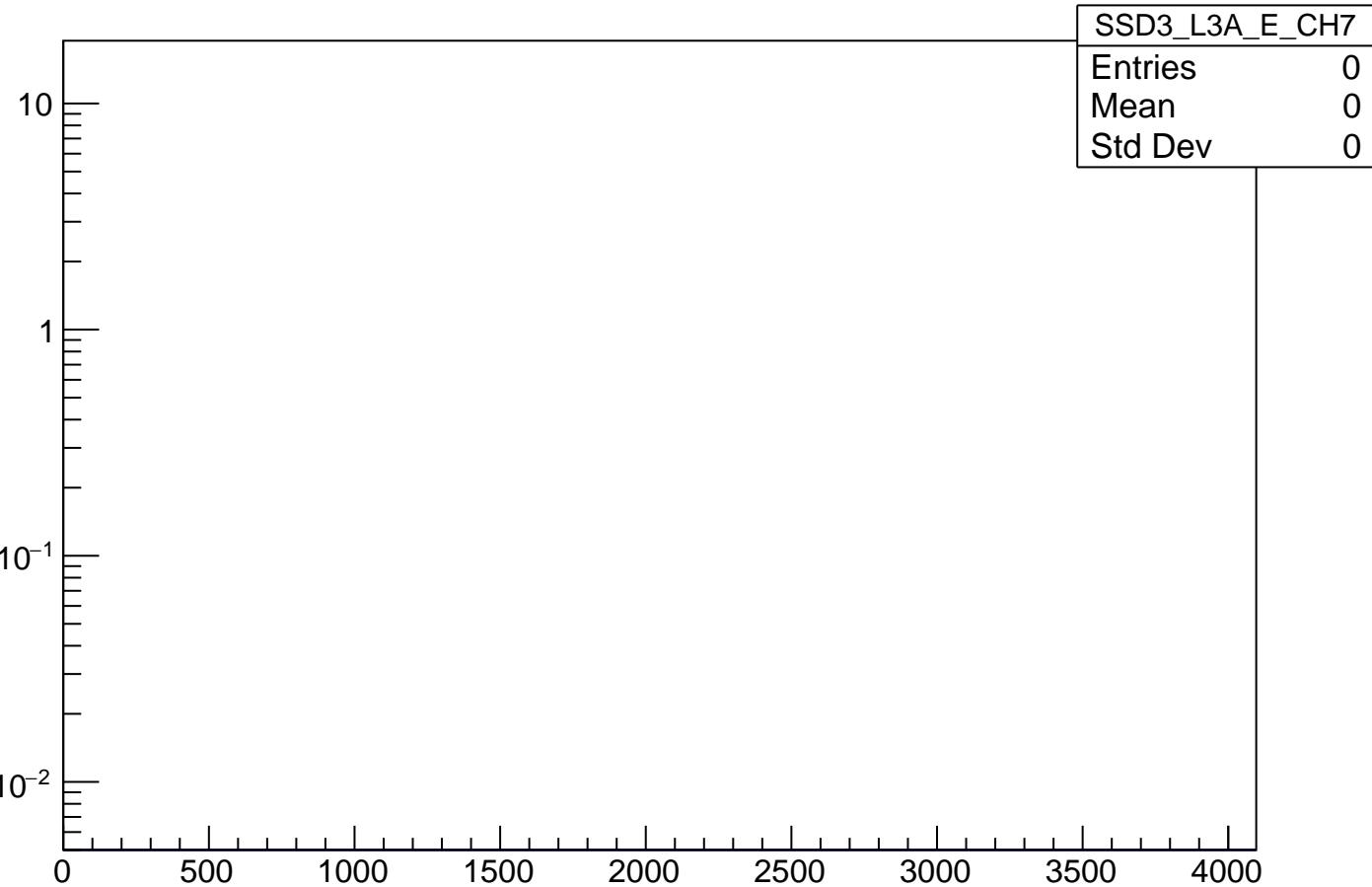
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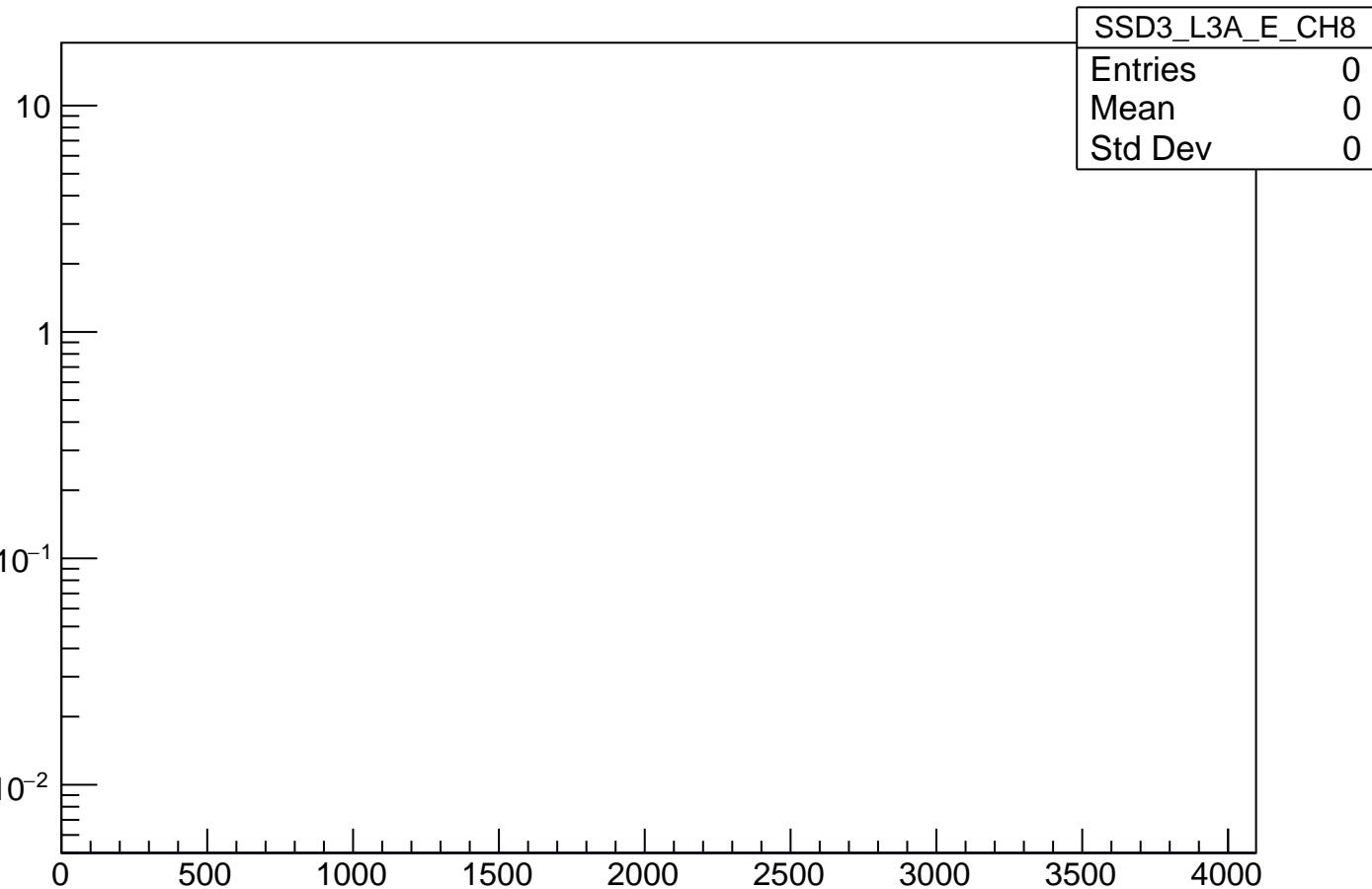
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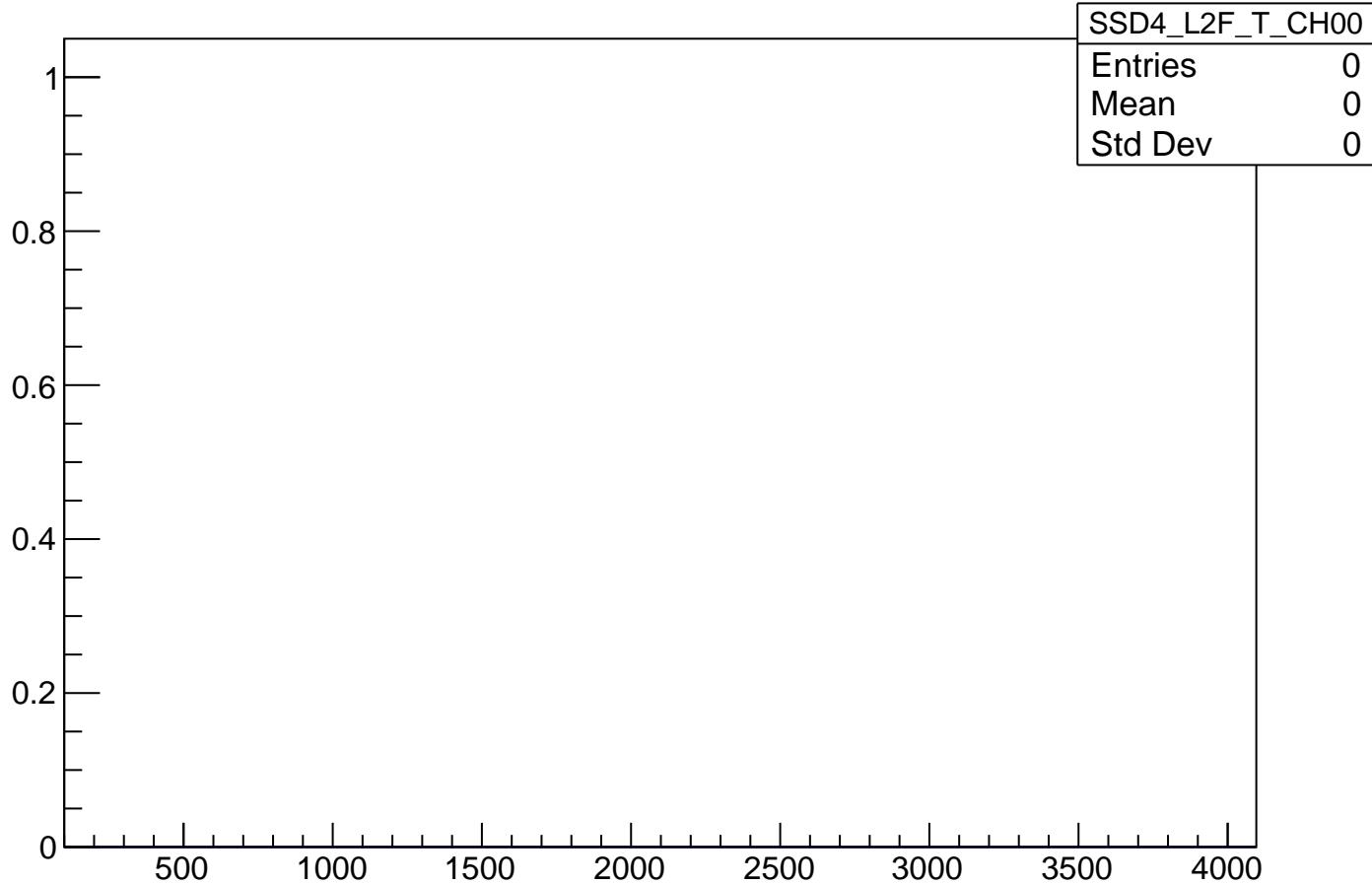
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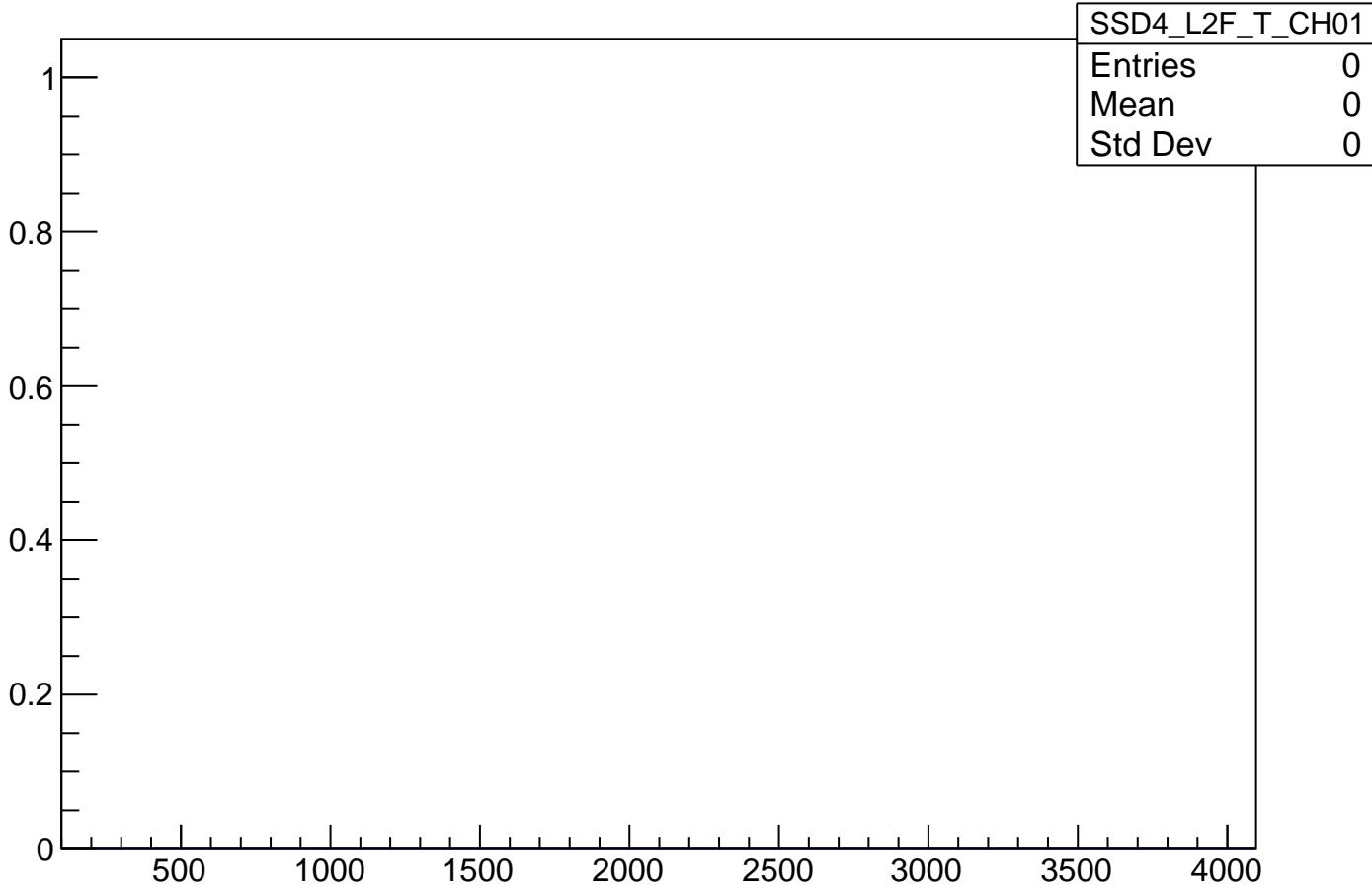
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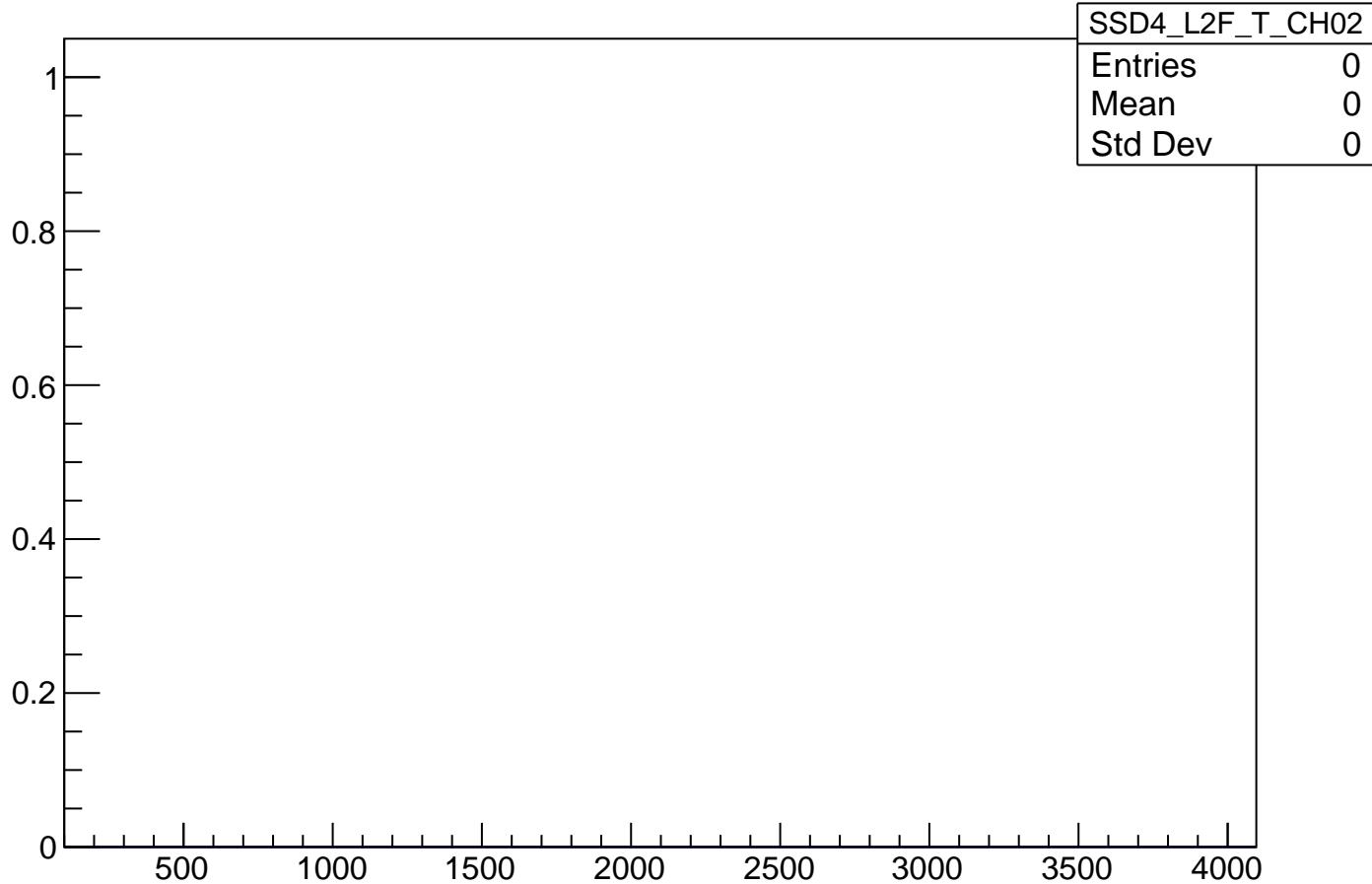
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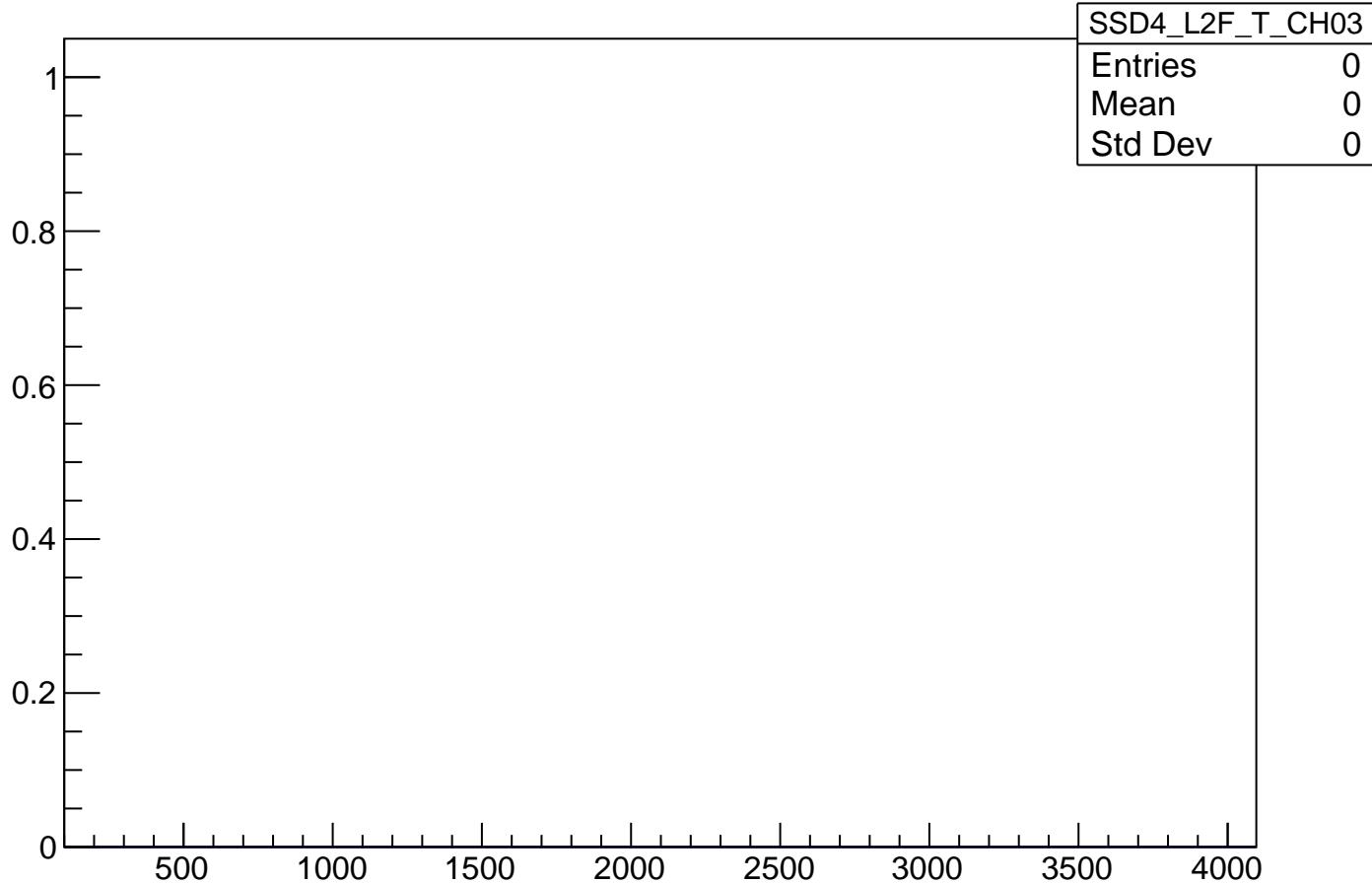
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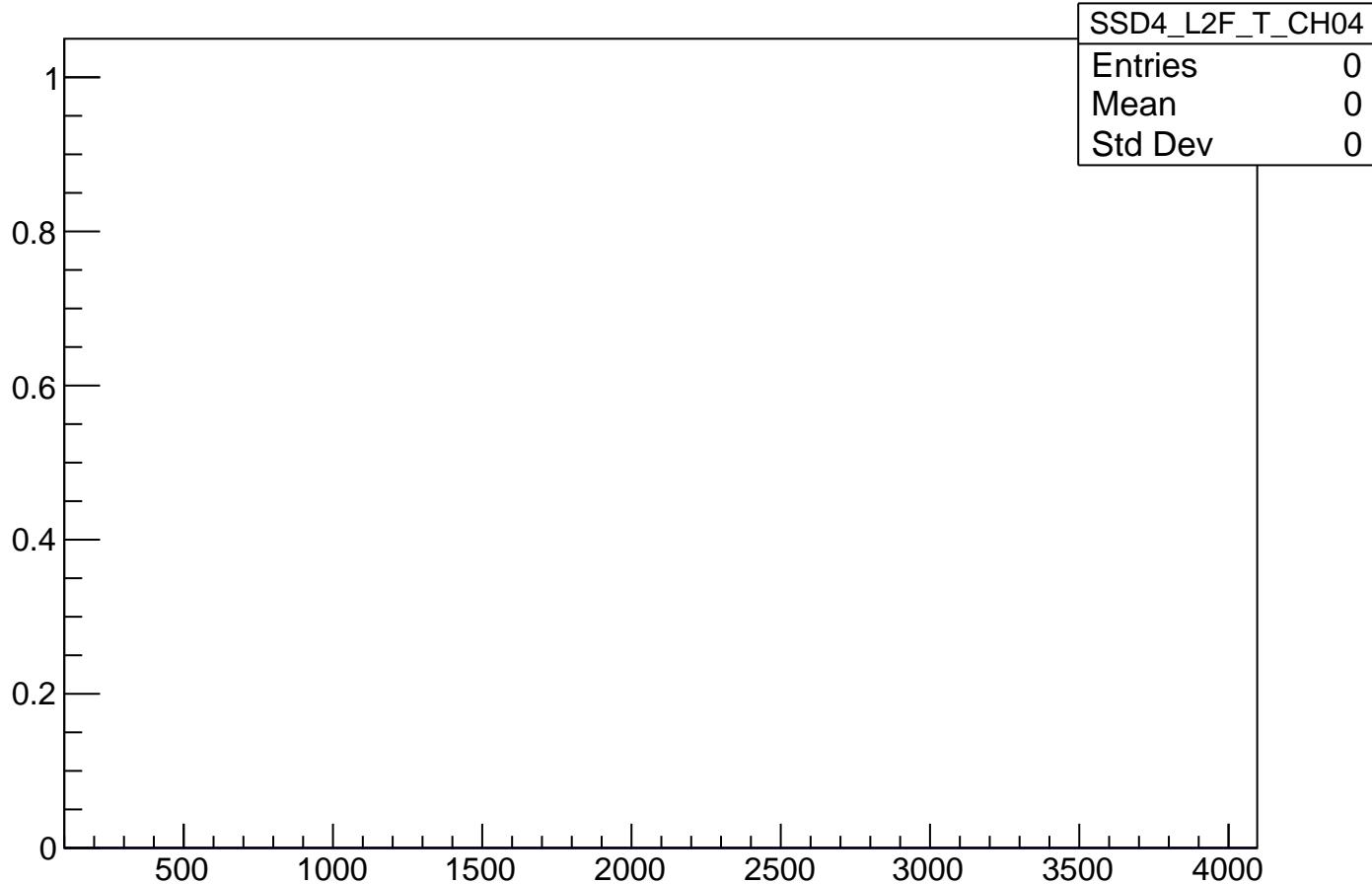
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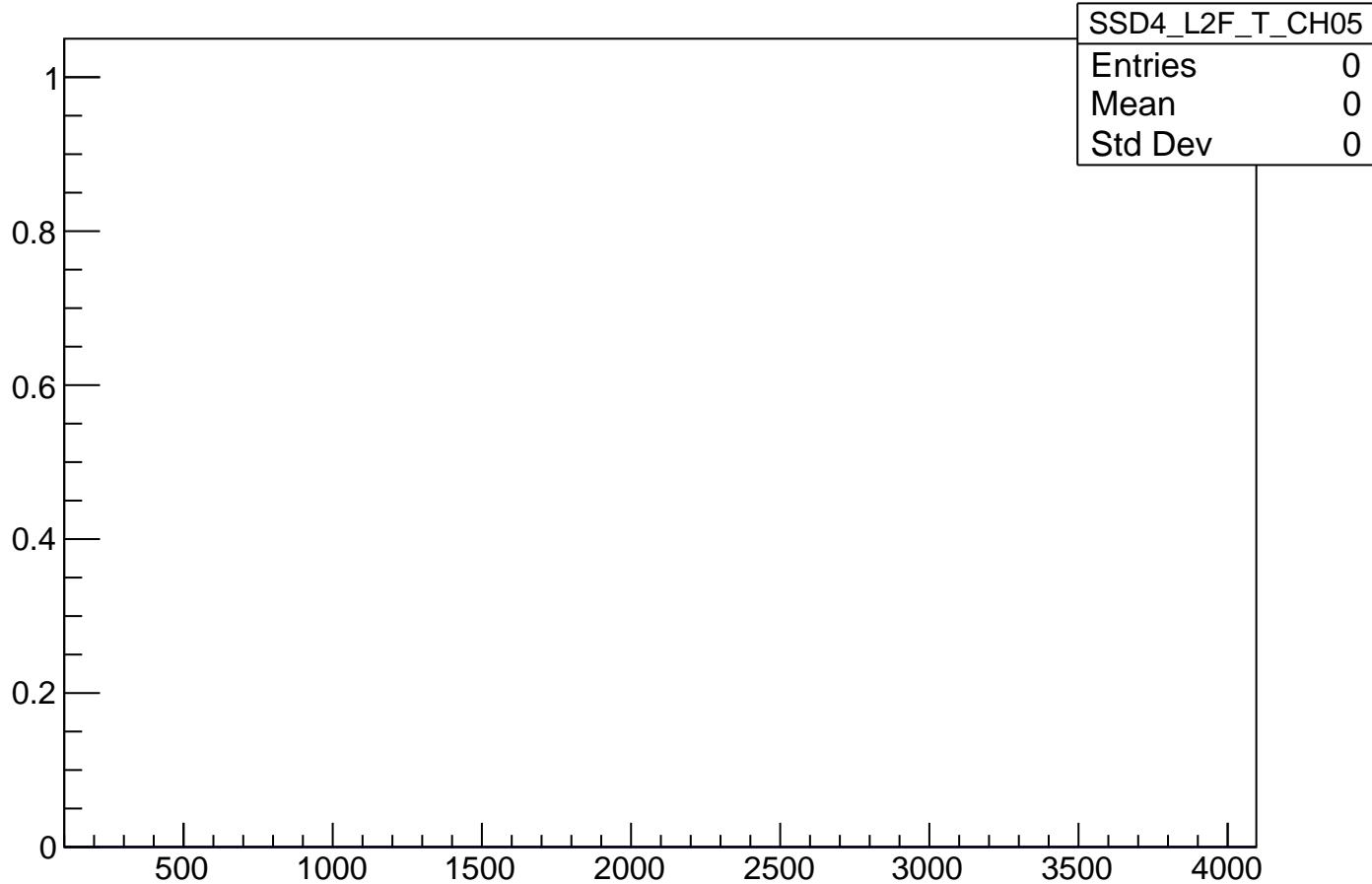
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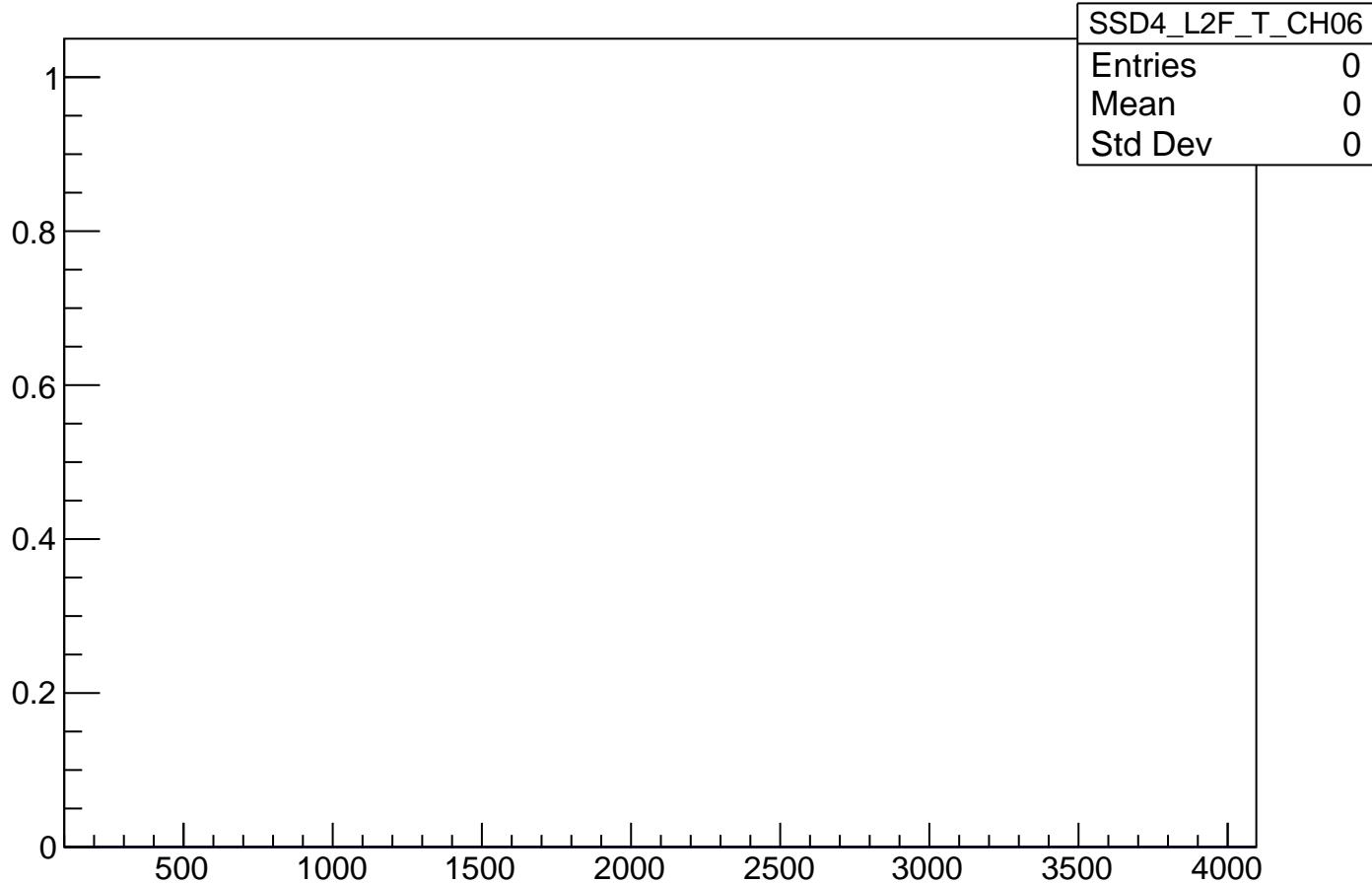
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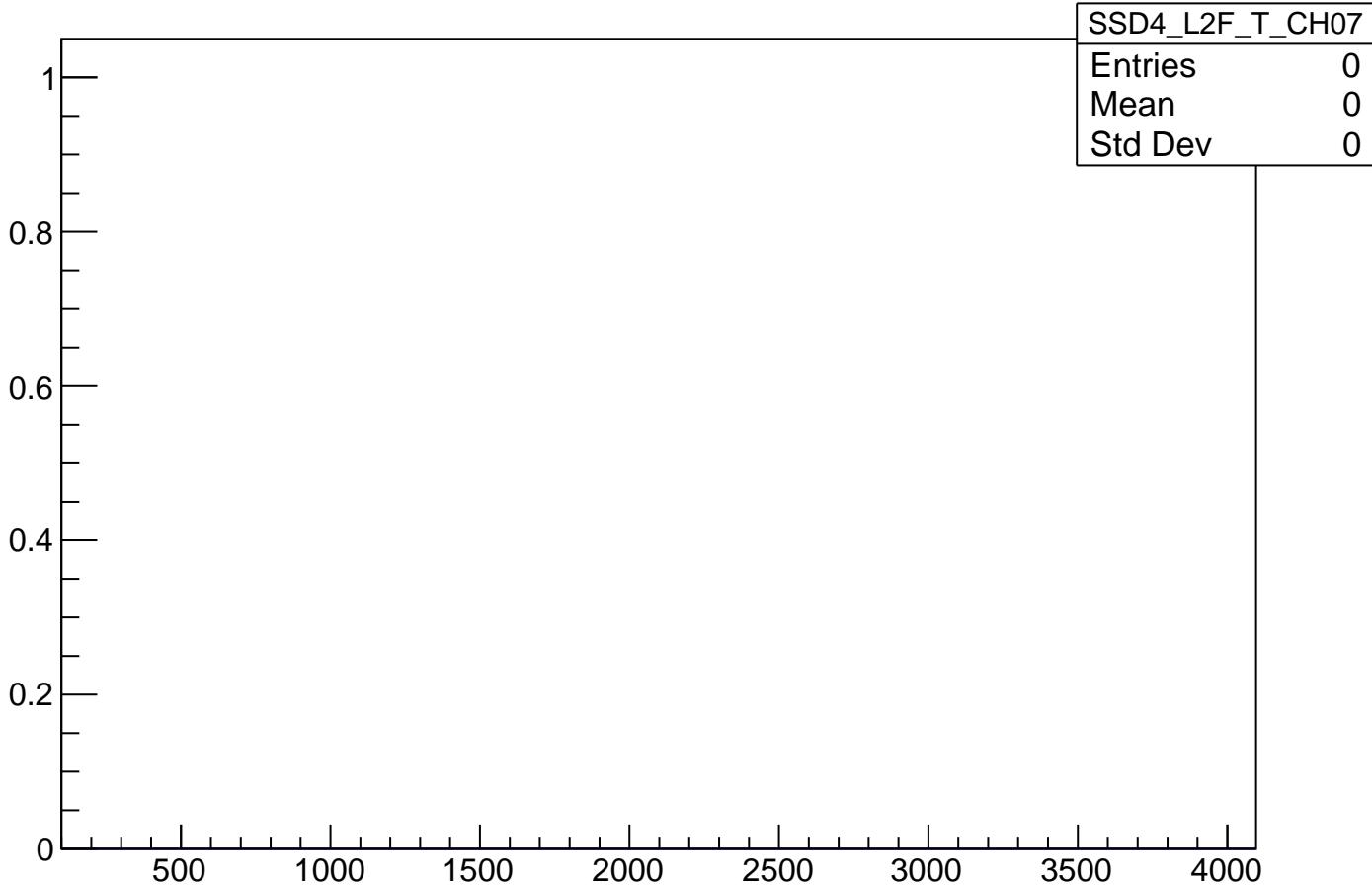
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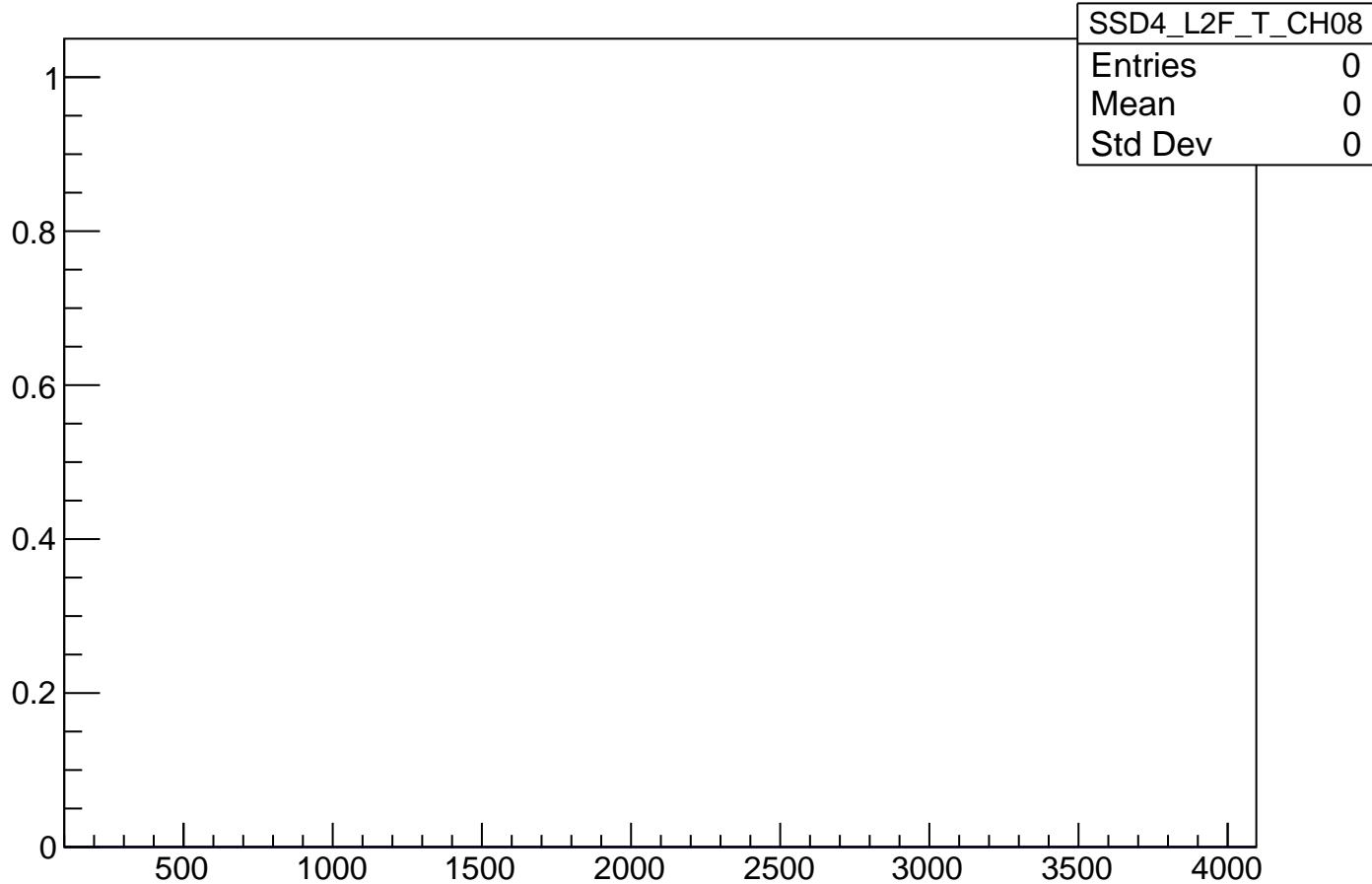
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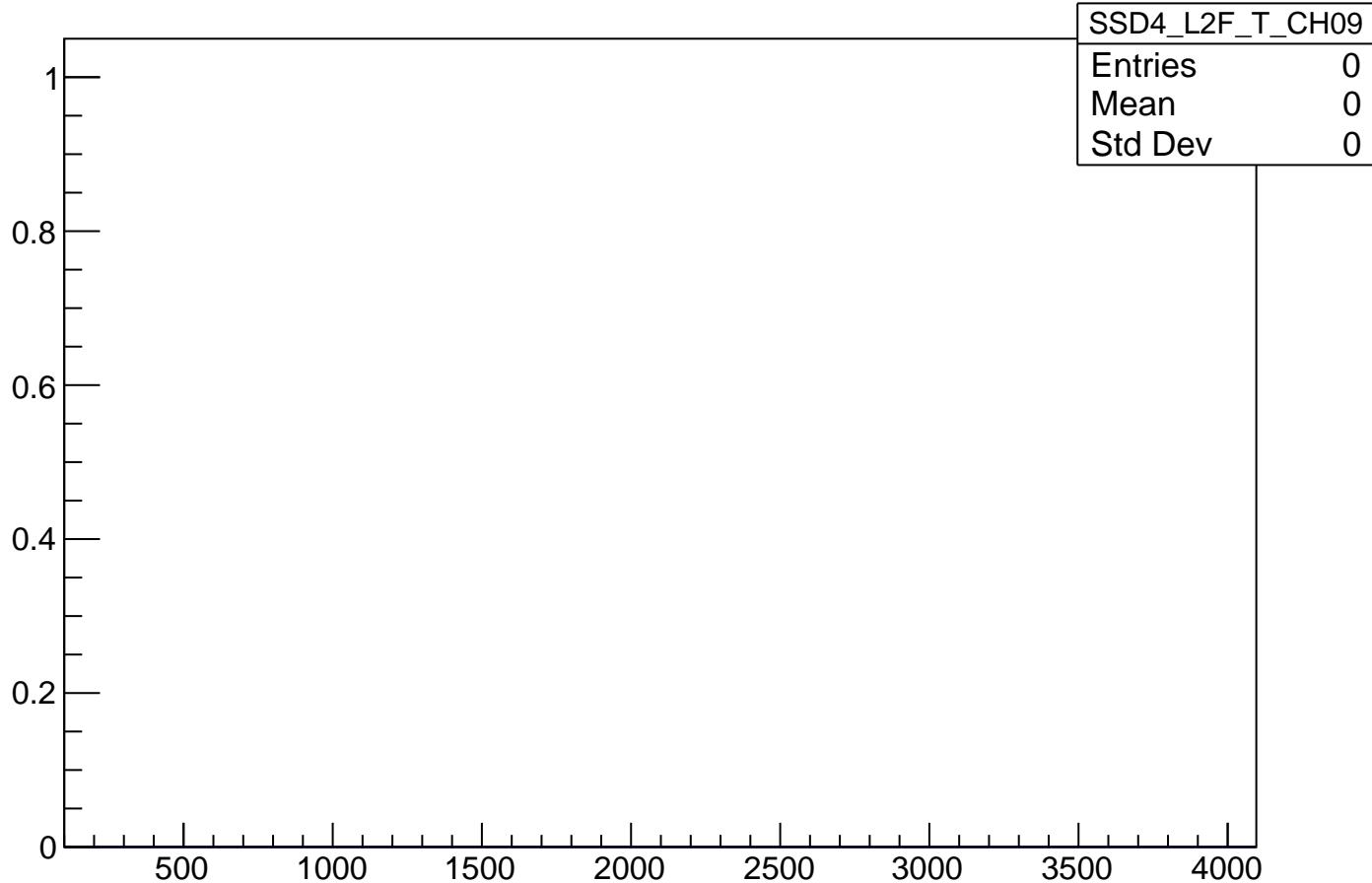
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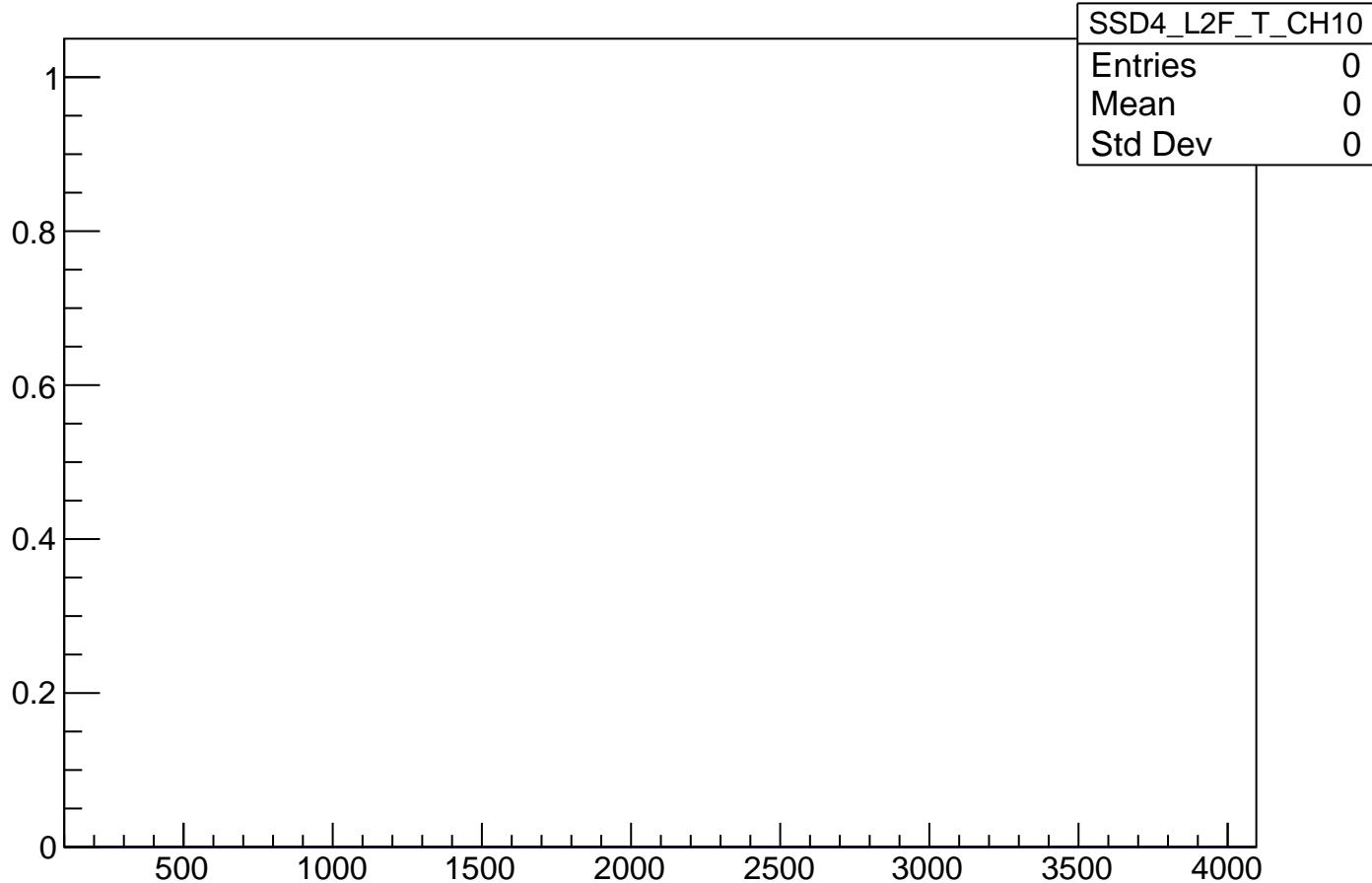
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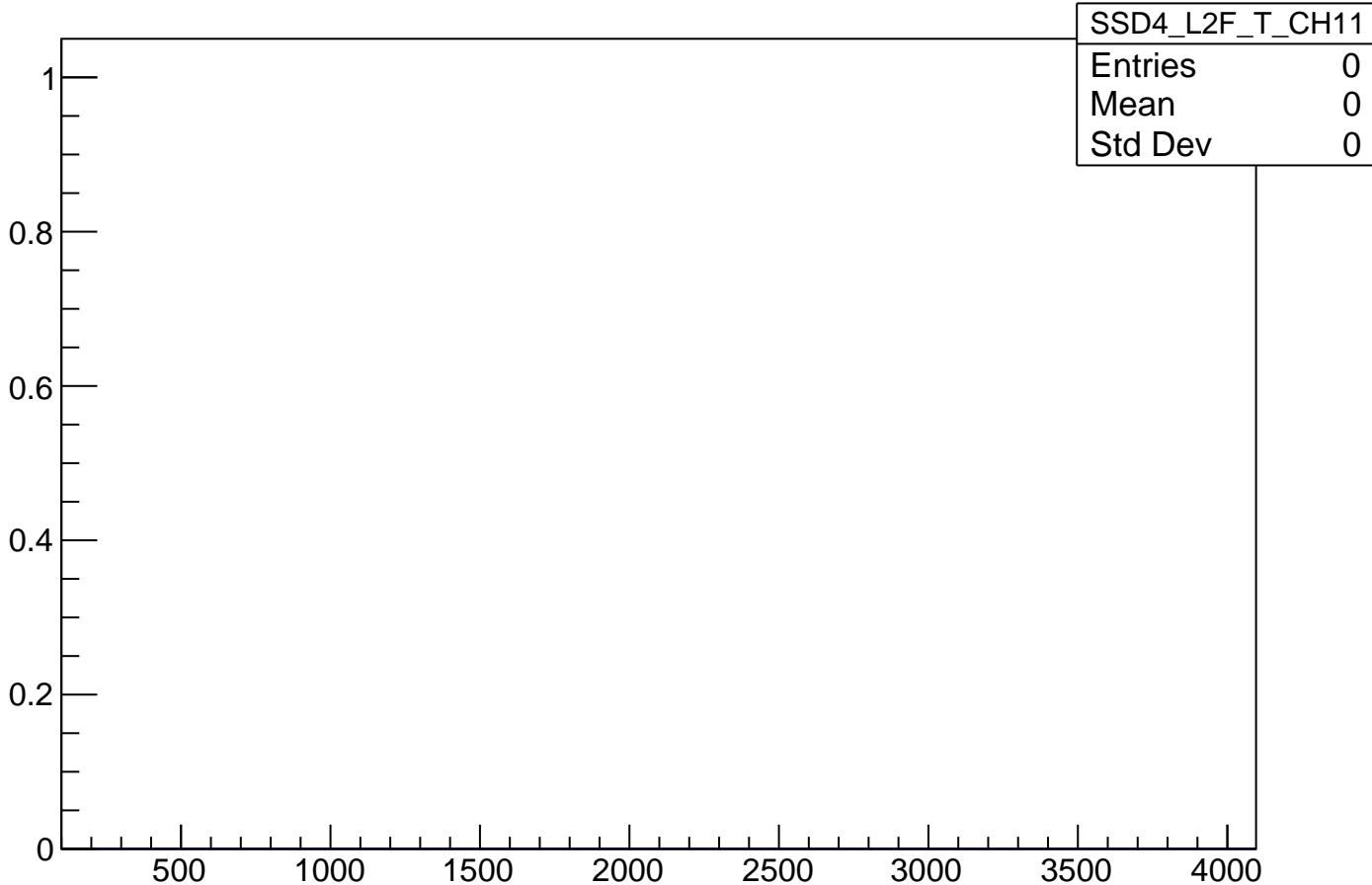
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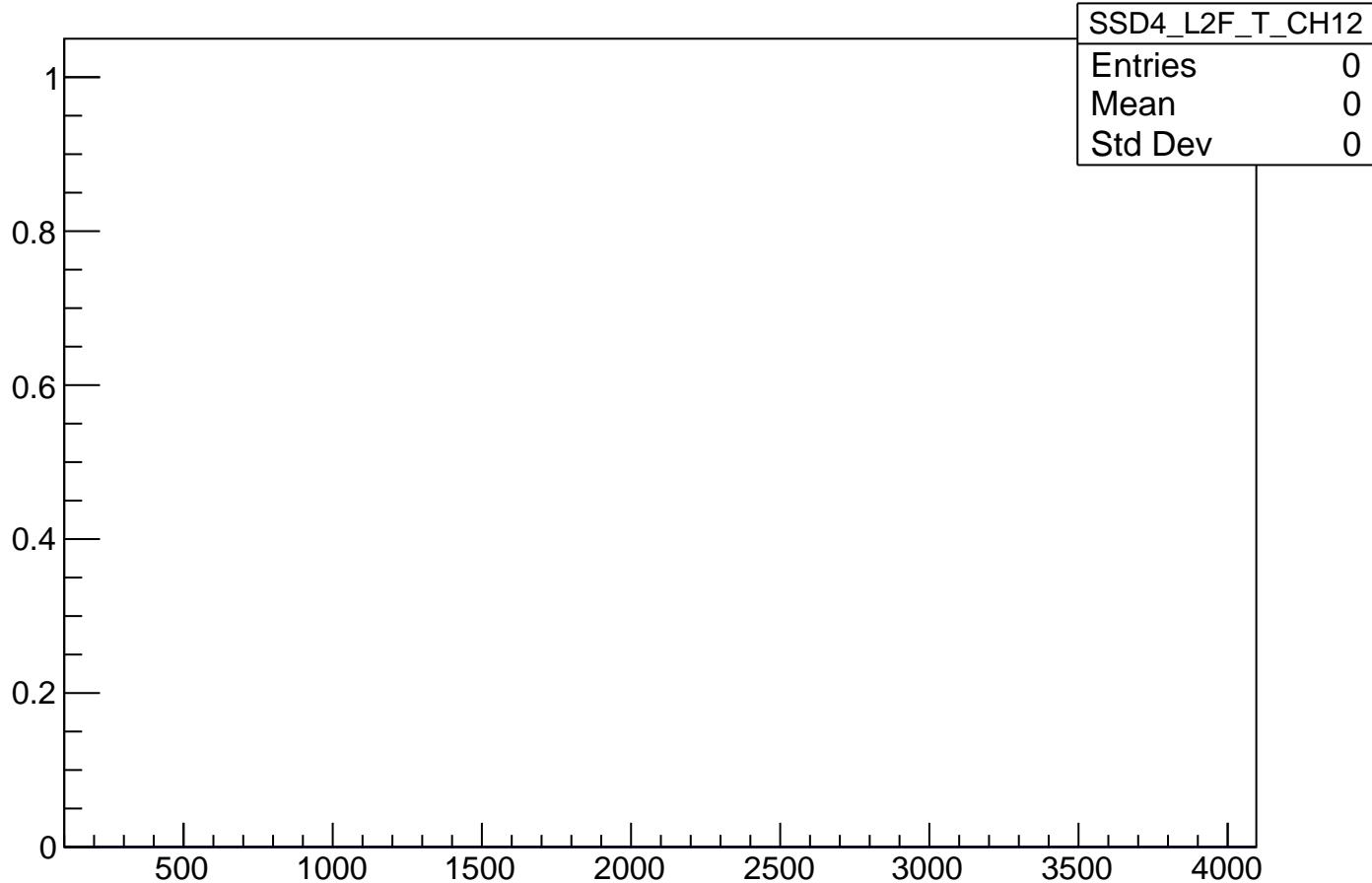
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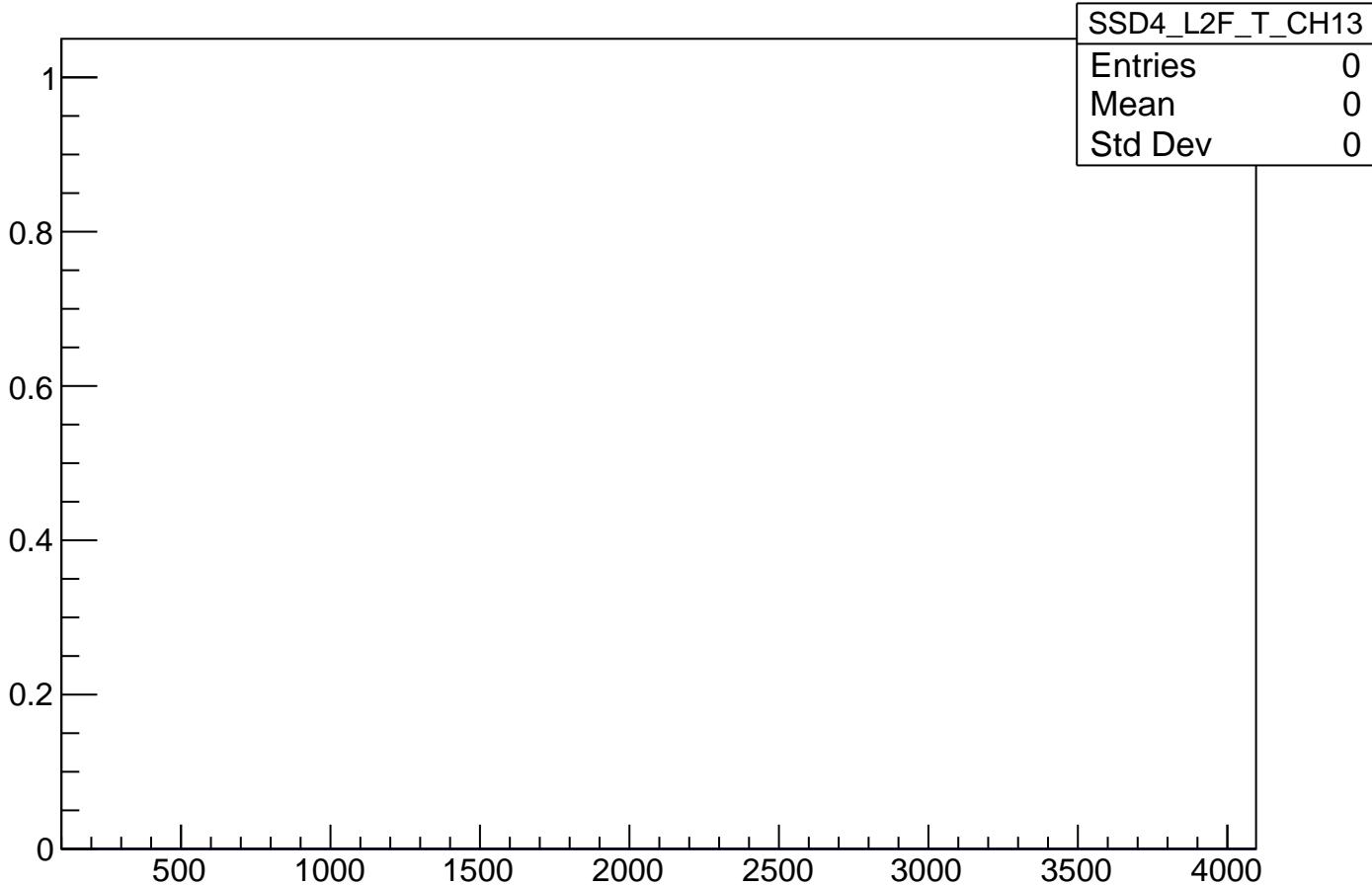
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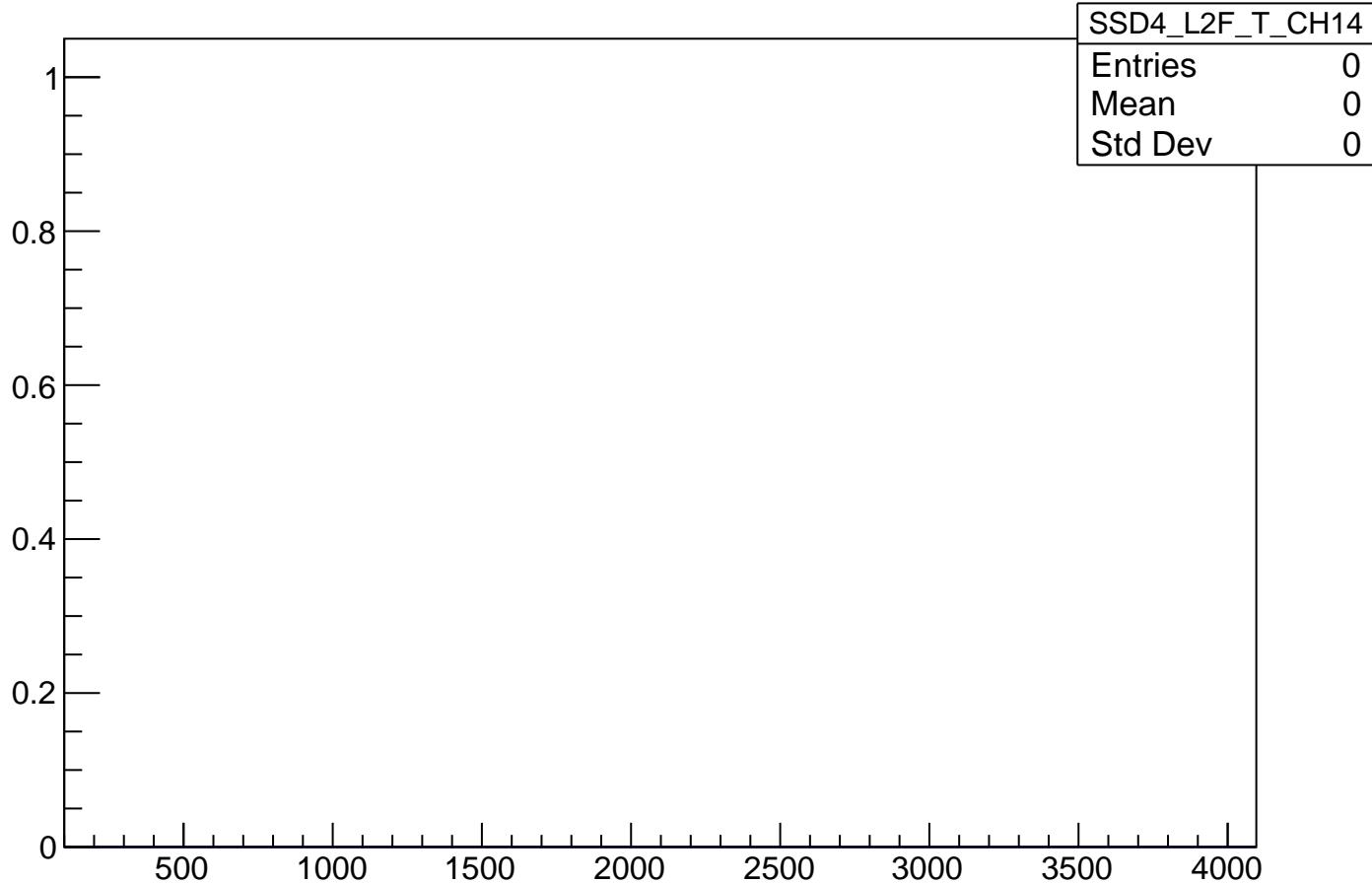
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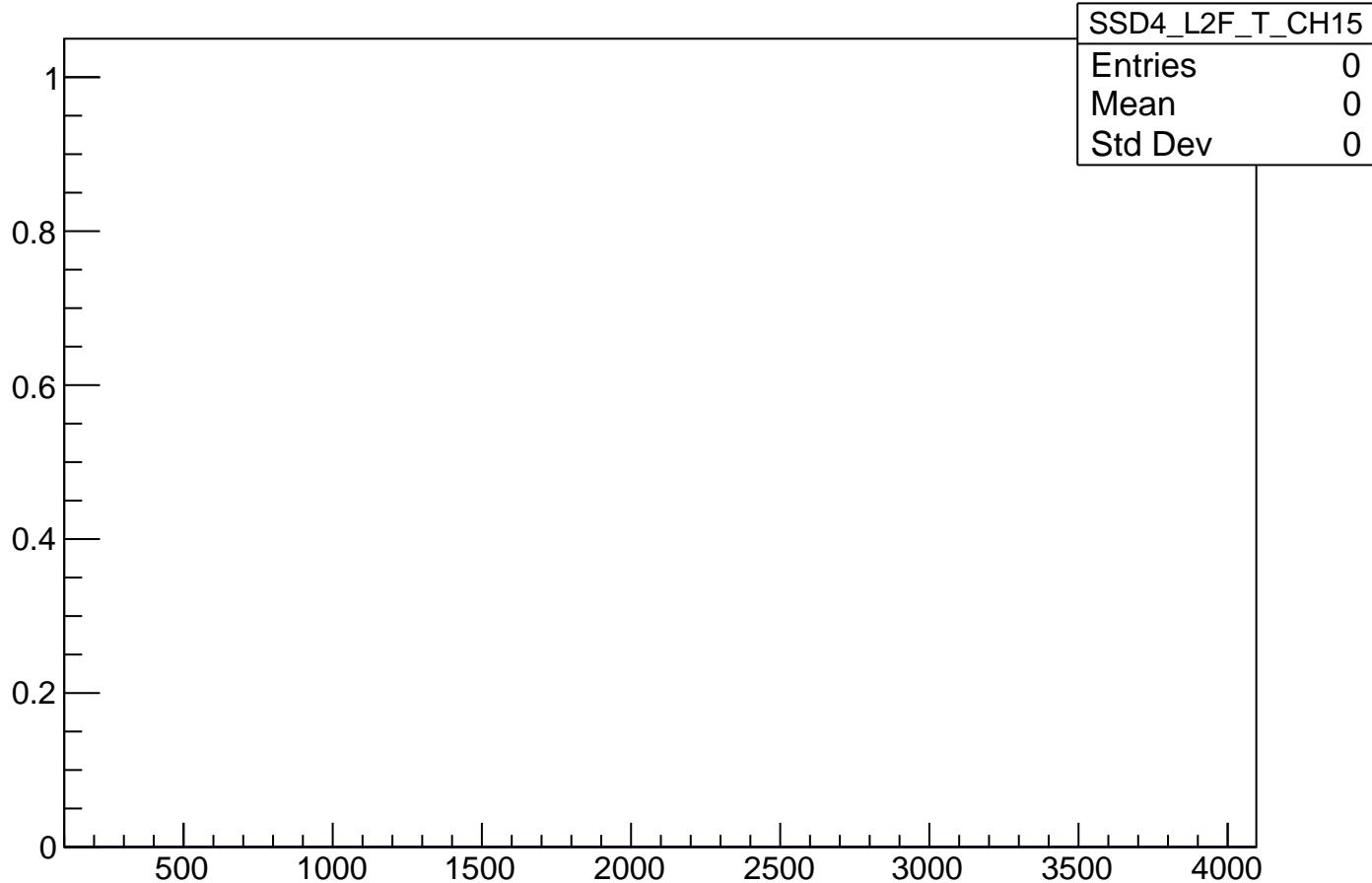
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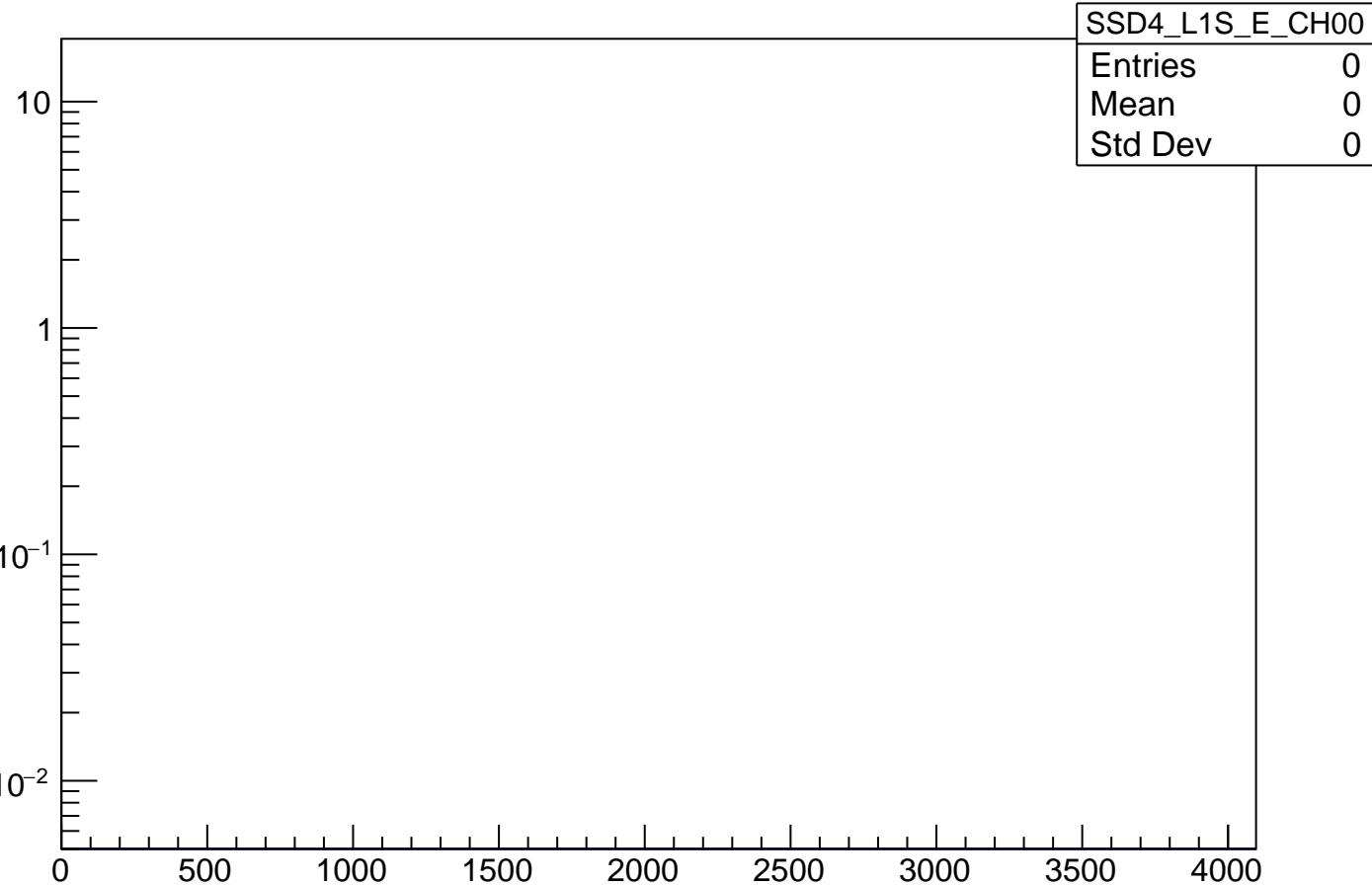
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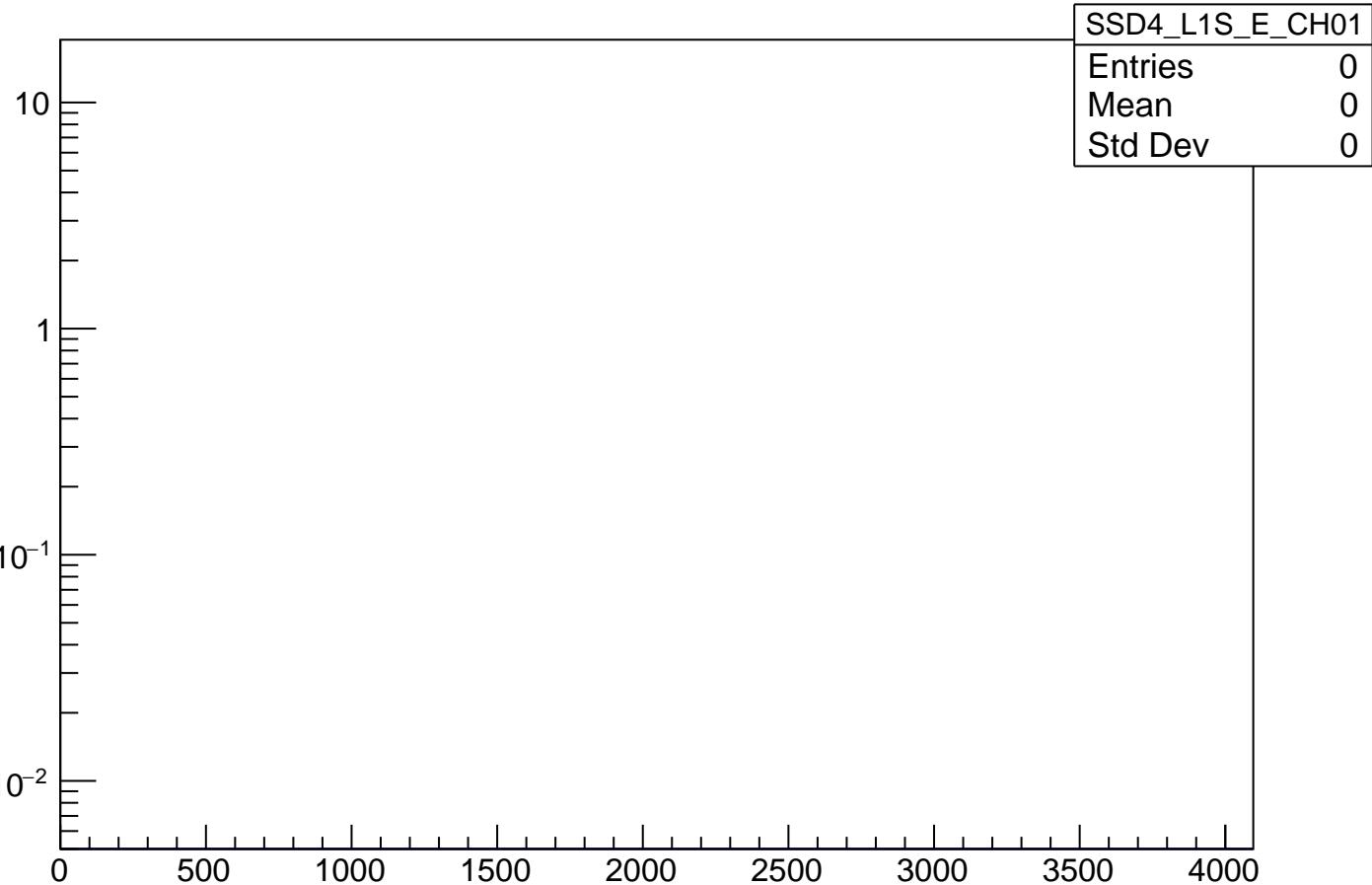
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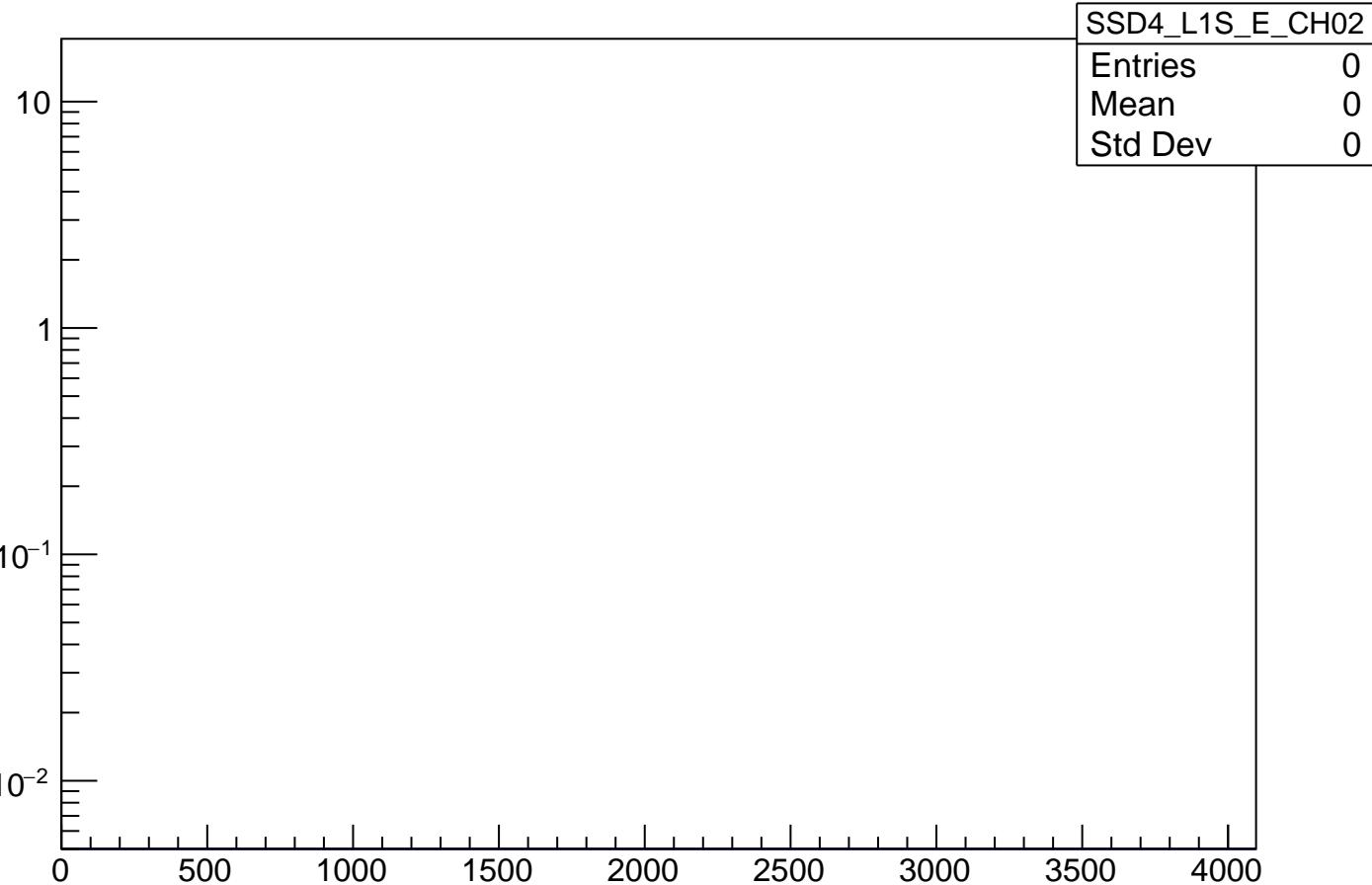
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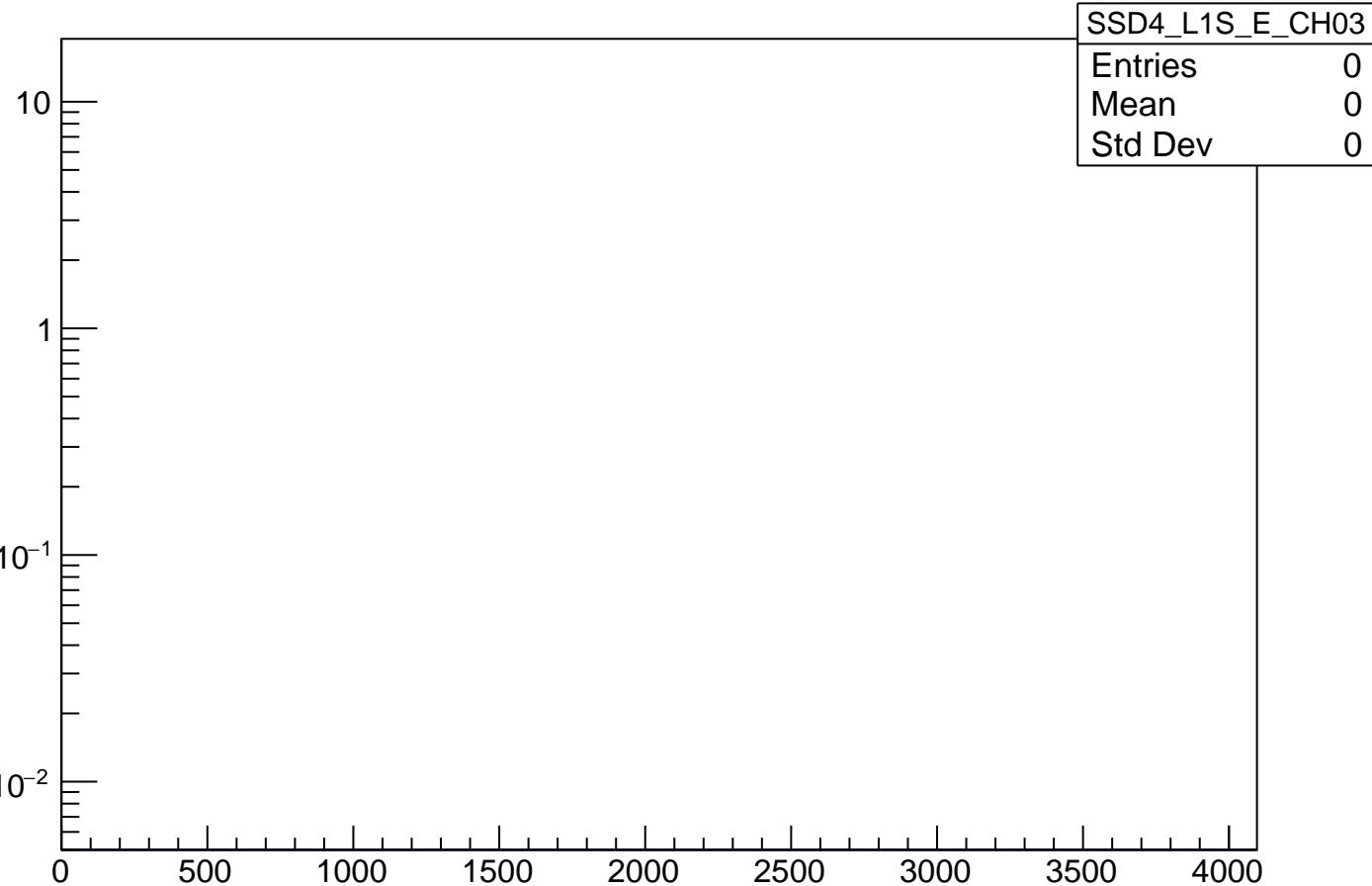
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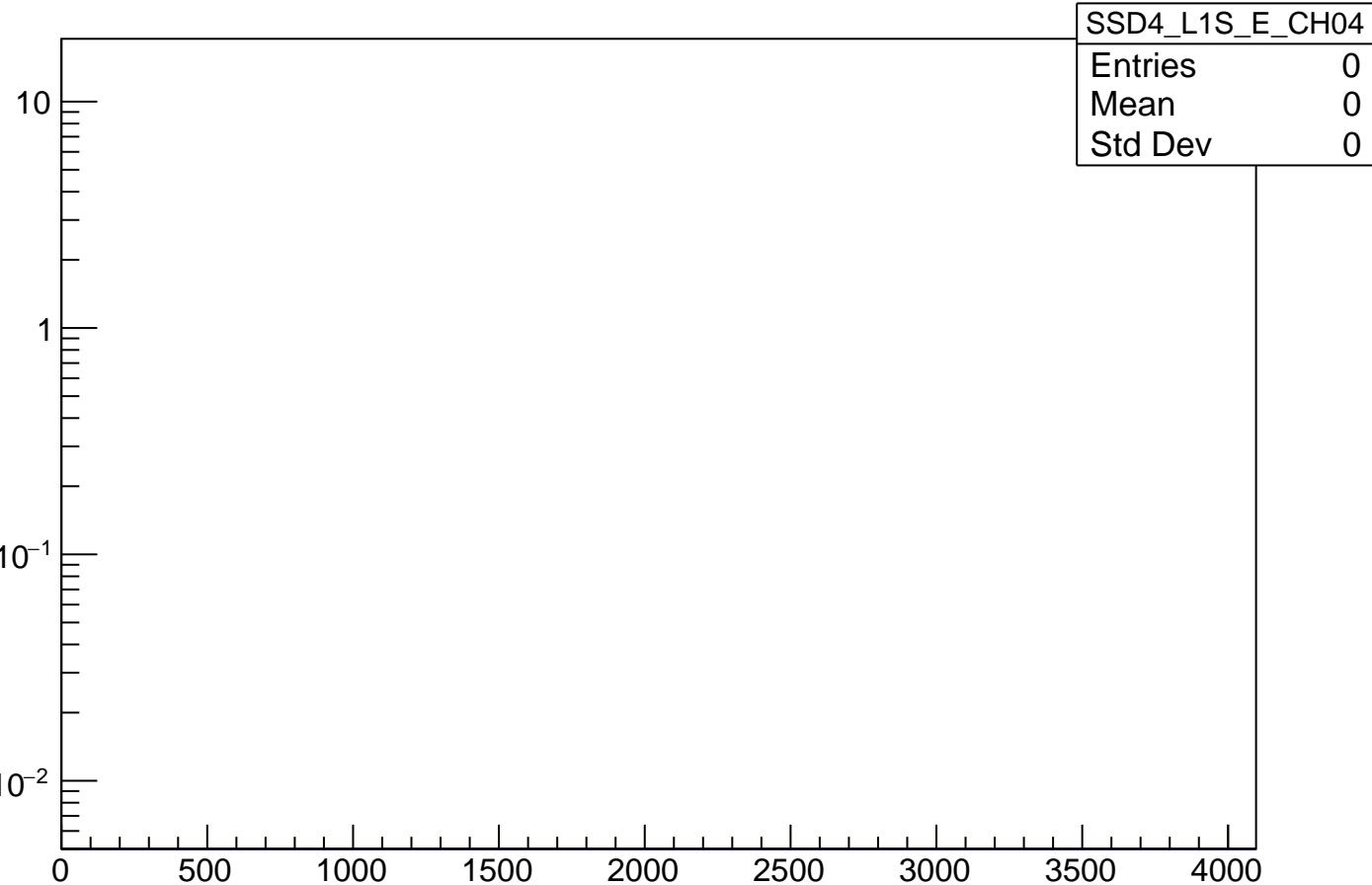
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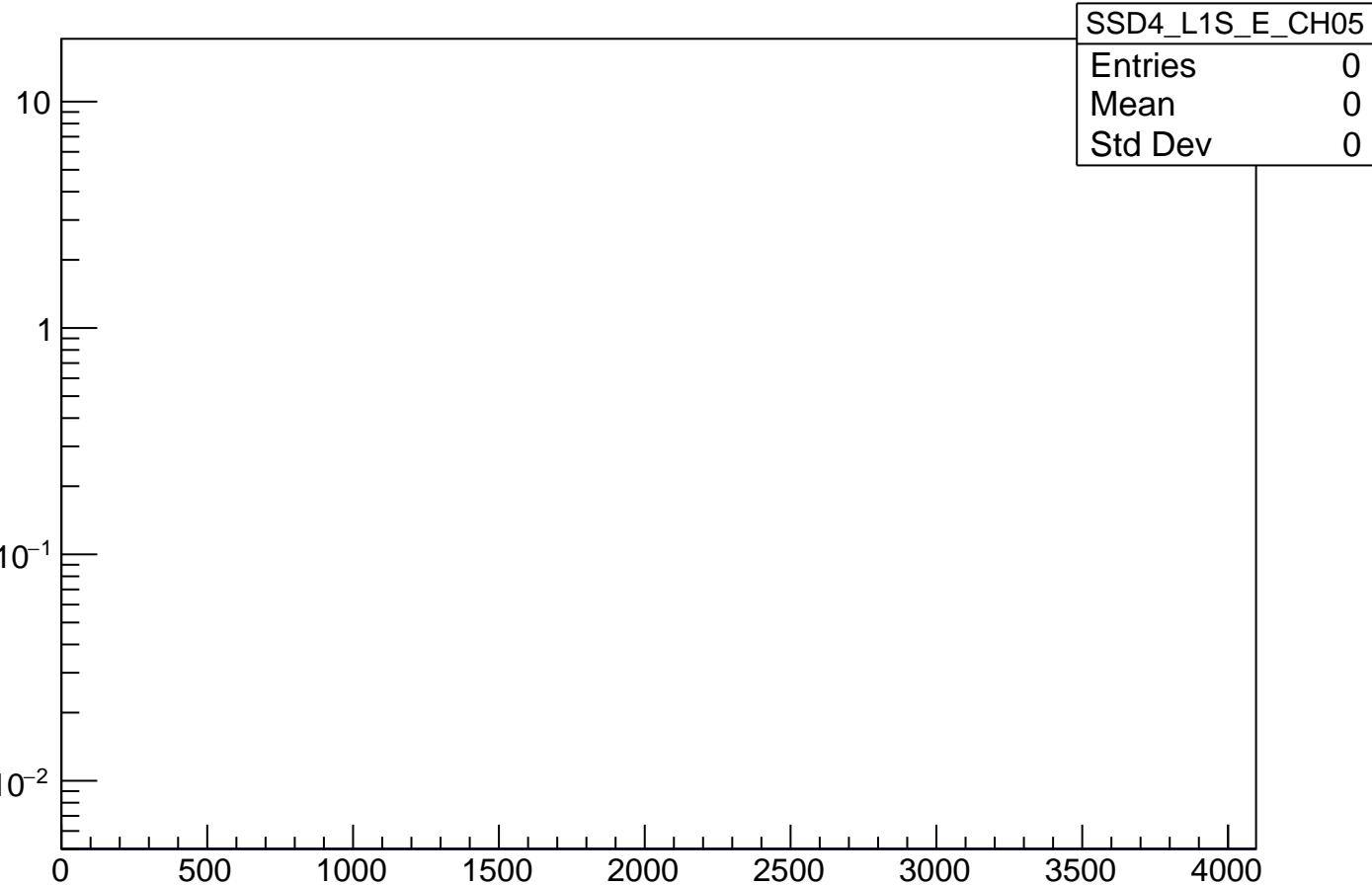
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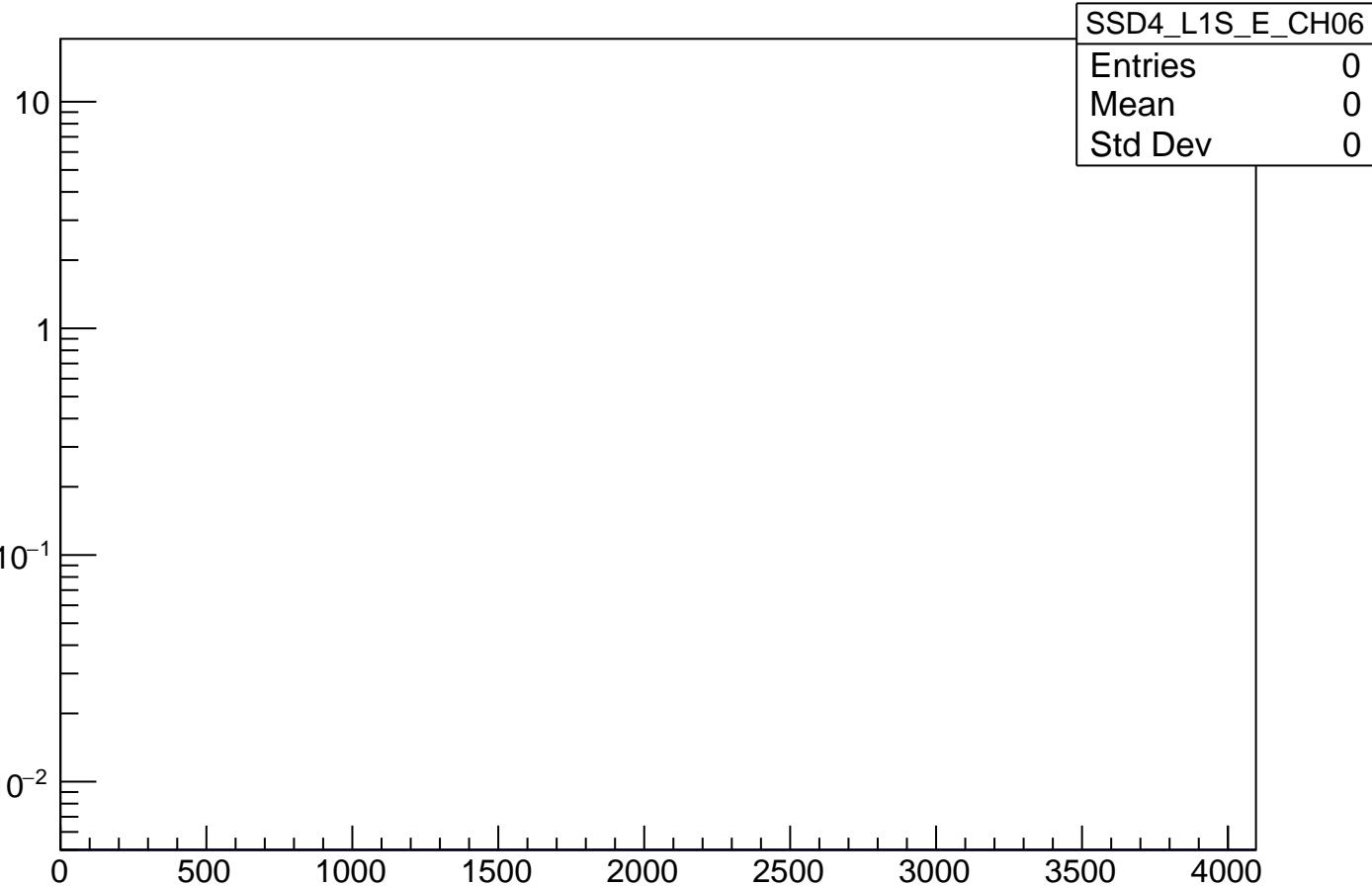
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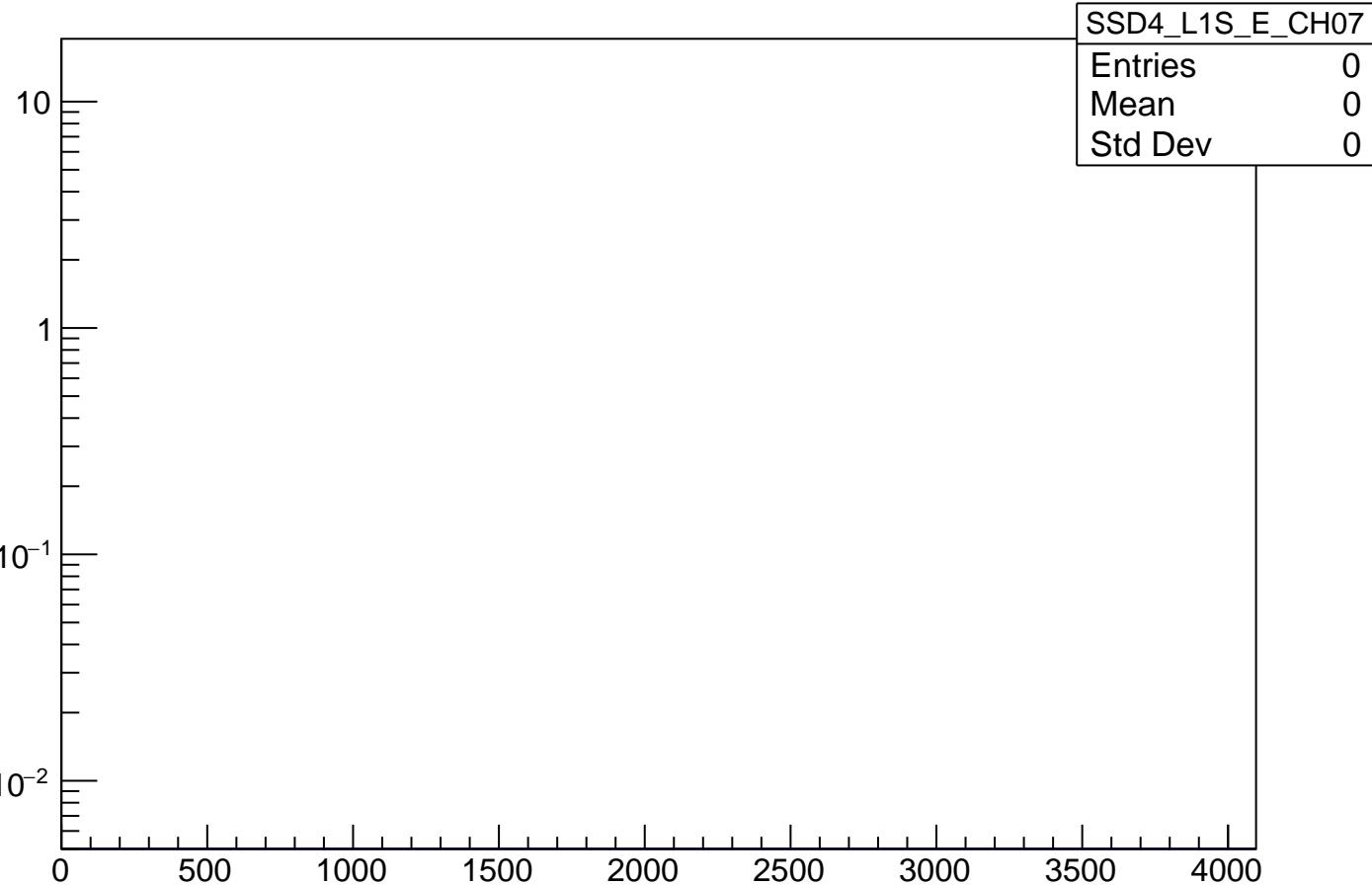
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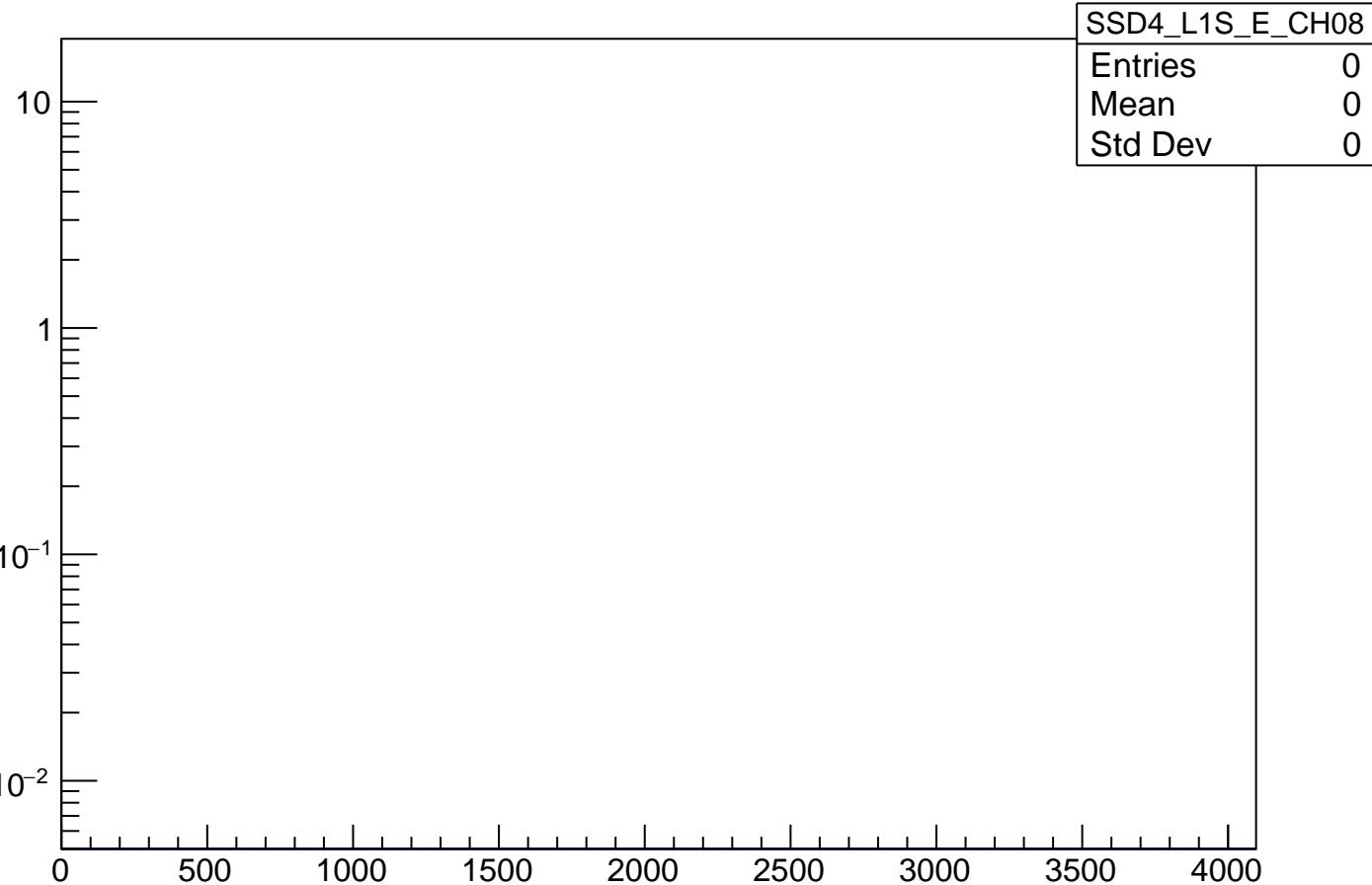
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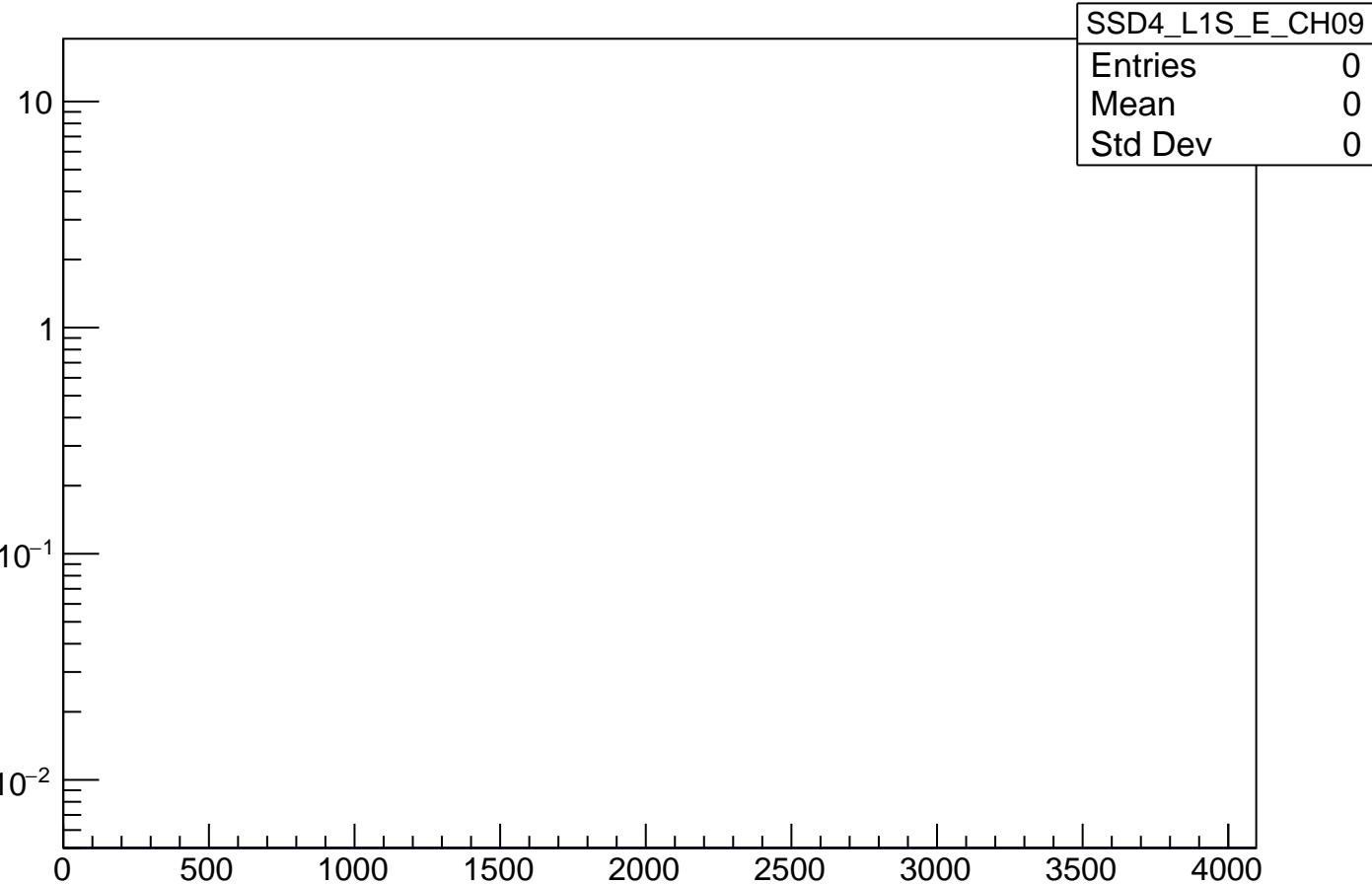
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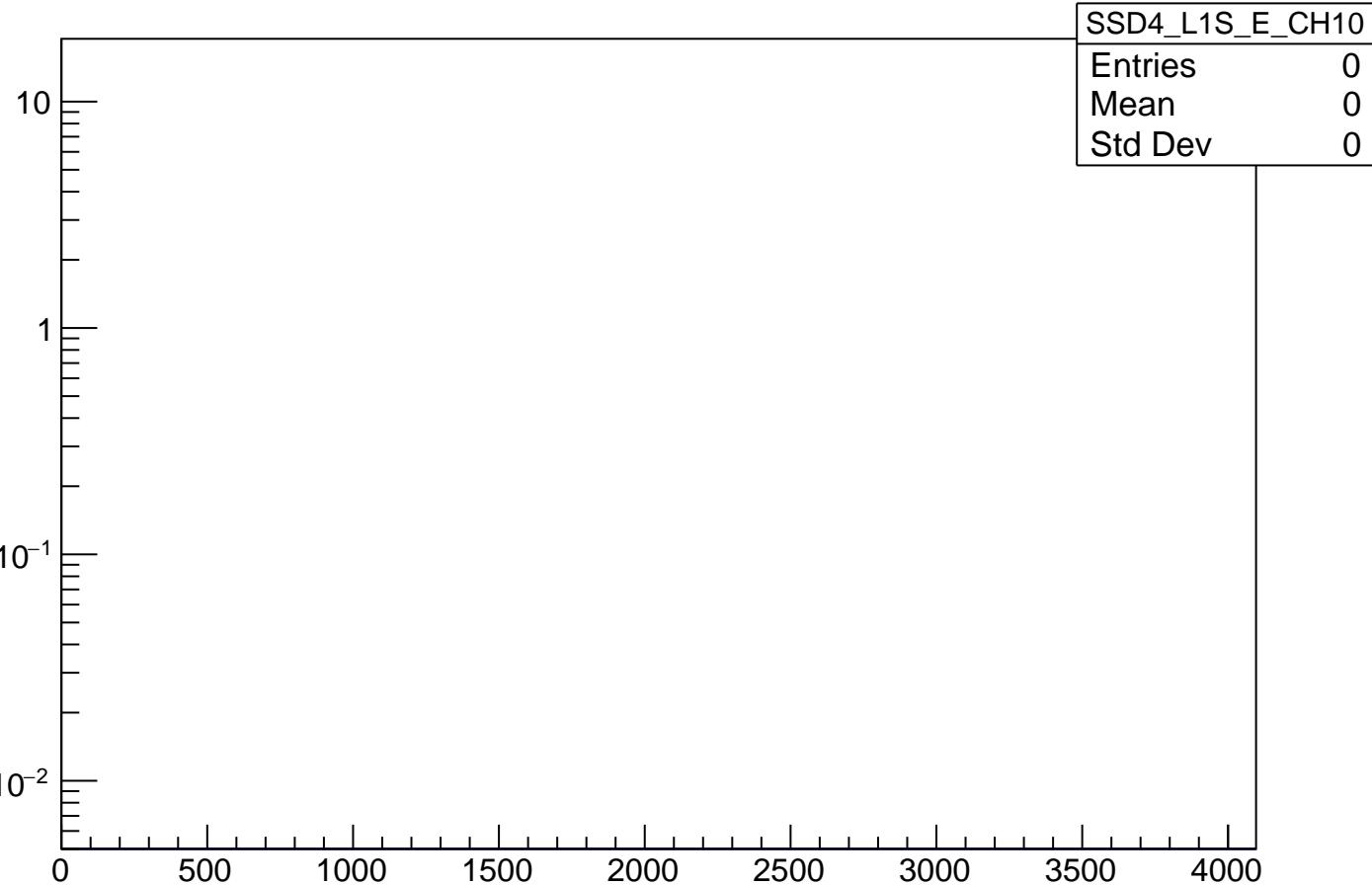
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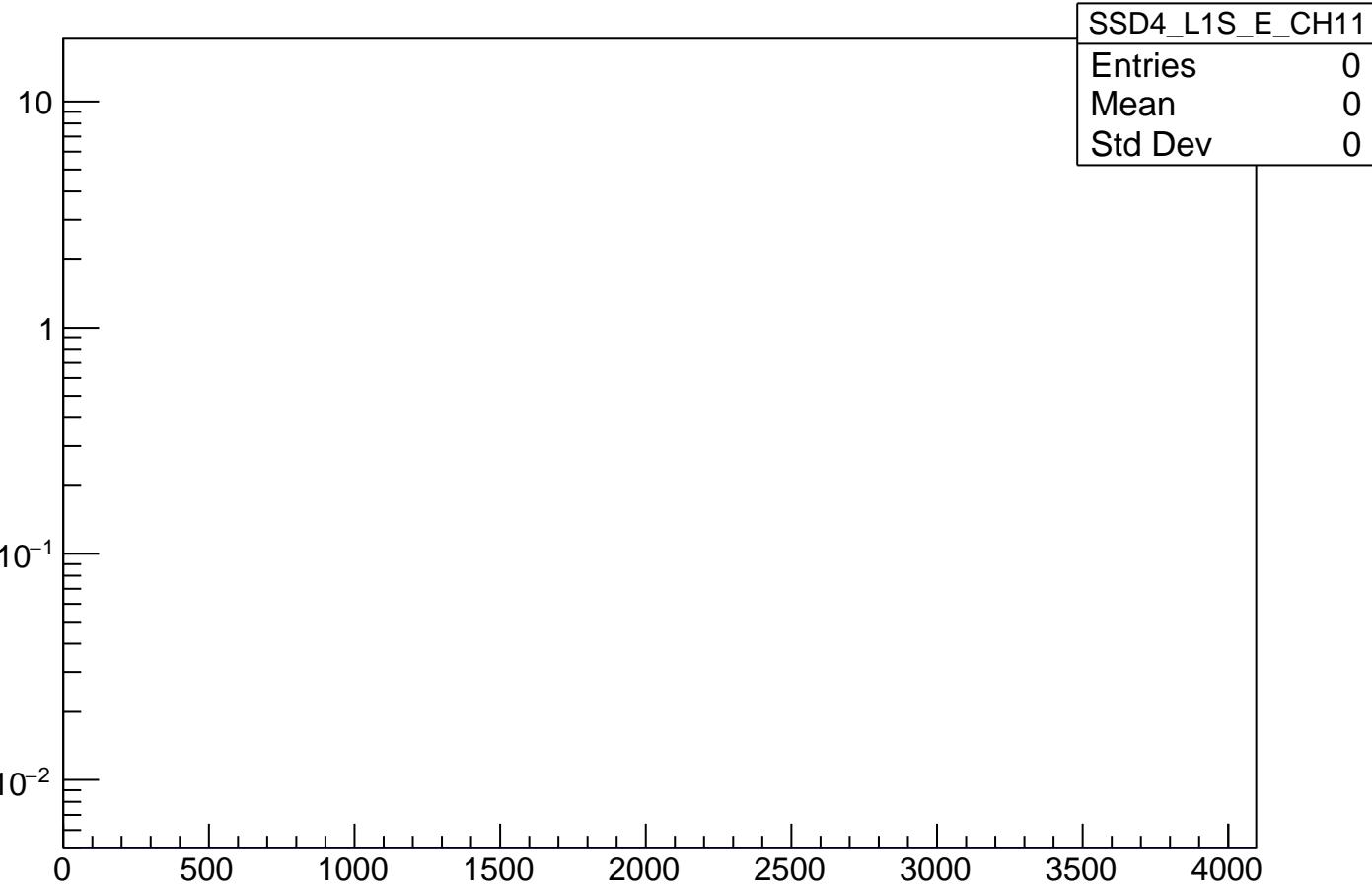
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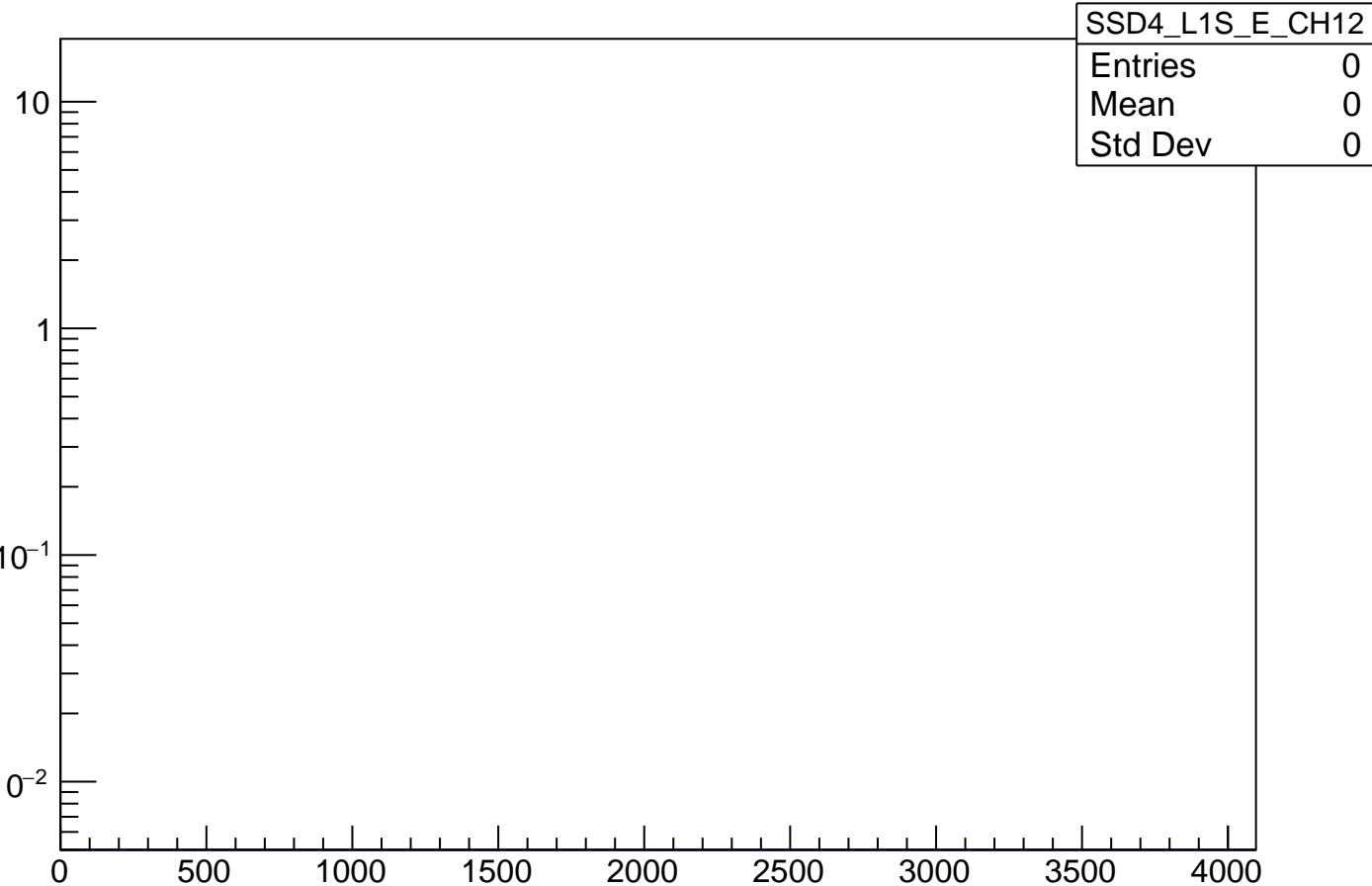
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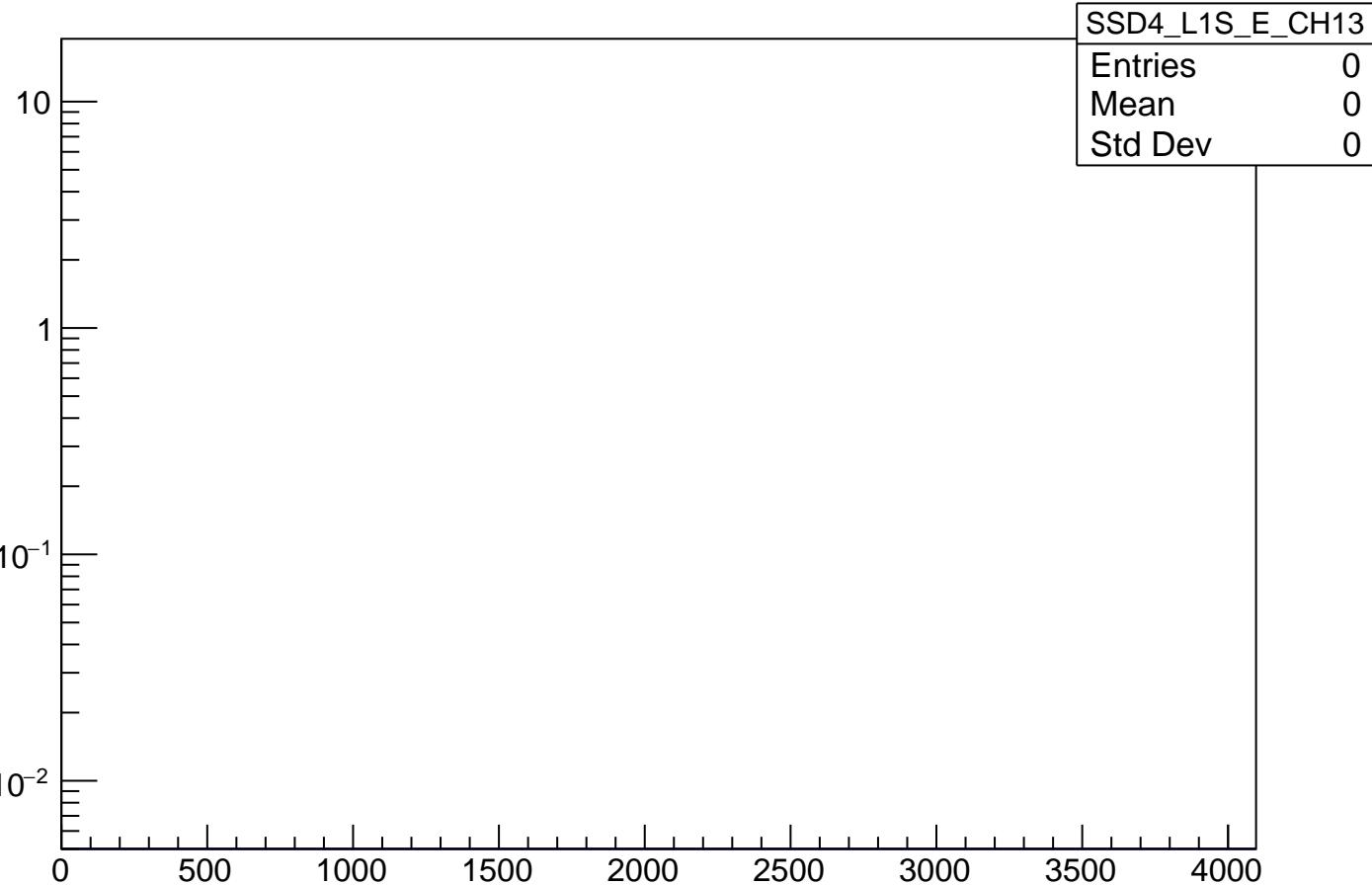
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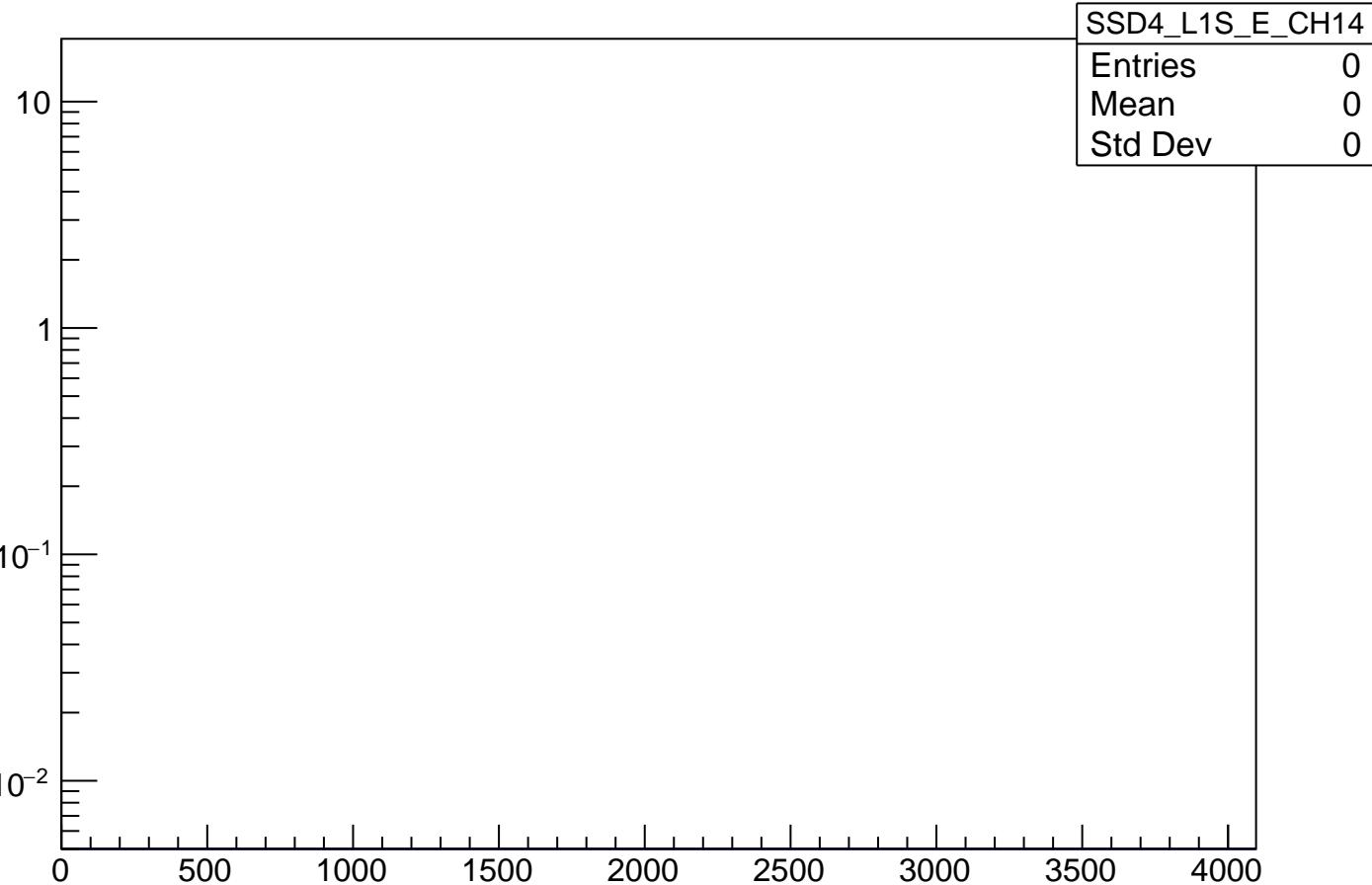
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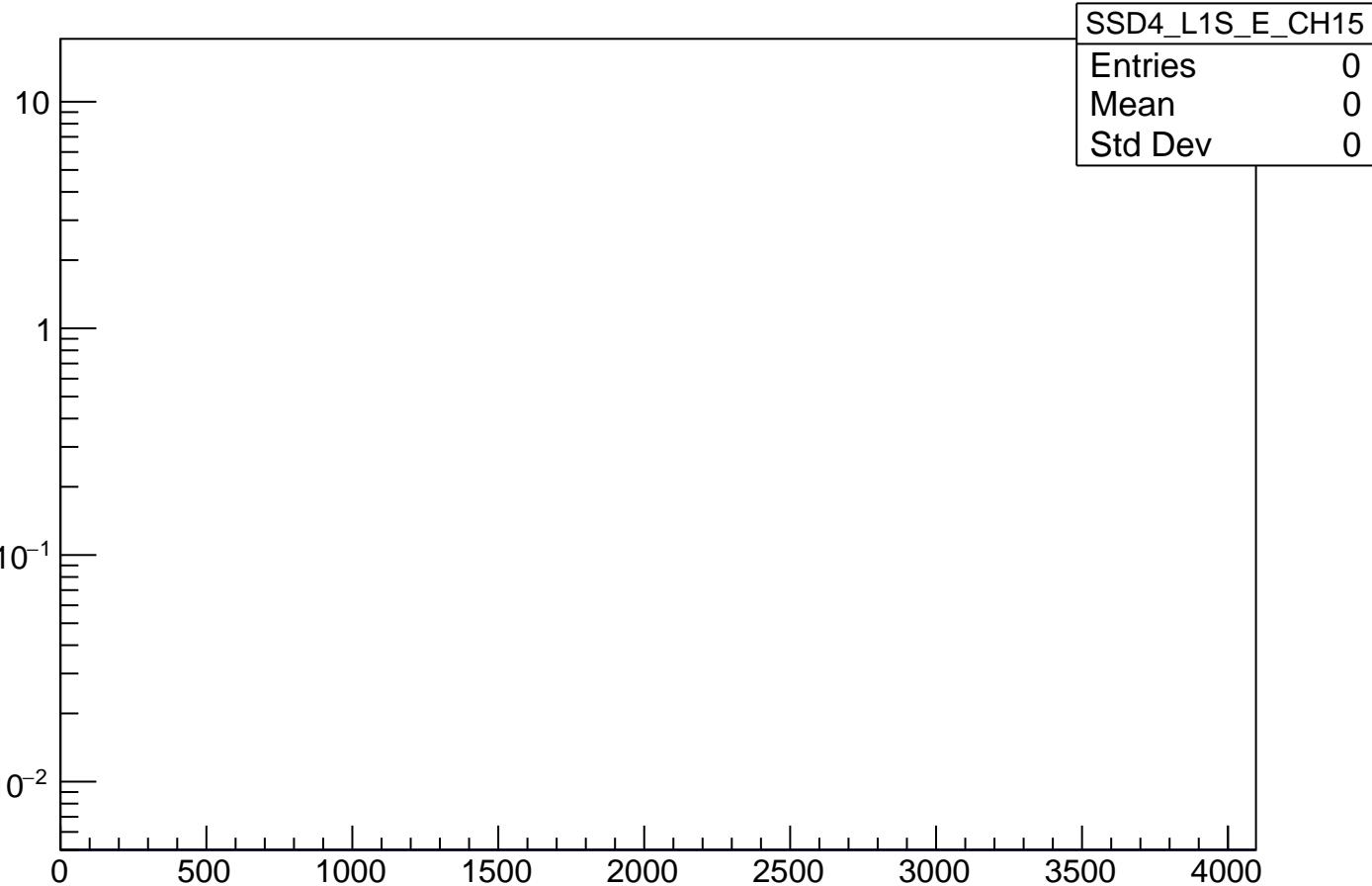
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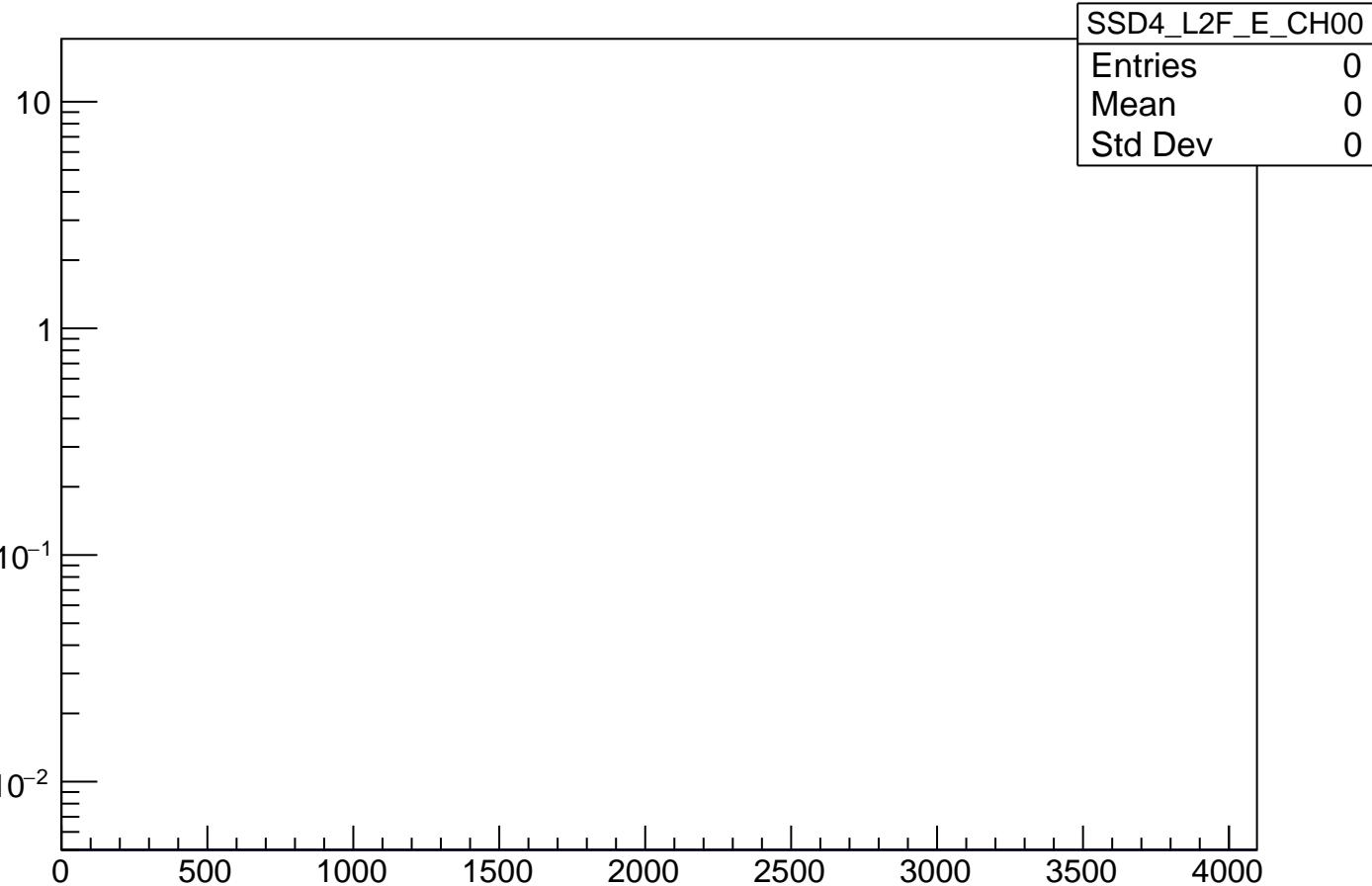
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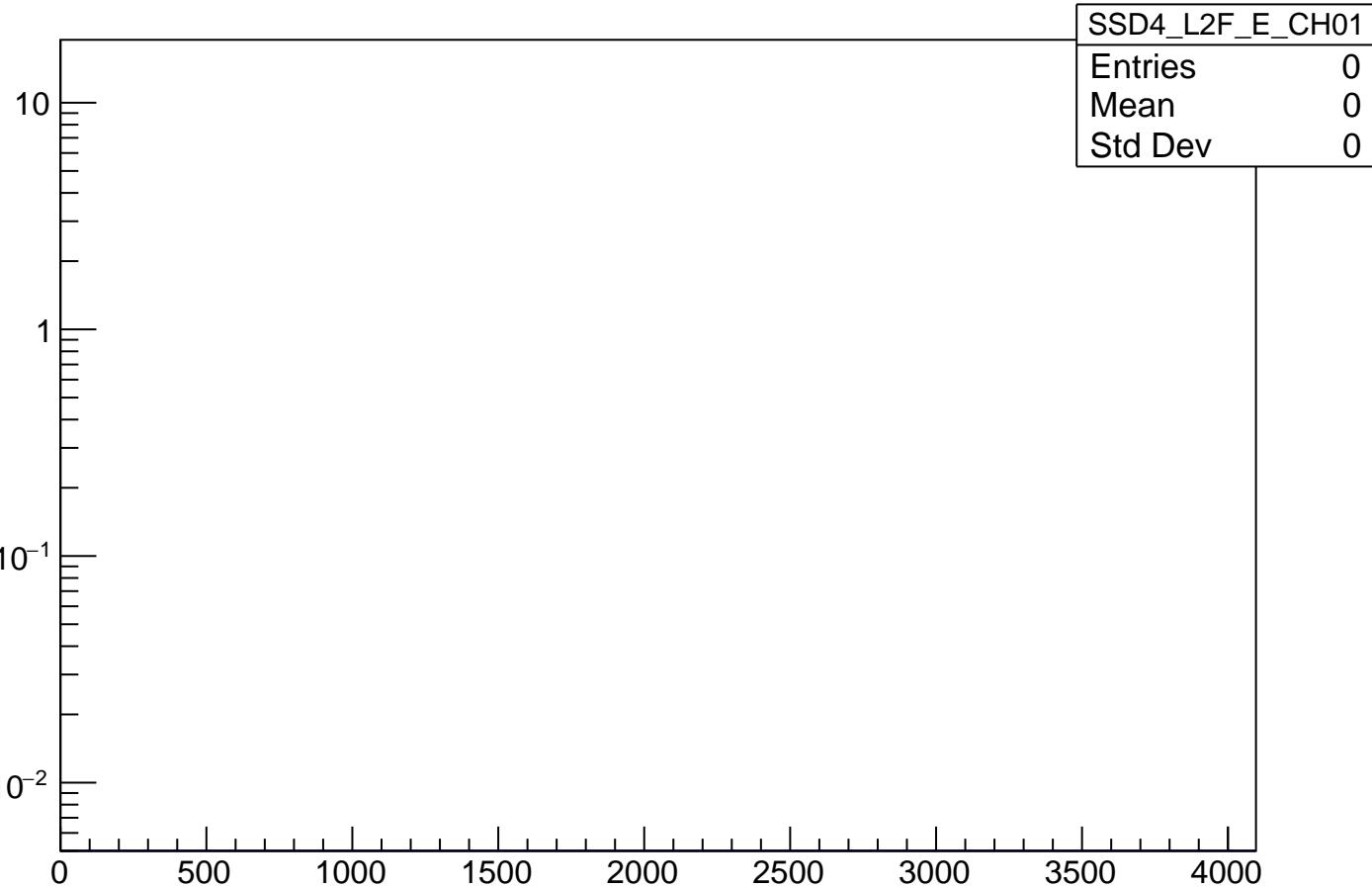
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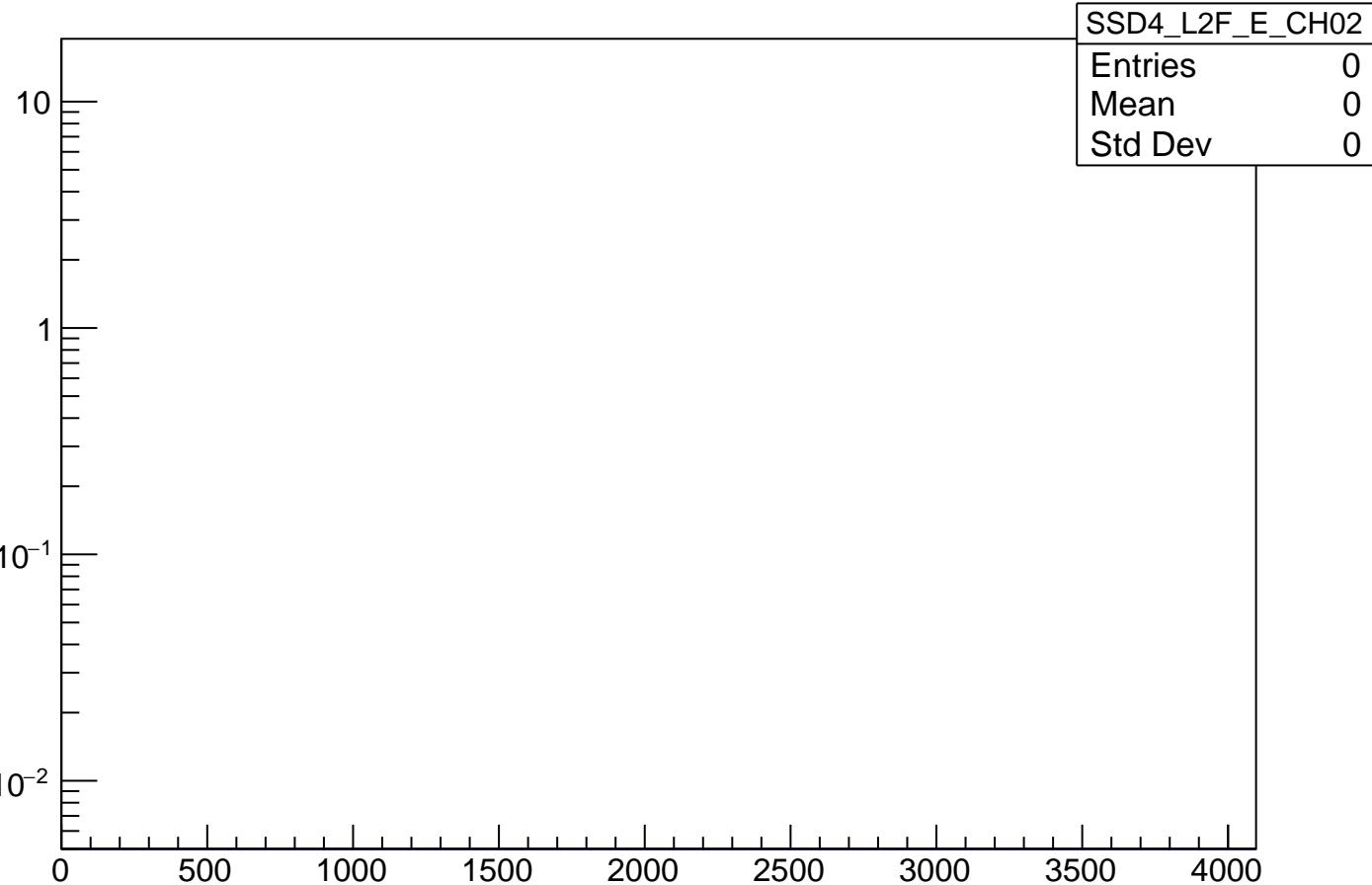
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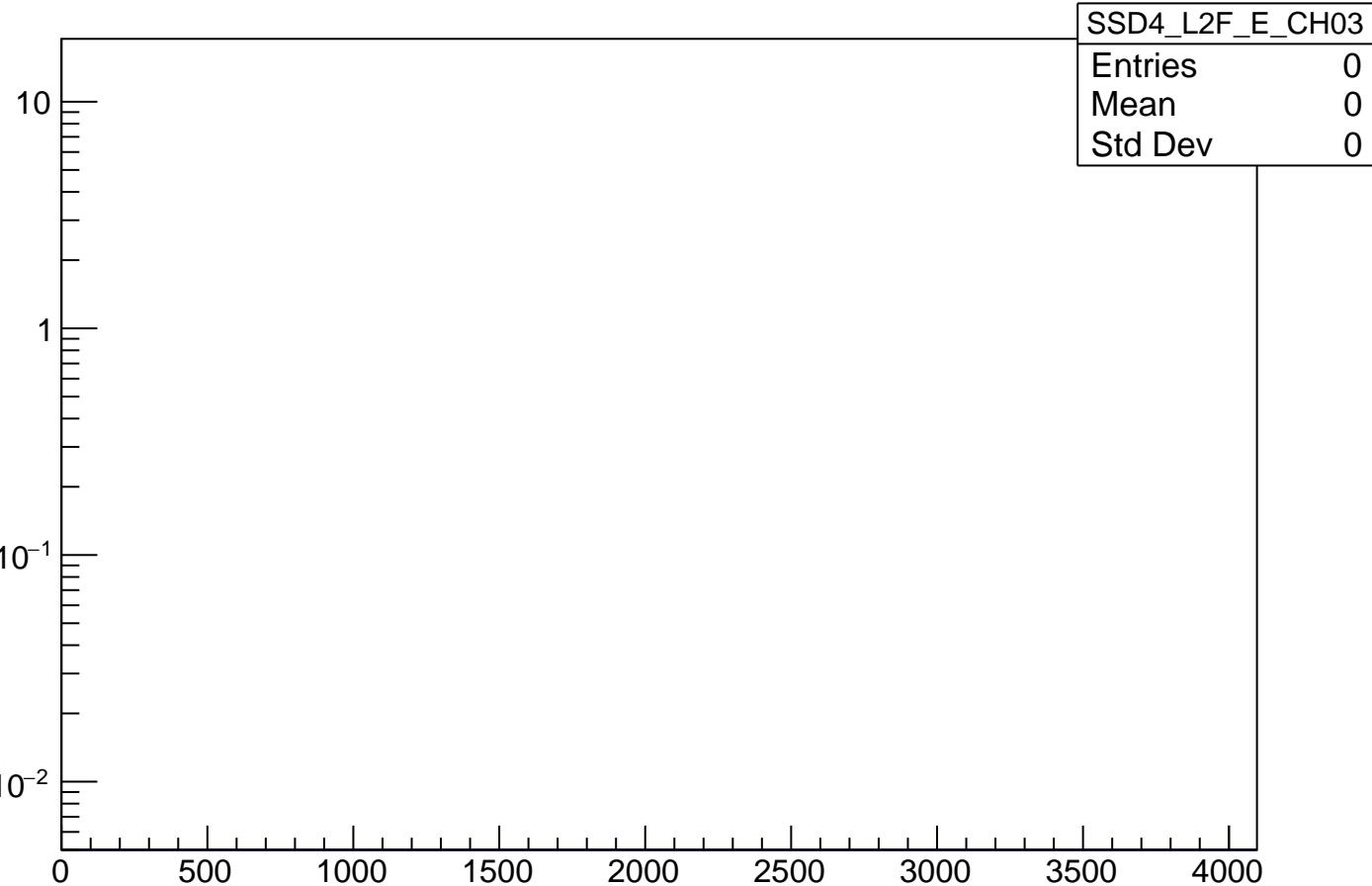
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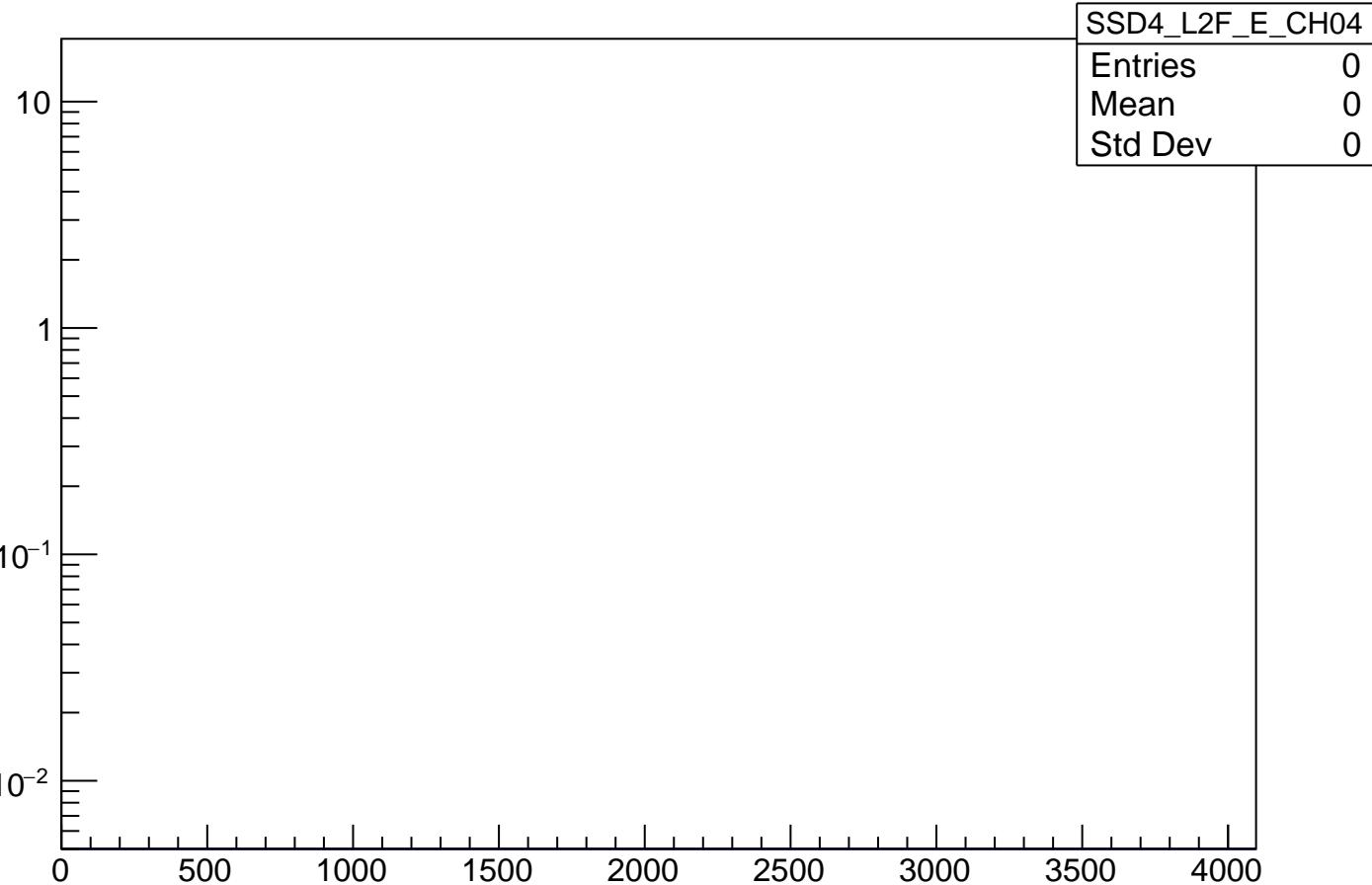
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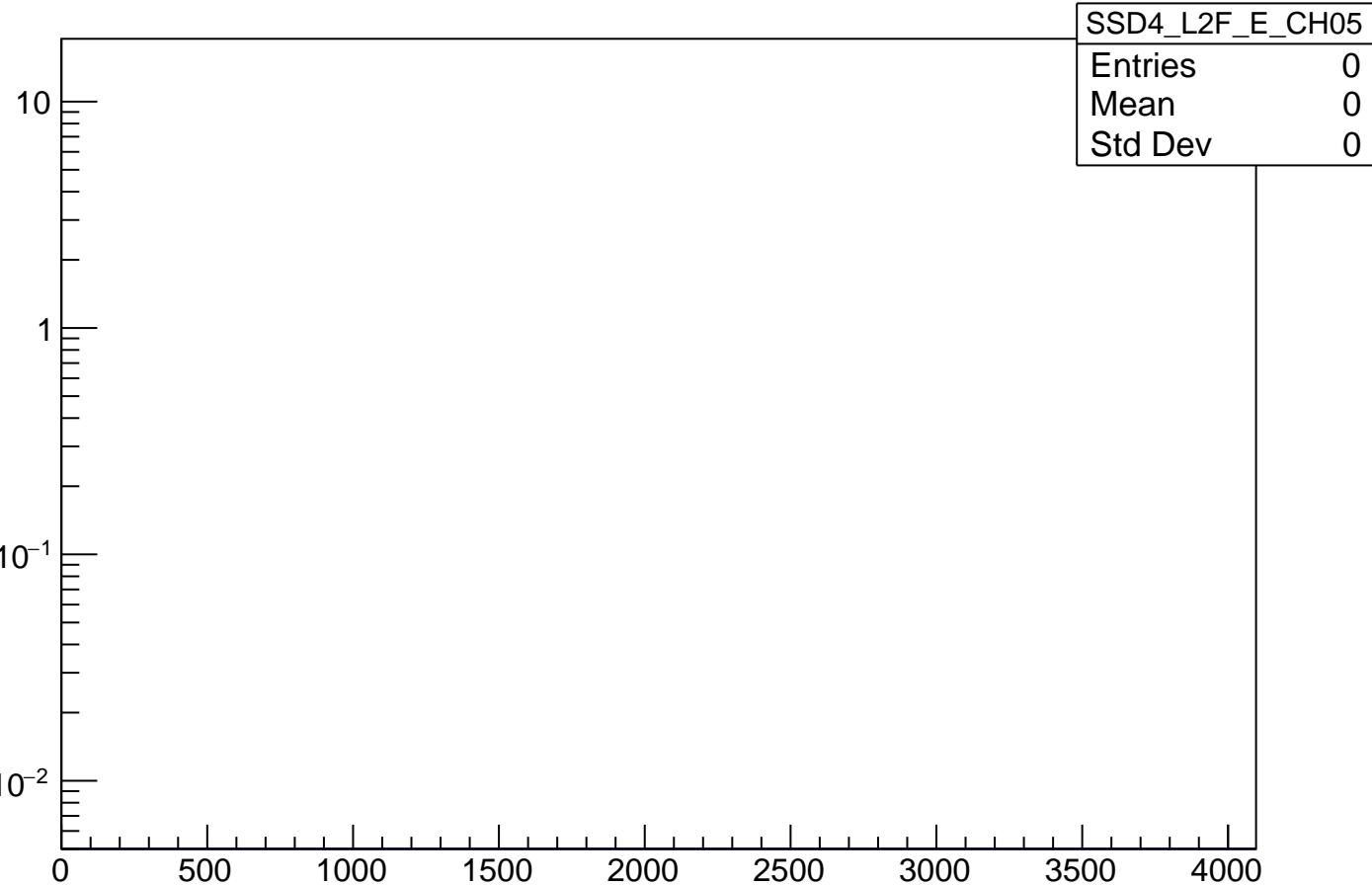
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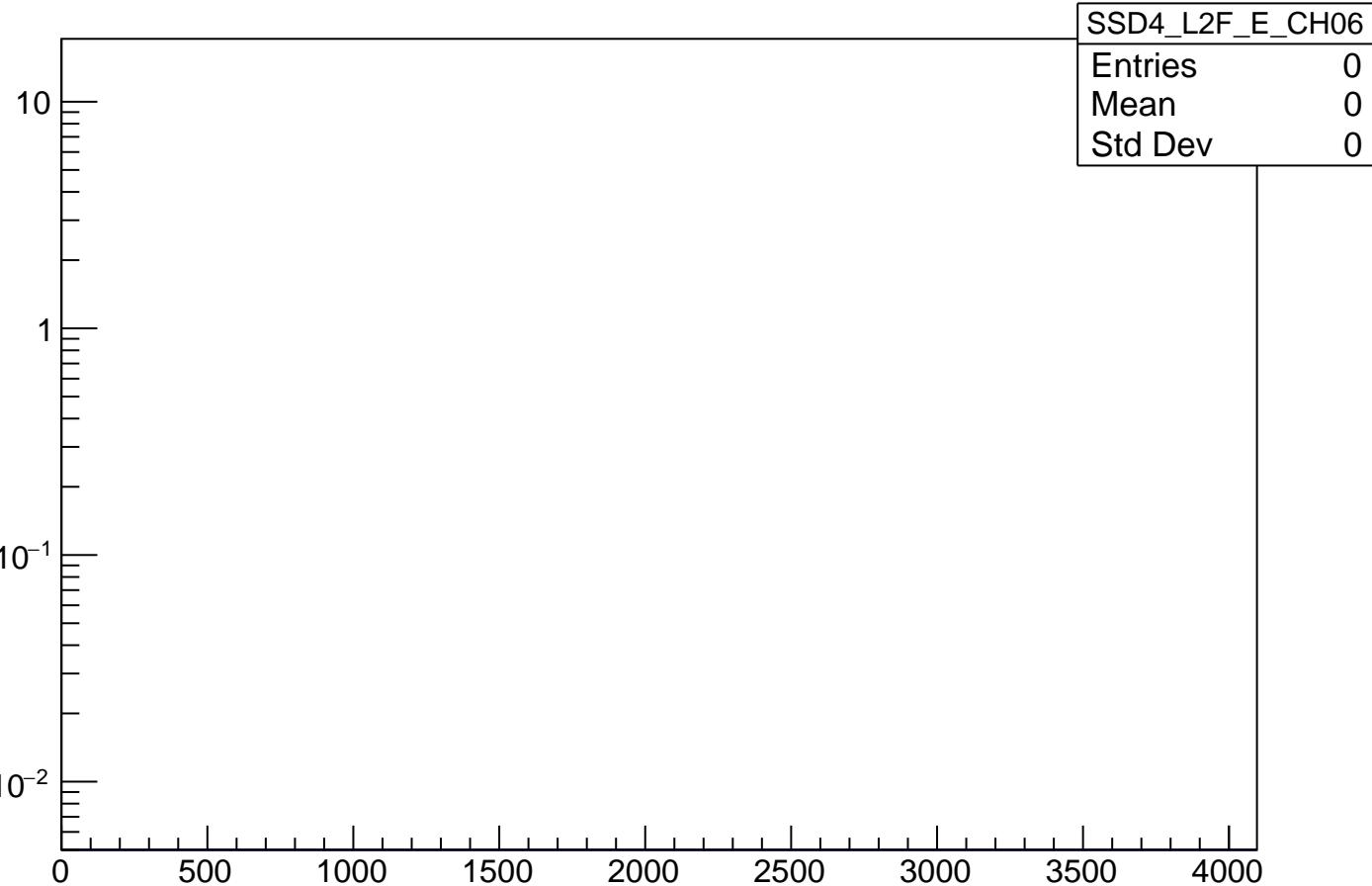
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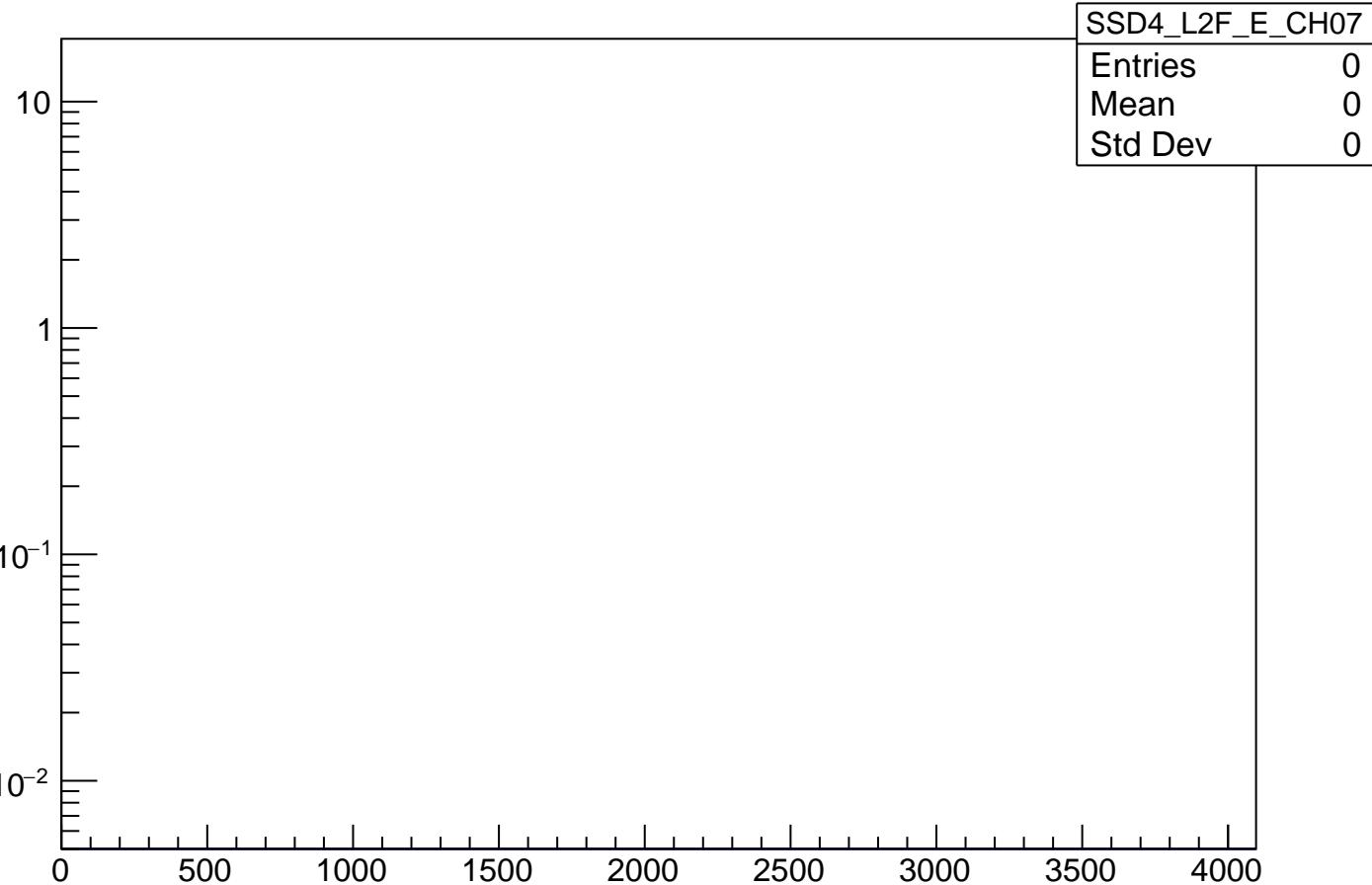
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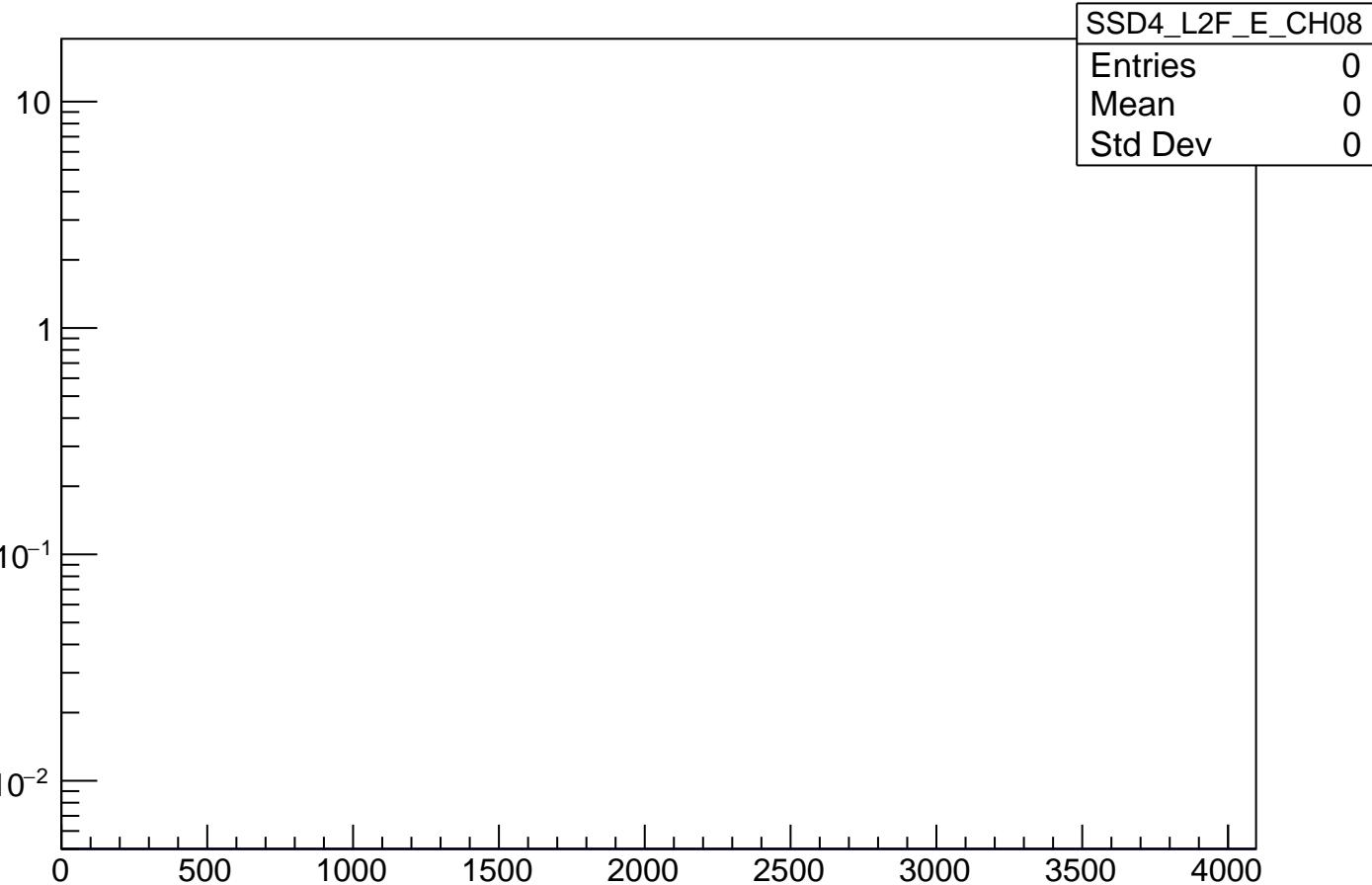
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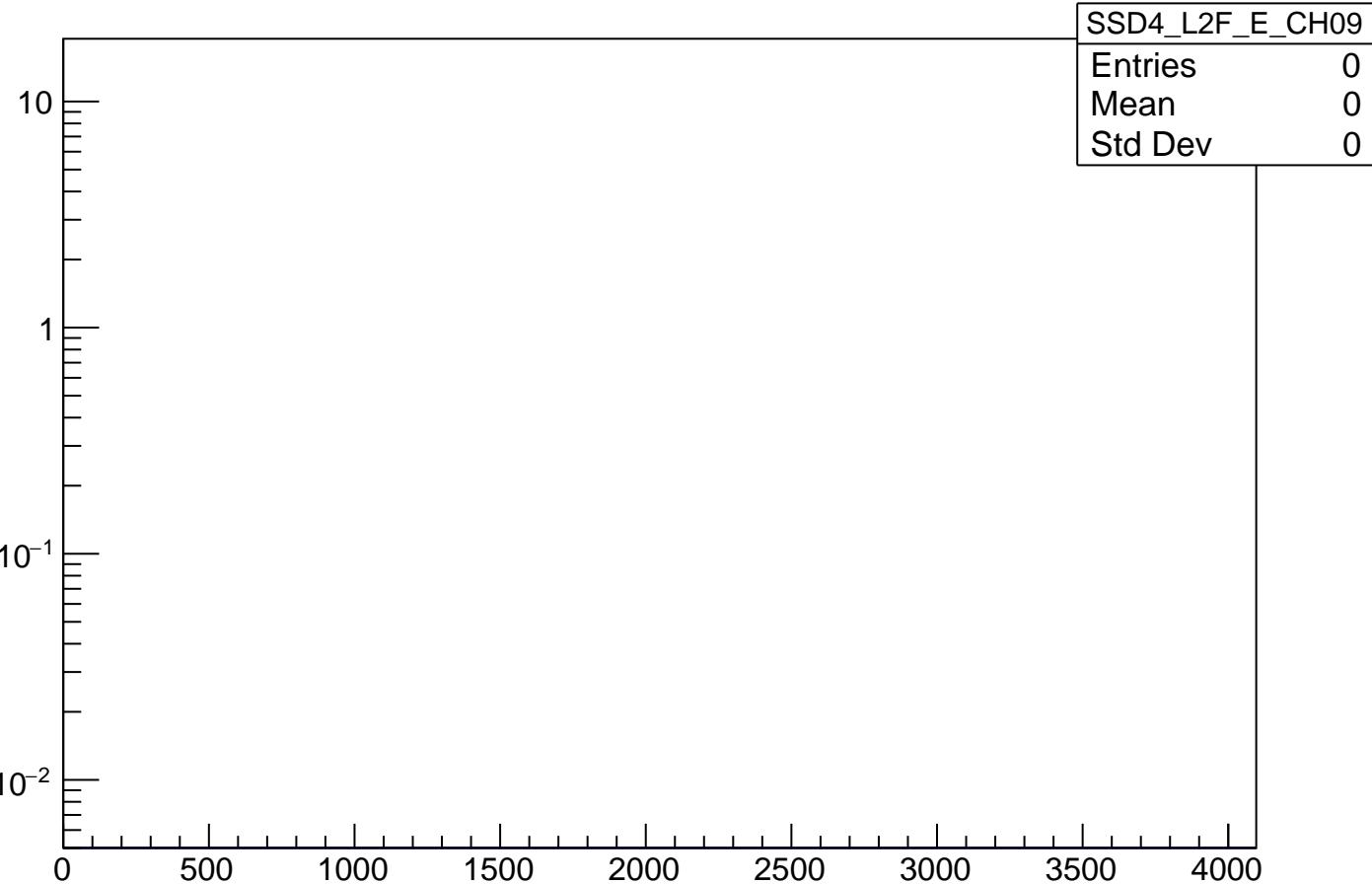
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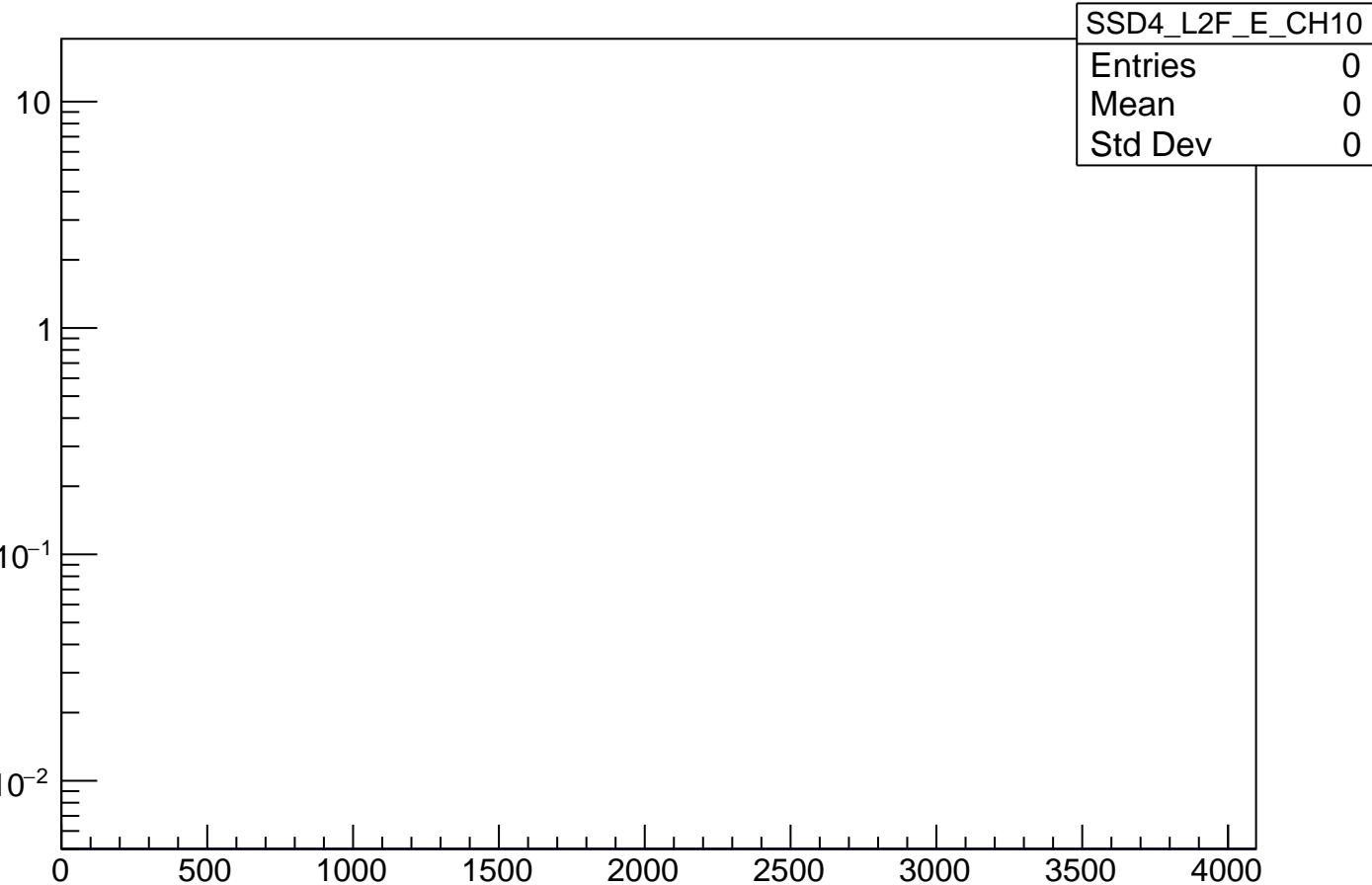
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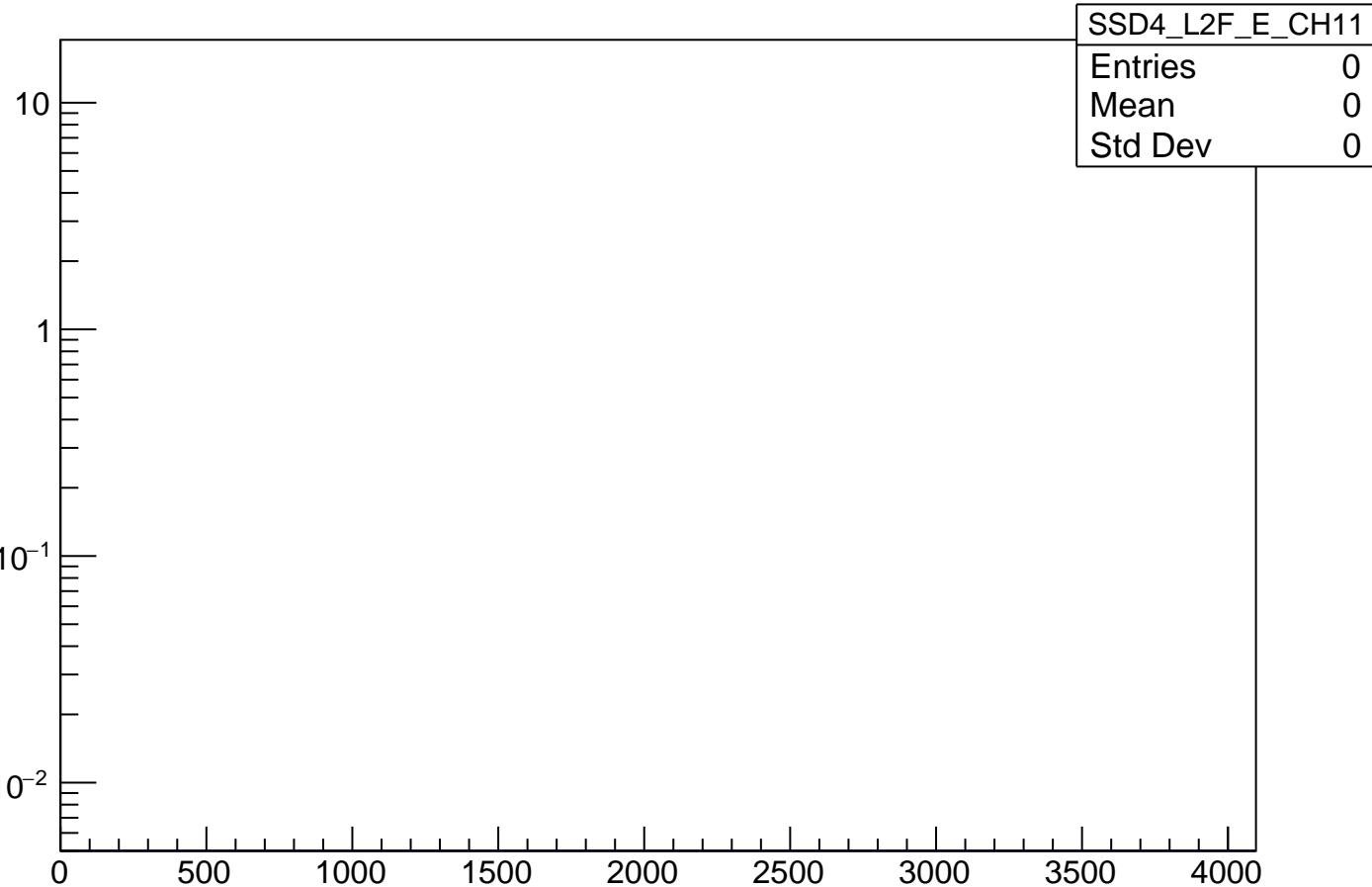
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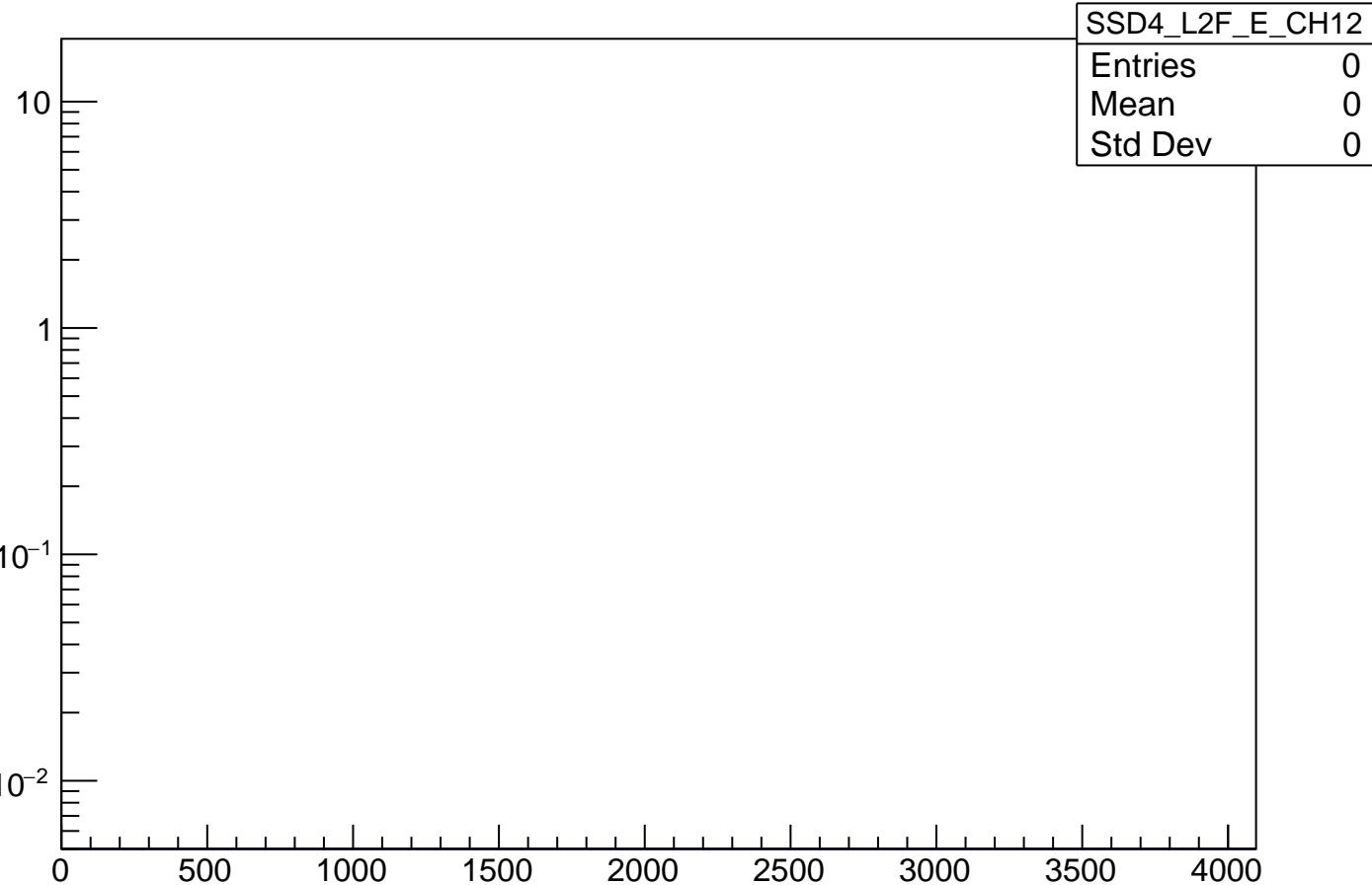
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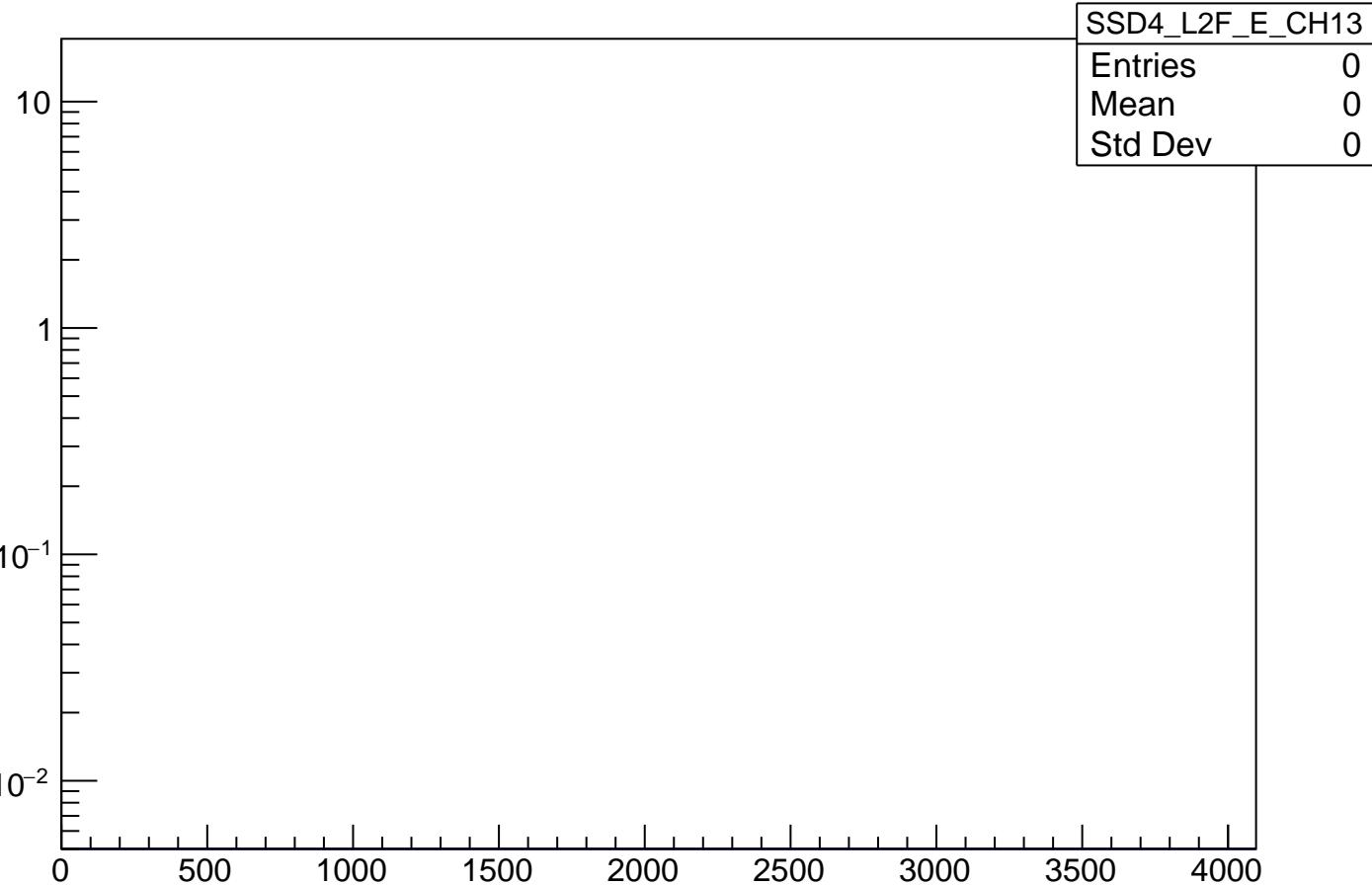
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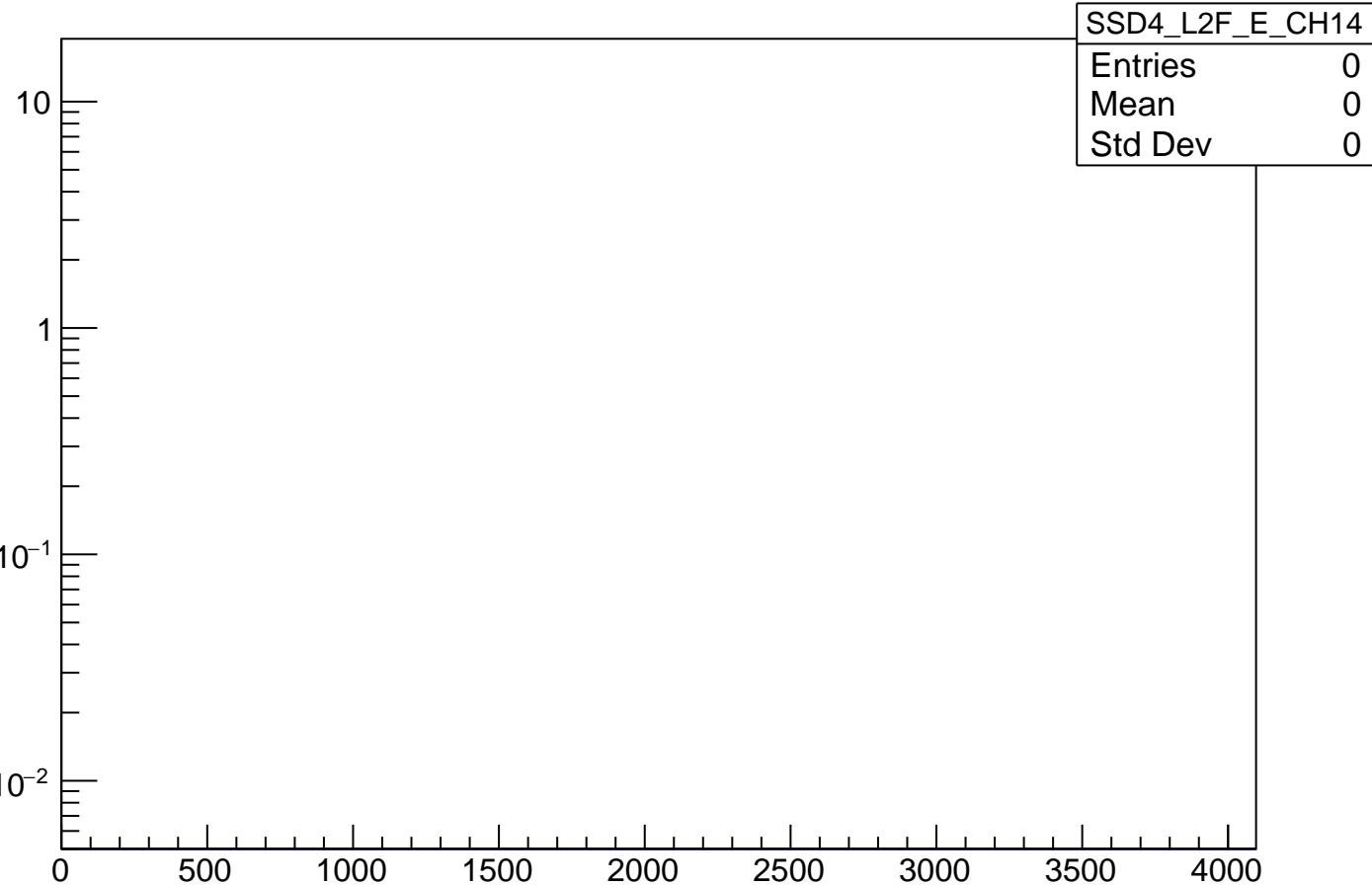
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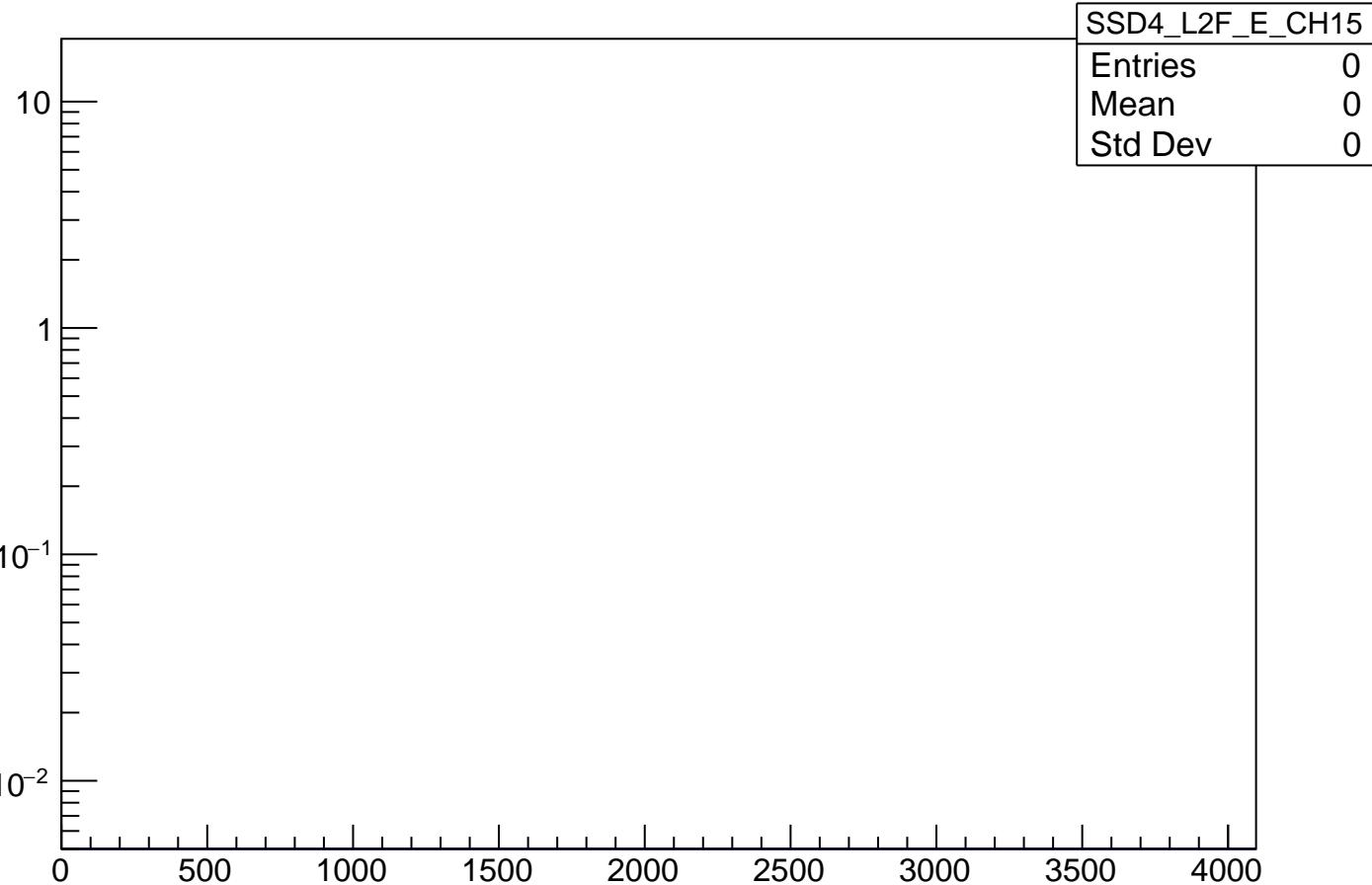
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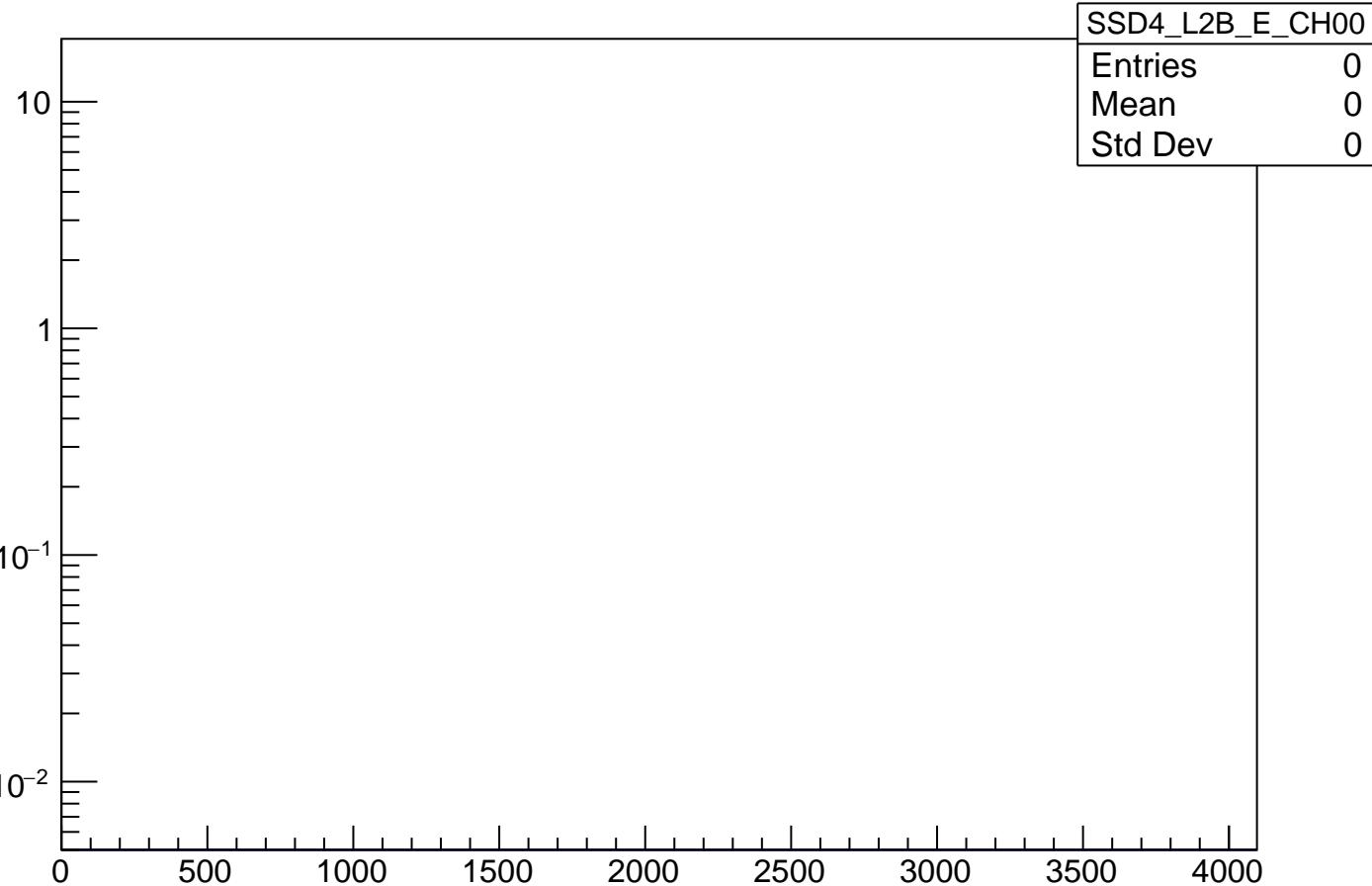
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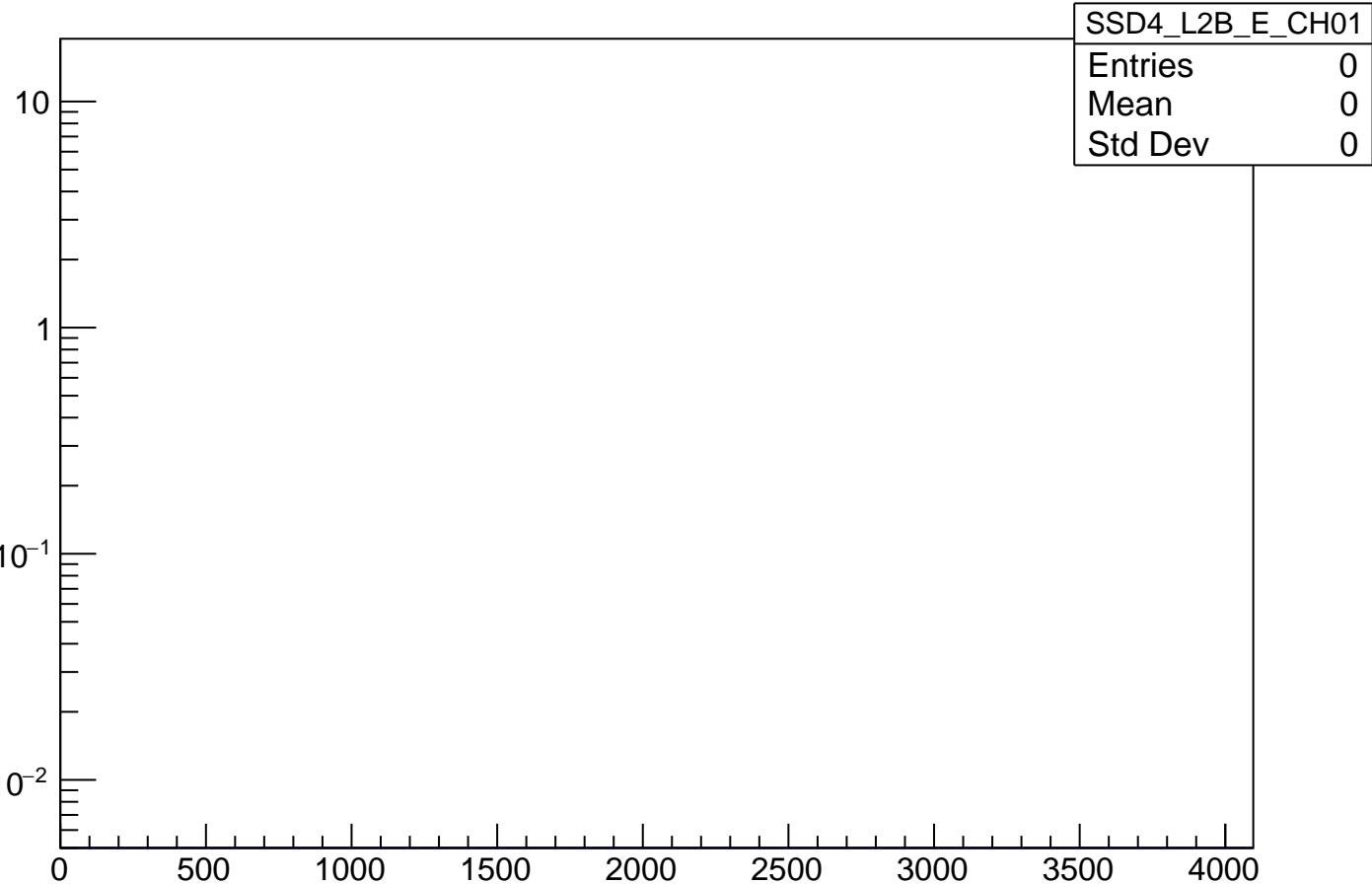
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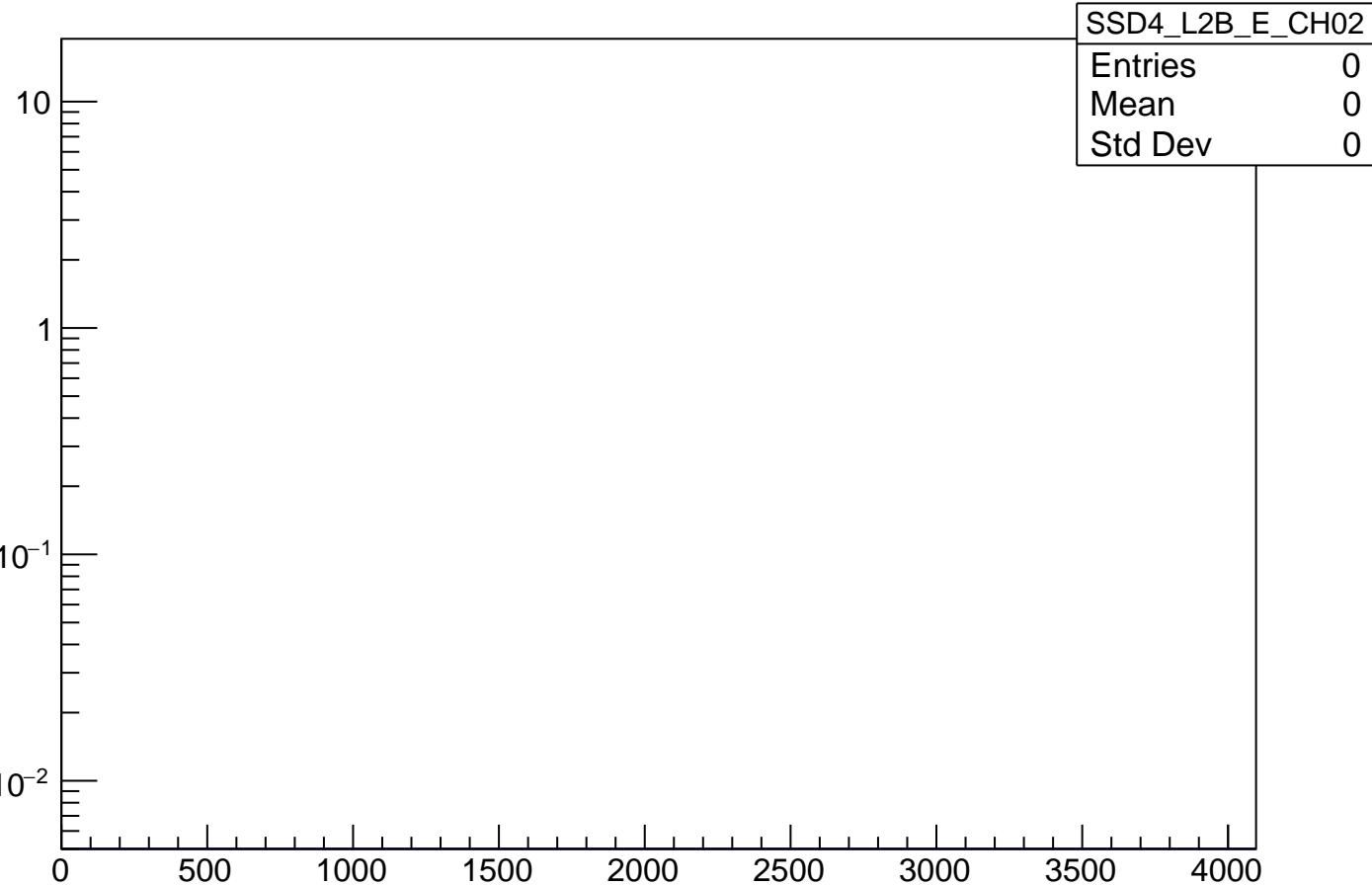
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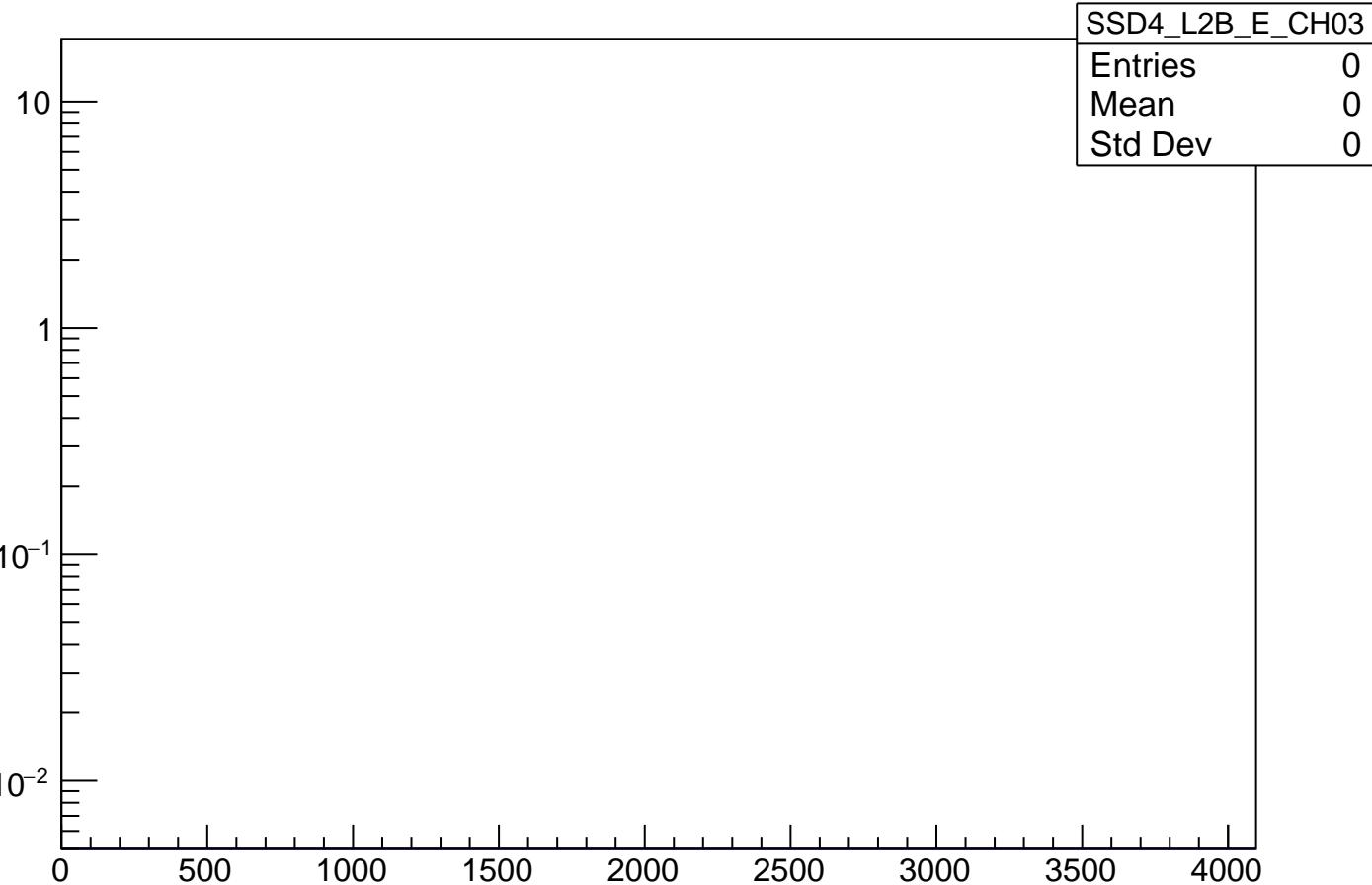
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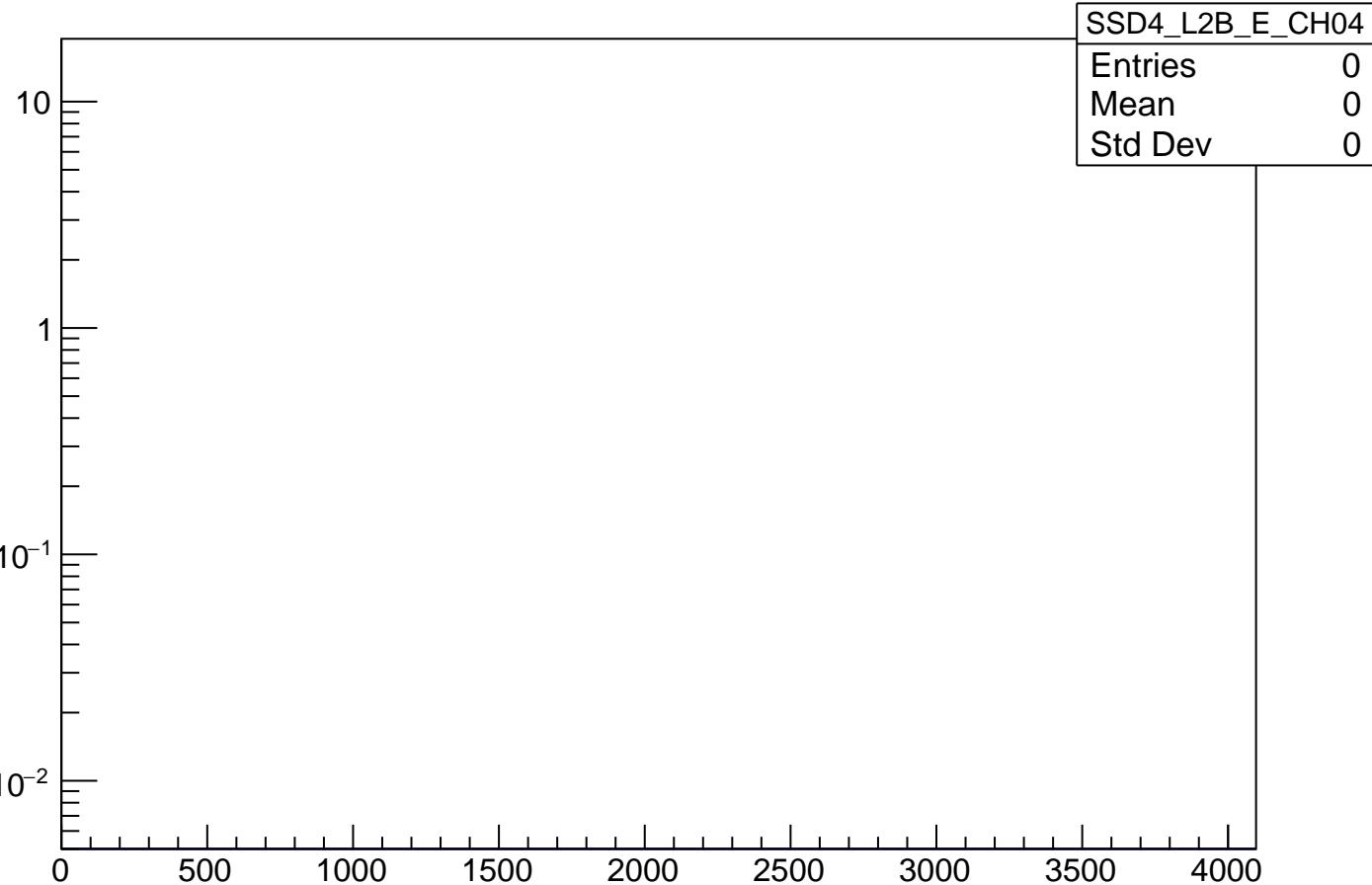
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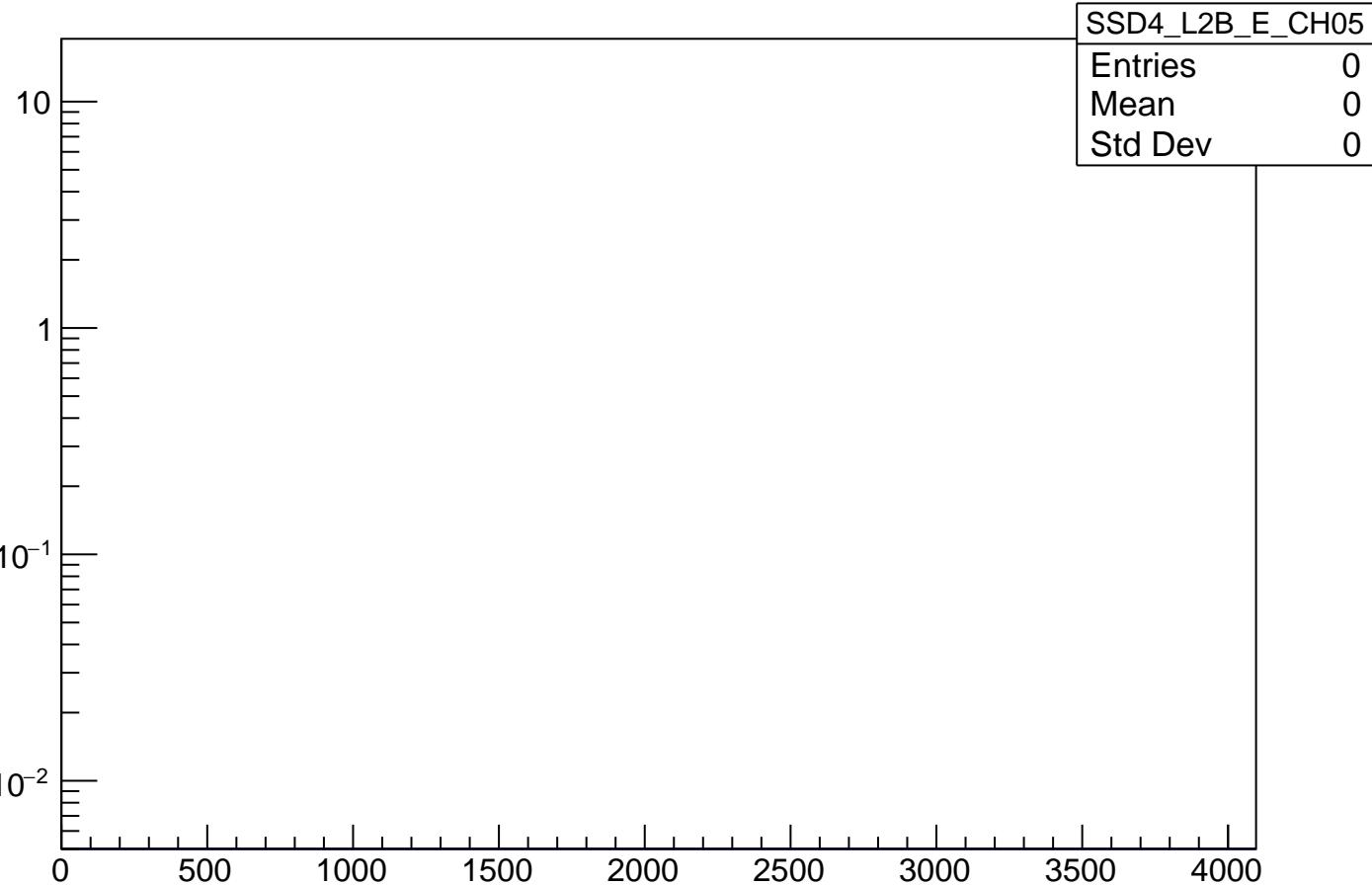
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SSD4_L2B_E_CH04

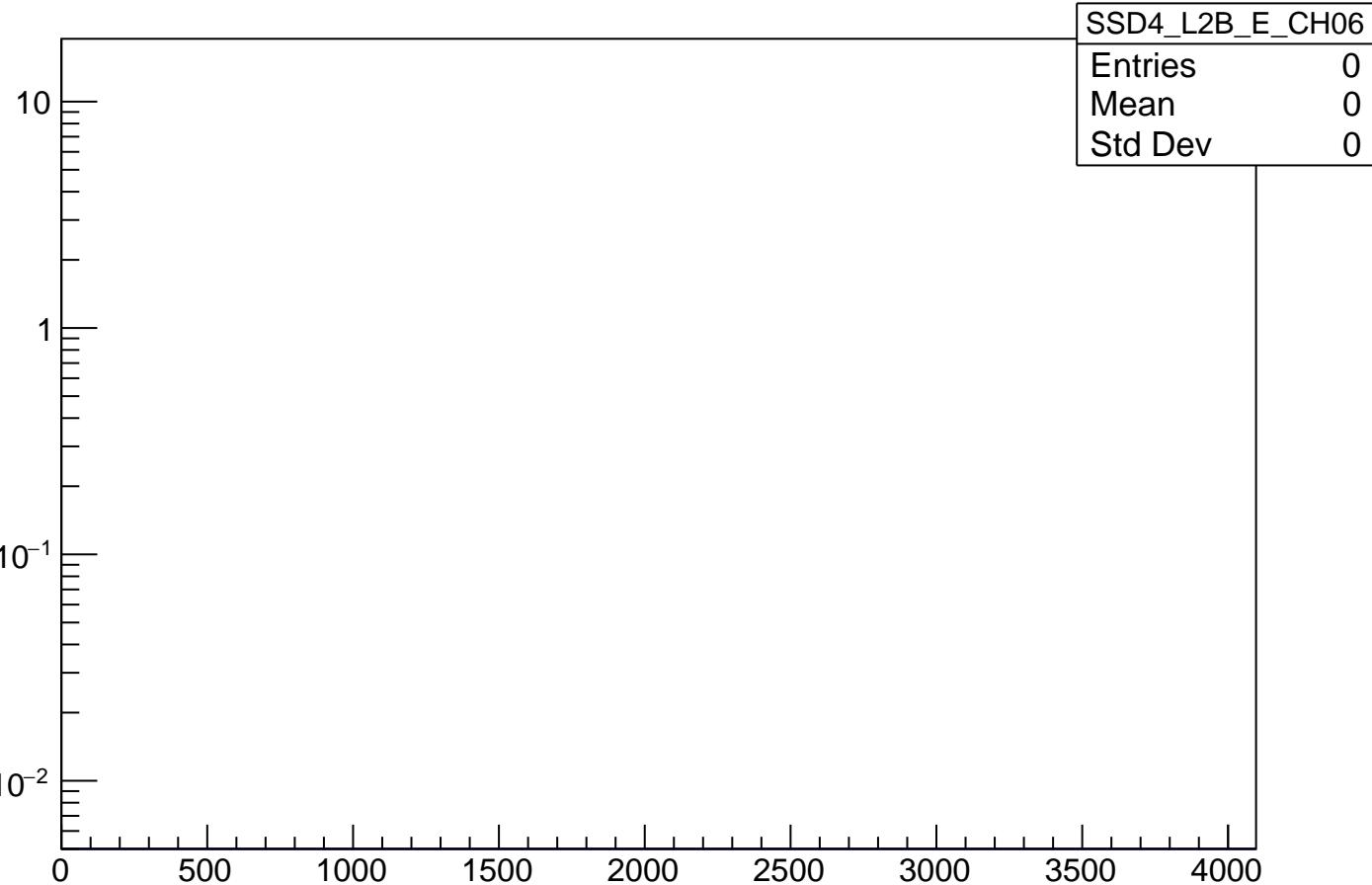


SSD4_L2B_E_CH05

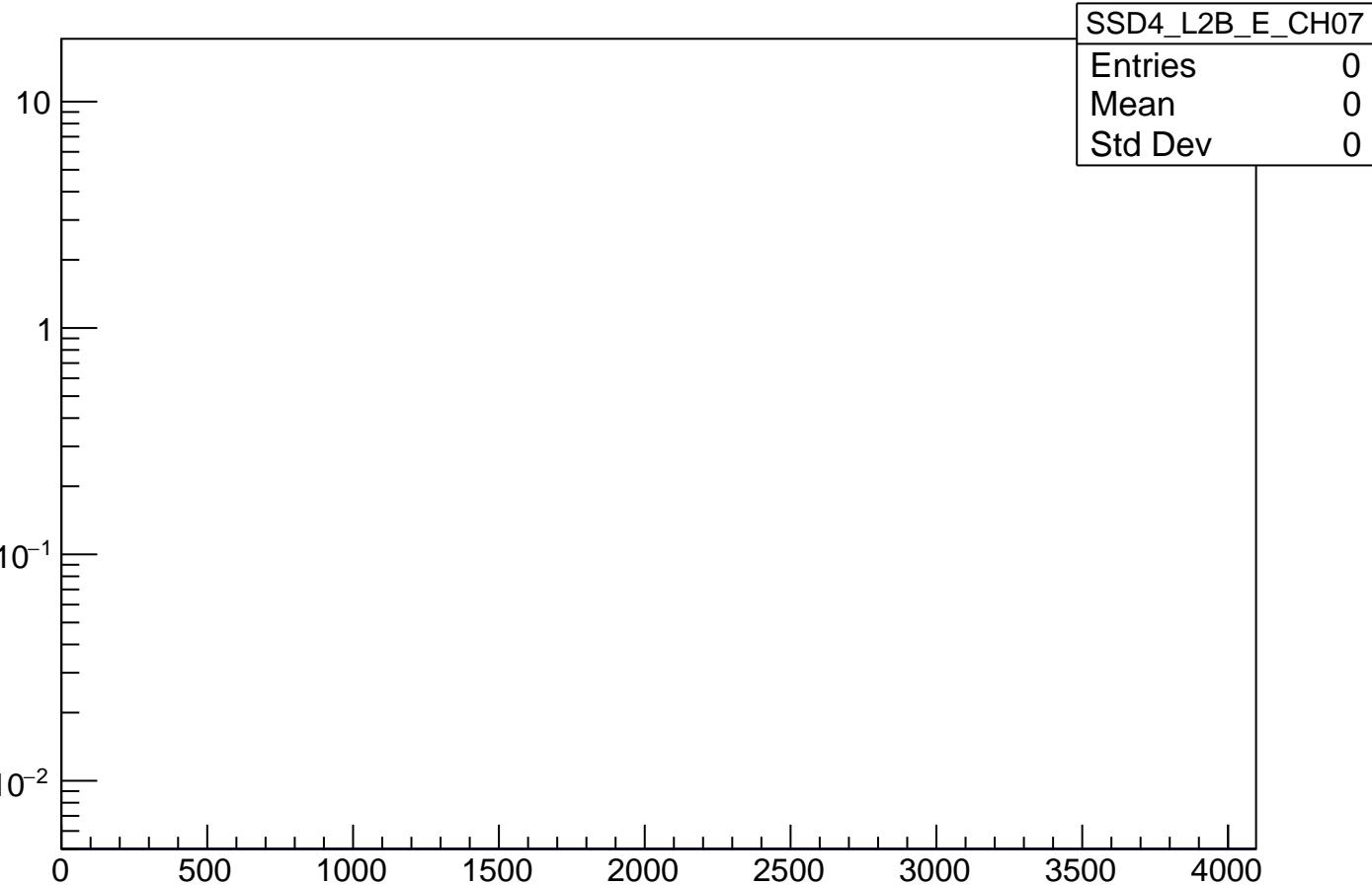


SSD4_L2B_E_CH05	
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Mean	0
Std Dev	0

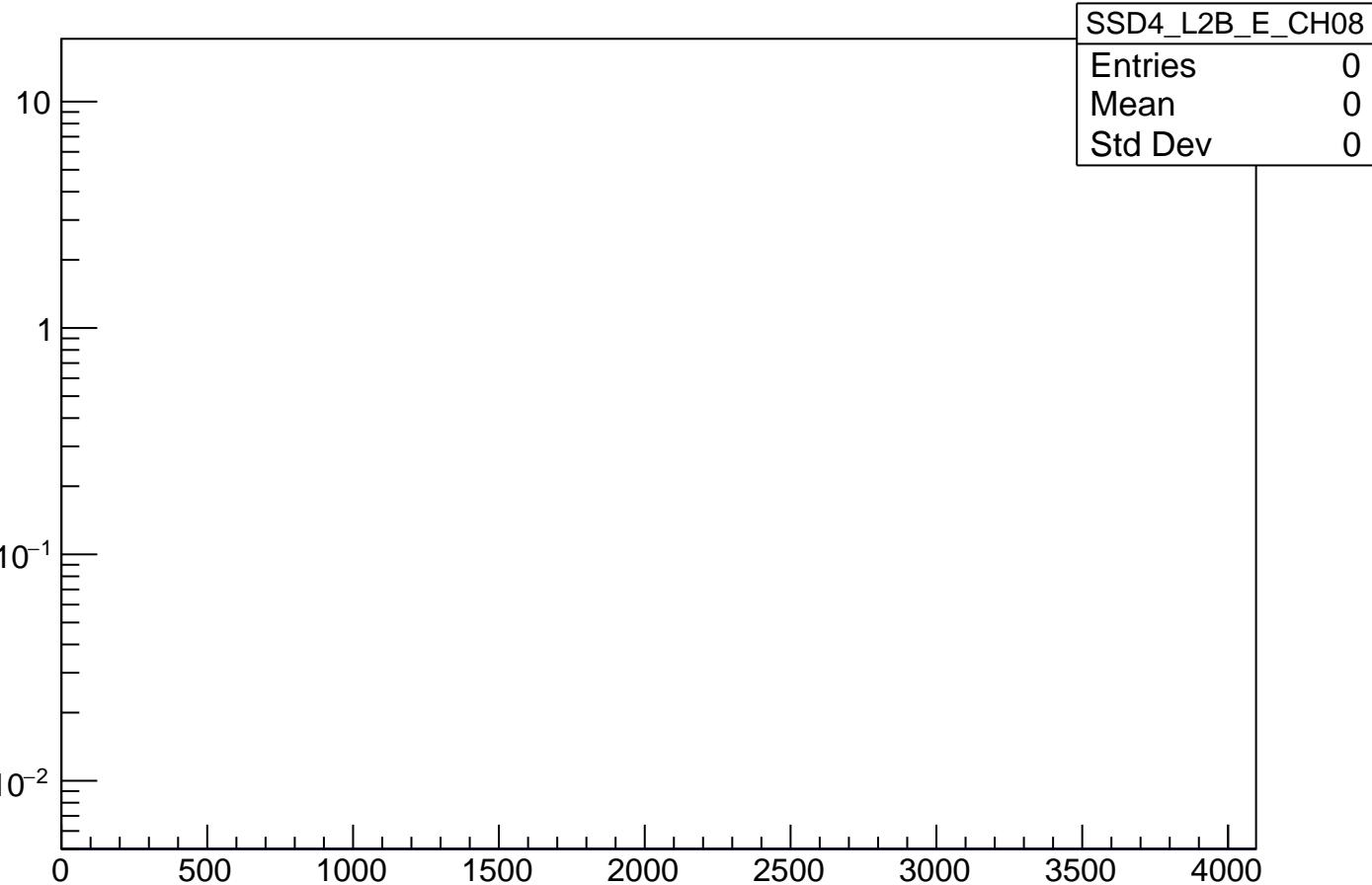
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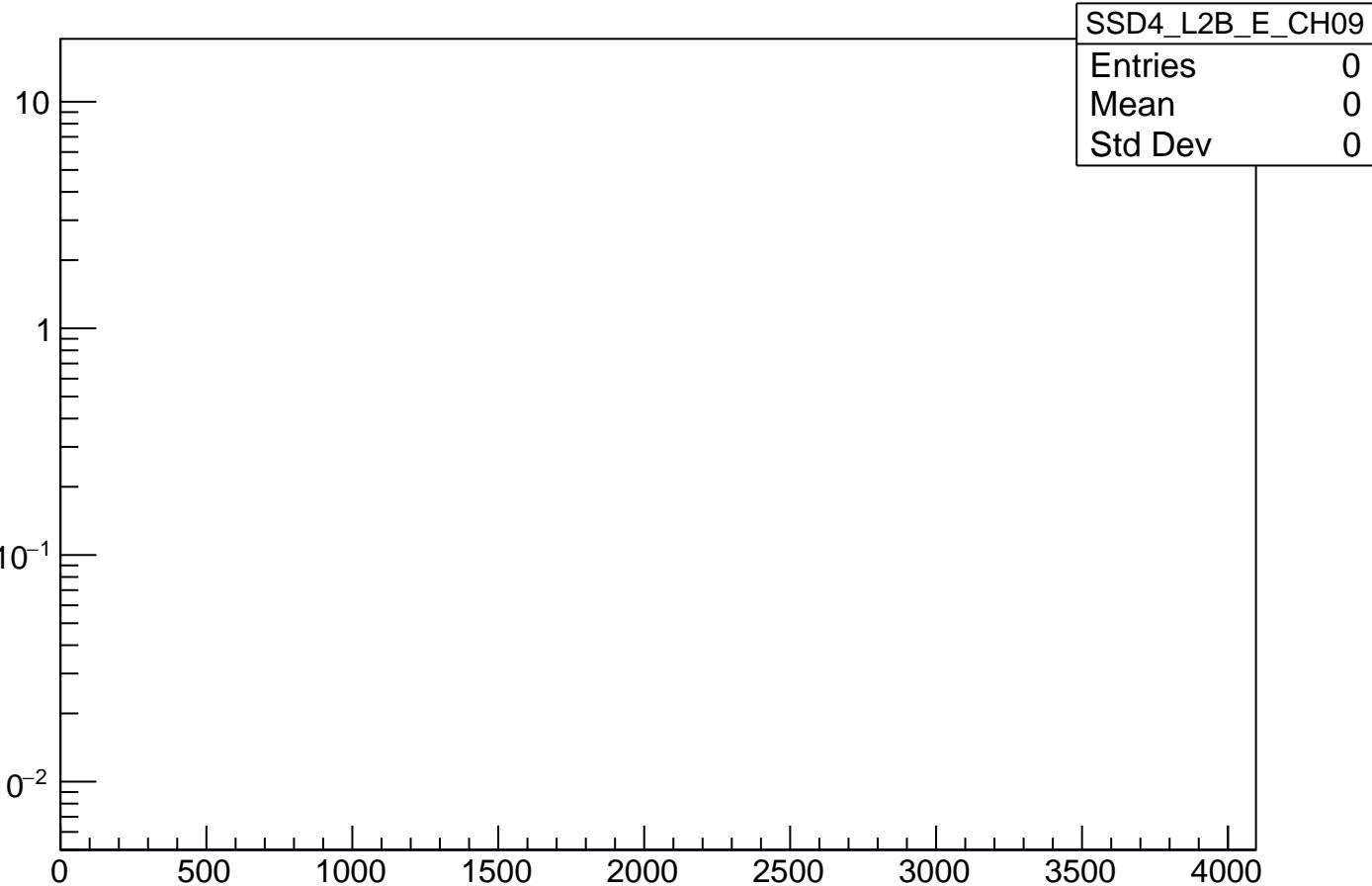
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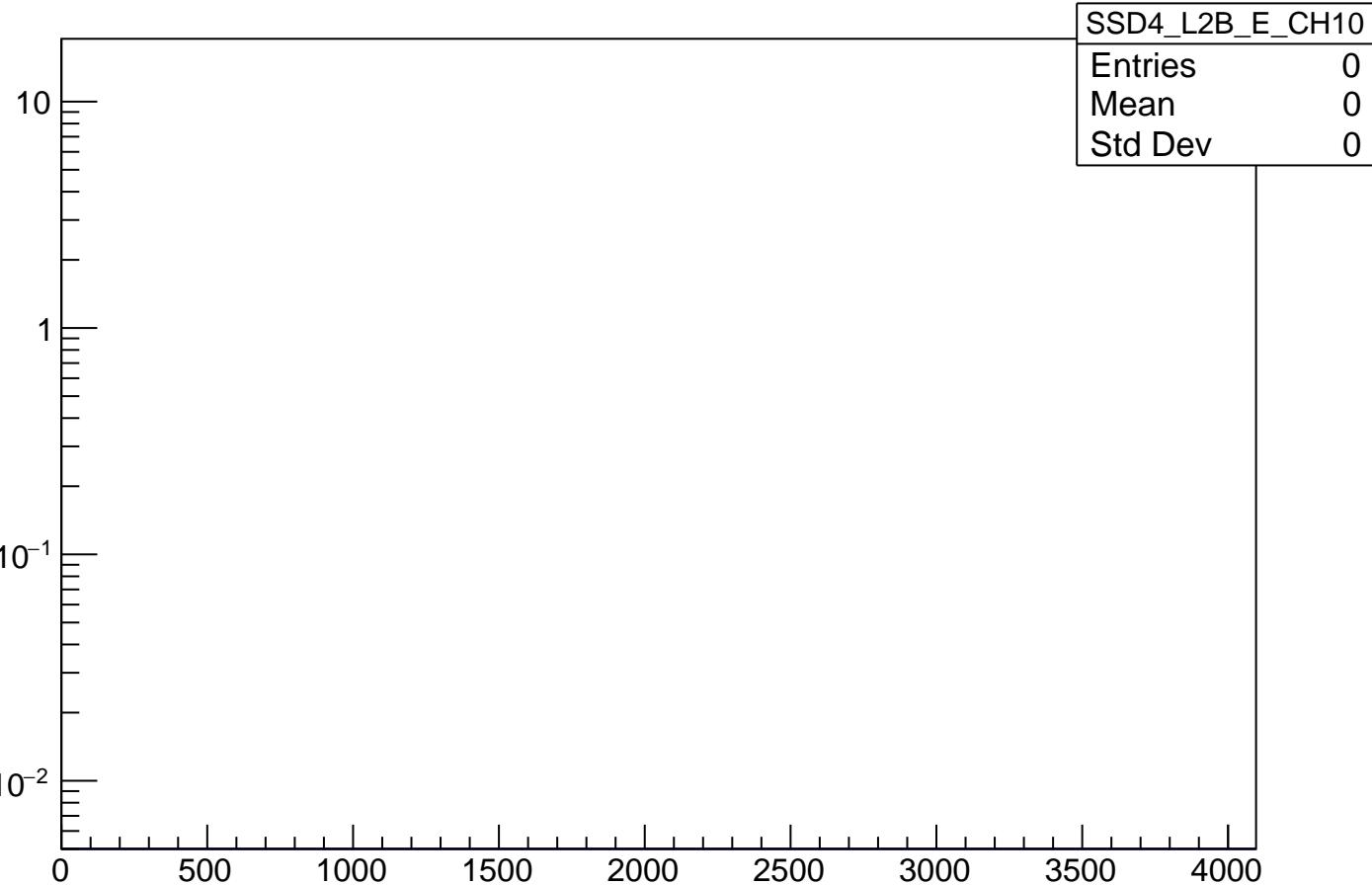
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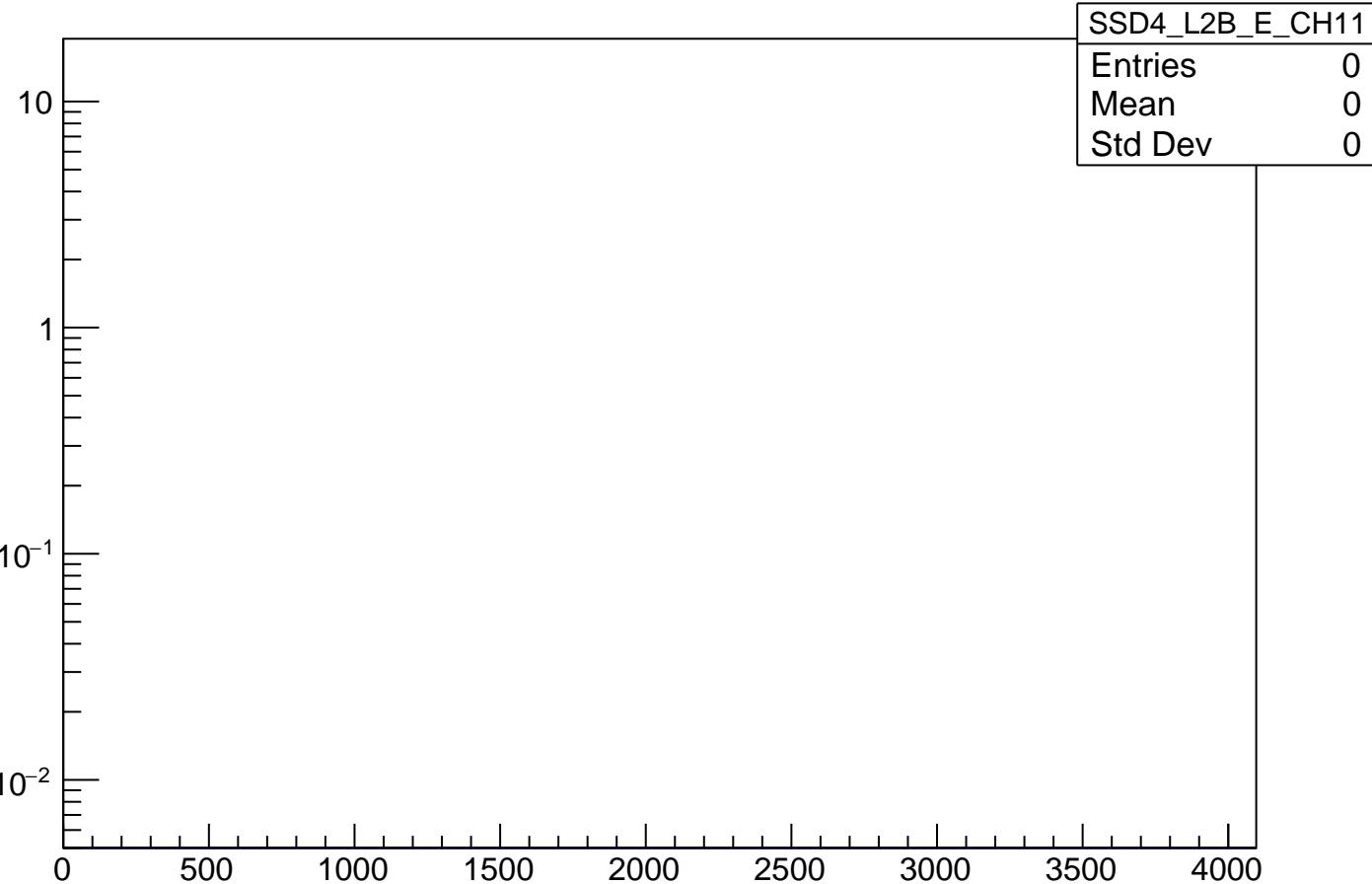
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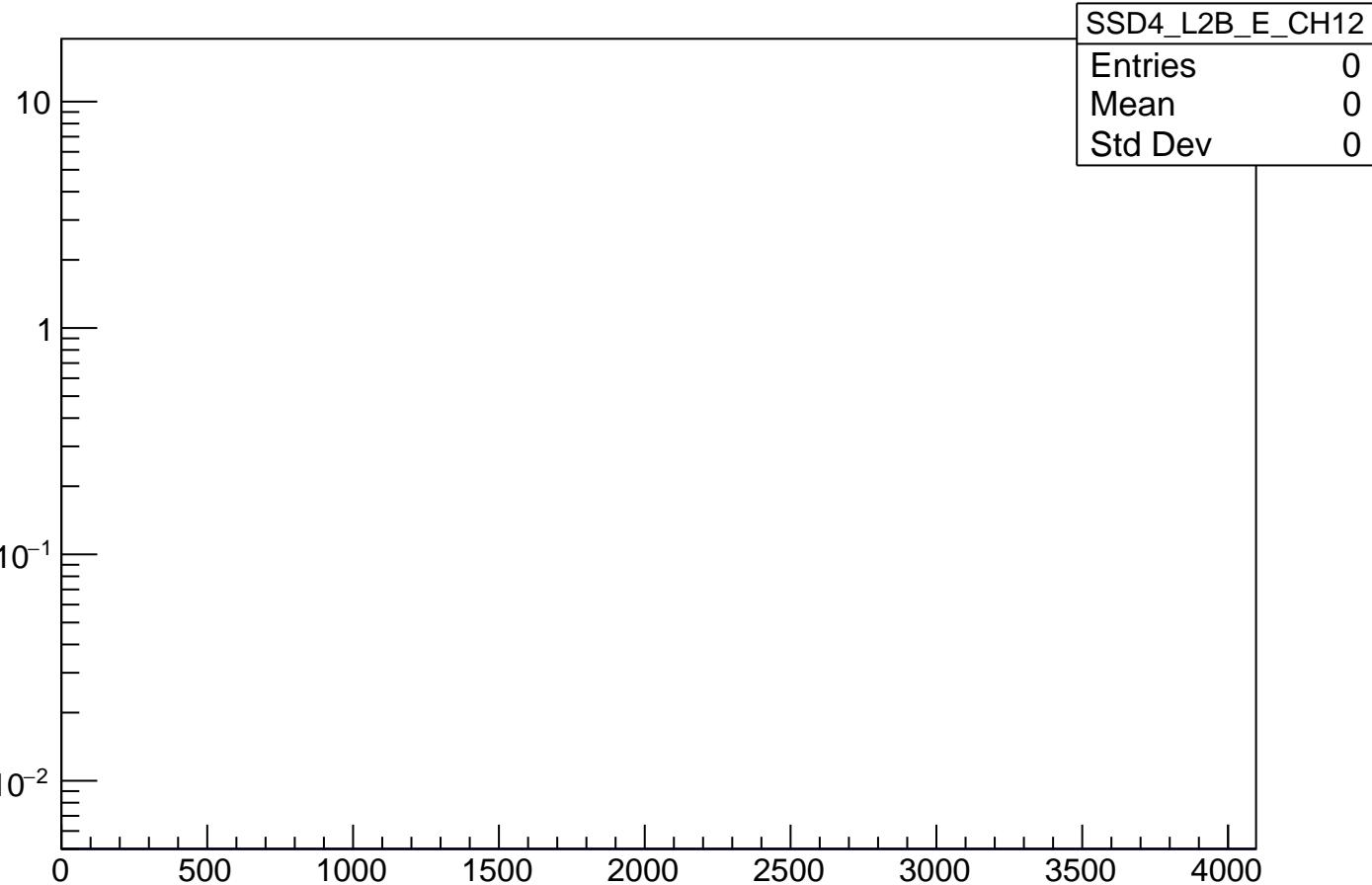
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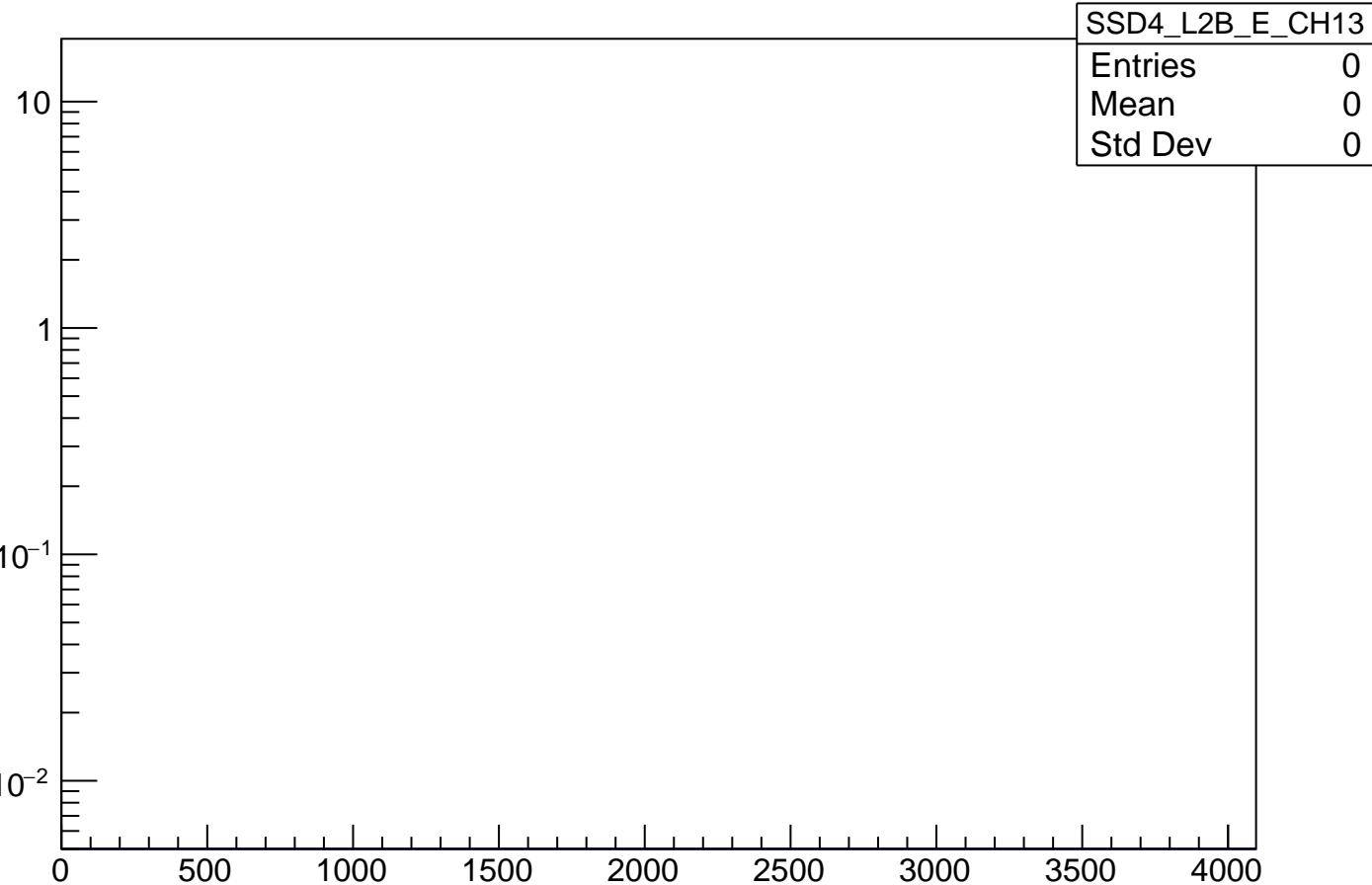
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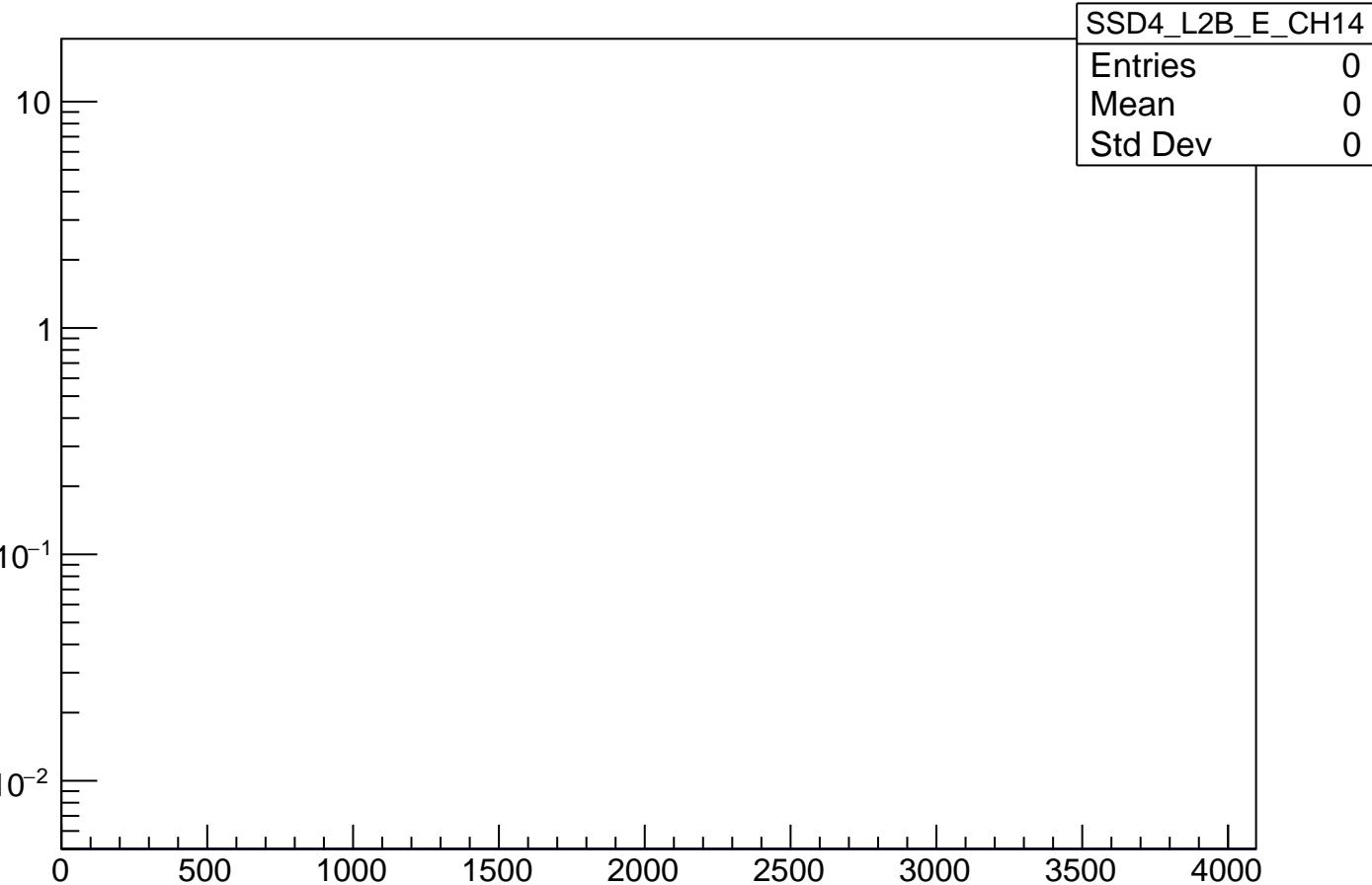
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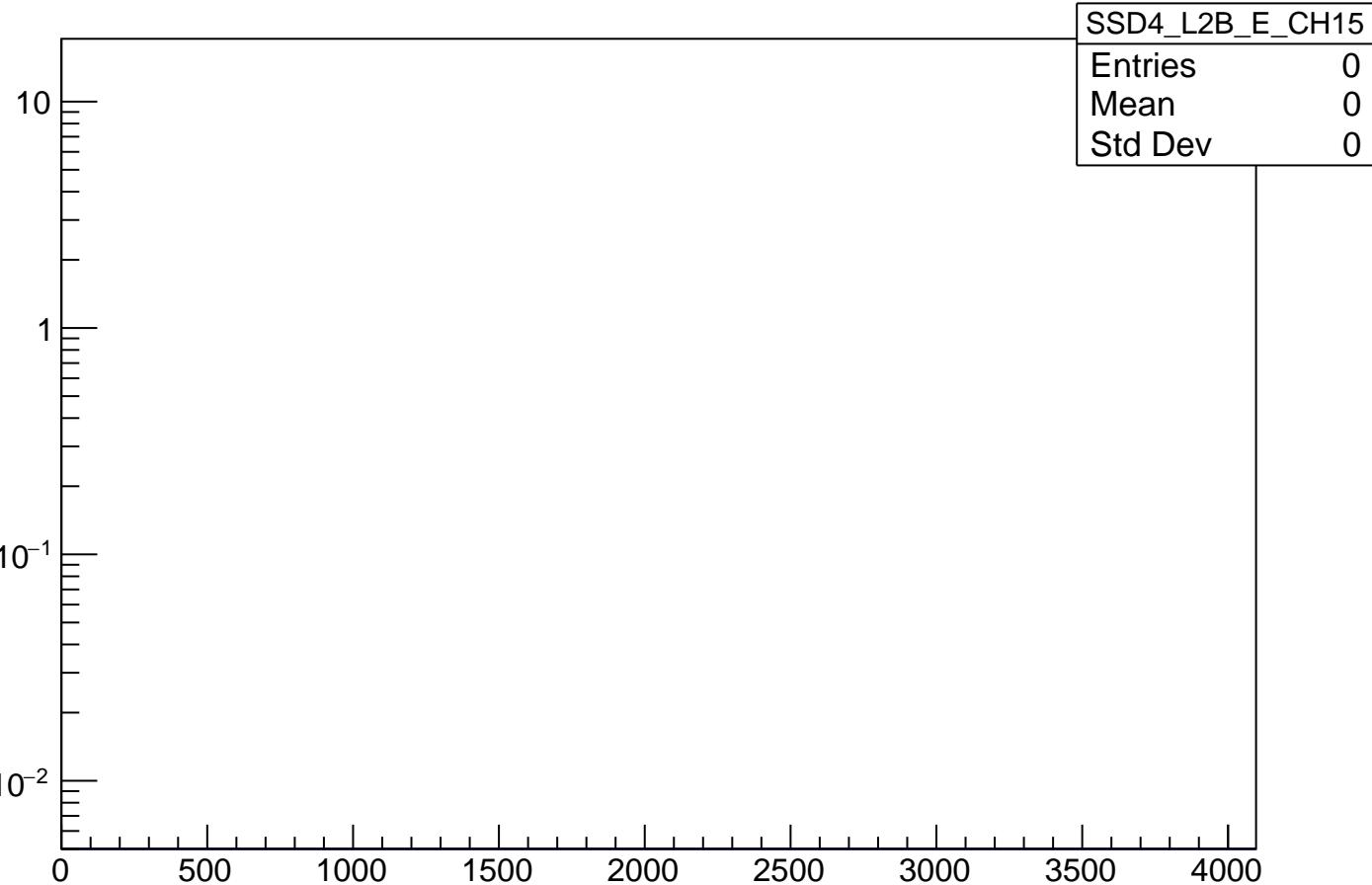
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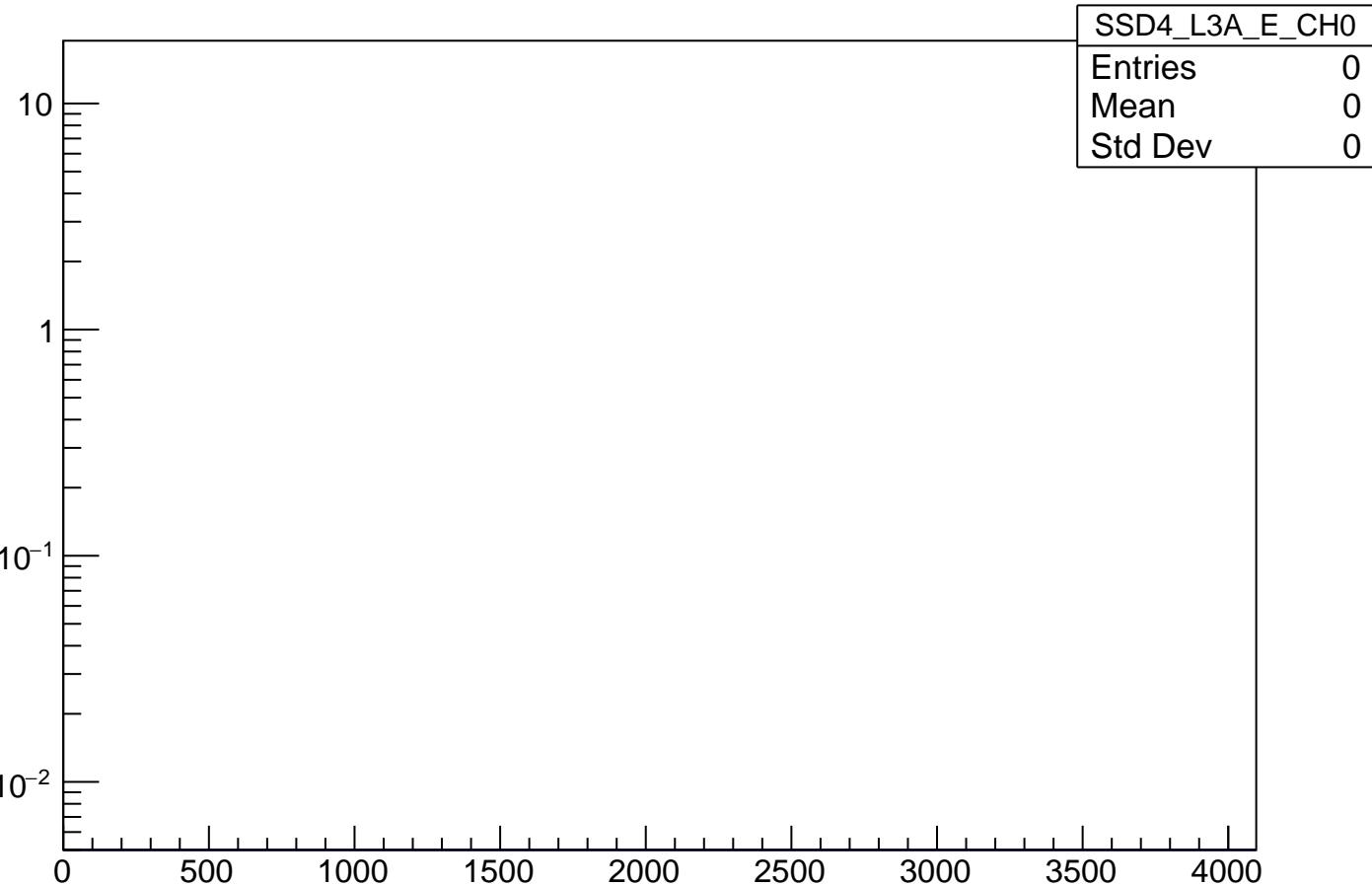
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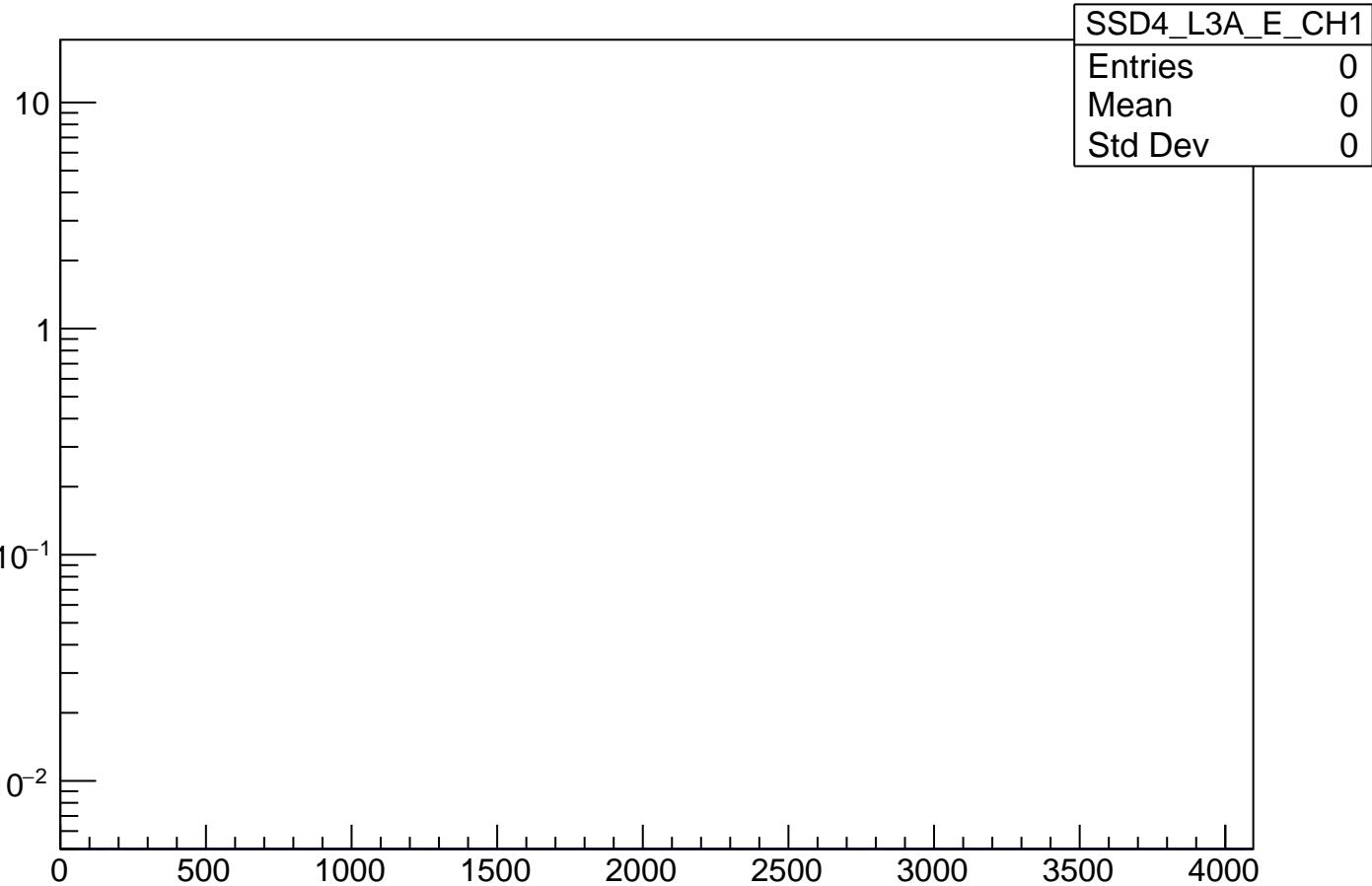
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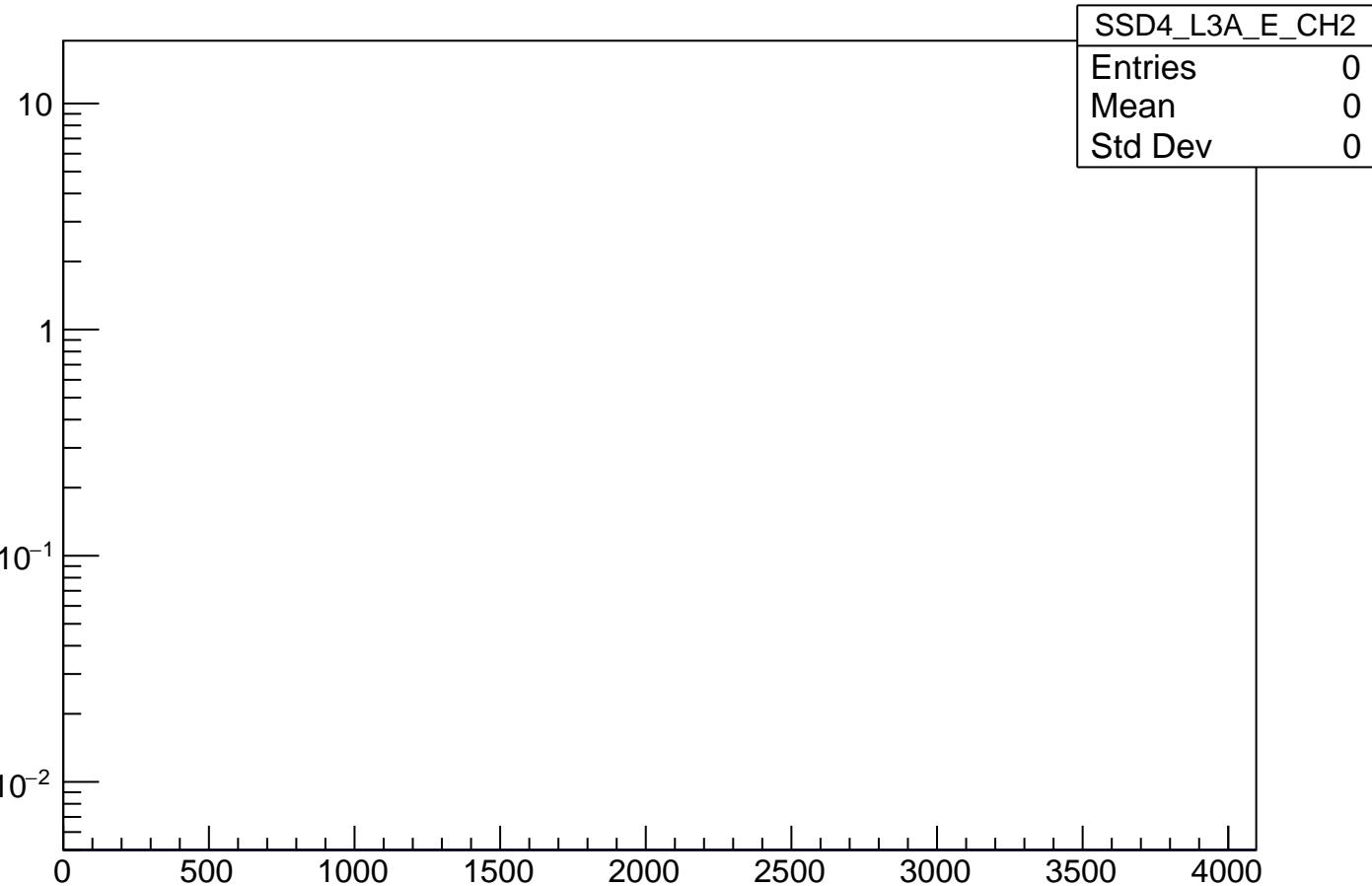
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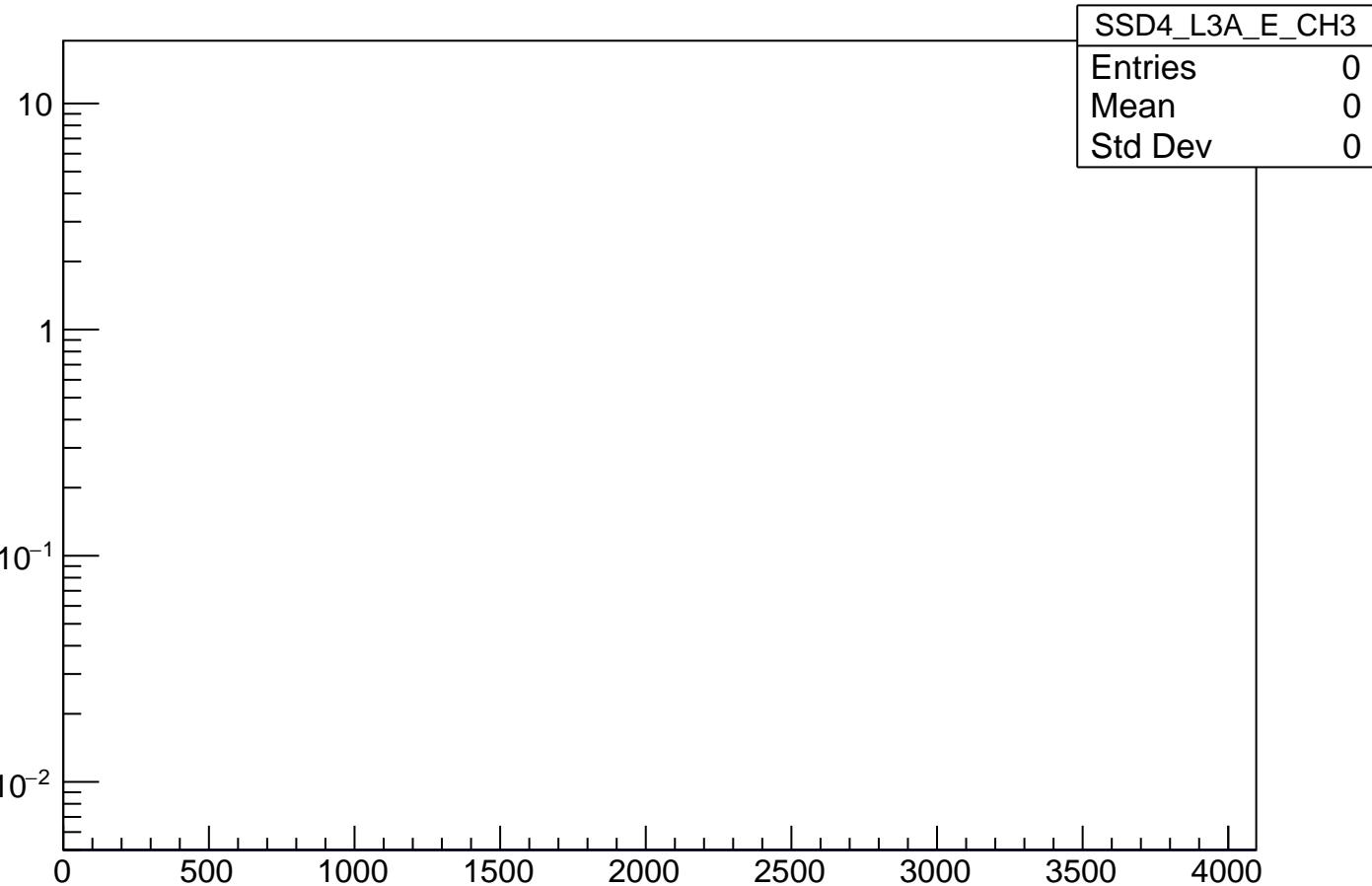
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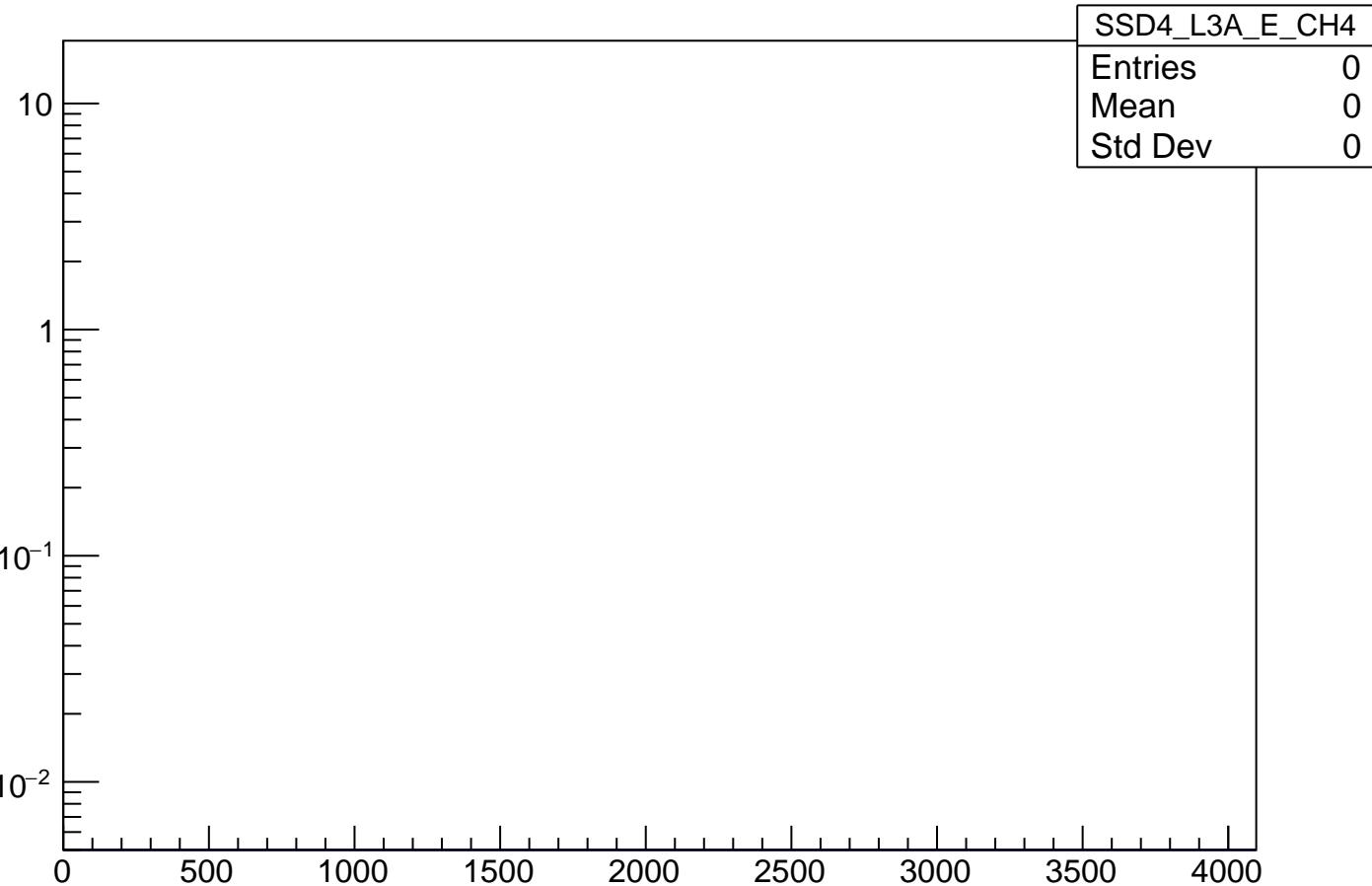
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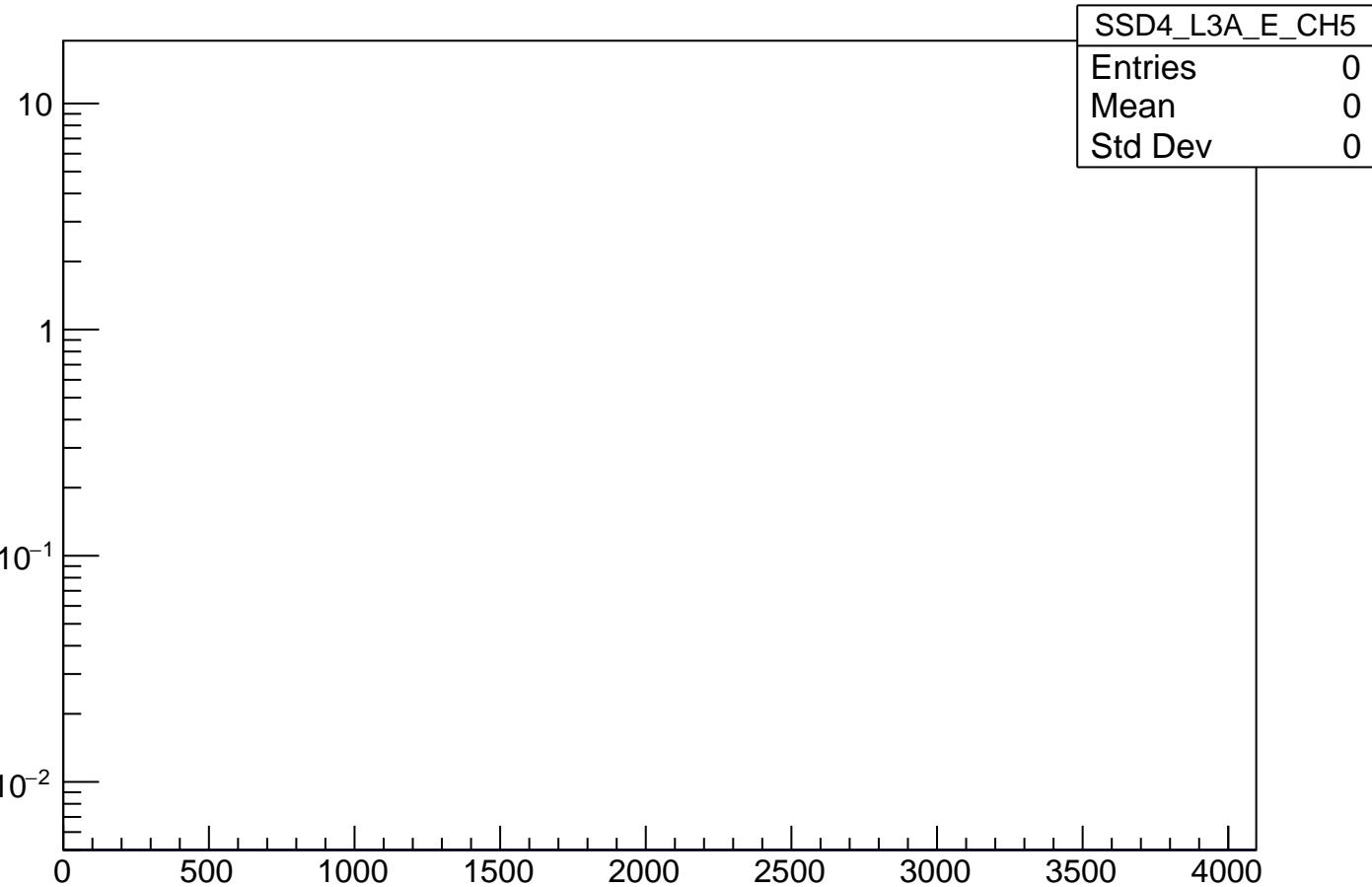
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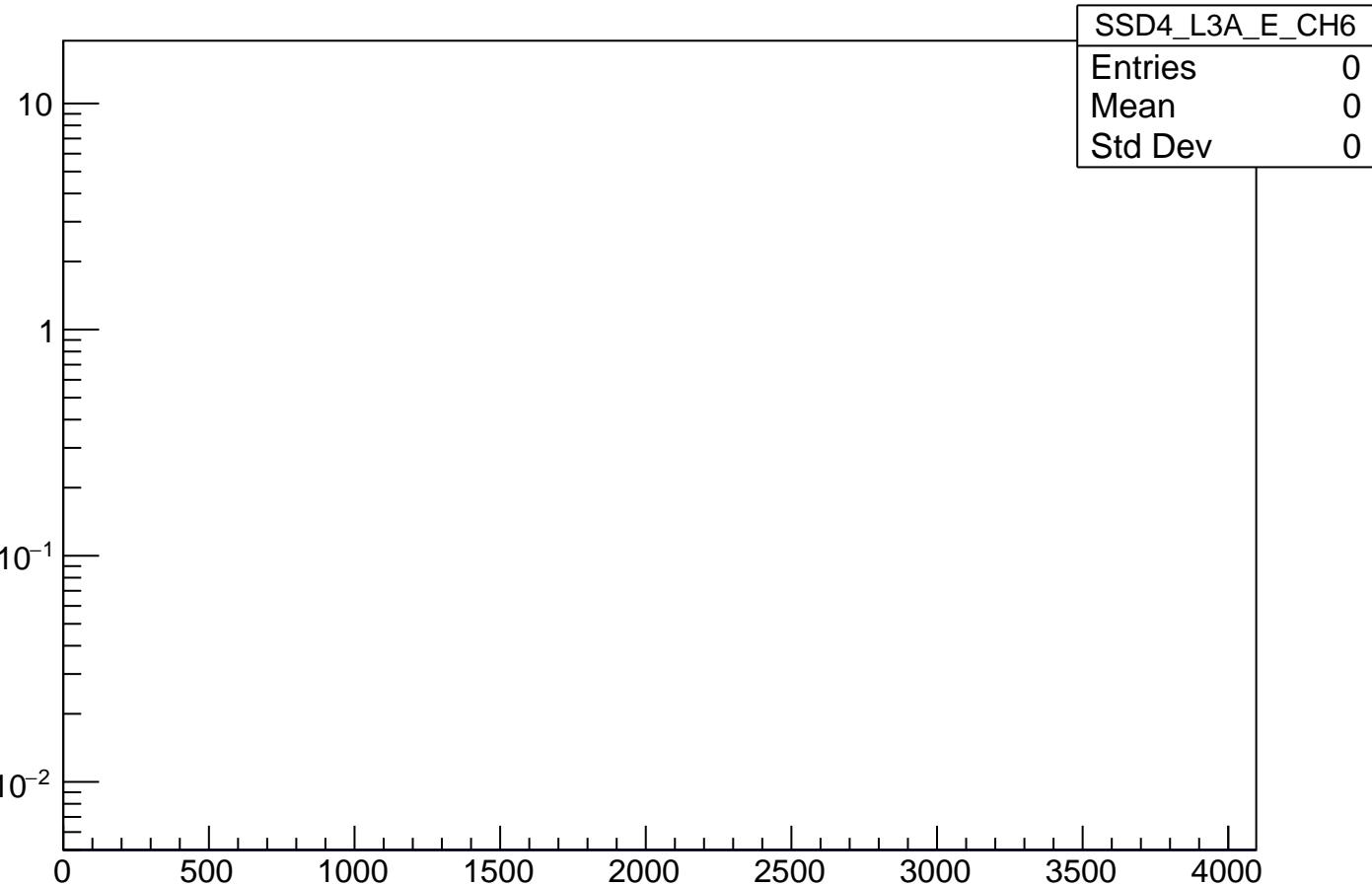
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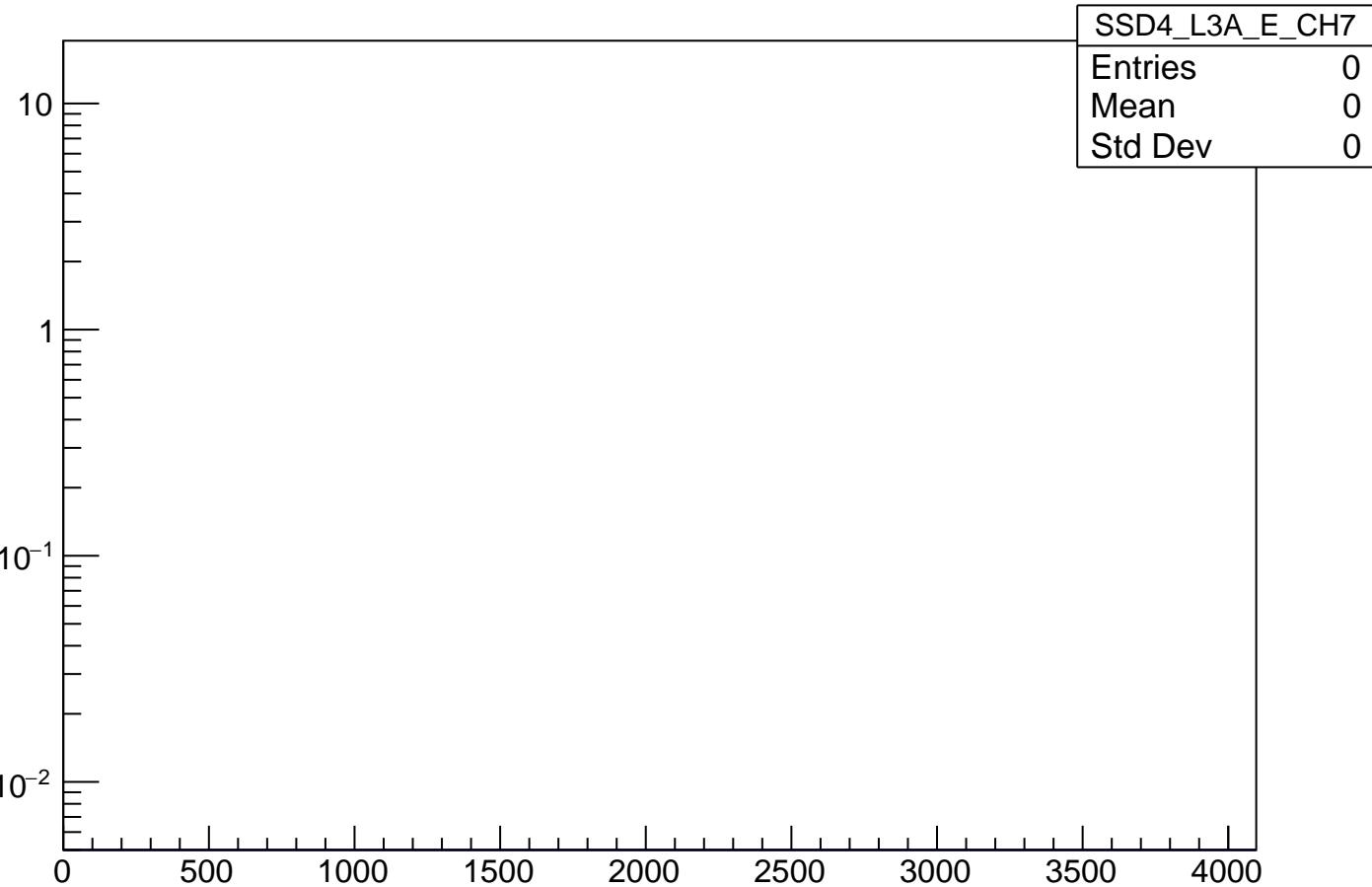
SSD4_L3A_E_CH5



SSD4_L3A_E_CH6



SSD4_L3A_E_CH7



SSD4_L3A_E_CH8

