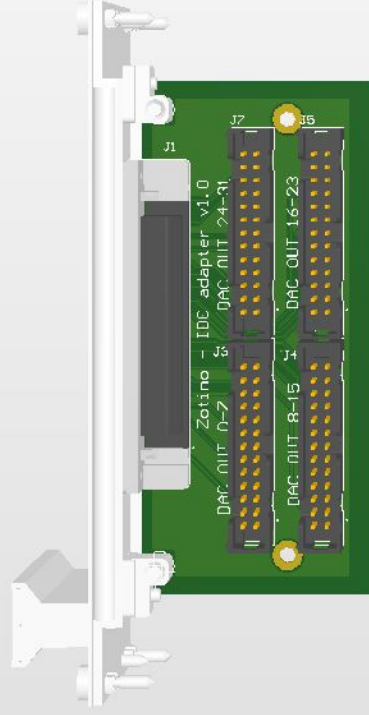
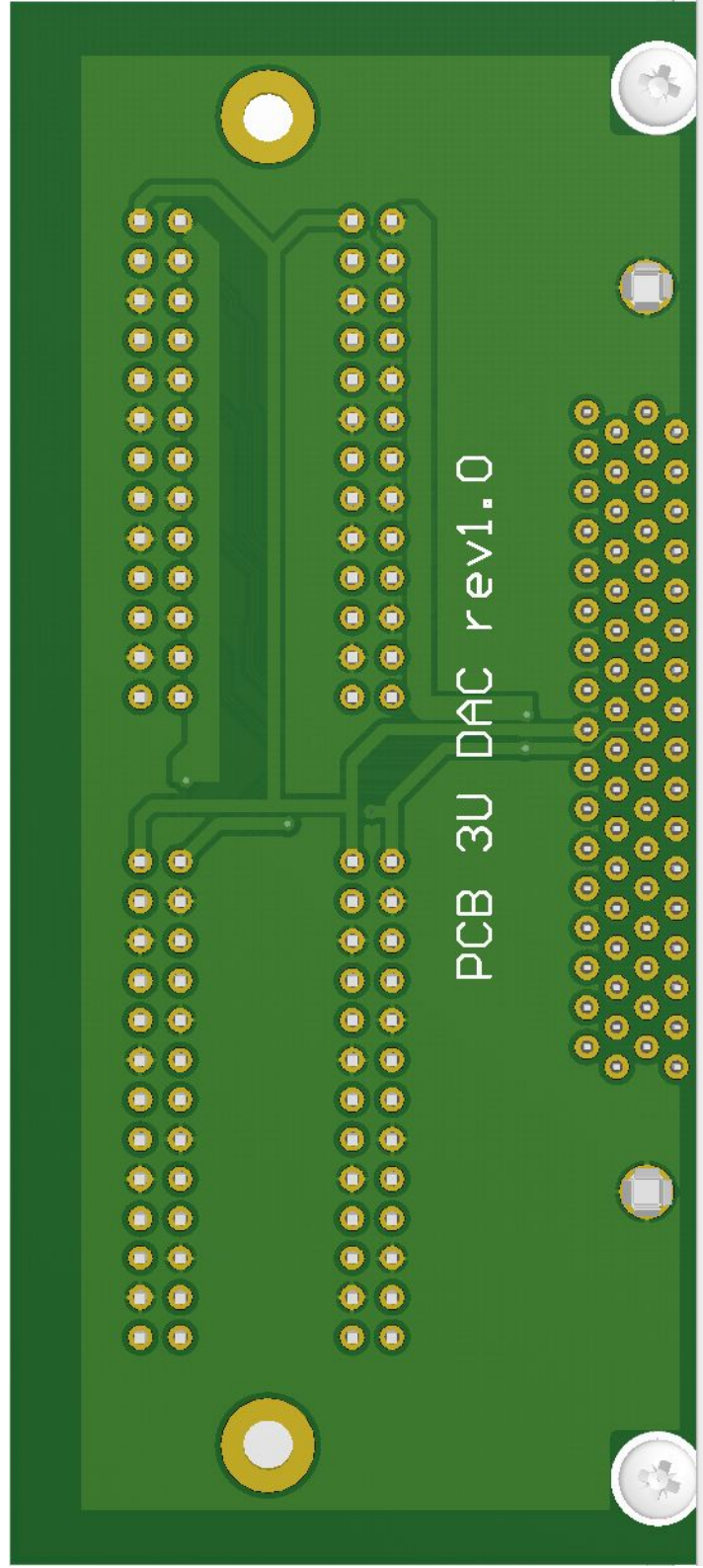


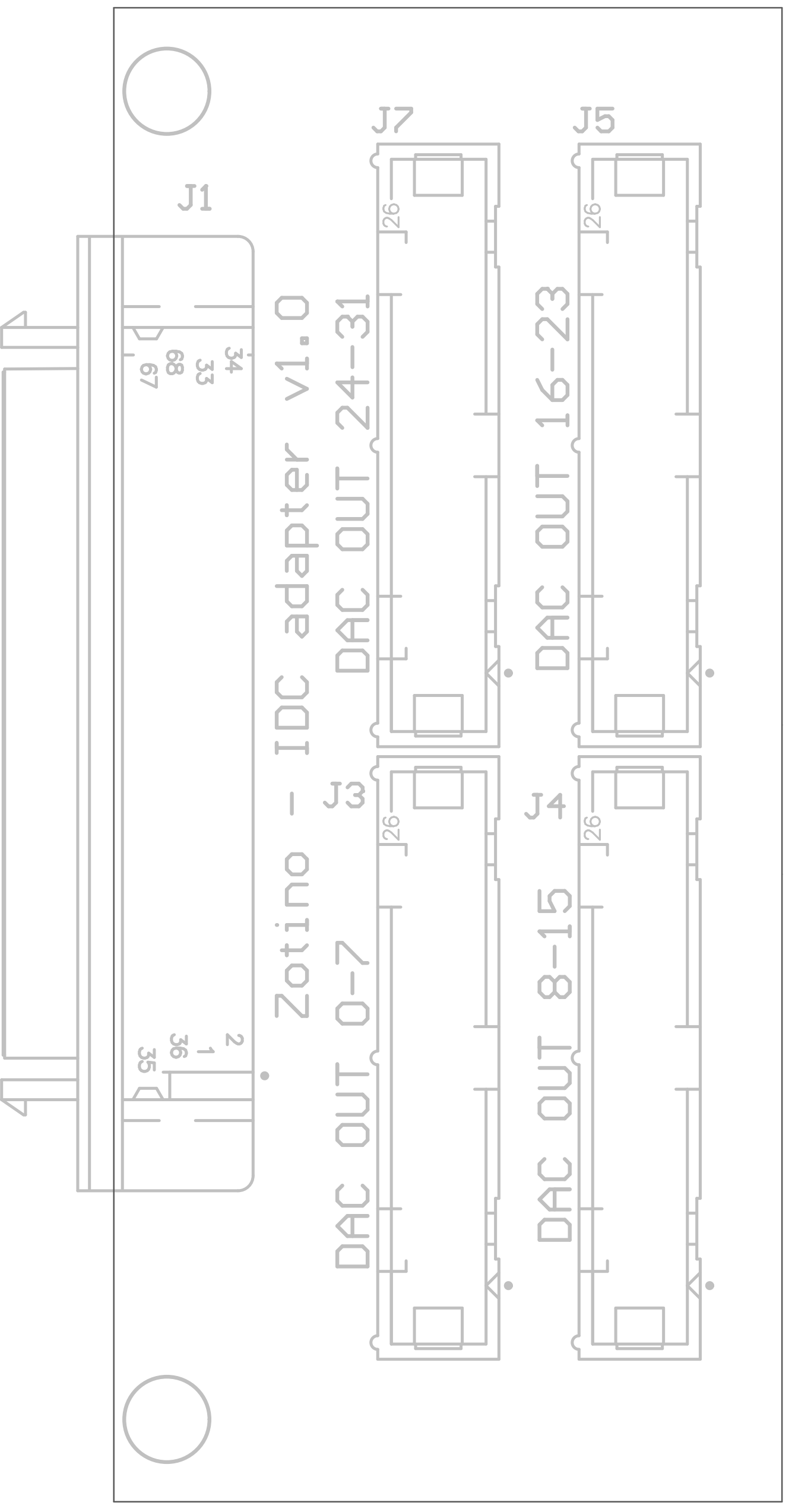
Copyright WUT 2017

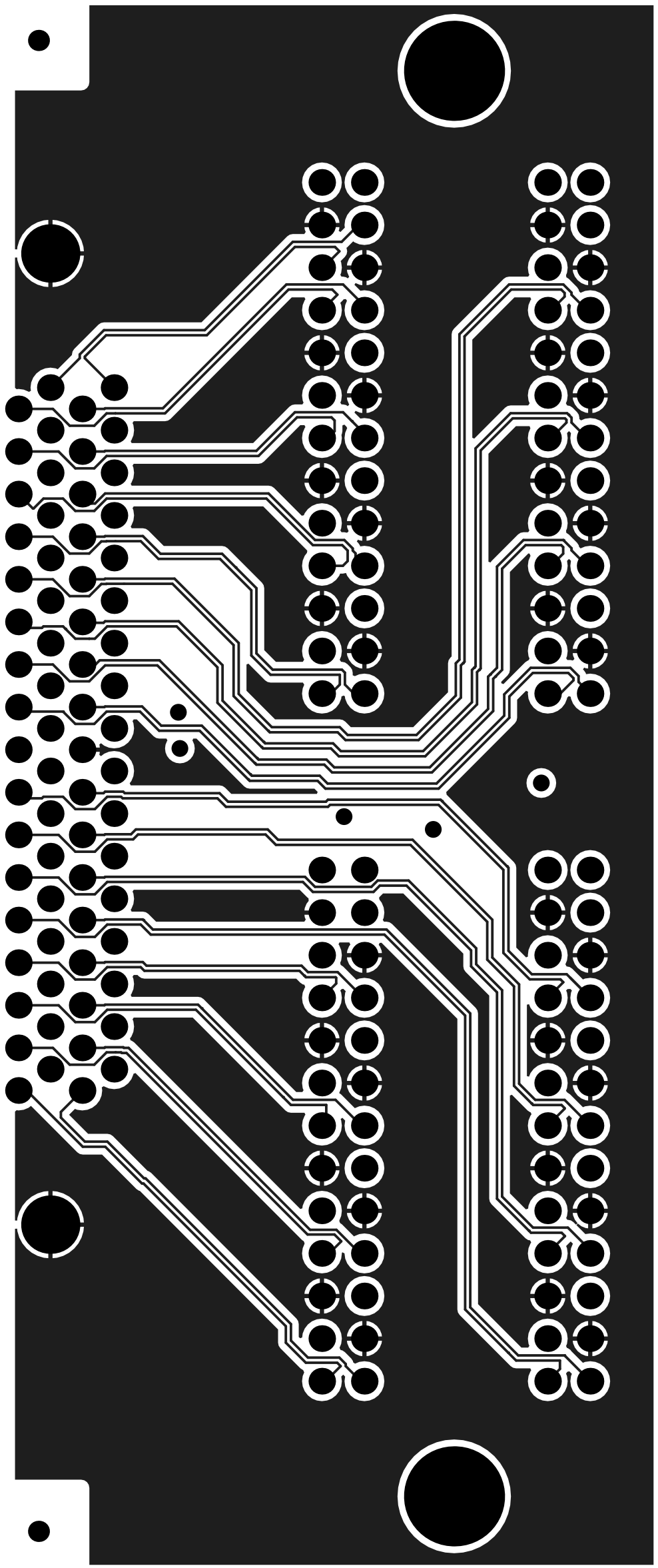
This documentation describes Open Hardware and is licensed under the CERN OHL v1.1. You may redistribute and modify this documentation under the terms of the CERN OHL v1.1. (<http://ohwr.org/CERNOHL>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v1.1 for applicable conditions.

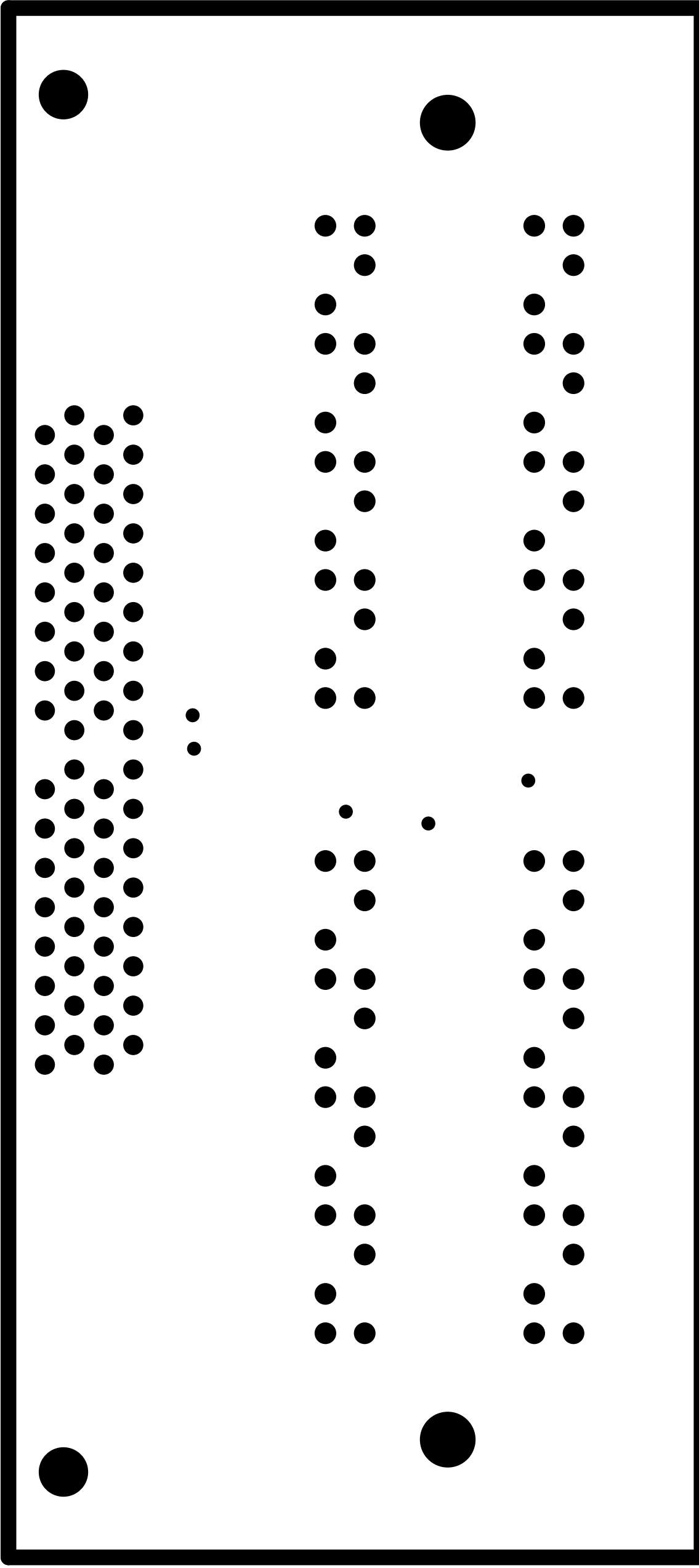
Project/Equipment		ARTIQ/SINARA	
Document  Cannot open file D:\Dropbo x\DESIGN S\MTCA projects\SI	<div>IDC to BNC adapter</div>		Designer G.K.
			Drawn by G.K.
			Check by -
			Last Mod. -
			File Zotino_HD68_Adapter.SchDoc
Print Date 14.01.2018 23:29:37		Sheet 3 of 7	Size A3
Warsaw Univeristy of Technology Nowowiejska 15/19		ISE	Rev -
		ARTIQ	

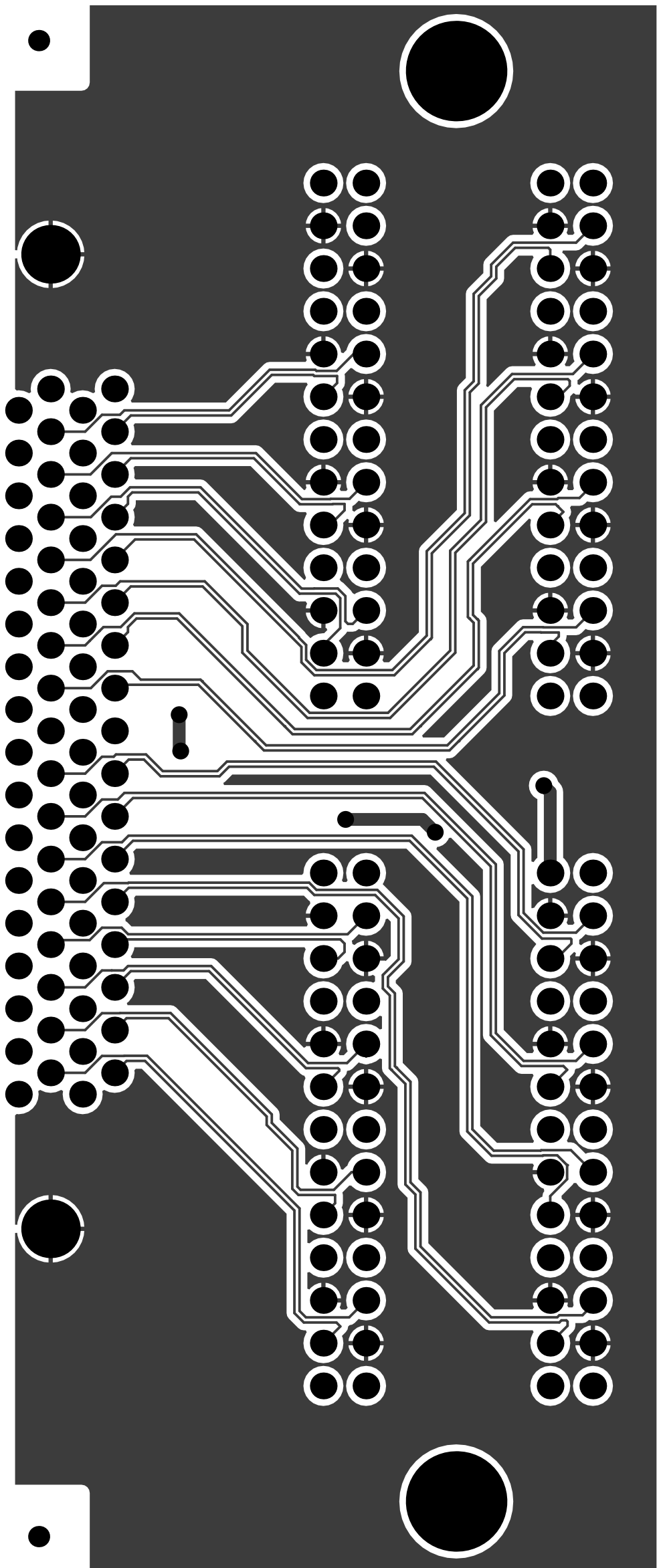


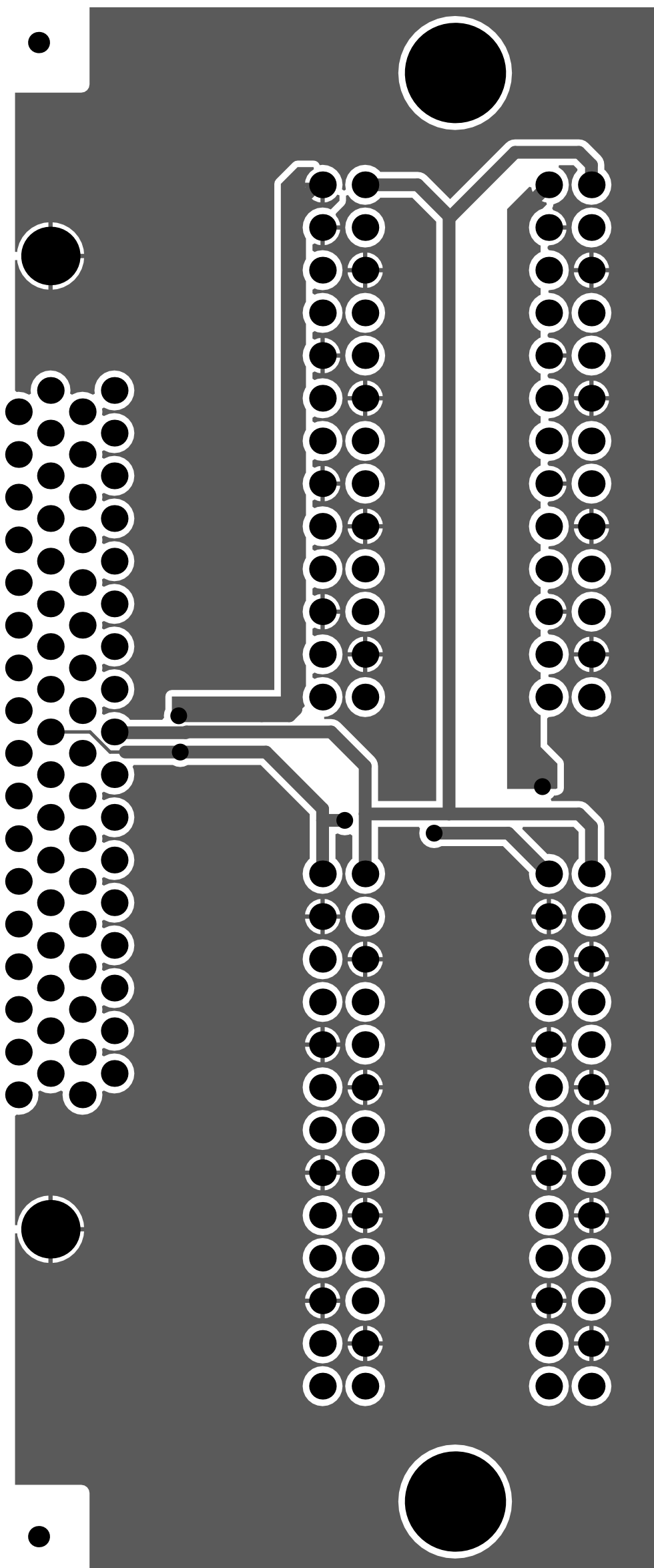














0.1v91 DAC UC BCD