Quiz # 02   **Course Title:** Peripherals, Interfacing & Embedded Systems   **Total Marks:** 15
Course ID: CSE - 4619   **Time:** 25 Mins.

……………..……………………………………………………………………………………………

Q1. Draw the timing diagram of a 8255 PPI, when its Port-A is used as input port having interrupt **08** request with a 8-bit data of all '0' and Port-B is used as output port having no interrupt requested with a 8-bit data of all '1'; If Port-C is in the handshaking mode then also derive the command register value.

Q2. Suppose, your student ID is $180041\underline{X_1X_2X_3}$. Now, for CAN Bus Protocol consider two **07** component nodes N_1 having 11-bit Identifier $\underline{X_1X_2X_3}$ and N_2 having 11-bit Identifier $\underline{X_3X_2X_1}$. Using the identifier values draw the timing diagram for CSMA/CD Non-Destructive Arbitration (NDA) concept of CAN. Your answer should clearly state the *bit-by-bit* scenario to show which node becomes the dominant to access the CAN Bus and transmit data first.

………………………………………………….……………………………………………………………