Performance Analysis of N-Body Simulations
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Contents:
Introduction:

In this report, we are presenting a performance analysis of a 3-Body simulation which has a complexity of O(N^2) since the force applied on each particle depends on other particles, therefore this simulation has a an arithmetic intensity of: ….

We will start by presenting our kernels, then we will present our target architecture and the methodology used during our benchmark and optimizations made for each version.

This simulation is composed of three kernels:

In the first kernel we calculate the necessary arguments to calculate Newton's law, then it calculates the net force, this kernel has a complexity of O(N) and an arithmetic intensity which is originated from: 11 loads + 5 stores + 3 multiplication +3 Addition..

In this kernel we have multiple multiplications, divisions and a square root.

This first kernel is nested into our second kernel, which is where the complexity of O(N^2) is coming from. Here, we notice that a softening operation is applied in order to minimize the effects of singularities in our equation, therefore we are altering our system.

The second kernel is where we calculate the final velocities of our particles, therefore it is composed of a dot prod which is an operation heavily used in real life HPC applications.

This is the reason we have chosen to benchmark this simulation since it includes multiple real life operations we come across during our applications.

And the last kernel is also a dot prod where we calculate thé final positions of our particles according to théorie vélocities.

Our performance analysis takes into account various aspects that can play on the efficiency of our code, such as: compilation environments, optimization flags, size of our inputs, in which cache line they adapt, what kind of optimizations are made…

Optimizations made:

AoS vs SoA : this optimization aims to have a regular access to our data with regular strides by switching it from array of Structs to struct of arrays.

Alignment: with aligned_alloc we give a signal to our compiler to align our data so that can have regular access to data to increase our efficiency.

 Frequency reduction: unnecessary elements that can be calculated outside of the loop are calculated this way. This method aims to decrease number of calculations, it doesn't play on complexity but it plays on the arithmetic intensity of our programme.

Unroll: compiler is under exploited if we give it scalar calculations. Unrollling our loops helps us to take advantage of our cpu by giving it more work to do at the some time. We have to remind that it is possible only under some conditions, such as independency of the data. And we have to highlight that where we can unroll we can also vectorize our code.

Compiler optimizations:

Compiler optimisations can vectorize our code to get a better performance out of SIMD instructions. They can also in-line some functions to reach them faster.

V0: it's our baseline application. This version applies array of structs, without any alignment,

V1: this version applies stuct of array, without any alignment. Our purpose is to show the

influence of different methods to access the data. By comparing it with V0.

V2: This version applies Array of Struct, with alignment.

V3: This version applies array of structs, without any alignment, and with unrolling.

V4: this version applies struct of arrays, with alignment and unrolling. Here we have to point out that if there's a dependency between the loops we can hardly unroll them. Here we can see the dependency between the first and the second kernel.

V5: this version implements struct of arrays, with alignment, and unrolling, but this time it also implements frequency reduction.

V7: this version is V0 compiled with the vectorize compiler option.

V8: this version is V0 but instead of making a call to pow() function it calculates the power and sqrt within the code. Sqrt is implemented with Lomont reverse squareroot then reversed. Although it is susceptible to have less numerical precision.

V11: This version applies SIMD AVX2 instructions with fma instructions and structs of arrays with alignment. Only the third kernel is transformed to assembly, to keep the numerical accuracy. The rest is same as V1.

Additional versions that we have prepared:

V12: it's the same as V11 but here all the kernels are transformed into assembly codes. A numerical error that we were unable to traceback is committed therefore this version is not included in our benchmarks.

V13: although this version should be imitating V11 with the only difference being the usage of multiply + add instructions instead of fma instructions, it doesnt give the same results, a numerical error has been committed that we were unable to traceback during our experiments.

Challenges faced:

Unfortunately our script to generate data from all the variations that we have created takes more than an hour to finish. At this moment it's still running and it's been two hours and it's not even half way done. Although I am unable to understand why it takes that long apparently number of particles for different cache sizes plays a lot on our programme. And the fact that we are so forgetful that we had forgotten to put BW measurements, it's a painful lesson to learn and an example for future students.

From now on I'll re run the script with less particles so that I can have some plots to make examinations from.

Future Work:

Since we have used different SIMD instructions such as instructions for FMA or simple multiply add, or different methods to calculate square root etc our calculations are susceptible to numerical differences/errors. Therefore examining numerical stability in our calculations is a must for our future work.

We had written two versions of the same code, one with FMA instructions the other one with multiply-add. But we were unable to trace back the numerical error we had committed during our calculations. We aim to correct those mistakes and make a comparaison on number of calculations per second between these two methods.

To reduce timing biaises, we could apply different timing measurements such as elapsed time with clock get time etc.

Since some of our loops are independent from each other we could parallaxe them with openMp directives.