IEEE International Conference on Cluster ComputingNewport Beach, California, USA October 8-11, 2001

DECK-SCI: High-Performance Communication and Multithreading for SCI Clusters

Federal University of Rio Grande do Sul Institute of Informatics Group of Parallel and Distributed Processing Porto Alegre, RS, Brazil Catholic University of Rio Grande do Sul High-Performance Research Center Porto Alegre, RS, Brazil

Fábio Abreu Dias de Oliveira

Rafael Bohrer Ávila Marcos Ennes Barreto Philippe Olivier Alexandre Navaux César Augusto Fonticielha De Rose

Outline

- ⇒ Introduction and motivations
- ⇒ Overview of DECK-SCI
- ⇒ Key characteristics of DECK-SCI's communication protocols
- ⇒ Description and evaluation of the communication protocols
- ⇒ Concluding remarks

Introduction and motivations (1)

- ⇒ SCI (Scalable Coherent Interface) as a high-performance network for clusters
 - ⇒ low latency (direct access to remote memory addresses)
 - communication via CPU loads and stores
 - ⇒ high bandwidth
- ⇒ Several efforts have been made towards offering messagepassing libraries for SCI clusters
 - \Rightarrow SCIPVM
 - ⇒ PVM-SCI
 - \Rightarrow ScaMPI
 - ⇒ SCI-MPICH
 - ⇒ CML (Common Messaging Layer)

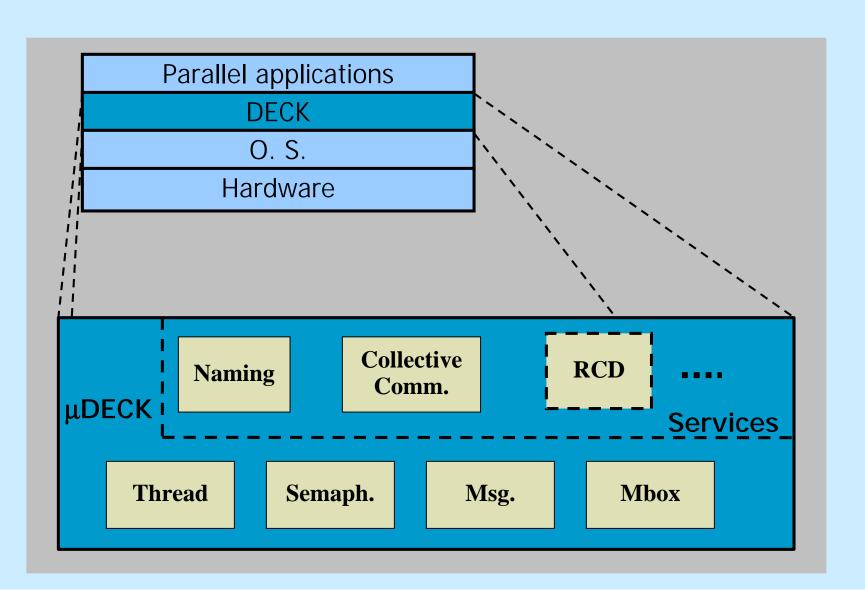
Introduction and motivations (2)

- ⇒ Previous implementations of DECK
 - ⇒ Fast Ethernet
 - \Rightarrow Myrinet
- ⇒ Main motivations for developing DECK-SCI
 - ⇒ to allow the integration between Myrinet and SCI clusters
 - to support to the "MultiCluster" model
 - ⇒ to devise a new library for programming SCI clusters based on
 - very efficient message-passing
 - multithreading at the API level

Overview of DECK (1)

- ⇒ DECK: Distributed and Execution Communication Kernel
- ⇒ SPMD model + multithreading
- ⇒ Basic abstractions
 - \Rightarrow messages and mail boxes
 - point-to-point communication
 - \Rightarrow threads and semaphores

Overview of DECK (2)



Overview of DECK-SCI

DECK Programming Interface SISCI API MMX User space Instructions **SCI** Driver Kernel space **SCI** Hardware

User space

Devised and implemented communication protocols

⇒ Objectives

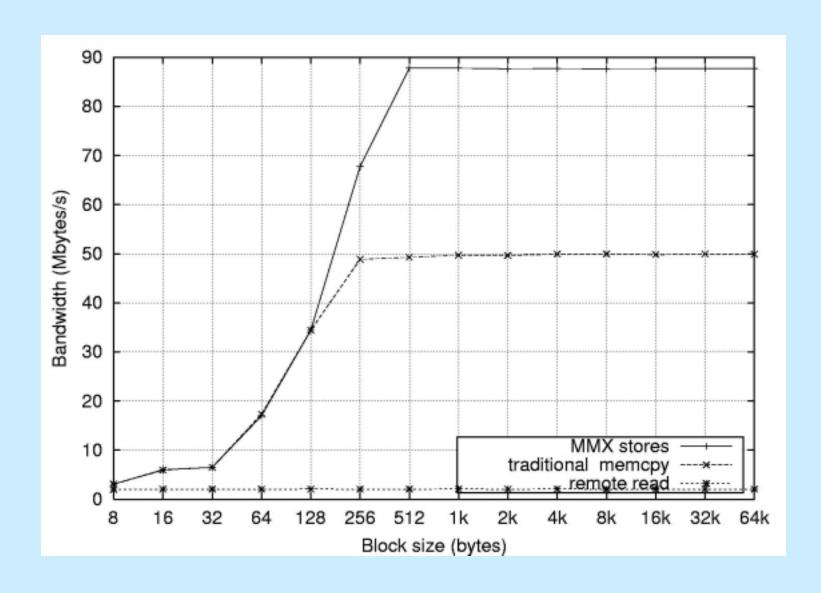
- ⇒ to compose a communication kernel aimed at fully exploiting the high-performance capabilities of the SCI network
- ⇒ to obtain a performance near the limits of the SCI hardware
 - low latency for short messages
 - high bandwidth for large ones

⇒ Hardware platform

- ⇒ Cluster composed of four PCs equipped with dual Pentium-III (500 MHz), 256 Mbytes of RAM and SCI NICs for the PCI bus (32 bits and 33 MHz)
- ⇒ Three different protocols transparently chosen according to the size of the message to be sent

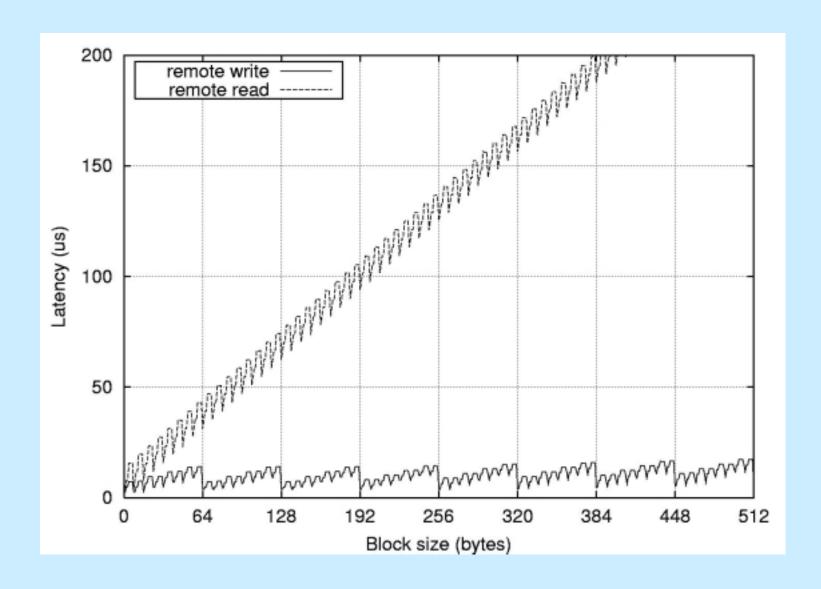
Key features of the implemented communication protocols (1)

- ⇒ Communication strictly accomplished by remote writes
 - \Rightarrow write-only protocols
- ⇒ Use of MMX instructions for message transfers, in order to increase the maximum achievable bandwidth, by
 - \Rightarrow avoiding the traditional memcpy routine
 - \Rightarrow generating a write burst on the PCI bus



Key features of the implemented communication protocols (2)

- ⇒ Exploitation of the PCI-SCI adapter's stream buffers
 - ⇒ eight stream buffers with 64 bytes
 - pipeline with eight stages

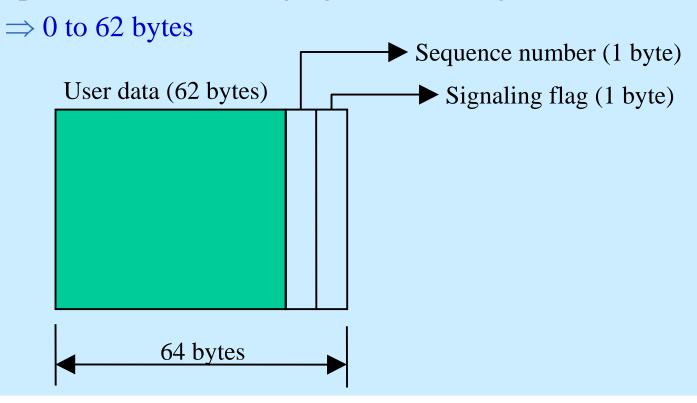


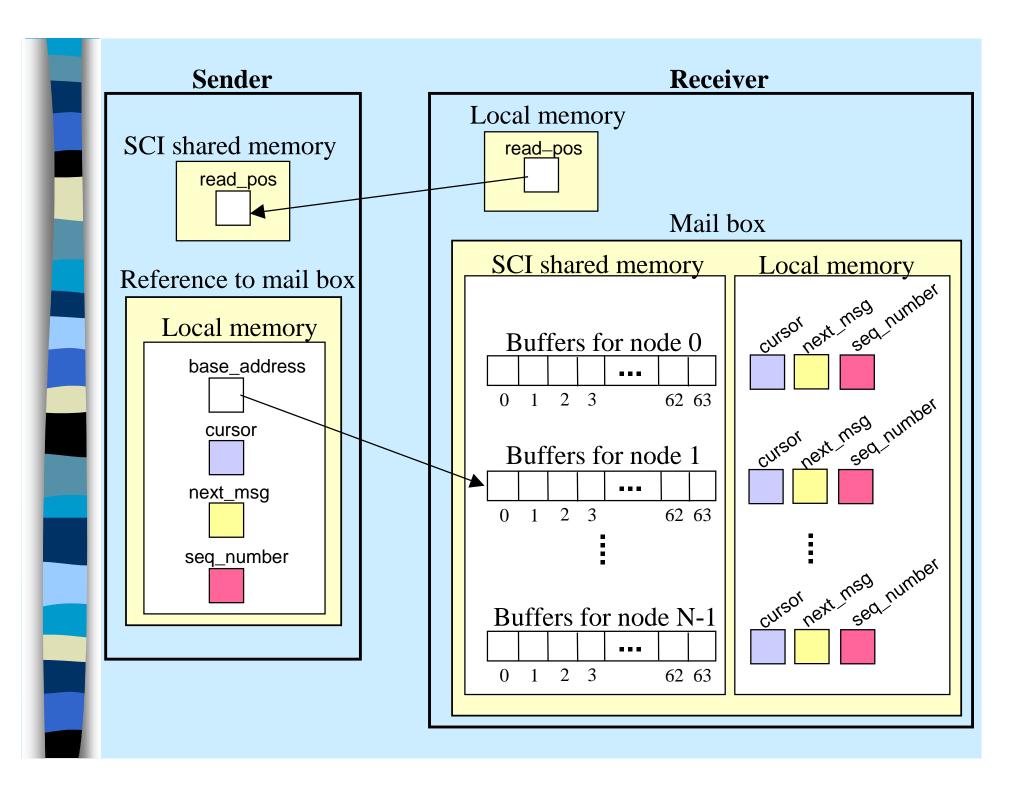
Key features of the implemented communication protocols (3)

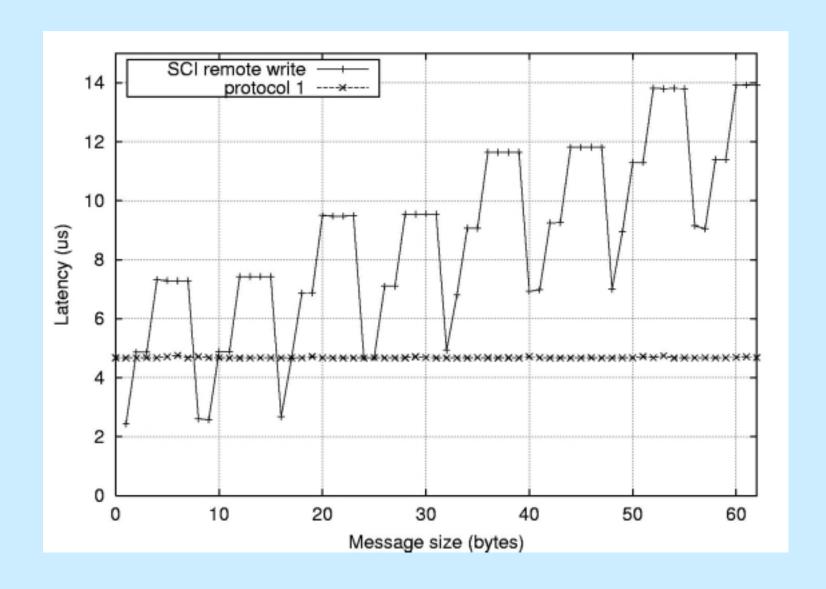
- ⇒ Polling-based message passing
 - ⇒ avoid interrupts for signaling the arrival of a message
- ⇒ Thread-awareness and thread safety

"Protocol 1": short messages

- ⇒ Objective: very low-latency for short messages
 - \Rightarrow minimal overhead
 - ⇒ signaling and communication using a single SCI packet
- ⇒ Specialized in exchanging short messages

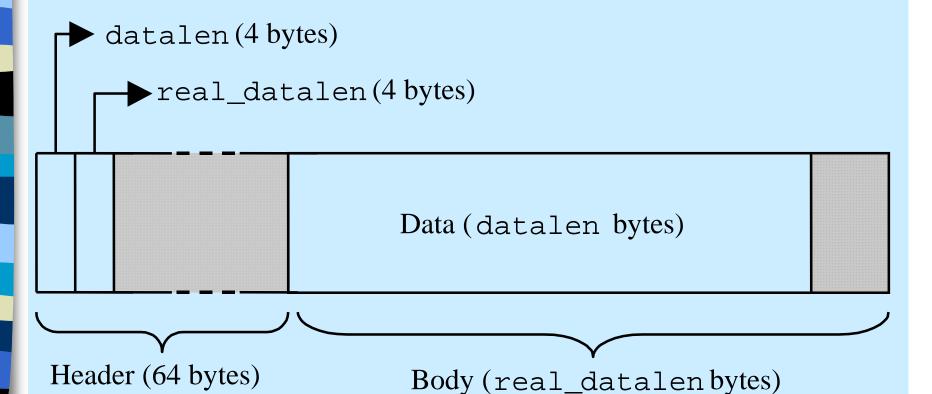


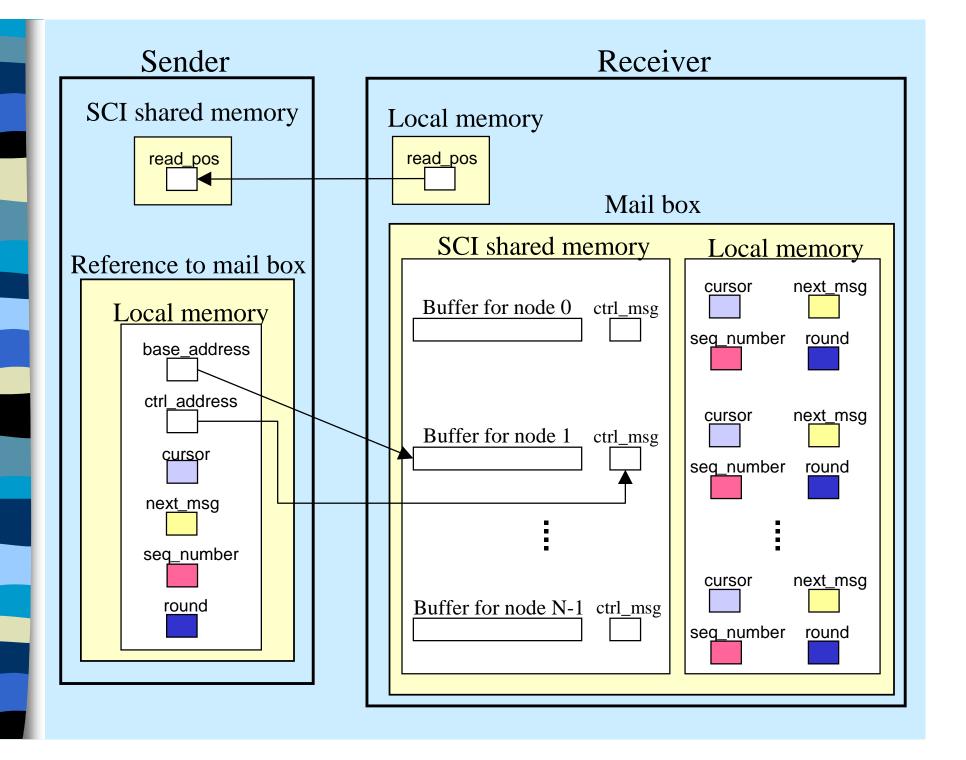




"Protocol 2": general-purpose

- ⇒ It handles messages greater than 62 bytes
- ⇒ Signaling with an additional SCI packet

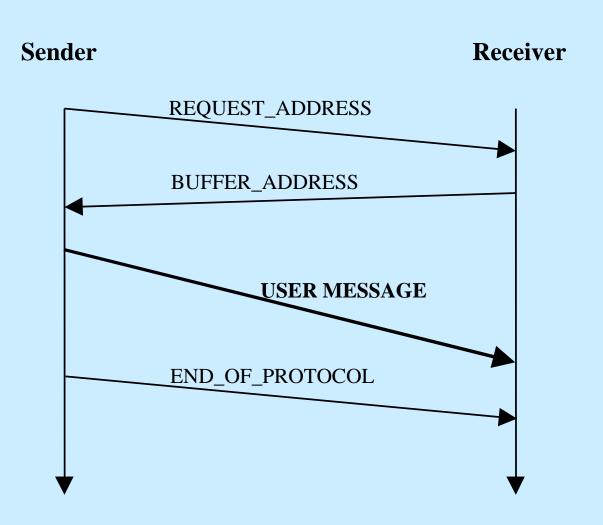


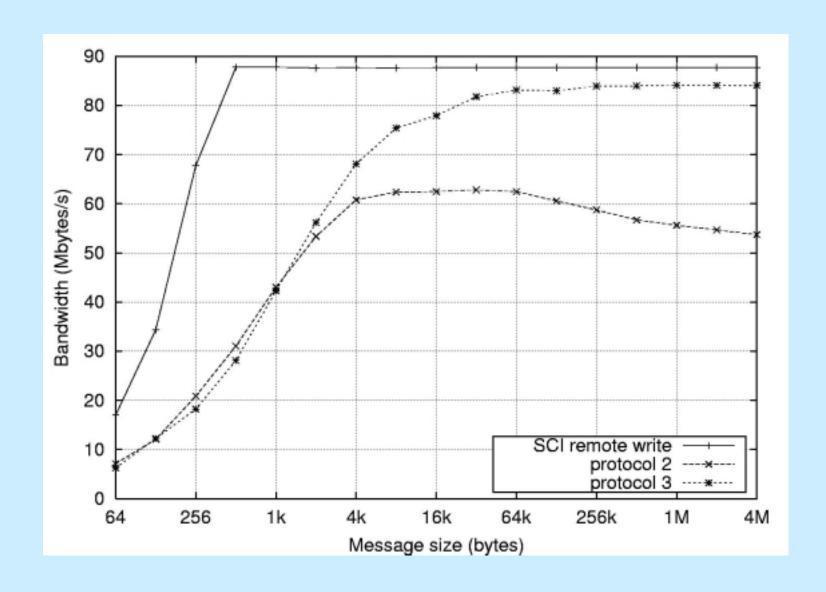


"Protocol 3": zero-copy mechanism (1)

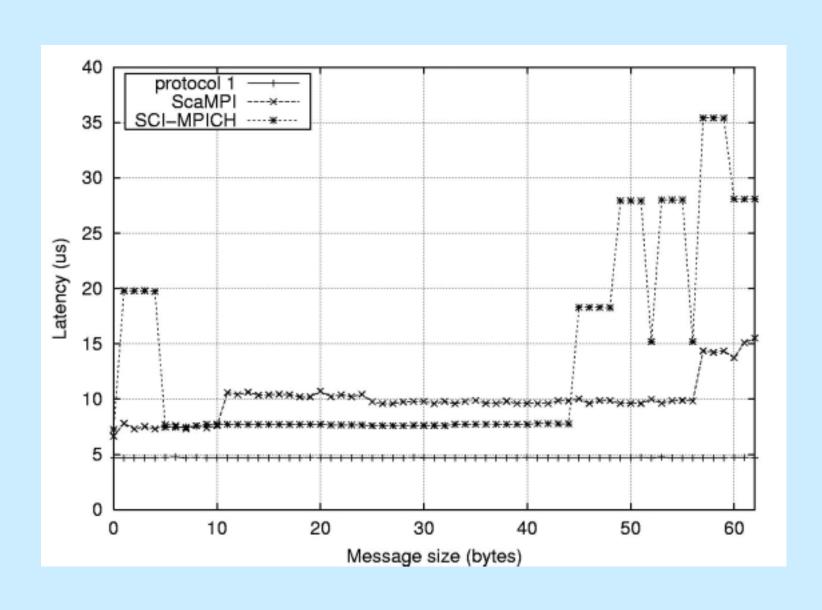
- ⇒ Objective
 - ⇒ to get as much as possible from the SCI network, boosting the bandwidth near the limits imposed by the SCI hardware
- ⇒ Messages are directly sent to the user buffer, without additional data movement into local memory
- ⇒ Handshaking between sender and receiver
 - ⇒ synchronous protocol
 - ⇒ there is no need of flow control

"Protocol 3": zero-copy mechanism (2)

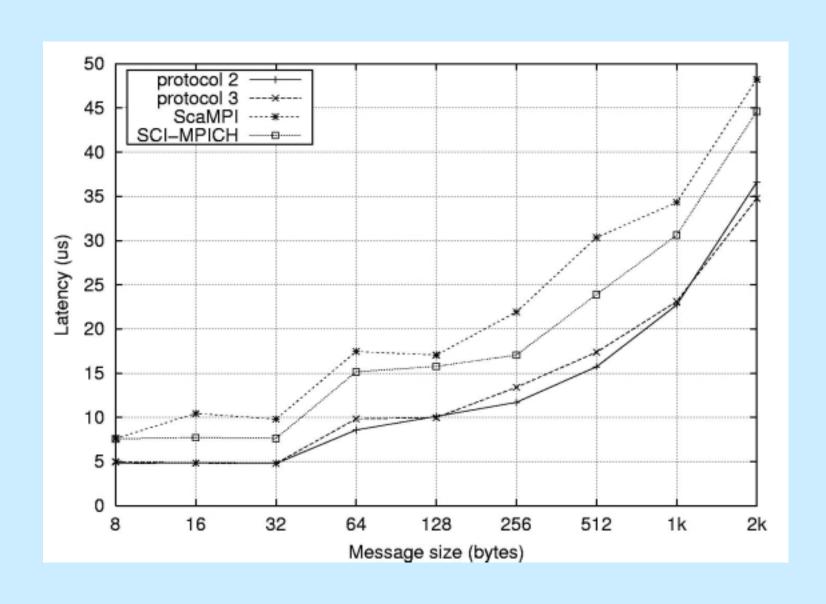




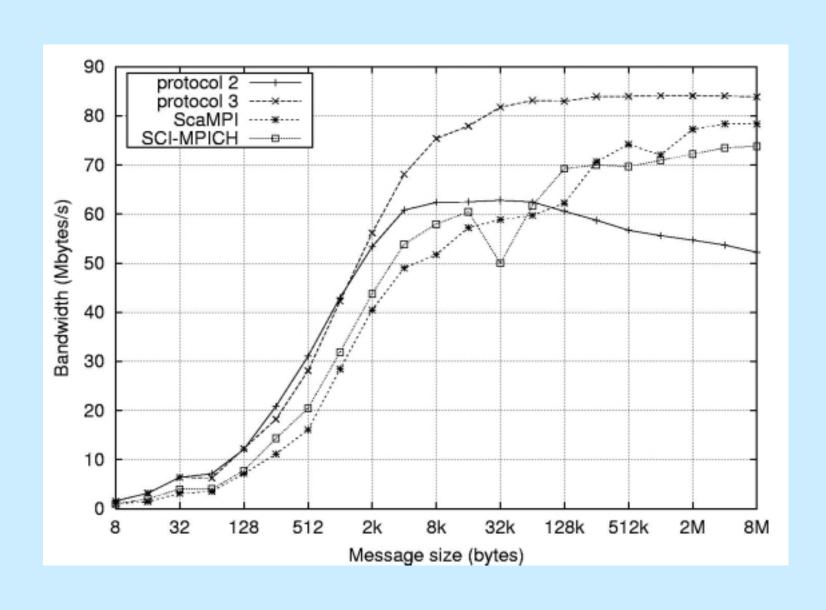
DECK-SCI, SCI-MPICH and ScaMPI (1)



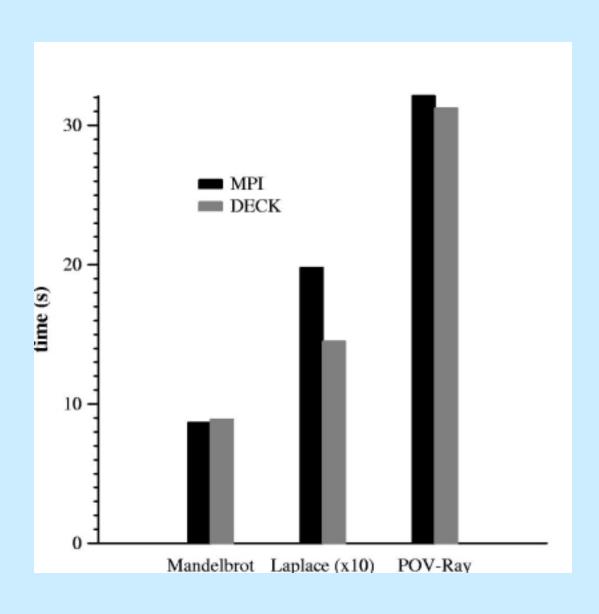
DECK-SCI, SCI-MPICH and ScaMPI (2)



DECK-SCI, SCI-MPICH and ScaMPI (3)



DECK-SCI and **ScaMPI**



Concluding remarks (1)

Library	Maximum bandwidth	Use of the maximum SCI bandwidth
DECK-SCI	84.12 Mbytes/s	95.9 %
ScaMPI	78.35 Mbytes/s	89.3 %
SCI-MPICH	73.80 Mbytes/s	84.1 %

Library	Minimal latency
DECK-SCI	4.66 μs
ScaMPI	6.63 µs
SCI-MPICH	$7.26~\mu_{\rm S}$

Concluding remarks (2)

- ⇒ Gains obtained with raw communication
 - ⇒ maximum bandwidth
 - gain of 7,36% compared to ScaMPI
 - gain of 13,98% compared to SCI-MPICH
 - ⇒ minimal latency
 - reduction of 29,71% compared to ScaMPI
 - reduction of 35,81% compared to SCI-MPICH

Concluding remarks (3)

- ⇒ Results with applications show that DECK-SCI performs at least as well as ScaMPI
- ⇒ Another alternative for SCI programming
 - ⇒ integration of communication and multithreading

Contact information

Fábio Abreu Dias de Oliveira fabreu@inf.ufrgs.br

Federal University of Rio Grande do Sul
Institute of Informatics
Group of Parallel and Distributed Processing
http://www-gppd.inf.ufrgs.br