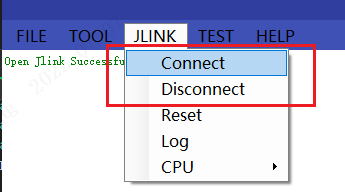
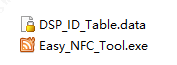
**Print Log For Tester**

**————————————————————————————————**

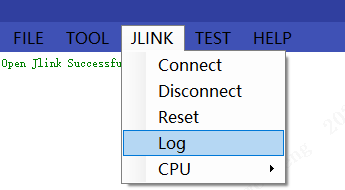
**1.Connect Device With Jlink**



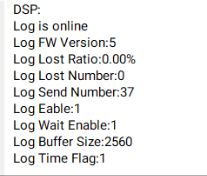
**2.Put “*DSP\_ID\_Table.data*” of FW in work Directory。**



**3.Open Log Form**



**4.Check Log Status**



**————————————————————————————————**

**Add Code For Developer**

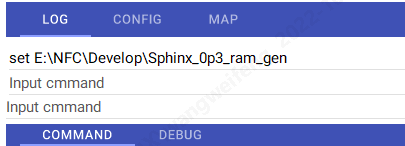
**————————————————————————————————**

1. **Add printf\_wilf.c/.h To Dsp Project**
2. **Call printf\_rtt to output log**

* Int Var = 123;
* printf\_rtt(0,main) //ID=0 for function
* Printf\_rtt(1~250,Var) //ID>0 for variable, reserve:251~255

1. **Generate DSP\_ID\_Table.data**

* Set DSP Project Directory at first(input command “Set [DspProjectPath]“)

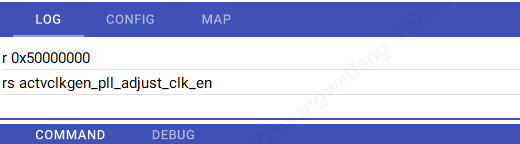


* Open Log Form will Generate **“*DSP\_ID\_Table.data”*** Automatically In Current Work Directory

**————————————————————————————————**

**Read/Write Memory**

* ws register\_name value
* rs register\_name
* w address value
* r address value



**MARK:Enter For Execute**

**————————————————————————————————**

**Map Signal To Pin**

* **For Eaxmple: Map pll lock Signal To AOGPIO1 For Test**

