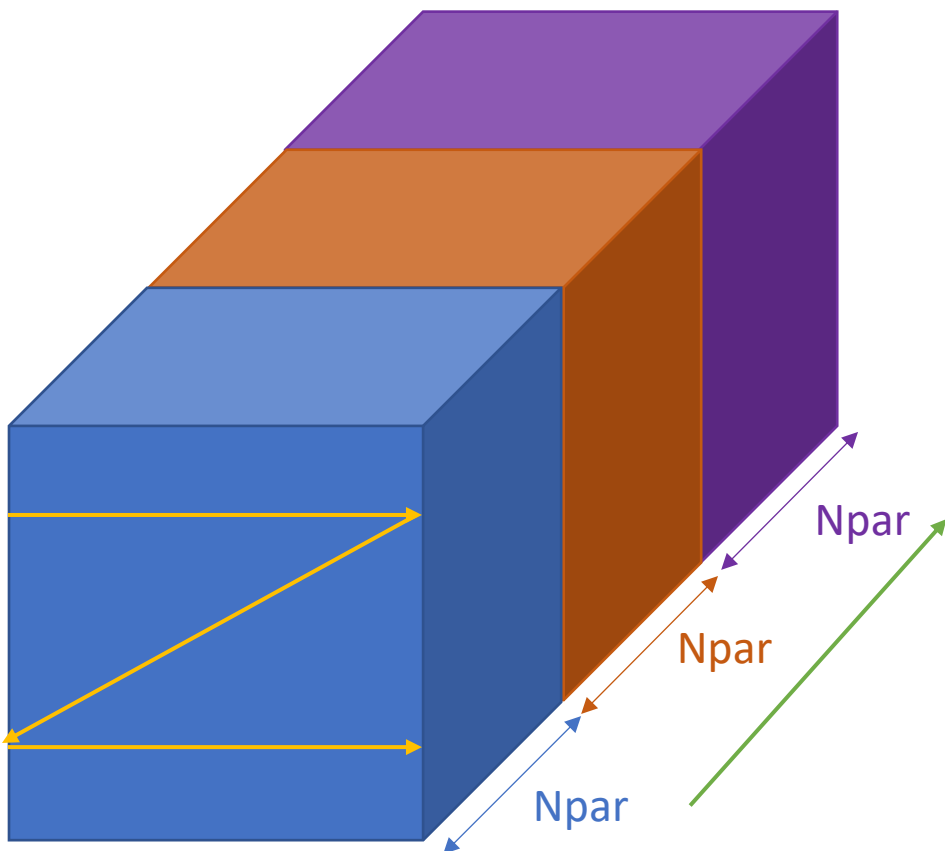
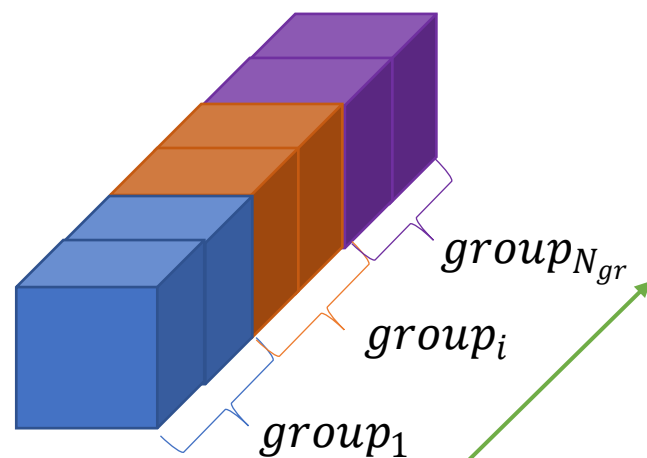
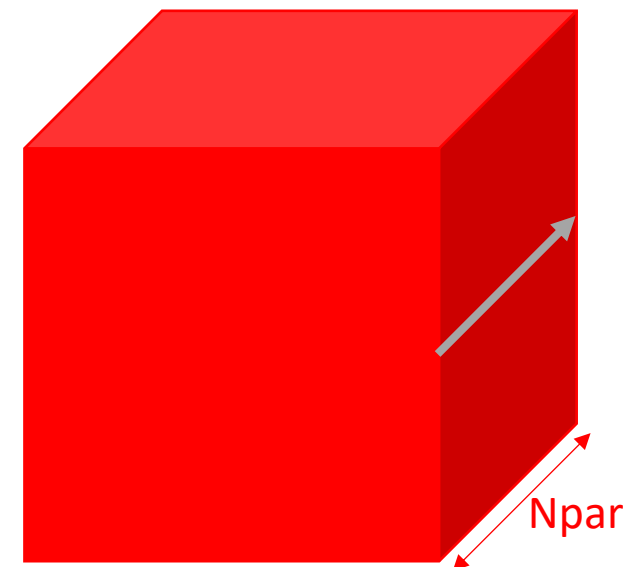


Input FM

 $1 \times 1$  kernel $\otimes$ 

Intermediate FM



: Loop 3

: Loop 2

: Loop 1