Wheastone Bridge Optimaization Using Factorial Experiments Gavin Gray and Christos Anastasiades

rule of thumb; below which model accuracy was

1. Control factor ranges

Methods

-10-50% variation on factors was a safe

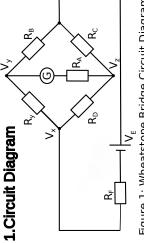


Figure 1: Wheatstone Bridge Circuit Diagram.

2.Iterative Design

varying each control factor by, initially, 10%. The result The iterations were performed by running experiments of these experiments was the AY value indicated on the right. Using this result it was then possible to regress the input factors into a model using RS/1 and use this model to find optimum values to minimise ΔY .

On each iteration the optimisation would find factors imited at the edges of the range chosen in the experiments. The range chosen for the next experiment chosen based on intuition - a percentage of the value was then based on this observation. These ranges were the factor was found to be limited at.

The graph on the right shows the AY acheived on each teration. This had to be plotted on a logarithmic scale for the higher iterations to be visible. Figure 2: Description of the design at different $^{10^{ ext{-4}}}$ terations and the improvements in ΔY .

Problem: When galvanometer current is between ± 0.2 mA, the indicated value is zero. The resistance change that causes currents and used in optimisation ΔY_2 two ΔY values seen here. Above graph plots the shaded area Aim: to detect 2Ω resistors as accurately as possible using a is the maximum of the The ΔY value reported Original AY over 100 times the height of this graph n this range to flow therefore cannot be detected -0.2 0.0 0.2 Galvanometer current(mA) ΔX_1 1.995 acheived in iteration 16 The final AY value Wheatstone Bridge circuit was 0.0002 Ω 14 1.15 9 and 10 2.005 (\Omega)_000.5 1.990 10^{1}_{1}

As an arbitrary number of experiments could be run a cubic model was built using a full factorial improved by applying a y-transform, variance Fit of the model can be estimated by the reported R^2 (fit of the model to data) value. This can be R² is composed of two square sums of observed experiment to increase accuracy. Observed SSobs of real responses weights or bisquare weights. Regressed SS_{reg} of model and predicted responses: badly affected. validate the optimised A final experiment is Perform experiment 5. Optimise variables Test specifications always required to 6. Confirmation run Check model fit

in the Python code. Thevenin's theorem was applied at node V_{x} whole was added as a response to solve for voltages V_v and V_z. resistor and the circuit Power dissipation of

> each circuit to ensure it would balance at 2Ω. This was acheived in the Python code using the following equations, by

It was necessary to calculate $R_{\scriptscriptstyle
m C}$ for

Calculations

 SS_{obs} SS_{reg}

 R^2

additional response, estimating standard Yield was also added as deviations able to detect 2Ω . οę number the

 $R_B \times R_{\overline{D}}$

 $R_C = -$

 $R_y = 2\Omega$

 $R_C = \frac{R_B \times R_D}{}$

asserting R_y at 2Ω .

Iteration

3.Results

	VE=	R₄=	R_{B} =	$R_{\rm c}$ =	R _D =	RF=	
Power: Optimisation 2	$V_E = 1.500 \text{ V}$	$R_A=1.0006 \Omega$	$R_B = 3.6994 \Omega$	$R_c = 7.2199 \Omega$	$R_D = 3.9033 \Omega$	$R_F = 1.0001 \Omega$	
ΔY: Optimisation 1	$V_E = 33.810 \text{ V}$	$R_A = 1.0000 \Omega$	$R_B = 1.3413 \Omega$	$R_c = 1.2310 \Omega$	$R_D = 1.8356 \Omega$	R _F =1.0002 Ω	

΅	$V_{E}=1.500$	R _A =3.00	$R_{\rm B} = 12.7$	$R_c=40.2$	$R_{\rm D} = 12.0$	R _F =1.00	:
Power: Optimisation 2	$V_E=1.500 \text{ V}$	$R_A=1.0006 \Omega$	$R_B = 3.6994 \Omega$	$R_{\rm C} = 7.2199 \Omega$	$R_D = 3.9033 \Omega$	$R_F{=}1.0001~\Omega$	
ΔΥ: Optimisation 1	E = 33.810 V	Ω 0000.1=	_B =1.3413 Ω	c=1.2310Ω	$_{\rm D}$ =1.8356 $_{\rm D}$	==1.0002 Ω	: : :

			R _D =3.9033 Ω	$2 \Omega \mid R_F = 1.0001 \Omega \mid R_F = 1.00 \Omega$	
_E =33.810 V		_=1.2310 Ω	$_{0}$ =1.8356 Ω	==1.0002 Ω	

possible the 1	+ · · · · · · ·
o obtain the greatest accuracy possible the	orations were simply entimising AV without o
To obtain the	orottion work

first set of section 2. The high voltage and low resistance mad terations were simply optimising AY without consi any other design objectives. These are illustra impractical design.

A common battery voltage of 1.5 V was chosen as a reasonable V_E. The power dissipated in each resistor was constrained below 0.1W in subsequent iterations.

Yield (σ) 6.53 Total power=167 power (mW) Dissipated Max.(6σ) 19.2 12.4 115 N/A 3.1 Component rating (mW) 00 00 00 00 00 Component Tolerances N/A(-20) 끆 1 # # 1 +36.0=76.2Ω nal Chosen mponents a

idering	idering $\;\;$ The resulting design had a ΔY of 7 m $\Omega.$ Unfortunately,
ated in	ited in this design required 0.1% resistors to be viable. Only
de it an	de it an 1% resistors were available from online suppliers in
	the required sizes.

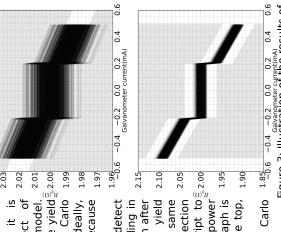
Setting a yield of 60 as a design constraint allowed the design to become viable and the final Monte Carlo test produced a worst case $6\sigma \Delta Y$ of $70m\Omega$.

Statistical Analysis

However, in order to optimise the yield $\mathbb{S}^{2.00}$ of the circuit easily a Monte Carlo simulator was written in Python. Ideally, this would be run on the model because Using the distrib command it is to estimate the effect of on the model. it involves many experiments. tolerances possible resistor

2 by using the Monte Carlo script $to_{\frac{2}{6}2.00}$ output worst case 60 values for power dissipation and ΔY . The bottom graph is $^{1.95}$ plotted in the same manner to the top, 1.90 optimisation 2. Optimisation of yield regression model approach as in section The region of designs unable to detect the top graph - showing the design after a 20 resistor is indicated by shading in the same through but for the final design. was possible

For confirmation, a final Monte Carlo $^{1.85}_{0.6}$ $^{-0.4}$ $^{-0.2}$ was also run.



Monte Carlo on two optimised designs. Figure 3: Illustration of the results of

The final circuit is guaranteed to be able to detect a 2Ω resistor to within 70mΩ, while consuming 167mW when balanced and for a resistor cost of £0.047