

Wheatstone Bridge Optimisation Using Factorial Experiments

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1.Circuit Diagram

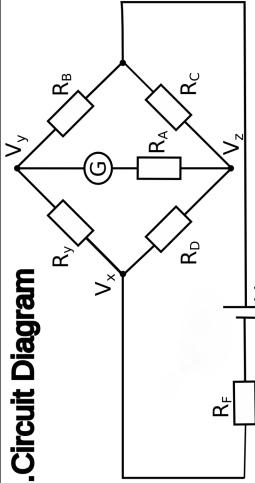


Figure 1: Wheatstone Bridge Circuit Diagram.

2.Iterative Design

ΔY was used as a measure of the accuracy of the circuit and is defined in figure 2. A program provided could calculate required R_y for a given out of balance current - allowing ΔY to be calculated from the expected resistance at plus or minus 0.2mA. This response was used to regress a model in RS/1 which could be used to optimise the circuit.

On each iteration the optimisation would find factors limited at the edges of the range chosen in the experiments. The range chosen for the next experiment was then based on this observation. These ranges were chosen based on intuition - a percentage of the value the factor was found to be limited at.

The graph on the right shows the ΔY achieved on each iteration. This had to be plotted on a logarithmic scale for the higher iterations to be visible.

Figure 2: Description of the design at different iterations and the improvements in ΔY .

3.Results

ΔY : Optimisation 1	Power: Optimisation 2
$V_E=33.810\text{ V}$	$V_E=1.500\text{ V}$
$R_A=1.0000\text{ }\Omega$	$R_A=1.0006\text{ }\Omega$
$R_B=1.3413\text{ }\Omega$	$R_B=3.6994\text{ }\Omega$
$R_C=1.2310\text{ }\Omega$	$R_C=7.2199\text{ }\Omega$
$R_D=1.8356\text{ }\Omega$	$R_D=3.9033\text{ }\Omega$
$R_E=1.0002\text{ }\Omega$	$R_F=1.0001\text{ }\Omega$

To obtain the greatest accuracy possible the first set of iterations were simply optimising ΔY without considering any other design objectives. These are illustrated in section 2. The high voltage and low resistance made it an impractical design.

A common battery voltage of 1.5 V was chosen as a reasonable V_E . The power dissipated in each resistor was constrained below 0.1W in subsequent iterations.

Final Chosen Components	Component Tolerances (%)	Component Power rating (mW)	Max.(6 σ) Dissipated power (mW)	Yield (σ)
$V_E=1.500\text{ V}$	N/A(-20)			
$R_A=3.00\text{ }\Omega$	± 1	100	N/A	
$R_B=12.70\text{ }\Omega$	± 1	125	115	
$R_C=40.20+36.0=76.2\text{ }\Omega$	± 1	20 & 50	19.2	6.53
$R_D=12.00\text{ }\Omega$	± 1	50	3.1	
$R_F=1.00\text{ }\Omega$	± 1	62.5	12.4	

Total power=167

The resulting design had a ΔY of 7 m Ω and consumed 488mW. Unfortunately, this design required 0.1% resistors to be viable. Only 1% resistors were available from online suppliers in the required sizes.

Setting a yield of 6 σ as a design constraint allowed the design to become viable and the final Monte Carlo test produced a worst case 6 σ ΔY of 70m Ω while consuming 167mW.

2.1.Methods

- Control factor ranges \rightarrow 10-50% variation on factors was a safe rule of thumb; below which model accuracy was badly affected.
- Test specifications \rightarrow As an arbitrary number of experiments could be run a cubic model was built using a full factorial experiment to increase accuracy.
- Perform experiment
- Check model fit
- Optimise variables
- Confirmation run

A final experiment is always required to validate the optimised design. Fit of the model can be estimated by the reported R^2 (fit of the model to data) value. This can be improved by applying a y-transform, variance weights or bisquare weights.

R^2 is composed of two square sums of observed and predicted responses: Observed SS_{obs} of real responses, Regressed SS_{reg} of model

2.2.Calculations

It was necessary to calculate R_C for each circuit to ensure it would balance at 2 Ω . This was achieved in the Python code using the following equations, by asserting R_y at 2 Ω .

$$R_C = \frac{R_B \times R_D}{R_y} \quad R_C = \frac{R_B \times R_D}{2}$$
$$R_y = 2\Omega$$

Power dissipation of each resistor and the circuit as a whole was added as a response in the Python code. Thevenin's theorem was applied at node V_x to solve for voltages V_y and V_z . Yield was also added as a additional response, estimating the number of standard deviations able to detect 2 Ω .

2.3.Statistical Analysis

Using the distrib command in RS/1 it is possible to estimate the effect of resistor tolerances on the model. However, in order to optimise the yield of the circuit easily a Monte Carlo simulator was written in Python. Ideally, this would be run on the model because it involves many experiments.

The region of circuits unable to detect a 2 Ω resistor is indicated by shading in the top graph - showing the design after optimisation 2. Optimisation of yield was possible through the same regression model approach as in section 2 by using the Monte Carlo script to output worst case 6 σ values for power dissipation and ΔY . The bottom graph is plotted in the same manner to the top, but for the final design.

For confirmation, a final Monte Carlo was also run.

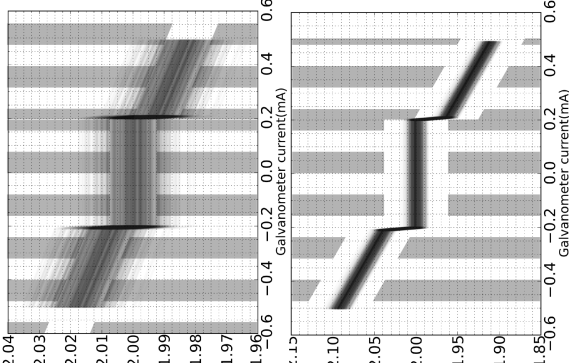


Figure 3: Illustration of the results of Monte Carlo on two optimised designs.

The final circuit is guaranteed to be able to detect a 2 Ω resistor to within 70m Ω , while consuming 167mW when balanced and for a resistor cost of £0.047.