

Variant: FIXED 5V

12/10/2021
V1I2

RELEASED DD-MMM-YYYY

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DESIGN CONSIDERATIONS

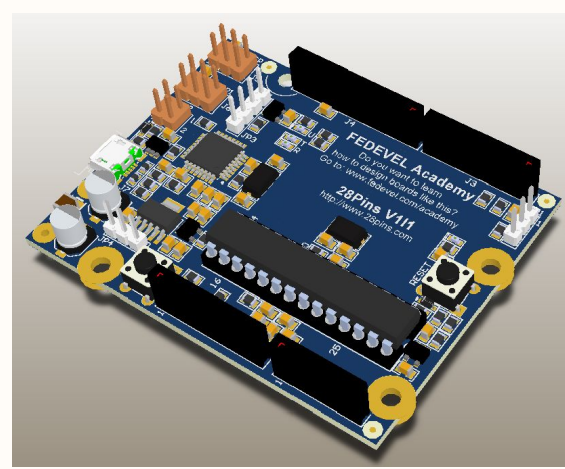
DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for cautionary
design notes.

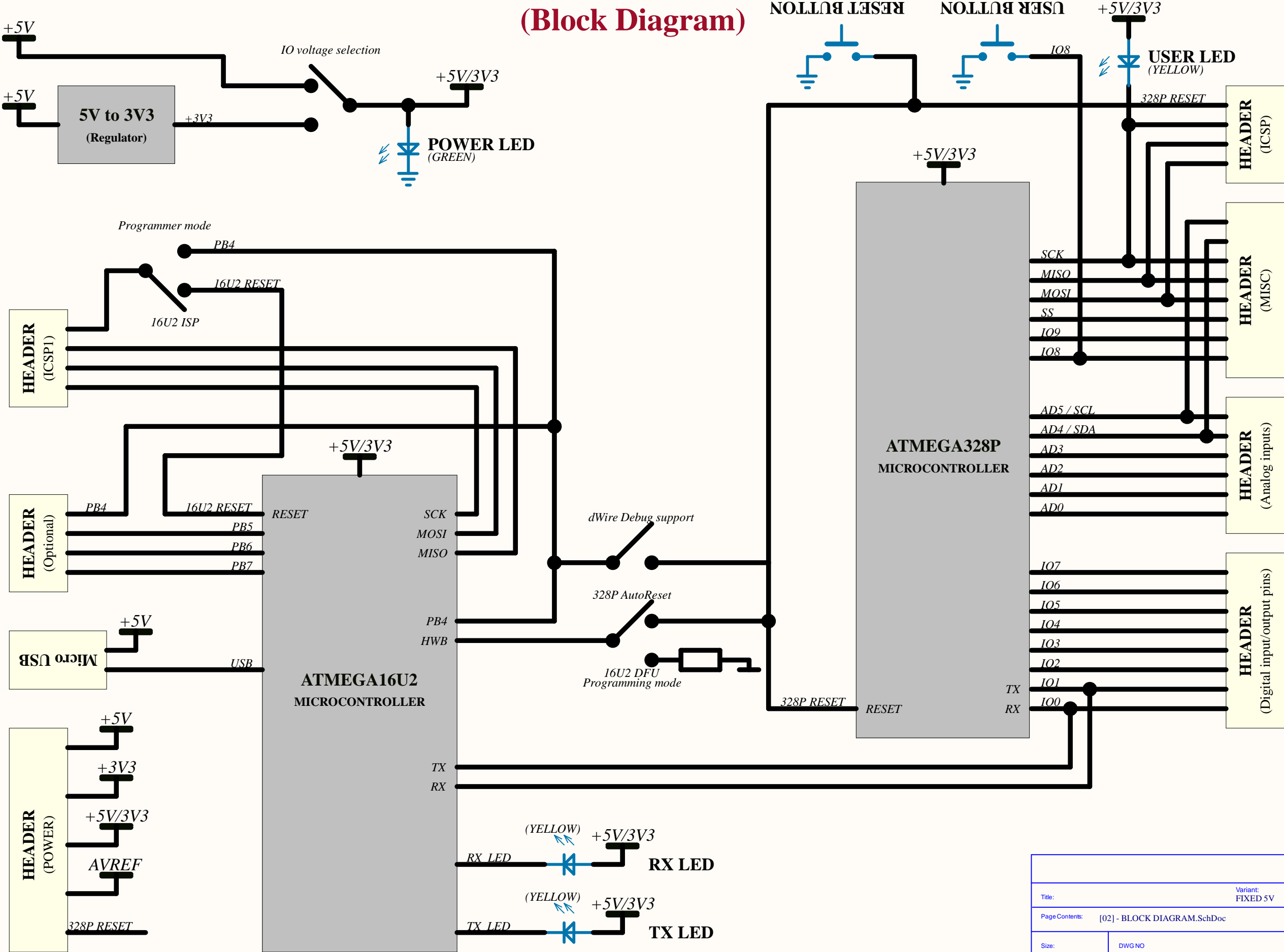
DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.



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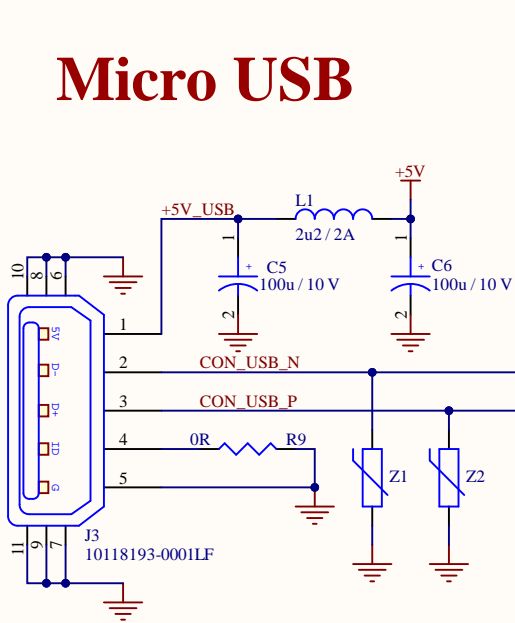
(Block Diagram)



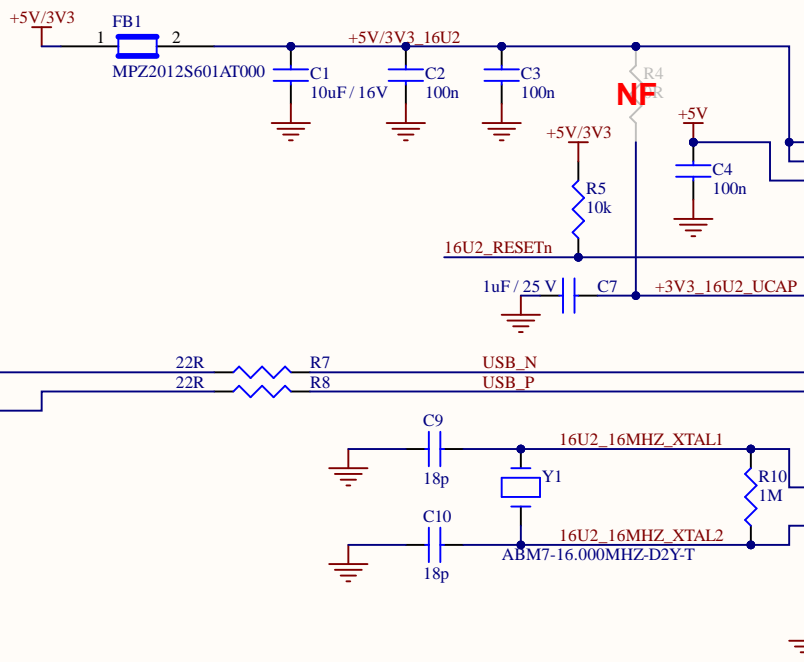
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28 PIN - Schematics

Micro USB

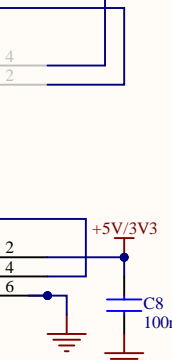


16U2



J1

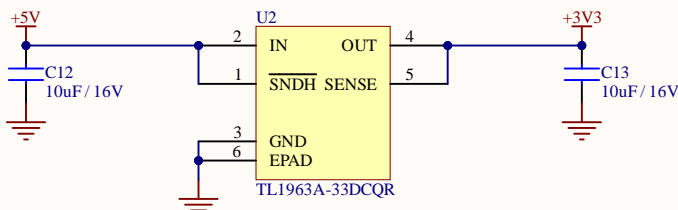
ICSP1



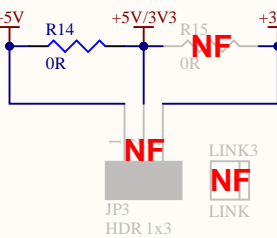
JP1

JP2

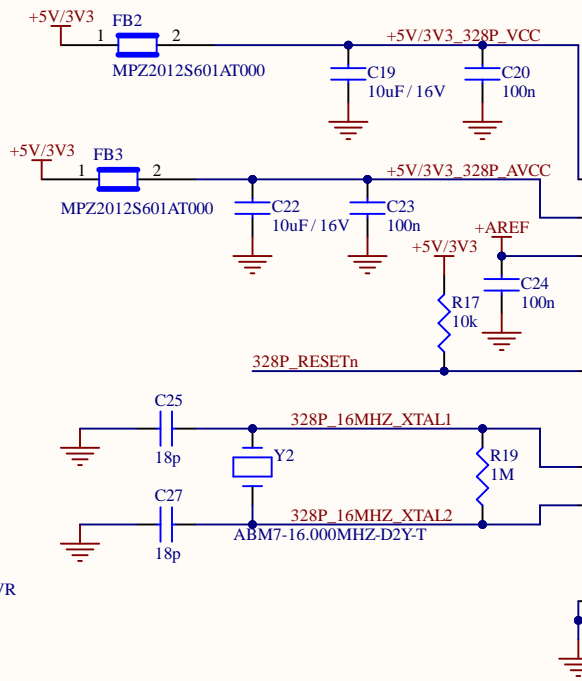
3V3 LDO



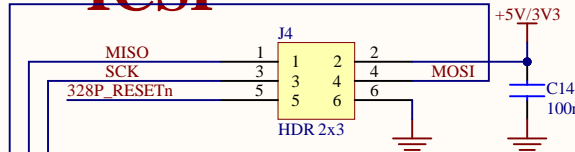
Power Selection



328P



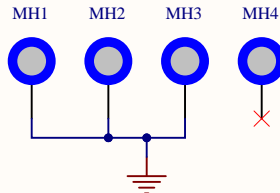
ICSP



DIP SOCKET

28-DIP Socket

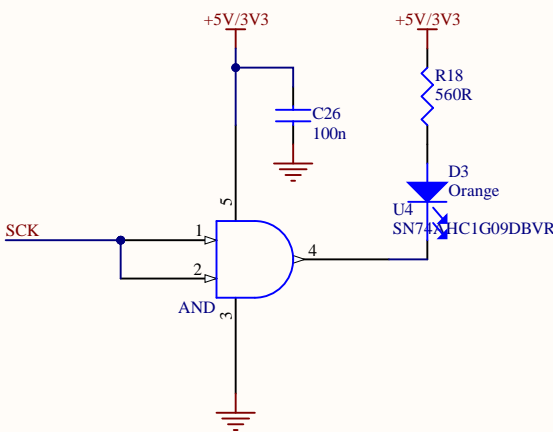
MOUNTING HOLES



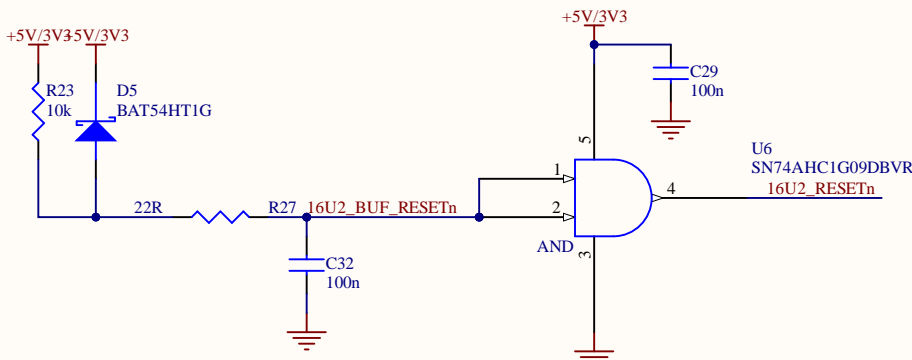
POWER LEDS



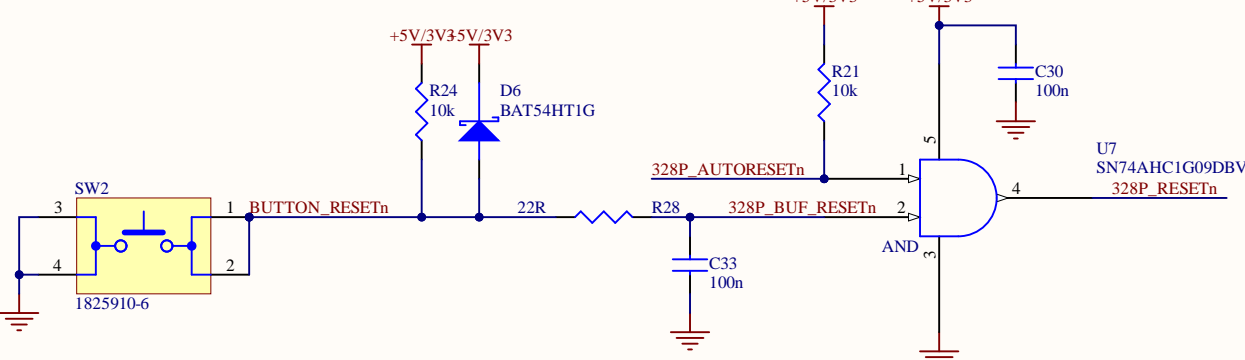
USER LED



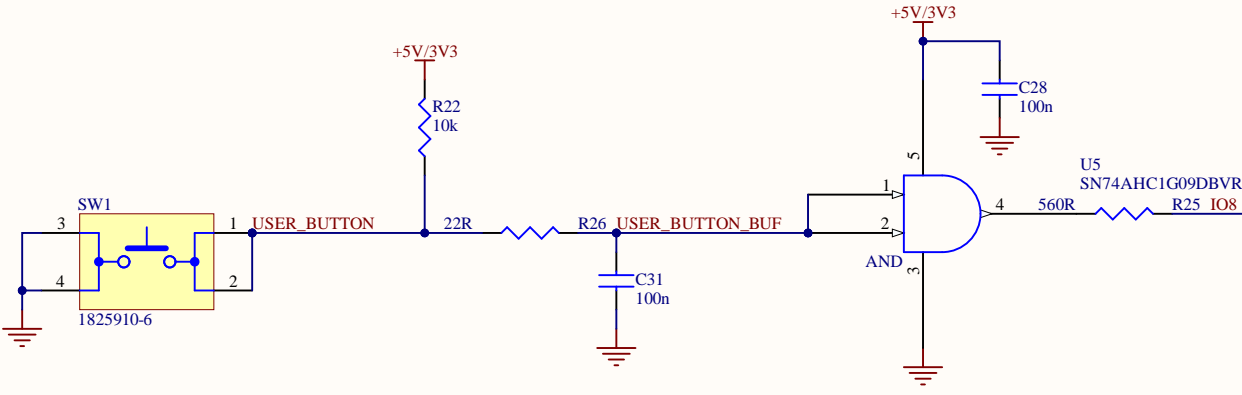
RESET (16U2)



RESET (328P)



USER BUTTON



FIDUCIALS

FID1 FID2 FID3 FID4

Title		
Size	Number	Revision
A2		
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REVISION HISTORY																																																
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B								B																																								
C								C																																								
D							<table><tr><td colspan="8"></td></tr><tr><td colspan="4">Title:</td><td colspan="4">Variant: FIXED 5V</td></tr><tr><td colspan="2">Page Contents:</td><td colspan="4">[04] - REVISION HISTORY.SchDoc</td><td colspan="2">Checked by</td></tr><tr><td colspan="2">Size:</td><td colspan="5">DWG NO</td><td>Revision: V112</td></tr><tr><td colspan="2">Date:</td><td colspan="3">12/10/2021</td><td colspan="3">Sheet 4 of 5</td></tr></table>									Title:				Variant: FIXED 5V				Page Contents:		[04] - REVISION HISTORY.SchDoc				Checked by		Size:		DWG NO					Revision: V112	Date:		12/10/2021			Sheet 4 of 5			D
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B								
C								
D							<div><div></div><div>Title:Variant: FIXED 5V</div><div>Page Contents:28Pins_Project_V111 Project.SchDocChecked by</div><div>Size:DWG NORevision: V112</div><div>Date:12/10/2021Sheet5of5</div></div>	
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NOTES

Mark Not Fitted Components as

NF

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.