LB8, LB28, LB2Q & LB3Q

A new modulation and phase extraction technique for digital communication via electromagnetic waves.

Inventor and Author: Lawrence Byng

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Preliminary test results over AWGN channel, indicate the new modes can consistently decode <u>at</u> the Shannon limit (-1.59 dB Eb/N0) with a 0.0 Bit Error Rate.

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Inspiration

The inspiration for these modes originally came from using other ham radio digital modes and observing some of the limitations. This inspiration was originally in two main areas:-

- 1) A character encoding scheme to allow maximum flexibility and functionality while at the same time efficiently representing the data in a compact form and
- 2) A mode that does not require any special synchronization to time clocks or to consensus timing offsets to achieve optimal decodes.

Additionally, while researching digital modes and combining the different modulation techniques, I came across many instances where combining FSK with PSK was discouraged as being either ineffective, overly complex or impossible to achieve. In reality, combining FSK with PSK, when done using an effective design and technique, offers immense potential, not only for ham radio but for telecommunications in general.

Design Points

Points of consistency.

A key design consideration is how to modulate and encode the data so that each section is self contained. A well defined structure is paramount for building a process to go from known points of consistency to less well defined points and even garbled data points. This key aspect of the design facilitates an accurate demodulation process.

A modulation scheme of 2FSK + 8PSK was initially chosen. An 8PSK signal is modulated by 2 FSK carriers. The 2 FSK carriers function together to provide a clock signal anchor point. This anchor point is used in conjunction with a block format to represents 1 character i.e. 6 bits for a base 64 character encoding scheme.

The easiest way to explain how the modulation holds together is to explain it on the basis of a sequence of blocks. Each block represents 1 character. A block spans both the frequency domain and the time domain and has a specific format depending on which modulation scheme is being used. The simplest 'AB' block format has 2 FSK in the frequency domain and 2 x 3 bit codes of 8PSK in the time domain. Each of these frequencies will contain an 8PSK signal for half of the block. First the low frequency 8PSK signal then switching to the higher of the two frequencies for an additional 8PSK signal. The frequencies can be very tightly packed; the 20 character per second mode uses a 100 Hz spacing and the weak signal interpolated modes have 10 Hz spacing between the frequencies.

The block gives complete referential integrity and provides a point of consistency from which to start a decode process.

Scalability

Another key aspect of the design is scalability. The reference platform LB28 modes span from 20 characters per second on the higher baud rate with lower noise resilience end of the spectrum to a 6.4 seconds per character mode (0.15625 CPS) for lower baud rate with higher noise resilience at the other. This spectrum can be extended with additional carriers on the higher baud rate end and with more pulse repeats on the higher noise resilience end. Going in order from high baud rate to low baud rate, each successive mode is half as fast as the prior mode as it uses double the pulses per character but is twice as resilient in terms of signal to noise due to the doubling of the pulses per character. When adding additional carriers, blocks can be overlapped to increase carrier density thus resulting in more carriers per unit bandwidth of spectrum. This is described in greater detail in a later section.

It would also be possible to change modulation mid-stream i.e. in real time if the BER changes significantly, as any changes to the block format would become immediately apparent. This could be utilized for example over a fully synchronous full duplex communication link or over a half duplex communication link as is often used in ham radio for sending critical information with an ARQ process.

Block Formats

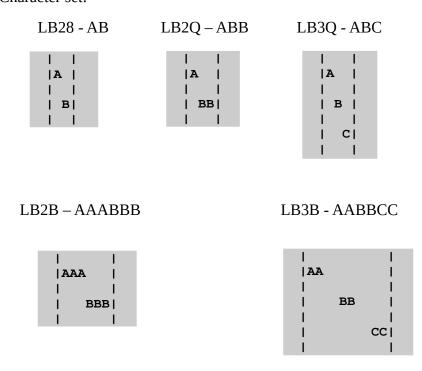
The diagram below gives a visual representation of 5 character blocks from left to right. The encoding scheme is LB28; 2 FSK and 8 PSK. 'A' represents a 3 bit pulse. Each pulse is repeated 4 times. 'B' represents a different 3 bit pulse and is repeated 4 times. Frequency on the vertical axis and Time on the horizontal axis. The complete set encodes for 5 x base 64 characters. This is the basic AB format.

	1	I		I	1 1
Frequency 1:	AAAA	AAAA	AAAA	AAAA	AAAA
1 0	1	1	l	I	1 1
Frequency 2:	BBBB	BBBB	BBBB	BBBB	BBBB
	1	1	l	I	1 1

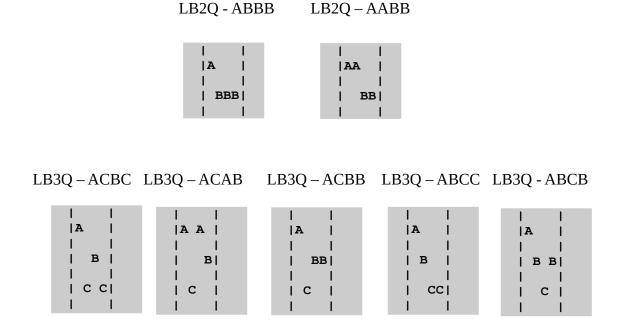
Time

Detailed below are diagrammatic representations of some of the main block formats for base 64 character set and base 256 character set using both 2FSK and 3FSK combined with 8PSK, QPSK and BPSK with 1 pulse per 3 bit encode for 8psk, 1 pulse per 2 bit encode for QPSK or 1 pulse per 1 bit encode for BPSK.

• Base 64 Character set:-



• Base 256 Character Set:-



There are additional block formats not mentioned here, however the above details should give an idea of how the different block formats are utilized for the various modulations.

Modulation

The modulation process proceeds in order as follows:-

Two symbols waves, one for each frequency, are derived as follows:-

• Each symbol_wave is then further modulated with the root raised cosine (RRC) pulse shaping. For a signal with two pulses per symbol. The RRC shaping is as follows:-

• Each of the above steps are performed once for each character. Each shaped symbol wave is joined onto the end of the previous to form a modulated stream of data. Each character comprises 2 x 8psk symbols to achieve a 6 bit character encoding of one of 64 characters.

Demodulation

Decode steps vary slightly between the non-interpolated modes and the interpolated modes, with the non-interpolated mode process being a subset of the interpolated mode process. Essentially, the non-interpolated modes skip the interpolation process.

The precise steps for the interpolated modes are as follows:-

- The received wave is sectioned into chunks of signal each approximately 32 characters long although this can vary.
- Locate the RRC peaks of the incoming signal and determine the location of the first full pulse in the received chunk. This process involves the following:-

```
""" For each sample i in the received chunk, """

test_peak = signal[i * symbol_block_size : (i * symbol_block_size) + symbol_block_size]
test_max = np.max(test_peak)
test_min = np.min(test_peak)
max_indices = np.where((test_peak*(100/test_max)) > parameters[5])
min_indices = np.where((test_peak*(100/test_min)) > parameters[5])

""" Each of the minimum and maximum indices x are appended to a list of all indices:- """
all_list.append(max_indices[0][x] % pulse_width)
all_list.append(min_indices[0][x] % pulse_width)
And the median value is then used to determine the most likely first peak location :-
pulse_start = (int(np.median(np.array(all_list))) % pulse_width) + pulse_length / 2
```

- A Fast Fourier Transform (FFT) bandpass filter is applied to each of the two signals. The width of this filter is absolutely critical for accurate decoding. For extreme at the limit decodes, this filter needs to be no more than 2 Hz wide.
- A similar process to the RRC peak location process described above is then used to locate all RRC pulse shaped pulses for each of the two frequencies over the entire received chunk of data. The result is two lists of indices representing each of the pulses for each of the two frequency streams. Ideally, the lists of indices will be nice clean lists that match exactly the set of pulses that were modulated and transmitted. In reality, the lists will be anything but, especially if the signal has been distorted by noise; the lists will have indices missing at the start and end making them shorter, indices will be missing from the middle and some indices will appear in both lists. An algorithm is then used to sort out and reconstruct the most probable two lists of indices. The more accurate and complete the resulting lists, then the more accurate can be the decoding that follows at a later stage.
- Apply an interpolation algorithm to process the received lists. The interpolation algorithm is described in the next section. python code excerpts from the test reference platform are included in appendix 1.
- The original received signal is passed through a matching RRC filter and then filtered using a very narrow FFT bandpass filter with sharp cutoff. The filter width is absolutely critical. For decoding at the limit, the bandpass filter width can be no more than 2Hz wide.

- The processed signal is then sent to a Costas Loop to convert to baseband and determine the
 phases along the full length of each of the two frequency streams for the entire chunk of
 received data.
- A process of mean averaging is used to average all baseband pulses in a given stream that correspond with indices in the lists recovered in the prior steps. The averaging process effectively cancels out any remaining noise to a level of 1/N where N is the number of list indices in a given stream. For the LB28 mode which uses 2FSK + 8PSK and 256 pulses per block, this is equivalent to a noise reduction to a level of 1/128th if the list indices are recovered in full. At the same time, the baseband phase values remain unchanged. This equates to an amplification of the signal relative to the noise by a factor of 128. The net result is that the noise is reduced to minimal background and the signal is effectively amplified to allow for a successful decode.
- A phase value is extracted from the mean averaged data using the median index of the respective list.
- The result is two phase values, one from each frequency (2*8psk). This is then used to decode for the transmitted character of the base 64 character set.

The above process is the basis of all of the LB28, LB2Q and LB3Q interpolated modes. The specifics vary slightly for example:-

- The LB2Q modes which use 2 FSK + QPSK, have 2 streams of 4 PSK pulses per block arranged in a abb configuration on the block. Note: all configurations aab, aba, bab, bba are tantamount to the same aab block configuration for 64 character modes. Also abbb/aabb can be used for 256 character modes.
- The LB3Q modes utilize 3 FSK carriers + QPSK. The block configurations include abc for 64 character modes and acbc/acab/acbb/abcc for 256 character modes. Other than that the process will be practically identical.

The modulation technique is also extended to BPSK:-

- LB2B consists of 2 FSK carriers + BPSK. The block encoding for this is an abbb for base 64 character set and an abbbb for base 256 character set.
- LB3B consists of 3 FSK carriers + BPSK. The block encoding for this is aabbcc for base 64 character set. A block encoding of aaccbbcc for base 256 character set.
- The modulation technique can in theory also be extended to other combinations such as FSK + QAM

Phase Extraction - Pulse Train Standing Waves

This section describes the new technique for phase extraction using Standing Waves. Under certain conditions, standing waves can be made to form along the intra-block pulse train of the interpolated modes. Please note this technique is experimental and is currently being assessed for viability. Having said that, the technique has demonstrated sufficent consistency for initial publication pending further prototype development and testing.

In the simple case, each pulse for a given character block of the signal is modulated using the same phase combination; one phase on the lower of the two frequencies and a different phase on the higher of the two frequencies. This codes for a single character. Each block is sequencial in time and provides a stream of characters forming the data transmission.

In a perfectly calibrated system, the sampling will be perfectly aligned with the signal as created and transmitted. This allows recreation of the original information without the need for correction of any sort. This is the simple case.

In reality there are many distortions that come into play which significantly impact the signal. Even distortions in the computer sampling, with respect to small timing delays, can have a significant impact on the phase of the received signal from phase rotation due to delays at points along the signal. These distortions modify the phase of the signal and present some interesting challenges to achieve perfect decodes. Many techniques exist to correct for these distortions. The LB28 interpolated modes utilize a new technique for absolute phase recovery based on phase standing waves.

Intra-Block Triple Pulse Groupings and Standing Waves

In the simple case, each pulse in the block has two phases encoded, one on each of the two frequencies. To create a standing wave, the pulse train is split in to groups of three pulses with each sequential pulse having a different phase. The combination of phases as a relative phase to the original character phase ϕ and are as follows:- $\phi - \frac{1}{4}$ wavelength, ϕ , $\phi + \frac{1}{4}$ wavelength.

Referring back to the simple case of how phases combine, the above combination would normally leave the wave phase unchanged however, because each phase is encoded on a different pulse in the pulse train for the block, the resulting interaction from interference of wave pulses and phases is significantly more complex than the simple case.

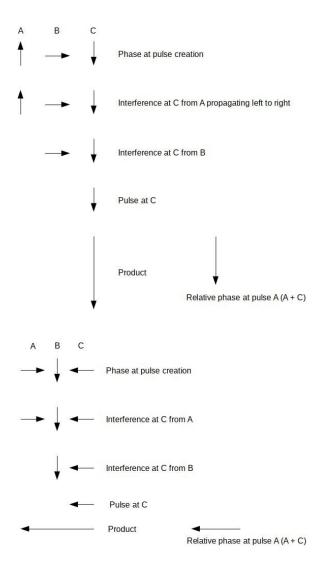
The following explanation uses an analogy similar to a modified triple slit experiment. Each slit represents the signal from one of the pulses. The central slit is the in-phase ϕ character phase and the one above this is $\phi + \pi/2$ and the one below $\phi - \pi/2$. The simulation shows the phase space as an X Y plane. The pulse chain is aligned with the Y axis. Pulse offset is relative to wavelength.

The central concept of this technique is the formation of standing waves along the pulse train. This is achieved by

- 1. Sectioning each pulse train into repeating groups of 3 pulses. i.e. the lower pulse train for the lower frequency and the higher pulse train for the higher frequency with the pulses proceeding in order ABC ABC ABC or BCA BCA BCA or CAB CAB CAB etc. and
- 2. Adjusting the phase of each pulse according to where the pulse exists along the pulse train. The phase adjustment for each pulse must adhere to certain principles which will be described in more detail later in this section.

The combined effect of these two steps is the formation of a standing wave of greater amplitude than the neighboring pulses along the pulse train. The standing waves repeat once at every 3 pulses. The phasor representation in the diagram below shows interference between the pulses and how the phase pulses propagate in time with both the phase rotation and amplitude being the important factors. Two phase vectors are shown; one at 0' and one at 90'. Similar product results are obtained with all other initial phase angles.

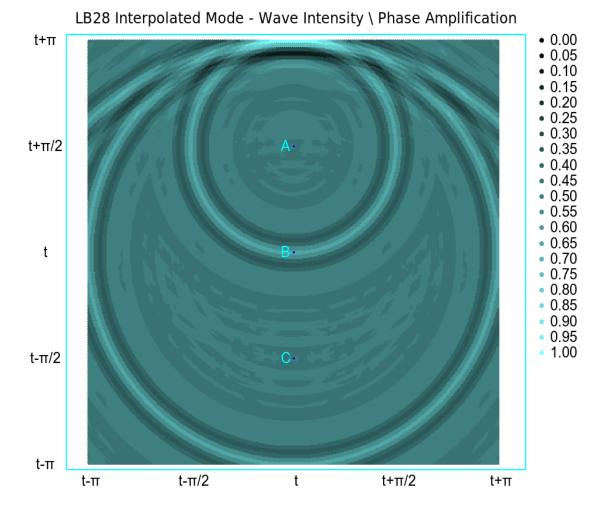
Phasor Diagram



As can be seen in the above diagram, the relative phase obtained from the relative pulse addition of pulse A and pulse C gives an absolute phase value. The only inputs to deriving this phase are the phase of pulse A and the phase of pulse C. This result is due to the magnitude difference between the larger standing wave pulse and the other pulses along the pulse train.

The diagram below is a simulation of this by coding the phase of the propagating pulse as an intensity value. Pulse origination points A, B and C emit pulses at different phases as described earlier. As cen be seen there is a significantly increased magnitude pulse above the location of the repeating pulse C along the pulse train. This pulse is the standing wave.

Standing Wave Simulation



As can be seen in the simlation diagram, when the phase pulses combine, a standing wave of significantly greater intensity is formed at an offset of π . The standing wave at π represents the phase code ϕ of the original character. This standing wave is essentially an amplified phase pulse.

The significance of the above phase derivation from relative pulse phase, is that the presence of an amplified phase pulse together with the normal pulses provides an absolute frame of reference for phase value extraction that is immune from what the individual waves are doing and consequenctly does not need to be synchornized, tracked or compared relative to anything else. This removes the need for phase tracking and simplifies the phase extraction process. In addition, the extra stage of amplification further completements the interpolated mode's signal to noise amplification process.

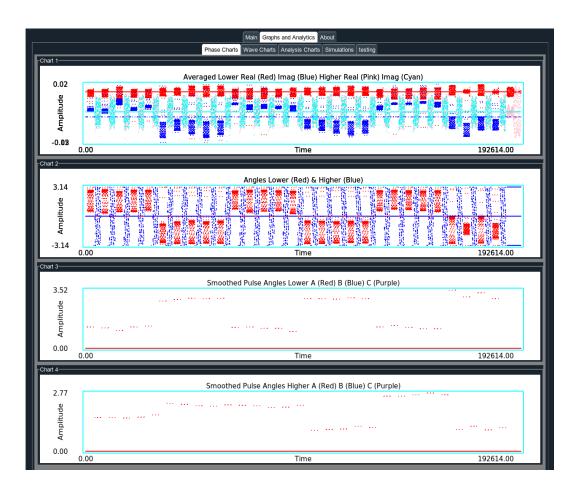
The phase difference at each of the three pulse nodes along the pulse train can be calculated by expressing the time difference between two adjacent pulses as an angle relative to the carrier frequency. In the example above, this spacing is $\pi / 2$ or $\frac{1}{4}$ of the wavelength. As the pulse spacing is many multiples of the wavelength, the standing wave that forms over every third pulse along the pulse train can be clearly differentiated from the other two pulses. Phase extraction can then be achieved in the downconversion process by taking the exponential of the standing wave pulse t and adding it to the exponential of the adjacent pulse at time pulse t+1 and applying a low pass filter to

the resulting baseband phase. When the subsequent amplification process is performed by averaging every pulse along the pulse train to increase the signal intensity relative to the noise, the pulses retain these phase differences.

The phasor diagram shows that the resulting phases are a result of both the magnitude and phase of adjacent pulses along the pulse train. Due to the standing wave effect from pulse interference, the inter pulse phase difference between the standing wave pulse and the adjacent pulse at t+1 of the received wave, differs from the inter pulse phase difference of the original transmitted wave by an amount equivalent to the phase encoding.

This technique can be extended to other groupings of pulses i.e. groups of 2 pulses or groups of 4 pulses. The key point is the calculation of the relative phases for the pulses along the pulse train so as to create a standing wave relative to the pulse chain.

Charts of Prototype Sample Data



The image above shows actual test data from a prototype of a sequence of repeating characters. There are 6 characters in the sequence and each one is repeated 5 times in succession. The test is over an AWGN channel using the new LB28-6400-64-2-15-I3 mode; I3 at the end denotes this is an interpolated mode using intra-block 3 phase pulse deliniation. As can be seen, there is great clarity to the signal. All phases shown are derived from the relative phase obtained from two neighboring pulses in the baseband signal downconversion. The mode used has the following characteristics:-

Lower Frequncy: 1300 Hz

Higher Frequency: 1315 Hz

Block Size: 6400 samples

Sample Rate: 8000 Samples per Second

Lower Frequency Pulse Offsets: ϕ , ϕ + 2 * π * 0.625, ϕ + 2 * π * 0.875

Higher Frequency Pulse Offsets: ϕ , ϕ + 2 * π * 0.09375, ϕ + 2 * π * 0.78125

The above phase values have been calculated to form a standing wave at ½ pulse length distance between two pulses.

Baseband Downconversion using Relative Phase

The code snippet below shows the actual code used to derive the relative phases from two pulses (sig1 and sig2).

```
complex_exponential_lower = np.exp(-1j * 2 * np.pi * frequency[0] * time)
complex_exponential_higher = np.exp(-1j * 2 * np.pi * frequency[1] * time)
complex_exponential = [complex_exponential_lower, complex_exponential_higher]

def deriveProduct(low_hi_index, sig1, sig2):
   baseband_sig = complex_exponential[low_hi_index]*sig1 + complex_exponential[low_hi_index]*sig2
   return self.filter_low_pass(baseband_sig, 1200)
```

LB8 - Single Carrier 8PSK Interpolated Mode

The ability to extract phase on an absolute basis now makes possible the use of a single carrier mode i.e. LB8. LB8 uses 2 blocks per character. The character sequence is enclosed in bookends; one or two repeats of a unique special character at the start of the sequence and one or two repeats of an additional special character at the end. Each 3 bit part of the 8psk sequence is then sent as a serial stream i.e. 3bits + 3bits + 3bits + 3bits etc on a single carrier with 8psk.

Interpolation Algorithm

- The first step is to remove any list indices that appear in both lists. These are clearly erroneous and no determination can be made at this stage about which list the index should belong to.
- Next, interpolate the lists so that a sequence with gaps is filled in resulting in a contiguous set of indices. This involves several steps:-
 - First, find the median list index and remove any indices that deviate by more than num pulses per block / 4. This represents half a stream for a 2 frequency decode.
 - If any indices were removed at the conclusion of this process, a new median index is found and the process repeats until there are no more statistical outliers in the list.
 - As this process proceeds, the median tends towards truth. The resulting median value is a more accurate median. This truth median is then used to purge any statistical outliers from the original list, a copy of which was saved at the start of this process. Take a final truth median measurement and compare with previous truth median. Repeat the above steps to a max of 5 interations or until the difference between the final truth median and the prior truth median is 0.
- Missing values are then interpolated using a walk algorithm to walk thru the list indices and determine by distance what the sequence is and ultimately determine how the indices wrap around i.e. do the indices proceed from low index to high index or high index to low index with wrap around. The end result is a clean contiguous set of indices. These represent the points along the signal wave.
- Additional interpolation steps are then perfored to further maximize the completeness of the list of indices as follows:-
 - Firstly, if either of the two processed lists is a complete list of indices i.e. it has a full set of num_pulses_per_block / 2 and also if the other list is less than complete, then the short list is filled out by adding the corresponding partner index into the partner list.
 - Next, the same process is done if one list is longer than the other but the longer list is not a full set of indices.

These steps are sufficient to provide a most probable set of indices for both signals. Each index directly relates to exactly where the data is hiding in the received signal block. This is paramount for the decode process that follows.

The following additional optimizations are also possible but have not been included in the preliminary testing:-

- Determine if the last index of one list is adjacent to the first index of the partner list. If so, then all indices can be computed.
- Iterate through a list and fill out the partner list with the corresponding partner index if the index is not already present in the partner list. This needs to be done both ways i.e. on the first list and then on the second list
- The list indices are originally derived from iterating over the entire chunk of data. In the preliminary tests, these chunks are approximately 33 characters in length. To further enhance signal amplification in the final stage where the phase value is mean averaged for the entire detected set of indices, a process could be incorporated to correlate indices over multiple chunks. The goal is to derive a full set of indices. At whatever point this occurs in the decode process, the full set of indices can then be used to pull out the data to the max for any previously received \ decoded data and any future data. For a given transmission, assuming that the transmit and receive stations are either stationary or moving at a constant velocity relative to each other, the signal will be essentially fixed to a clock and should not deviate on aggregate. Once the full set of indices is established, this effectively establishes a 'clock lock' and the full delineation of all blocks for this transmission, past present and future, is known i.e. where the block starts relative to the signal and also relative to a clock, where the block ends relative to its start point and where every piece of transmitted data is hiding inside the block. This data can then be extracted with maximum efficiency.

Carrier Optimization – Overlapping Blocks

Increasing the baud rate of the higher speed modes is achieved by adding additional carriers. Typically this is done by utilizing additional frequency blocks in the adjacent spectrum space.

To maximize carrier density, a technique of overlapping blocks can be implemented as follows:-

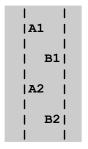
- Each parallel block is synchronized so that all parallel blocks are sending their respective modulated signals in-phase.
- By overlapping the blocks, it is possible to use bandwidth from the adjacent channel group space.

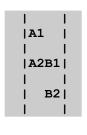
For example:-

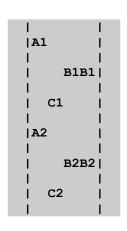
Lets assume the following modulations:- LB28 in AB format and LB3Q in ACBB format. These blocks can be overlapped in the spectrum space as follows:-

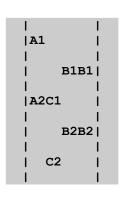
LB28 LB3Q

No overlap 4 Carriers, With overlap 3 Carriers No overlap 6 Carriers With overlap 5 Carriers



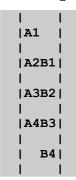






The above example shows that it is possible to achieve up to a 25% increase in carrier density by overlapping blocks in this manner. With more carrier groups this number tends towards a 50% increase for LB28 and other 2 carrier modes.

A key point to note is that the outer blocks in the frequency spectrum contain the information required to exactly deliniate all in-phase block boundaries. For example, four groups of LB28 will look like the following with overlapping blocks:-



This is a reduction from 8 carriers to 5. The outer two blocks A1 and B4 show the block boundaries.

A further point of note, is that the higher baud rate modes are able to achieve tight carrier separation through orthogonality. When adjacent carrier groups with overlapping blocks are used, to maintain orthogonality and keep ISI to a minimum, the adjacent group(s) must be offset or interlaced by timing equivalent to between one and three additional RRC pulses. The net effect of this is that initially increasing carriers and baud rate is offset by a reduction in baud rate due to the increased time required for interlacing however, the total net gain in carrier density is significant. Once interlacing has been established, any further multiplication of carriers achieves a true 1 for 1 multiplication in baud rate.

Parameter Block

Key parameters for each of the modulation modes appear in the parameter block section. There is one parameter block for each modulation type.

'LB28-0.15625-10-I':-

'info' : '0.15625 characters per second, 0.9375 baud (bits per second)',

'symbol block size' : 51200,

'symbol wave function': twohundredfiftysixths symbol wave function,

'sample_rate' : 8000,
'num_carriers' : 2,
'carrier_separation' : 10,
'detector_function' : 'mode',
'baseband conversion' : 'costas loop',

'phase extraction' : EXTRACT INTERPOLATE,

'fft_filter' : (-1, 1, -1, 1), 'fft_interpolate' : (-1, 1, -1, 1),

'pulses per block' : 256,

'parameters' : (600, 0.70, 0.9, 10000, 2, 98) },

The last 'parameters' items are as follows respectively:-

- 600 Number for phase value constellation extraction to delineate +1, 0 and -1,
- 0.70 Alpha value for the RRC wave shape
- 0.9 T value for the RRC wave shape
- 10000 Baseband normalization value to normalize the resulting extraction phase
- 2 Extract phase num waves. Number of waves used for phase level determination
- 98 Percentage value of max peak to determine if a peak qualifies 'at max'.

Reference Platform

A code reference platform has been included in Python to further illustrate each of the steps and the precise detail required. For optimal performance a JIT compiler could be used in conjunction with the costas loop method as this consumes the vast majority of the processing power for demodulation. Code excerpts from the reference platform are included in Appendix 1.

Table 1: Preliminary Test Results

Preliminary Test Results for LB28-0.625-10-I, LB28-0.3125-10-I and LB28-0.15625-10-I using 2 chunks of modulated data. 67 characters total over 2 decodes with 6 bit characters.

Mode Name	CPS/bits	Eb/N0 (dB)	BER	Equiv. SNR (dB)	Width	Total Bits
AWGN Factor 7.85+						
LB28-0.625-10-I	0.625/6	-1.54	0.017412	-27.54	10 Hz	402
LB28-0.625-10-I	0.625/6	-1.53	0.019900	-27.54	10 Hz	402
LB28-0.625-10-I	0.625/6	-1.54	0.0	-27.54	10Hz	402
LB28-0.625-10-I	0.625/6	-1.52	0.007462	-27.53	10 Hz	402
LB28-0.625-10-I	0.625/6	-1.56	0.017412	-27.54	10 Hz	402
LB28-0.625-10-I	0.625/6	-1.60	0.004975	-27.55	10 Hz	402
LB28-0.625-10-I	0.625/6	-1.57	0.012437	-27.54	10 Hz	402
LB28-0.625-10-I	0.625/6	-1.58	0.019900	-27.54	10 Hz	402
LB28-0.625-10-I	0.625/6	-1.59	0.007462	-27.55	10 Hz	402
AWGN Factor 7.68+						
LB28-0.3125-10-I	0.3125/6	-1.70	0.009950	-30.57	10 Hz	402
LB28-0.3125-10-I	0.3125/6	-1.64	0.0	-30.56	10 Hz	402
LB28-0.3125-10-I	0.3125/6	-1.57	0.0	-30.55	10 Hz	402
LB28-0.3125-10-I	0.3125/6	-1.58	0.0	-30.55	10 Hz	402
LB28-0.3125-10-I	0.3125/6	-1.57	0.007462	-30.55	10 Hz	402
LB28-0.3125-10-I	0.3125/6	-1.59	0.0	-30.56	10 Hz	402
AWGN Factor 7.16+						
LB28-0.15625-10-I	0.15625/6	-1.55	0.0	-33.56	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.59	0.0	-33.57	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.59	0.0	-33.57	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.60	0.007462	-33.57	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.60	0.0	-33.57	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.61	0.024875	-33.57	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.62	0.007462	-33.57	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.63	0.0	-33.57	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.66	0.0	-33.58	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.67	0.288557	-33.58	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.72	0.002487	-33.59	10 Hz	402
LB28-0.15625-10-I	0.15625/6	-1.76	0.074626	-33.59	10 Hz	402

Appendix 1: Interpolation Algorithm

The following code excerpts are taken from the python reference platform used for the tests.

```
""" Code snippet from higher level demodulation process """
      max occurrences lists = self.removeConflictingItemsTwoList([max occurrences lower,
max occurrences higher])
      max_occurrences_lower = max_occurrences_lists[0]
      max occurrences higher = max occurrences lists[1]
      interpolated_lower = self.interpolate_contiguous_items(max_occurrences_lower)
interpolated_higher = self.interpolate_contiguous_items(max_occurrences_higher)
      half = int(self.osmod.pulses per block/2)
      """ if either of the interpolated lists is complete, fill out the other interpolated list if
incomplete """
      if len(interpolated lower) == half and len(interpolated higher) < half:</pre>
        for i in range(0,self.osmod.pulses_per_block):
          if i not in interpolated lower and i not in interpolated higher:
            interpolated_higher.append(i)
      elif len(interpolated higher) == half and len(interpolated lower) < half:
        for i in range(0,self.osmod.pulses_per_block):
   if i not in interpolated_lower and i not in interpolated_higher:
            interpolated lower.append(i)
      """ if either of the interpolated lists is greater than the other list, fill out the other
interpolated list"""
      interpolated_lower = self.sort_interpolated(interpolated_lower)
      interpolated higher = self.sort interpolated(interpolated higher)
      if len(interpolated_lower) > len(interpolated_higher):
        for i in range(interpolated lower[0],interpolated lower[-1]):
          partner offset = self.osmod.pulses per block / self.osmod.num carriers
          partner index = int((i + partner offset) % self.osmod.pulses per block)
          if partner index not in interpolated_higher:
            interpolated_higher.append(partner_index)
      elif len(interpolated_higher) > len(interpolated_lower):
        for i in range(interpolated higher[0],interpolated higher[-1]):
          partner_offset = self.osmod.pulses_per_block / self.osmod.num_carriers
          partner_index = int((i + partner_offset) % self.osmod.pulses_per_block)
          if partner_index not in interpolated_lower:
            interpolated_lower.append(partner_index)
      """ if the two halves are adjacent then can interpolate all!"""
      #if int((interpolated_higher[-1] + 1) % self.osmod.pulses_per_block) ==
interpolated higher[0]:
    """ return the most probable and complete interpolated and sorted list set """
    return self.sort_interpolated(interpolated_lower), self.sort_interpolated(interpolated_higher)
```

```
""" interpolate contiguous by distance and determine wrap around """
 def interpolate_contiguous_items(self, list_items):
      saved_list_items = list_items
     have_suspect_items = True
     while have_suspect_items:
       have_suspect_items = False
        median_input_list = int(np.median(np.array(list_items)))
        for i in list items:
          if abs(median_input_list-i)>int((self.osmod.pulses_per_block/self.osmod.num_carriers)/2):
            list_items.remove(i)
            have_suspect_items = True
     median input list truth = median input list
      """ now repeat process using final truth value as median """
      list_items = saved_list_items
      for i in list items:
       if abs(median_input_list_truth-i)>int((self.osmod.pulses_per_block/self.osmod.num_carriers)/
2):
          list_items.remove(i)
      """ walk the list to determine extents """
     half = int(self.osmod.pulses_per_block / 2)
      start_value = list_items[0]
     min value = 0
     max value = 0
     walk = 0
     for i, j in zip(list_items, list_items[1:]):
       if abs(i - j) < half:
walk = walk - (i - j)
        elif abs(j - i) < half:
          walk = walk + (j - i)
        elif abs(j + self.osmod.pulses_per_block - i) < half:</pre>
          walk = walk + (j + self.osmod.pulses_per_block - i)
        elif abs(i + self.osmod.pulses_per_block - j) < half:
          walk = walk - (i + self.osmod.pulses_per_block - j)
        if walk > max value:
         max value = walk
        if walk < min value:
          min_value = walk
     interpolated list = []
      for x in range(start_value + min_value, start_value + max_value + 1):
        interpolated_list.append(x % self.osmod.pulses_per_block)
     return interpolated list
```

```
def removeConflictingItemsTwoList(self, max_occurrences_lists):
   max_occurrences_lower = max_occurrences_lists[0]
   max_occurrences_higher = max_occurrences_lists[1]
   in both lists = []
    for i in max_occurrences_lower:
      if i in max occurrences higher and i not in in both lists:
       in both lists.append(i)
    for i in max_occurrences_higher:
     if i in max_occurrences_lower and i not in in_both_lists:
       in both lists.append(i)
    for i in in_both_lists:
     max_occurrences_lower.remove(i)
     max_occurrences_higher.remove(i)
def sort interpolated(self, interpolated lower):
   half = int(self.osmod.pulses_per_block/2)
   normalized list = []
   restored_sorted_list = []
   for item in interpolated_lower:
     if item < half:
       normalized_list.append(item + self.osmod.pulses_per_block)
      else:
       normalized list.append(item)
   normalized_list.sort()
    """ does the data wrap around? """
   if self.osmod.pulses_per_block - 1 in normalized_list:
      for item in normalized list:
        if item < self.osmod.pulses_per_block:</pre>
         restored_sorted_list.append(item)
      for item in normalized_list:
        if item >= self.osmod.pulses_per_block:
          restored_sorted_list.append(item % self.osmod.pulses_per_block)
     return restored_sorted_list
    else:
      for item in normalized list:
       if item >= self.osmod.pulses_per_block:
          restored_sorted_list.append(item % self.osmod.pulses_per_block)
      for item in normalized list:
       if item < self.osmod.pulses_per_block:</pre>
         restored_sorted_list.append(item)
     return restored_sorted_list
```