

# Harish Kumar G

## Engineer

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### Professional Summary:

As an electrical engineer with a good understanding of circuits and compiler working. I possess a fervent passion for the VLSI industry. I have a proven track record of leading and managing teams in the development and integration of designs that seamlessly operate with systems.

### Key Competencies:

- Able to in learn and use new tools, with ease.
- Skilled in System Verilog randomizations, constraints and assertions
- Good in making layout abiding DRC (Design Rule Check) and ERC (Electrical Rule Check) processes

### Projects done

**PGDVLSI** – VGA, Bit slice ALU, Verilog async FIFO, CMOS model of RCA

**Engineering** – PWM controlled cooling fan, Unmanned level crossing, AI aided tank cleaning robot,

### Education

Post Graduation Diploma in VLSI

CDAC, ACTS Pune.

Bachelors of Engineering - Electrical and Electronics Engineering, 7.44 CGPA

Anna University, Chennai, Tamil Nadu

GATE EE 2020 score - 353

### Technical skills

Vivado, Questa Sim, Linux, Tcl bash python scripting, OrCAD.

### Managerial skills

Organised and conducted workshops, Quiz events college level

### Extra-curricular

Social work - at Ayyanar Kovil, Medical camp volunteer

### Hobbies

Calligraphy, Gardening, Walking.

**Languages Known** Tamil, English