PUSHPAK SUNILRAO GHATODE

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Academic Details:

Sr. No.	Name of Exam	Board/University	Year of Passing	Percentage /CGPA
1	SSC	Maharashtra State Board	2014	96.60
2	HSC	Maharashtra State Board	2016	89.04
3	B.TECH. (7 semesters)	SGGSIE&T, Nanded	2019	9.63

Projects Developed:

Title	LED Lamp with Driver Street Light (long life) Product	
Description	The Project was aimed at reducing the capacitor size and increasing the life of the LED Driver Circuit which gets damaged due to bursting or failure of the capacitors.	
Role	Team Leader	

Title	Smart Trolley	
Description	Trolley is modified by fixing a Raspberry Pi 3 based Device along with the Barcode scanner to scan the products and count the items as well as bills.	
Role	Member of team. I dealt the barcode scanner and trolley hardware part.	

Title	Implementation of FIFO memory on FPGA
Description	Using the ISE Xilinx software, a FIFO memory is implemented with a size of 1KB and tested on the Spartan FPGA board.
Role	Team member. I made the bidirectional SRAM needed as one of the block.

Title	Smart Automatic Biometric Attendance System for a Study Point	
Description	It is an embedded system using Raspberry pi controller, keypad, optical fingerprin	
	sensor, GSM module and LCD screen. It is programmed using Python language.	
	Data is managed by using SQLite. It has different specifications than devices	
	present in market. All requirements were given by owner of Study point center.	
Role	Team member. I programmed the interfacing of various components with	
	controller.	

Internship/Training:

Summer Internship Program 2019 of Technology Innovation and Entrepreneurship Centre (TIEC), SGGSIE&T, Nanded. "Hands on Training and Understanding of Projects to Product Building Basics."

Advanced Digital design and Verilog training by Mr. Vaibbhav Taraate (October 2019). I Worked on Advanced Digital Design Techniques. Learnt about applications and scope of FPGAs in various industries.

Work Experience:

From last 3 years I am teaching as a home tutor for students in class 6th to 10th and diploma students pursuing first year.

Technical Skills:

- Programming Languages: C, C++, Python.
- Database: SQL, SQLite.
- Markup Language: basics of HTML, JSON.
- Hardware Description Languages: Verilog HDL.
- Tools used: Cadence, Xilinx ISE 14.7, Proteus, Micro-wind, DBbrowser, MATLAB, ExpressPCB.

Strength:

- I have good communication skills.
- I am a team player and can easily interact with people.
- Languages: English, Hindi, and Marathi.
- I have good reasoning and problem-solving skills.

Extracurricular Activities:

- Class representative for 2nd and 3rd year of Engineering.
- I was member of Institute Student Council in 2017-18.
- I was member of Institute Innovation Council in 2018-2019.
- Participated in 8th and 9th National level Athletics competition in 2012 and 2013.

Hobbies:

playing chess, watching action movies, reading books, cooking.

DECLARATION:

I hereby declare that the above said information is true to my knowledge.

Date: 30 September 2020

Place: Nanded.

Pushpak Sunilrao Ghatode

(digitally signed)