Ghazi AOUSSAJI

Electrical engineer

Electronics and Micro-electronics

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ELECTRONICS SKILLS

Digital design
VHDL , Vivado
Analog design
Cadence Virtuoso
Simulation
MATLAB , QuestaSim

COMPUTER SKILLS

Programming languages
C, C++, python
Software
Qt creator, SoftConsole
Operating System
Ubuntu, Cent-OS, Windows

KITS

XILINX XCVU108 , Zynq7000 , Smartfusion2m2s050 , RaspberryPI , STM32F429,

LANGUAGES

English : fluent (TOEIC : 915)
French : fluent
Arabic : Native
German : basic

WORK EXPERIENCE

Electrical engineer

CODINTEK - ELGHAZELA Technopark - Since June 2019
Hardware and software development for High data rates wireless communication systems.
Key words: RF , SOC , Physical layer , RTOS , OFDM.

Intern(graduation project)

Codintek - Tunisia - From February 2019 to June 2019
Design and implementation of low complexity digital pre-distortion (DPD) algorithms for wireless transmitters.
Key words: MATLAB , DSP , VHDL , RF

EDUCATION

Electrical engineering

National Engineering School of Tunis - El-Manar, Tunisia
- From September 2016 to June 2019
This diploma benefits from the European accreditation EUR-ACE

Master thesis : Communication system

National engineering School of Tunis - Tunisia
- From September 2018 to November 2019

PROJECTS

Analog design of Low power VCO for UWB transmitter
Keywords: Tanner , TSMC250nm , Ultra Wide Band
Analog design of 3 bits flash architecture ADC
Keywords: Virtuoso, XFAB180, operational amplifier
Digital Predistortion Module for 480MHZ bandwidth modem
Keywords: VHDL , FFT1024 , RF amplifier
Crest factor reduction(CFR) module for mmWave frontend
Keywords: MATLAB , VHDL , FPGA , RF transceivers , Power amplifier
JESD link with AD9695 ADC for microwaves application
Keywords: RF , XILINX VCU108 , JESD204B , VHDL , C
Digital design of a DLX microprocessor
Keywords: VHDL , RISC , ModelSim , MATLAB
Data acquisition and SPI to USB conversion
Keywords: AD7324 , MCP2210 , QT , MQTT , NodeRed
PRBS generator and checker for optical link testing
Keywords: VHDL , XILINX KC705 , VIO , ILA , low powerGBT , TCL
Implementation of AES module for data encryption
Keywords: VHDL, Microsemi FPGA, IOT

INSTRUMENTS AND DEBUGGING

Spectrum analyser(HP)
Oscilloscope(LeCroy WaveRunner)
JTAG and UART interface
Charaterising RF equipments