1 Classical Planning Overview

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- 1.4.1 Blocks World

Listing 1: Blocks World Domain Description in PDDL

```
(define (domain BLOCKS)
      (:requirements :strips :typing)
      (:types block)
      (:predicates (on ?x - block ?y - block)
                   (ontable ?x - block)
                   (clear ?x - block)
                   (handempty)
                   (holding ?x - block)
10
     (:action pick-up
                 :parameters (?x - block)
                 :precondition (and (clear ?x) (ontable ?x) (handempty))
                 (and (not (ontable ?x))
                       (not (clear ?x))
                       (not (handempty))
                       (holding ?x)))
     (:action put-down
                 :parameters (?x - block)
                 :precondition (holding ?x)
                 :effect
                 (and (not (holding ?x))
                       (clear ?x)
                       (handempty)
26
                       (ontable ?x)))
     (:action stack
                 :parameters (?x - block ?y - block)
29
                 :precondition (and (holding ?x) (clear ?y))
30
                 (and (not (holding ?x))
                       (not (clear ?y))
                       (clear ?x)
                       (handempty)
                       (on ?x ?y)))
     (:action unstack
                 :parameters (?x - block ?y - block)
38
39
                 :precondition (and (on ?x ?y) (clear ?x) (handempty))
                 :effect
                 (and (holding ?x)
                       (clear ?y)
42
                       (not (clear ?x))
                       (not (handempty))
                       (not (on ?x ?y)))))
```

1.4.2 Towers of Hanoi

Listing 2: Towers of Hanoi Domain Description in PDDL

```
;; towers of hanoi
    (define (domain HANOI)
        (:requirements :typing)
        (:types disc peg)
        (:predicates
            (clear ?x)
            (on ?x - disc ?y)
            (larger ?d - disc ?e - disc)
        (:action stack-d
            :parameters (?d - disc ?e - disc)
            :vars (?l)
            :precondition (and
                 (on ?d ?l)
                 (not (on ?d ?e))
                 (not (= ?d ?e))
                 (not (= ?e ?l))
                 (larger ?e ?d)
                 (clear ?d)
                 (clear ?e)
24
            :effect (and
                 (not (on ?d ?l))
26
                 (not (clear ?e))
                 (on ?d ?e)
28
                 (clear ?l)
29
            )
        )
        (:action stack-p
            :parameters (?d - disc ?p - peg)
            :vars (?l)
            :precondition (and
36
                 (on ?d ?l)
                 (clear ?p)
38
                 (clear ?d)
                 (not (= ?p ?l))
40
            :effect (and
                 (not (clear ?p))
42
                 (not (on ?d ?l))
                 (on ?d ?p)
44
45
                 (clear ?l)
            )
46
        )
   )
48
```

1.4.3 Lin's Briefcase

Listing 3: Lin's Briefcase Domain Description in PDDL

```
;; briefcase domain

define (domain BRIEFCASE)
(:requirements :typing)
```

```
(:types latch)
        (:predicates
            (open)
            (latched ?l - latch)
        (:action flip-open
            :parameters (?l - latch)
            :precondition (latched ?l)
            :effect (not (latched ?l))
        (:action flip-closed
            :parameters (?l - latch)
18
            :precondition (not (latched ?l))
20
            :effect (latched ?l)
       )
        (:action open
            :parameters ()
            :precondition (and
                (forall (?l - latch)
                    (not (latched ?l))
28
                (not (open))
30
            :effect (open)
       )
   )
```

1.4.4 Electrical Circuit

Listing 4: Electrical Circuit Domain Description in PDDL

```
;; circuit domain
   (define (domain CIRCUIT)
        (:requirements :typing :conditional-effects)
        (:types wire gate level)
        (:predicates
            (wire-high ?w - wire)
            (gate-active ?g - gate)
            (gate-level ?g - gate ?l - level)
            (and-gate ?g - gate)
            (or-gate ?g - gate)
            (inv-gate ?g - gate)
(input-to ?w - wire ?g - gate)
            (output-from ?w - wire ?g - gate)
        )
        (:action activate-wire
            :parameters (?w - wire)
            :vars (?g2 - gate)
            :precondition (and
                 (not (wire-high ?w))
                 (input-to ?w ?g2)
                 (forall (?g - gate)
24
                     (not (output-from ?w ?g))
26
            :effect (and
```

```
(wire-high ?w)
            )
29
30
        (:action deactivate-wire
            :parameters (?w - wire)
            :vars (?g2 - gate)
34
            :precondition (and
36
                (wire-high ?w)
                (input-to ?w ?g2)
                (forall (?g - gate)
38
                    (not (output-from ?w ?g))
40
            )
            :effect (and
42
                (not (wire-high ?w))
        (:action activate-and-gate
46
            :parameters (?g - gate)
48
            :vars (?w1 - wire ?w2 - wire ?w3 - wire)
            :precondition (and
                (and-gate ?g)
50
                (not (gate-active ?g))
                (input-to ?w1 ?g)
                (input-to ?w2 ?g)
                (output-from ?w3 ?g)
                (wire-high ?w1)
                (wire-high ?w2)
                (not (= ?w1 ?w2))
                (not (= ?w1 ?w3))
58
                (not (= ?w2 ?w3))
60
            :effect (and
                (wire-high ?w3)
                (gate-active ?g)
            )
        )
        (:action activate-inv-gate
            :parameters (?g - gate)
            :vars (?w1 - wire ?w2 - wire)
70
            :precondition (and
                (inv-gate ?g)
                (not (gate-active ?g))
                (input-to ?w1 ?g)
                (output-from ?w2 ?q)
                (not (wire-high ?w1))
            :effect (and
                (wire-high ?w2)
                (gate-active ?g)
            )
80
        (:action activate-or-gate
84
            :parameters (?g - gate)
            :vars (?w1 - wire ?w2 - wire ?w3 - wire)
            :precondition (and
                (or-gate ?g)
87
                (not (gate-active ?g))
88
```

```
(input-to ?w1 ?g)
(input-to ?w2 ?g)
(output-from ?w3 ?g)
(or (wire-high ?w1) (wire-high ?w2))
89
90
91
92
                      )
93
                     :effect (and
(wire-high ?w3)
(gate-active ?g)
94
95
96
                     )
97
98
             )
99 )
```