

Bitwise Ops :

Left shift (\ll):

- shifting left is equivalent to multiplication by power of 2, for example:

- $6 \ll 1 = 6 \times 2$
- $6 \ll 3 = 6 \times (2^3) = 6 \times 8 = 48$

Right shift (\gg):

- shifting right is equivalent to division by power of 2, for example:

- $12 \gg 1 = 12 : (2^1) = 6$
- $12 \gg 2 = 12 : (2^2) = 3$

$$X = \sum_{k=0}^{w-1} x_k 2^k \quad (\text{unsigned integer})$$

$$X = \left(\sum_{k=0}^{w-2} x_k 2^k \right) - \boxed{x_{w-1}} 2^{w-1} \quad (\text{sign integer})$$

sign bit

- Unsigned integer, example:

8 bit word 0b10010110 represents the unsigned value, $150 = 2 + 4 + 16 + 128$

- Signed integer (two's complement), example:

8 bit word 0b10010110 represents the signed value $-106 = 2 + 4 + 16 - 128$

- We have 0b00...0 = 0

- What is the value of $x = 0b11...1$?

$$x = \left(\sum_{k=0}^{w-2} x_k 2^k \right) - x_{w-1} 2^{w-1}$$

$$= \left(\sum_{k=0}^{w-2} 2^k \right) - 2^{w-1}$$

$$= (2^{w-1} - 1) - 2^{w-1}$$

$$= -1$$

Complementary Relationship

$$X + \neg X = -1 \Rightarrow -X = \neg X + 1$$

Exp:

$$X = 0b011011000$$


$$\neg X = 0b100100111$$


$$-X = 0b100101000$$

* The prefix "0b" designates a Boolean constant.

To translate from hex to binary, translate each hex digit to its binary equivalent, and concatenate the bits.

Example: 0x DEC1DE2CODE4F00 is
→ the prefix 0x designates a hex constant

| | | | | | | | | | | | |
|---|------|------|------|------|------|------|------|------|------|------|------|
| 1101 | 1110 | 1100 | 0001 | 1101 | 1110 | 0010 | 1100 | 0000 | 1101 | 1110 | 0100 |
|  | | | | | | | | | | | |
| D | E | C | 1 | D | E | 2 | C | 0 | D | E | 4 |

| | | | |
|--|------|------|------|
| 1111 | 0000 | 0000 | 1101 |
|  | | | |
| F | 0 | 0 | D |

Bitwise operators :

| Operator | Description |
|----------|------------------------|
| $\&$ | AND |
| $ $ | OR |
| \wedge | XOR (exclusive OR) |
| \sim | NOT (one's complement) |
| \ll | Shift left |
| \gg | Shift right |

- Examples (8-bit word)

A = 0b 10 11 00 11

B = 0b 01 10 10 01

$A \& B = 0b 00 10 00 01$

$A | B = 0b 11 11 10 11$

$A \wedge B = 0b 11 01 10 10$

$\sim A = 0b 01 00 11 00$

$A \gg 3 = 0b 00 01 01 10$

$A \ll 2 = 0b 11 00 11 00$

PROBLEMS :

1. Set k th bit in a word x to 1.

- IDEA:

* SHIFT and OR.

$$y = x^i \quad (1 \leq k)$$

- EXAMPLE:

$$K = 7$$

Diagram illustrating the bit representation of a number x . A vertical line separates the bit string into two parts. The left part is labeled x and $1 \leq k$. The right part is labeled 7^{th} and shows a bit string 1011110101101101 with the 7th bit (the 10th bit from the left) highlighted in a box.

2. Clear the k th bit in a word x .

- IDEA :

- * SHIFT, complement, and AND

$$y = x^j \& \sim (1 \leq k)$$

- EXAMPLE :

$$K = 7$$

→ 7th bit

| | | |
|----------------------|--|--------------------------|
| x | | 10111101 <u>1</u> 101101 |
| $1 \ll k$ | | 000000000010000000 |
| $\sim(1 \ll k)$ | | 1111111101111111 |
| $x \& \sim(1 \ll k)$ | | 1011110101101101 |

3. Toggle / Flip the k th bit in a word x
 - IDEA:

* SHIFT and XOR

$$y = x \wedge (1 \ll k)$$

- EXAMPLE ($0 \rightarrow 1$):

$$k = 7$$

| | | |
|----------------------|--|---------------------------|
| x | | 10111101 <u>0</u> 1101101 |
| $1 \ll k$ | | 000000000010000000 |
| $x \wedge (1 \ll k)$ | | 1011110111101101 |

\nwarrow k th bit

4. Extract a bit field from a word x
 - IDEA:

* mask and shift

$$(x \& \text{mask}) \gg \text{shift}$$

- EXAMPLE:
shift = 7

| | |
|-------------------|-------------------|
| x | 1011110101101101 |
| mask | 0000011110000000 |
| x & mask | 0000010100000000 |
| x & mask >> shift | 00000000000001010 |

• this is a good trick to know if you're working with compressed or encoded data.

5. Set a bit field in a word x to a value y.

- IDEA:

* invert mask to clear, and OR the shifted value

$$x = (x \& \sim \text{mask}) \mid (y \ll \text{shift})$$

- EXAMPLE:

shift = 7 For safety's sake, $((y \ll \text{shift}) \& \text{mask})$

| | |
|---|------------------|
| x | 1011110101101101 |
| y | 0000000000000011 |
| mask | 0000011110000000 |
| x & ~mask | 1011100001101101 |
| $(x \& \sim \text{mask}) \mid (y \ll \text{shift})$ | 1011100111101101 |

6. Ordinary swap: swap two integers x and y .
 - the standard way of doing this is to use a temporary variable:

$$z = x$$

$$x = y$$

$$y = z$$

* we can do this by using bit tricks, swapping x and y without using a temporary.

$x = x \oplus y$ → Mark with 1's where bits differ.
 $y = x \oplus y$ → Flip the bits in y that differ from x .
 $x = x \oplus y$ → Flip the bits in x that differ from y .

For example:

| | | | | |
|-----|-------------|-------------|-------------|-------------|
| x | 10 1111 01 | 100 100 11 | 100 100 11 | 00 10 11 10 |
| y | 00 10 11 10 | 00 10 11 10 | 10 11 11 01 | 10 11 11 01 |

- why it works:

XOR is its own inverse:

$$(x \oplus y) \oplus y = x$$

| x | y | $x \wedge y$ | $(x \wedge y) \wedge y$ |
|-----|-----|--------------|-------------------------|
| 0 | 0 | 0 | 0 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 1 | 1 |
| 1 | 1 | 0 | 1 |

* when we xor something twice, it just cancel out and we get back the original thing.

- Performance :

Poor at exploiting "instruction-level parallelism (ILP)". (the naive approach is faster).

7. Find the minimum ' z ' of two integers x and y .

The standard approach will be :

if $(x < y)$:

$$z = x$$

else :

$$z = y$$

- Performance:

• one performance problem with this code is that there is a branch in this code (modern machines will do branch prediction, and for whatever branch predicts the code to take it's going to do prefetching and execute some of the instructions in advance). But the problem is, if it mispredicts the branch, it does a lot of wasted work, and the processor has to empty the pipeline and undo all of the work that it did.

- Caveat:

• the compiler is usually smart enough to optimize away the unpredictable branch, but maybe not.

? Is there a way to do a minimum without using a branch

$$r = y \wedge ((x \wedge y) \& -(x < y))$$

Why it works:

- The C language represents the Booleans TRUE and FALSE with the integers 1 and 0.
- If $x < y$, then $-(x < y) \Rightarrow -1$, which is all 1's in two's complement representation. Therefore, we have $y \wedge (x \wedge y) \Rightarrow x$
- If $x \geq y$, then $-(x < y) \Rightarrow 0$. Therefore, we have $y \wedge 0 \Rightarrow y$.

Info! Modern compilers can perform this optimization better than you can. (the branchless version is usually slower than the branching).

*** Why learn bit hacks if they don't even work?**

- Because the compiler does them, and it will help to understand what the compiler is doing when you look at the assembly code.
- Because sometimes the compiler doesn't optimize, and you have to do it yourself by hand.
- Because many bit hacks for words extend naturally to bit and word hacks for vectors.
- Because these tricks arise in other domains.
- Because they're fun! YESSS!!)

8. Modular Addition : compute $(x+y) \bmod n$, assuming that $0 \leq x < n$ and $0 \leq y < n$.

$$z = (x+y) \% n \quad (\text{division is expensive, unless by a power of 2})$$

$$z = x+y$$
$$z = z \text{ if } z < n \text{ else } z - n \quad (\text{Unpredictable branch is expensive})$$

$$z = x+y$$
$$z = z - (n \& - (z \geq n)) \rightarrow \text{same trick as minimum}$$