

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.  
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.  
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

Fri May 9 13:11:49 2014

REV	ECN	DESCRIPTION OF REVISION	CK APPD DATE
6	0002780389	ENGINEERING RELEASED	2014-05-09

# N56 MLB: CARRIER BUILD

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5	SOC:GND,VDDIO18,VDDIOD,VDD_VAR_SOC	N61_MLB 10/08/2013
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14	POWER:TIGRISK,VIBE DRIVER	N61_MLB 11/01/2013
15	DISPLAY:CHESTNUT,BACKLIGHT DRIVER	N61_MLB 11/01/2013
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17	IO:TRISTAR2	N61_MLB 11/01/2013
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21	SENSORS:MESA FLEX CONN	08/26/2013
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23	CAMERA:REAR FLEX CONN	N61_MLB 11/01/2013
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39	RF TRANSCIEVER (3 OF 3)	N56_RADIO_MLB 05/07/2014
40	QFE DCDC	N56_RADIO_MLB 05/07/2014
41	2G PA	N56_RADIO_MLB 05/07/2014
42	VERY LOW BAND PAD	N56_RADIO_MLB 05/07/2014
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52	WIFI/BT: MODULE AND FRONT END	N56_RADIO_MLB 05/07/2014
53		N56_RADIO_MLB 05/07/2014
54	JUMPER	N56_RADIO_MLB 05/07/2014
55	JUMPER	N56_RADIO_MLB 05/07/2014

SCH 051-0517  
 BRD 820-3675  
 MCO 056-6398

BOM 639-4924 (16GB) BOM 639-00196 (16GB,DTD)  
 BOM 639-6157 (32GB) BOM 639-00194 (32GB,DTD)  
 BOM 639-6158 (64GB) BOM 639-00195 (64GB,DTD)  
 BOM 639-00150 (128GB) BOM 639-00197 (128GB,DTD)

## N56 BOM CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-0517	1	SCH,MLB,N56	SCH	CRITICAL	?
820-3675	1	PCBF,MLB,N56	PCB	CRITICAL	?
825-6838	1	EEEE FOR 639-4924 16GB	EEEE_FKV8	CRITICAL	EEEE_16G
825-6838	1	EEEE FOR 639-6157 32GB	EEEE_FRR9	CRITICAL	EEEE_32G
825-6838	1	EEEE FOR 639-6158 64GB	EEEE_FRRC	CRITICAL	EEEE_64G
825-6838	1	EEEE FOR 639-00150 128GB	EEEE_G05J	CRITICAL	EEEE_128G
825-6838	1	EEEE FOR 639-00196 16GB NON-TDD-LTE	EEEE_G166	CRITICAL	EEEE_16G_DTD
825-6838	1	EEEE FOR 639-00194 32GB NON-TDD-LTE	EEEE_G163	CRITICAL	EEEE_32G_DTD
825-6838	1	EEEE FOR 639-00195 64GB NON-TDD-LTE	EEEE_G165	CRITICAL	EEEE_64G_DTD
825-6838	1	EEEE FOR 639-00197 128GB NON-TDD-LTE	EEEE_G164	CRITICAL	EEEE_128G_DTD

## NAND BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S0992	1	NAND,1VNN,16GX8,MLC,PPN1.5,64G,SNLGA60	U0604	CRITICAL	NAND_16G
335S0993	1	NAND,1VNN,32GX8,MLC,PPN1.5,64G,SNLGA60	U0604	CRITICAL	NAND_32G
335S1000	1	NAND,19NM,64GX8,MLC,PPN1.5	U0604	CRITICAL	NAND_64G
335S00010	1	NAND,19NM,128GX8,TLC,PPN1.5	U0604	CRITICAL	NAND_128G
138S00003	1	CAP,X5R,1.0UF,204,6.3V,0.45MH,HRZTL,0402	C0614,C0633	CRITICAL	NOSTUFF
138S0867	1	CAP,X5R,1.0UF,204,6.3V,0.45MH,HRZTL,0402	C0610,C0611,C0613,C0626,C0624,C0633	CRITICAL	NAND_16G
138S0867	1	CAP,X5R,1.0UF,204,6.3V,0.45MH,HRZTL,0402	C0610,C0611,C0613,C0626,C0614,C0633	CRITICAL	NAND_32G
138S00003	1	CAP,X5R,1.0UF,204,6.3V,0.45MH,HRZTL,0402	C0610,C0611,C0613,C0626,C0614,C0633	CRITICAL	NAND_64G

## INVENSENSE ACCEL/GYRO

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
338S00017	1	IC,CARBON,MPU-6700-12,LGA16	U2203	CRITICAL	INVSNS_GYRO
132S0395	1	CAP,CER,0.1UF,10%,6.3V,X68,0201	C2211	CRITICAL	INVSNS_GYRO

## BOSCH ACCEL/GYRO

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
338S00028	1	IC,CARBON,BMI162BC,LGA16	U2203	CRITICAL	BOSCH_GYRO
132S0395	1	CAP,CER,0.1UF,10%,6.3V,X68,0201	C2211	CRITICAL	BOSCH_GYRO

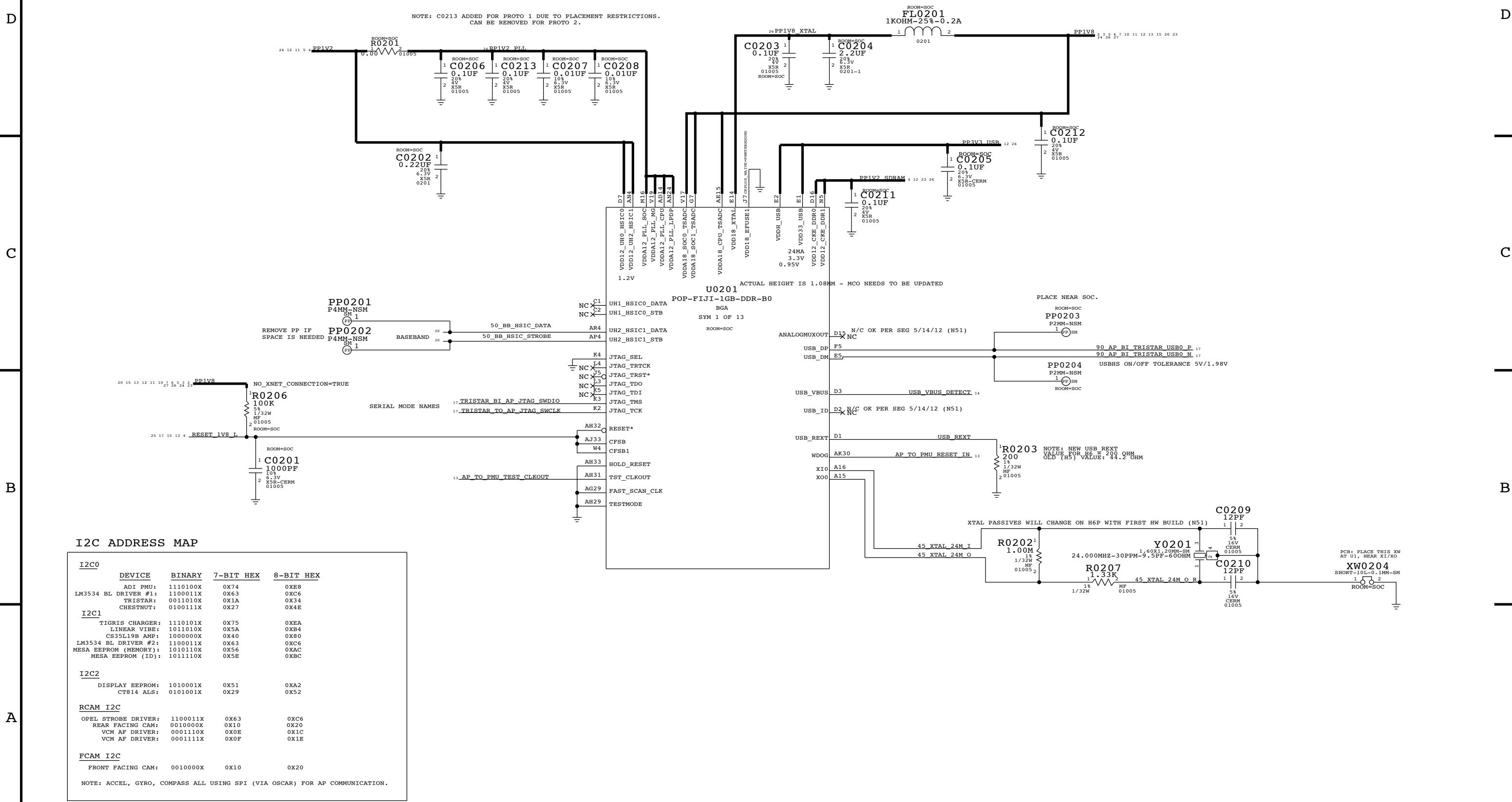
## ST ACCEL/GYRO

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
338S00029	1	IC,CARBON,AP6DS2AA,LGA16	U2203	CRITICAL	ST_GYRO
132S0391	1	CAP,CER,0.01UF,10%,25V,X5R,0201	C2211	CRITICAL	ST_GYRO

## ALTERNATE BOM OPTIONS

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
152S1844	152S1836	ALTERNATE	L1604	TY ALT INDUCTOR
152S1802	152S1849	ALTERNATE	L1519	CYNTEC ALT INDUCTOR
152S1842	152S1849	ALTERNATE	L1519	TY ALT INDUCTOR
197S0392	197S0369	ALTERNATE	Y1200	ESPON ALT XTAL
197S0399	197S0369	ALTERNATE	Y1200	NDK ALT XTAL
338S1285	338S1202	ALTERNATE	U1601	L21 BO
152S203				

# FIJI: JTAG, USB, HSIC, XTAI



WAKE\_BT  
BB\_RST\_L  
WLAN\_JTAG\_SWDCLK  
WLAN\_JTAG\_SWDIO

BT\_PCM\_CLK  
BT\_PCM\_SYNC  
BT\_PCM\_OUT  
BT\_PCM\_IN

BT\_UART\_RTS\_L  
BT\_UART\_CTS\_L  
BT\_UART\_TXD  
BT\_UART\_RXD

BB\_IPC\_GPIO1  
AP\_WAKE\_MODEM

STOCKHOLM\_SIM\_SEL

BB\_I2S\_CLK  
BB\_I2S\_WS  
BB\_I2S\_TXD  
BB\_I2S\_RXD

BB\_DEVICE\_RDY  
BB\_GPS\_SYNC  
BB\_HOST\_RDY  
BB\_RESET\_DET\_L  
PP1V8

STOCKHOLM\_RTS\_L  
STOCKHOLM\_CTS\_L  
STOCKHOLM\_UART\_TXD  
STOCKHOLM\_UART\_RXD

WLAN\_UART\_RTS\_L  
WLAN\_UART\_CTS\_L  
WLAN\_UART\_TXD  
WLAN\_UART\_RXD

RADIO\_ON\_L

PP1V8

PP1V8  
BB\_CORE\_DUMP

PCIE\_DEV\_WAKE

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7

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1

# FIJI: VDDCA, VDD1/2, VDDQ, VDD, VDD\_FIXED, VDD\_CPU, VDD\_GPU

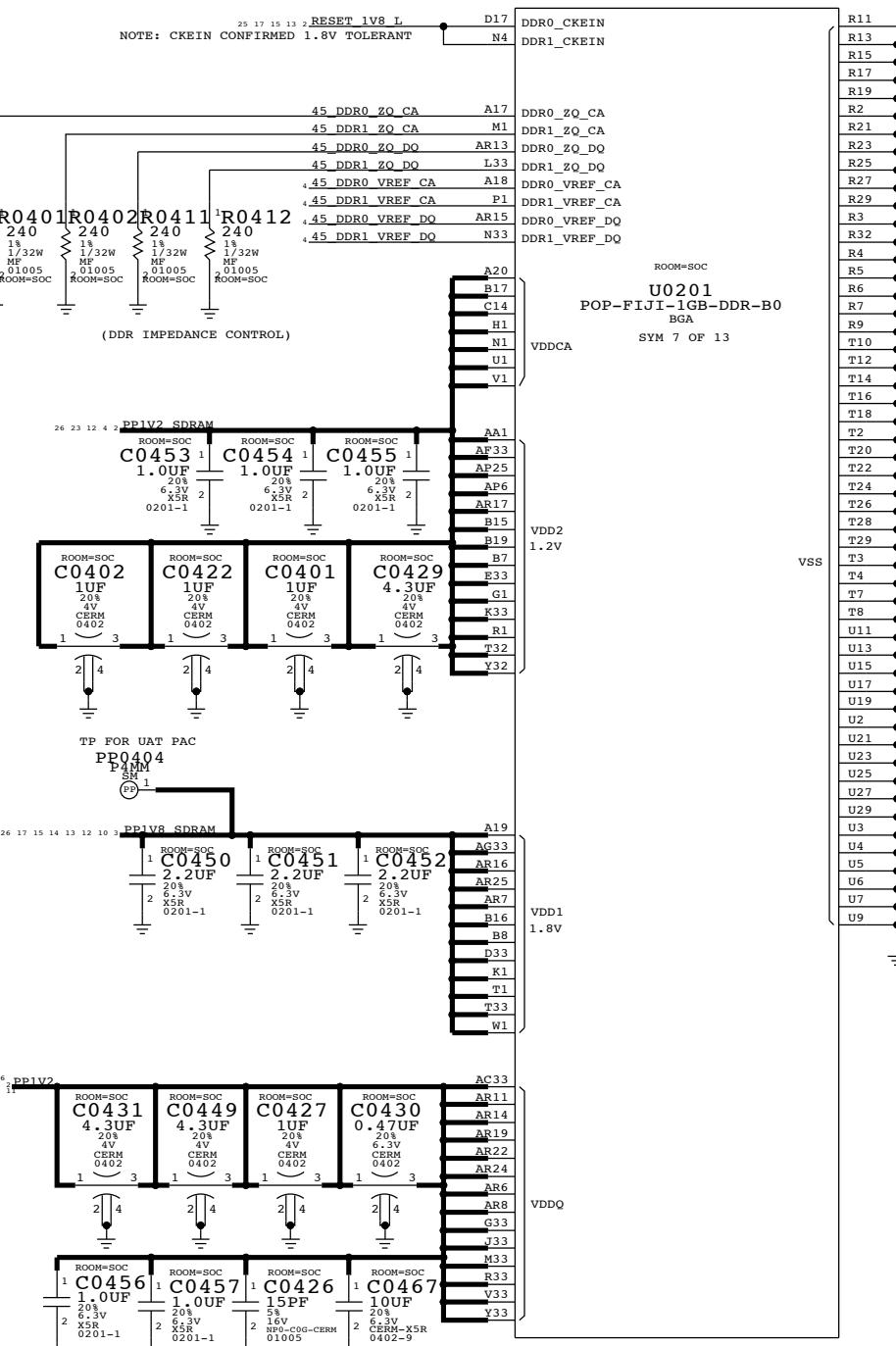


VDDCA, VDD1/2, VDDQ

VDD

VDD\_CPU, VDD\_GPU

D





# FIJI: NAND + 12x17 NAND PKG

SUPPORT FOR PPN1.5 (1.8V IO) ONLY

D

D

C

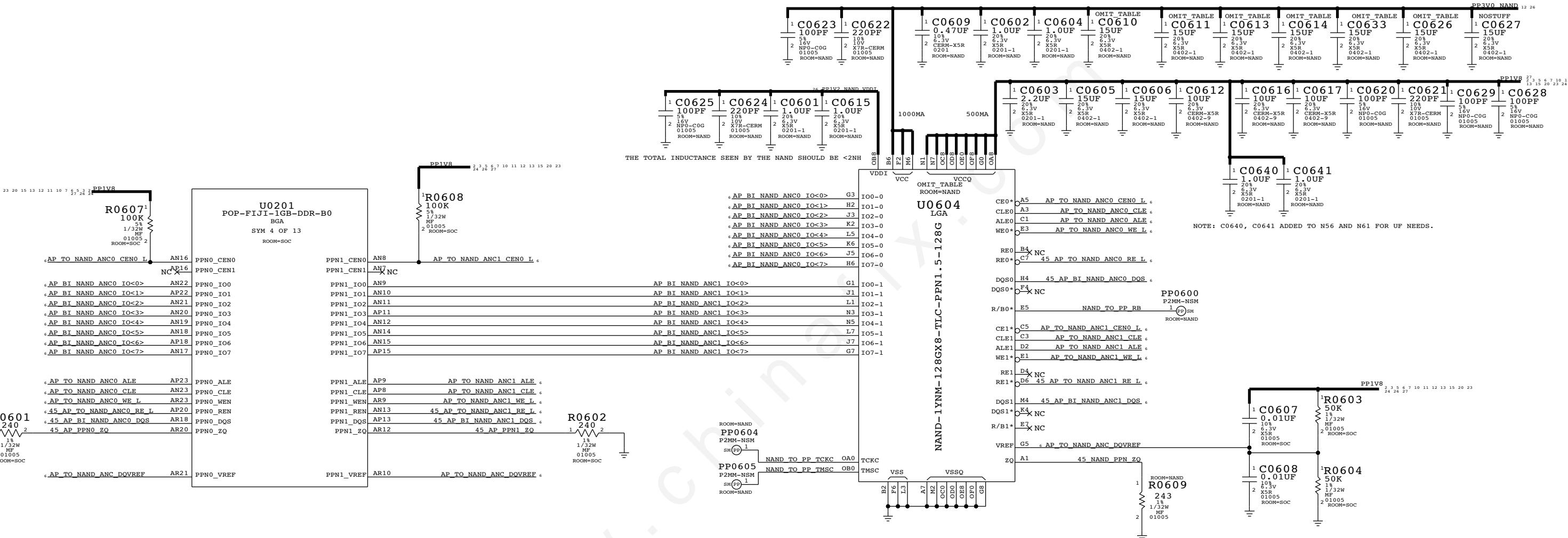
C

B

B

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A



NOTE: NAND PADS SHOULD BE SHIELDED FROM TRACES WITH A GROUND PLANE

PP0601  
P4MM-NSM  
SN 1  
ROOM=SOC  
AP\_BI\_NAND\_ANC0 IO<6>

PP0602  
P4MM-NSM  
SN 1  
45\_AP\_TO\_NAND\_ANC0\_RE\_L  
ROOM=SOC

PP0603  
P4MM-NSM  
SN 1  
45\_AP\_BI\_NAND\_ANC0\_DQS  
ROOM=SOC

# FIJI: HIGH SPEED DIG (CAM, LCD, LPDP, PCIE)

D

C

B

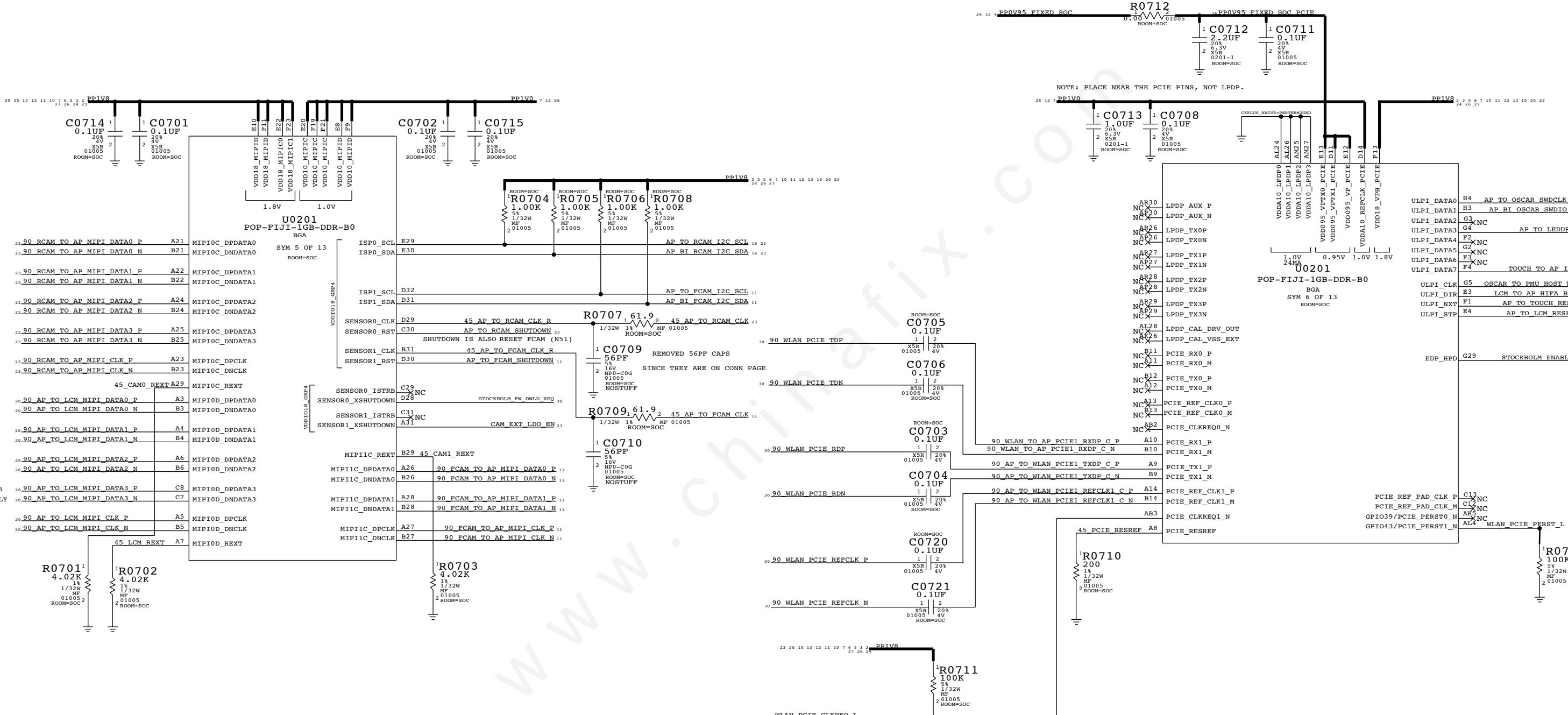
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# BUTTON FLEX (BUTTONS, ANC REF MIC, STROBE, STROBE\_NTC, WIFI FLEX PAC)

D

D

C

C

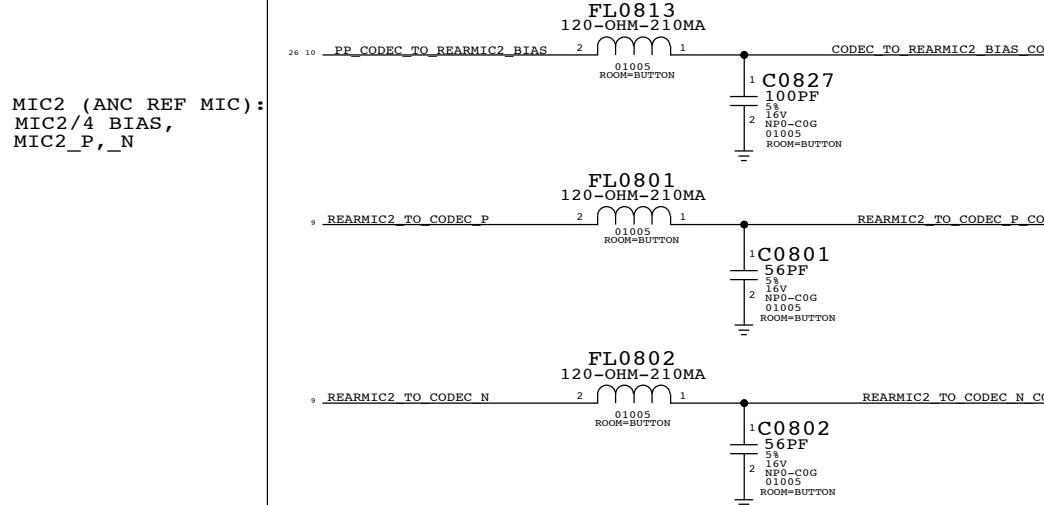
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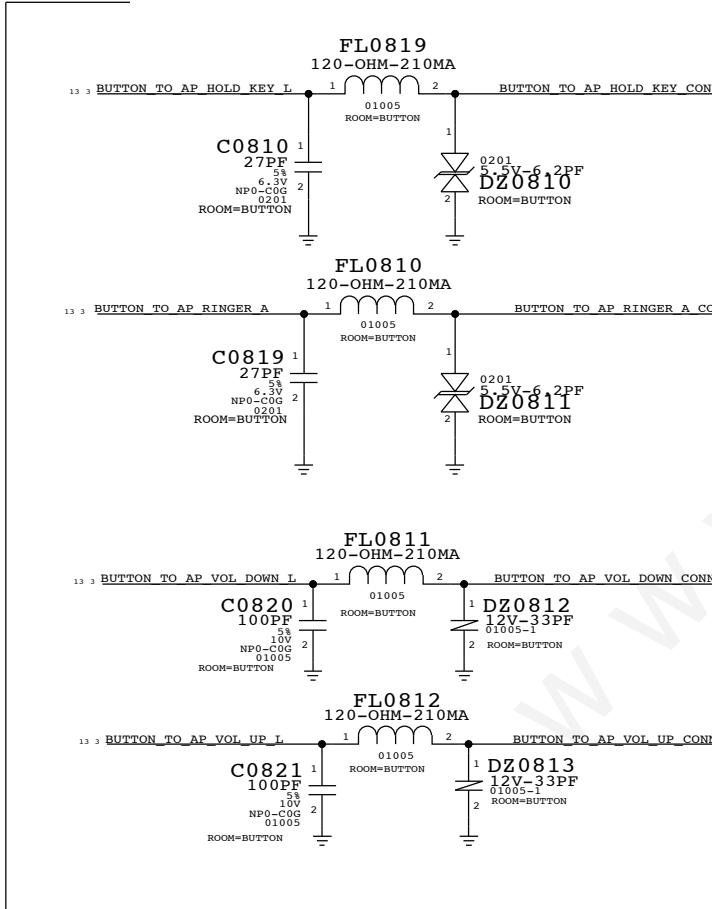
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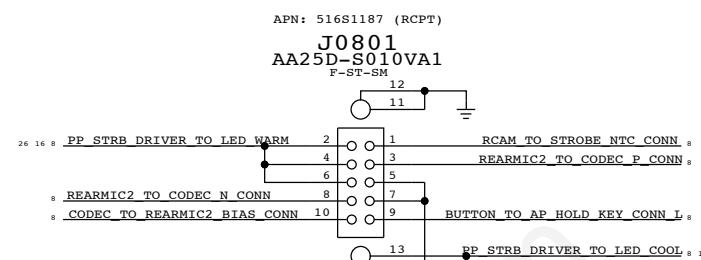
MIC2 (ANC REF MIC):  
MIC2/4 BIAS,  
MIC2\_P, N



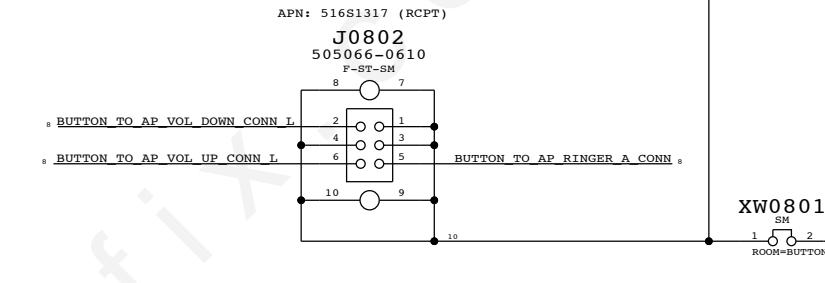
BUTTONS:  
RINGER, HOLD,  
VOL\_UP/DOWN,



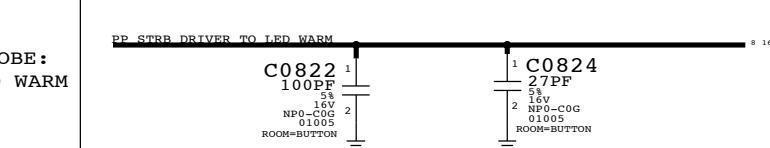
## RIGHT BUTTON FLEX B2B



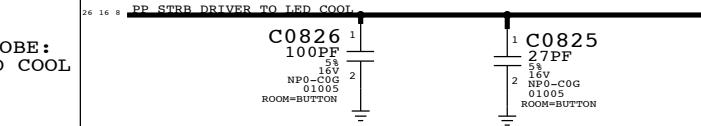
## LEFT BUTTON FLEX B2B



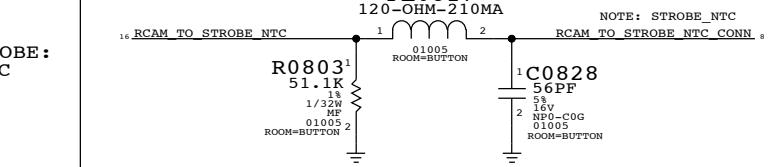
STROBE:  
LED WARM



STROBE:  
LED COOL



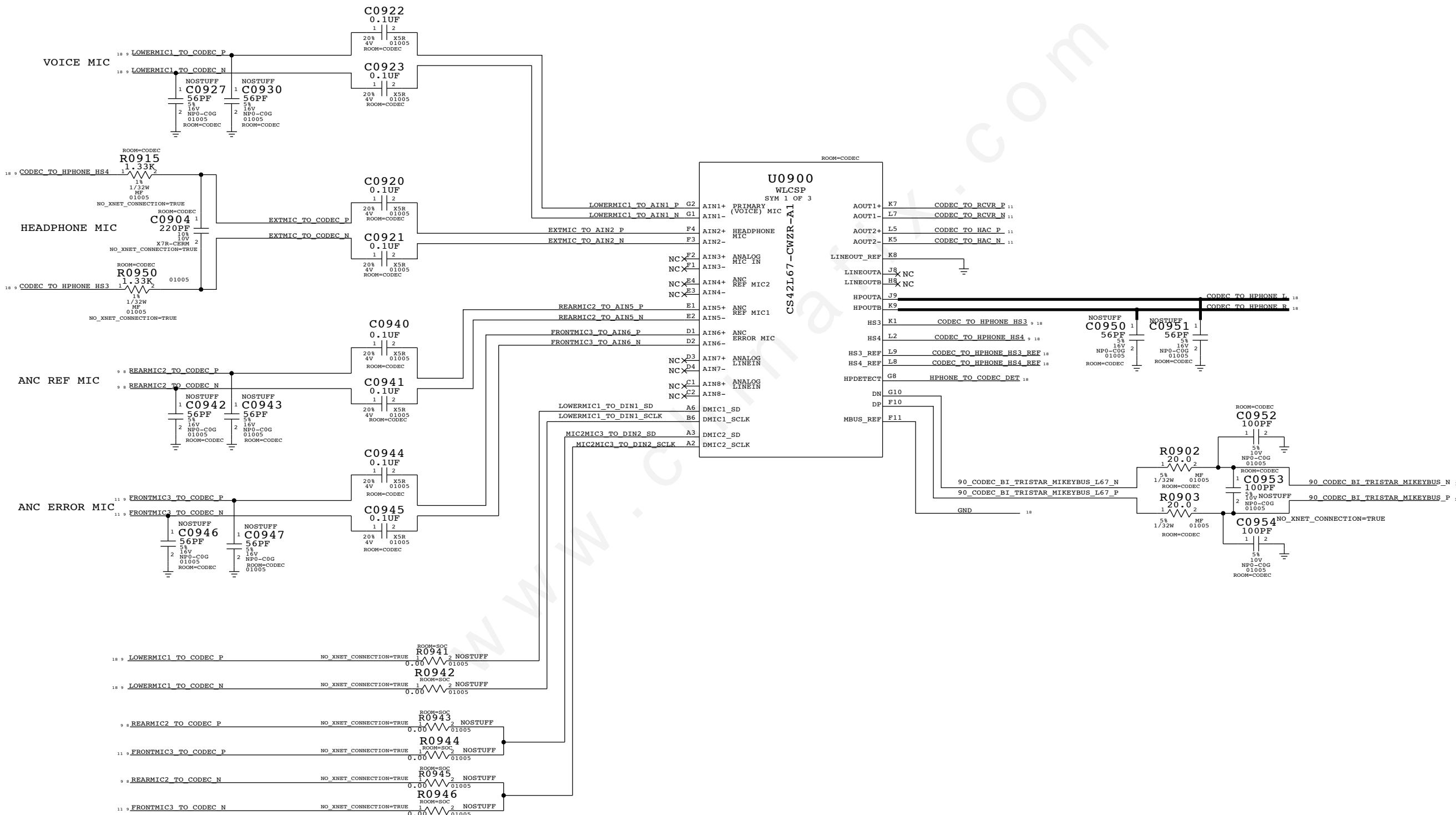
STROBE:  
NTC



# L67 AUDIO CODEC

## AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)



# L67 AUDIO CODEC

## POWER, MICBIAS

D

D

C

C

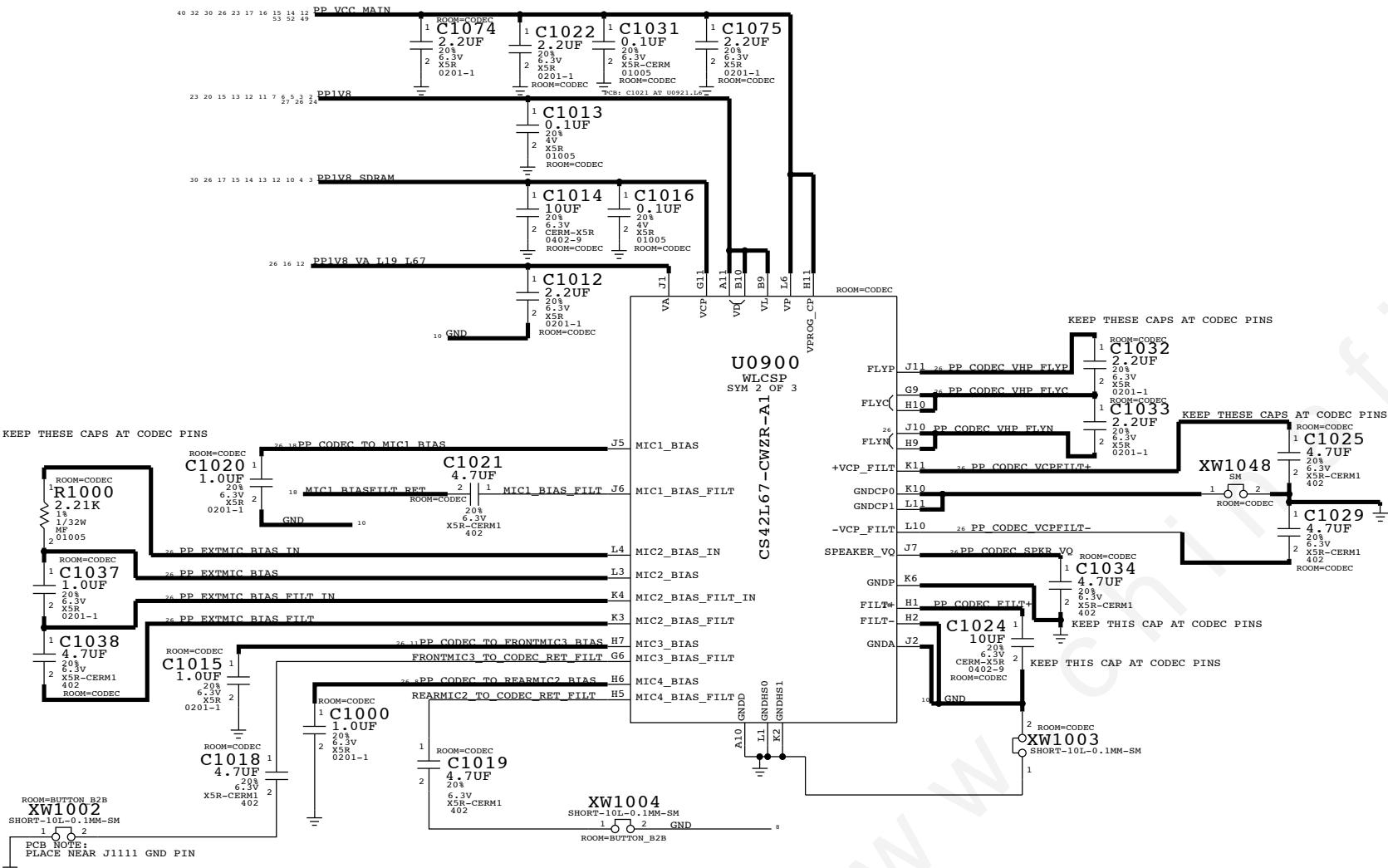
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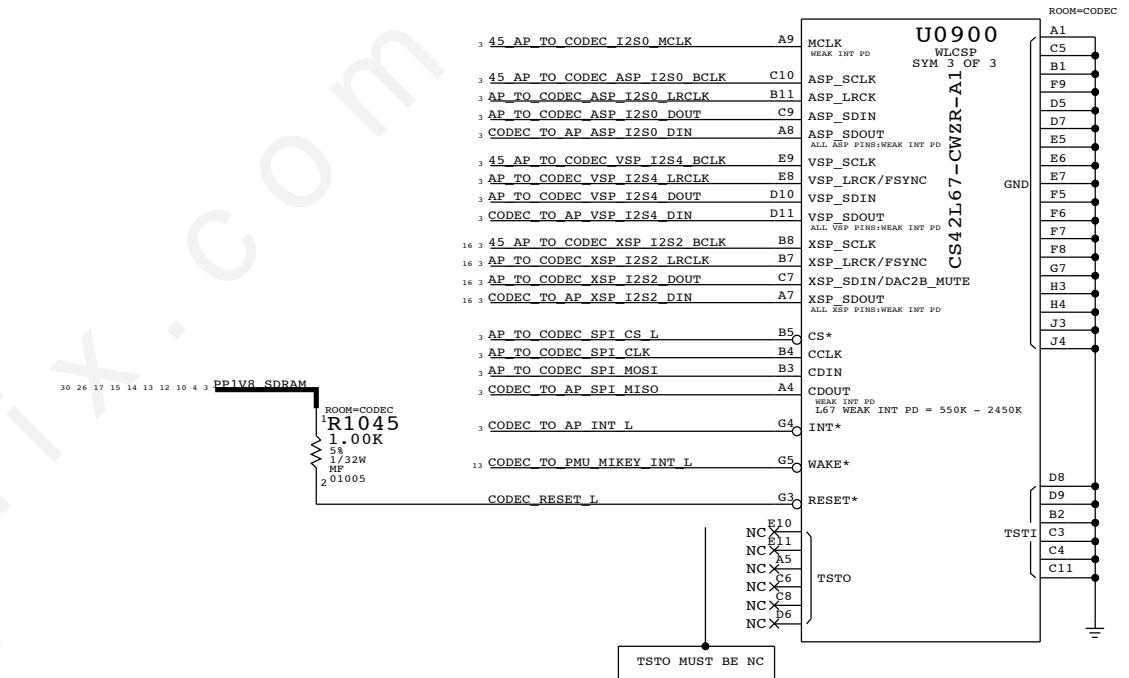
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NOTE: C1022 WAS REDUCED TO 2.2UF BECAUSE OF  
ADDITIONAL NEARBY VCC MAIN CAPS

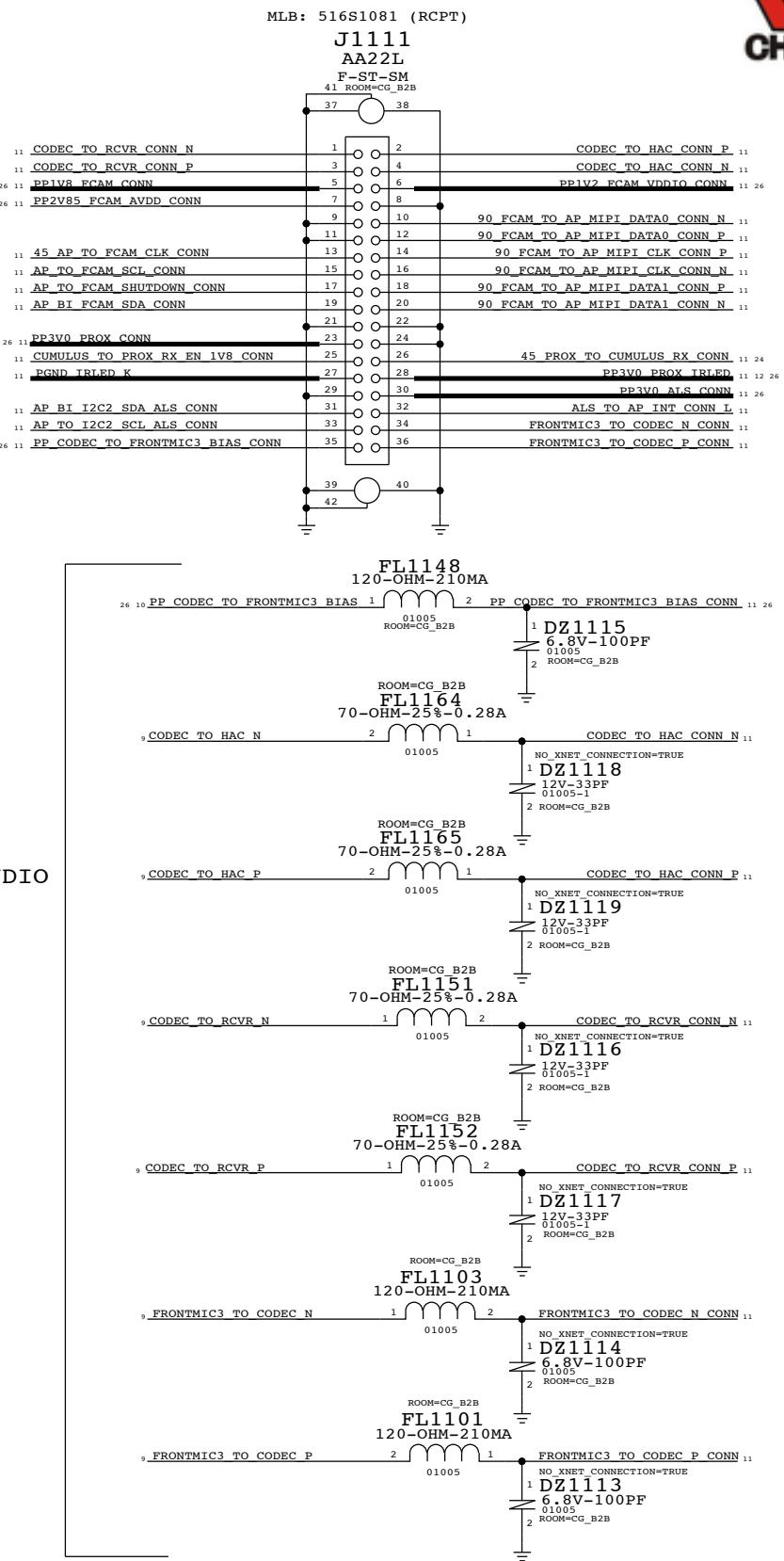
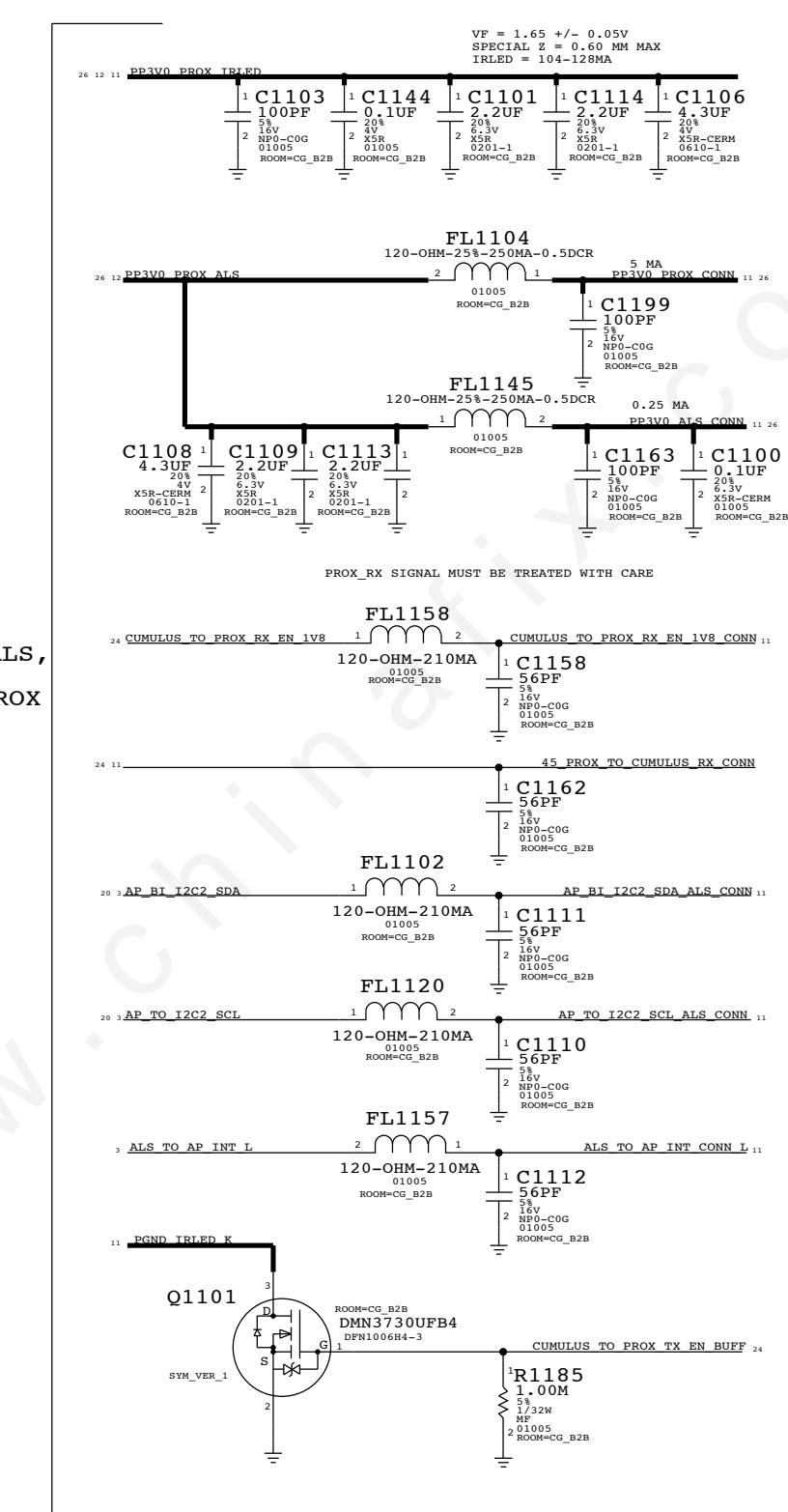
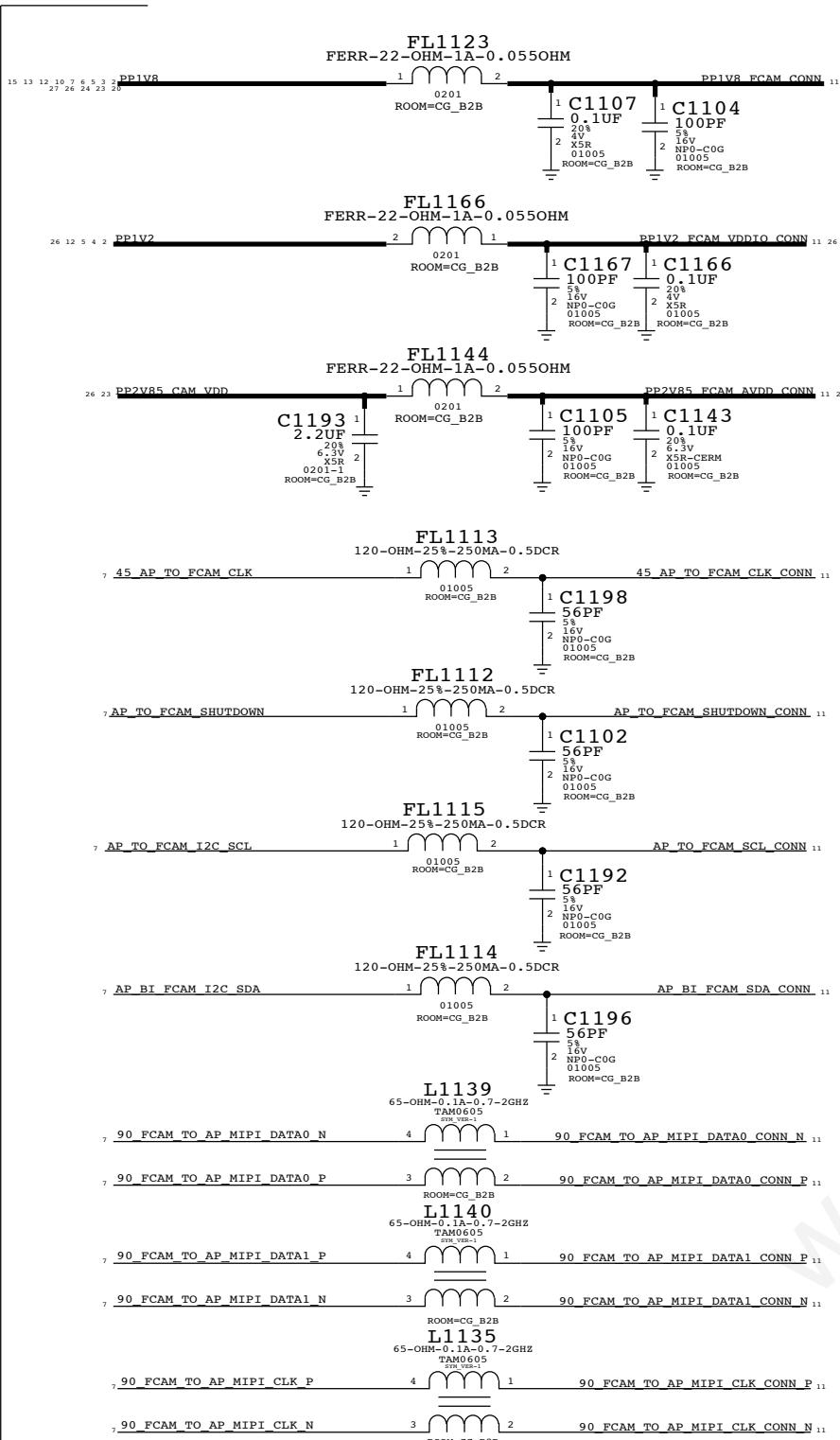


## DIGITAL SYSTEM I/O



# FRONT CAM FLEX B2B

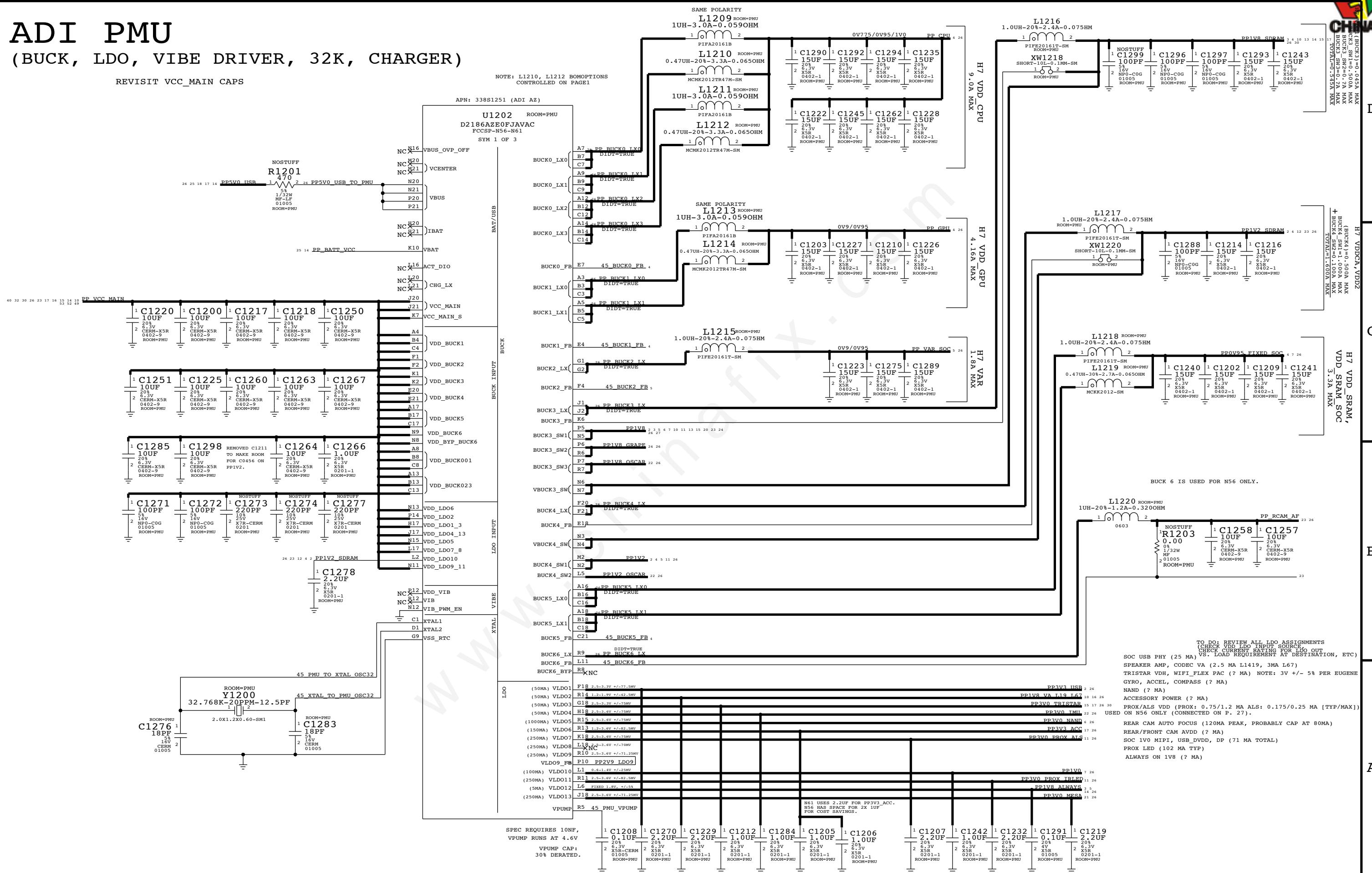
(FCAM, PROX, ALS, RECEIVER, ANC ERROR MIC)



# ADI PMU

(BUCK, LDO, VIBE DRIVER, 32K, CHARGER)

REVISIT VCC\_MAIN CAPS



# ADI PMU

(AMUX, GPIO, BUTTONS, ADC, THERMISTORS, SYSTEM I/F, GND)

D

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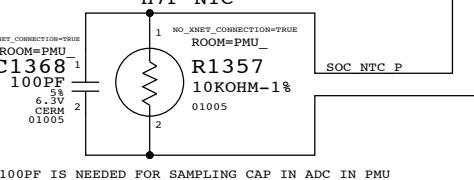
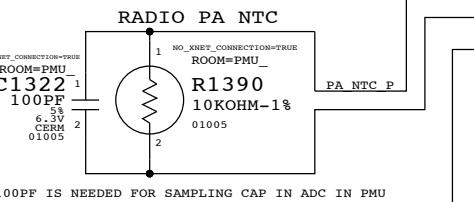
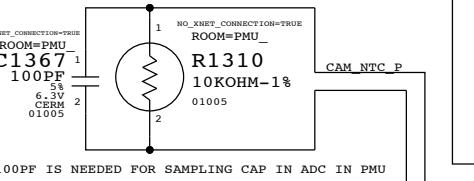
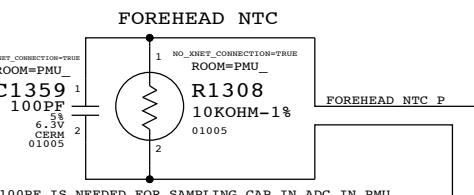
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AMUX VOLTAGE LIMIT IS APPROX. = VDD\_REF = PP\_VCC\_MAIN

APN: 338S1251 (ADI AZ)

1.8V ---->  
1.8V ---->  
1.8V ----> 13 8 ,BUTTON TO AP RINGER A  
1.8V ----> NC XA1 AMUX\_A0  
1.8V ----> NC XB1 AMUX\_A1  
1.8V ----> D2 AMUX\_A2  
1.8V ----> E1 AMUX\_A3  
1.8V ----> E2 AMUX\_A4  
3.33V ----> 20 15 LCM\_TO\_CHESTNUT\_PWR\_EN  
3.33V ----> 13 CHESTNUT\_TO\_PMU\_USB\_BRICKID\_R  
3.33V ----> 15 CHESTNUT\_TO\_PMU\_ADCIN7  
3.33V ----> 25 PMU\_TO\_TP\_AMUX\_AY  
3.33V ----> 30 VREG\_SMP31\_OV90  
3.33V ----> 34 PP\_LDO11  
BASEBAND ----> 1.8V ---->  
BASEBAND ----> 1.8V ----> 30 CLK32K\_AP  
BASEBAND ----> 34\_PP\_LDO5  
BASEBAND ----> 35\_AP\_TO\_PMU\_TEST\_CLKOUT  
BASEBAND ----> 36\_VREG\_SMP34\_2V075  
BASEBAND ----> 25 PMU\_TO\_TP\_AMUX\_BY

PCB: MAKE XW1328, XW1329 ACCESSIBLE!

17 15 13 ,AP TO I2C0 SCL  
17 15 13 ,AP\_B1\_I2C0\_SDA  
15 3 45 AP TO PMU AND BL\_DWI\_CLK  
15 3 45 AP TO PMU AND BL\_DWI\_DO  
15 3 45 AP TO PMU\_RESET\_IN  
17 TRISTAR\_TO\_PMU\_HOST\_RESET  
17 AP\_TO\_PMU\_SOCHOT1\_L  
25 17 15 4 ,RESET\_I8\_L  
25 17 15 4 ,RESET\_I9\_L  
25 17 15 4 ,RESET\_I10\_L  
25 17 15 4 ,RESET\_I11\_L  
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# TIGRIS CHARGER & VIBE DRIVER

D

D

C

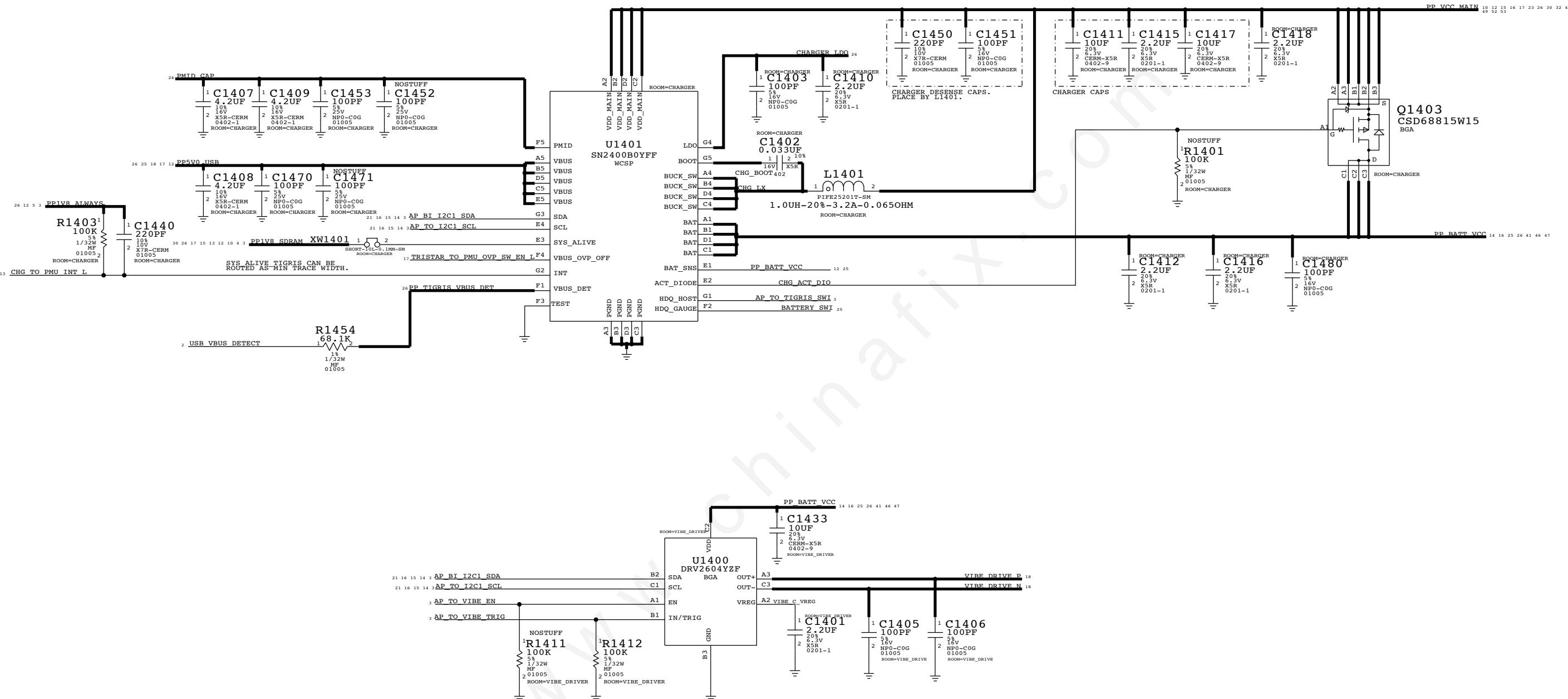
C

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B

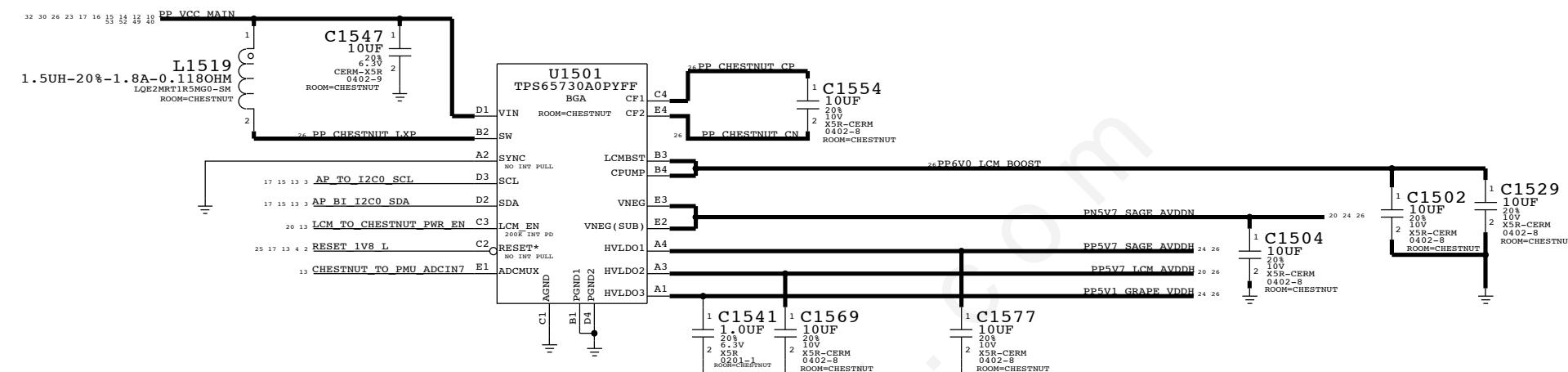
A

A

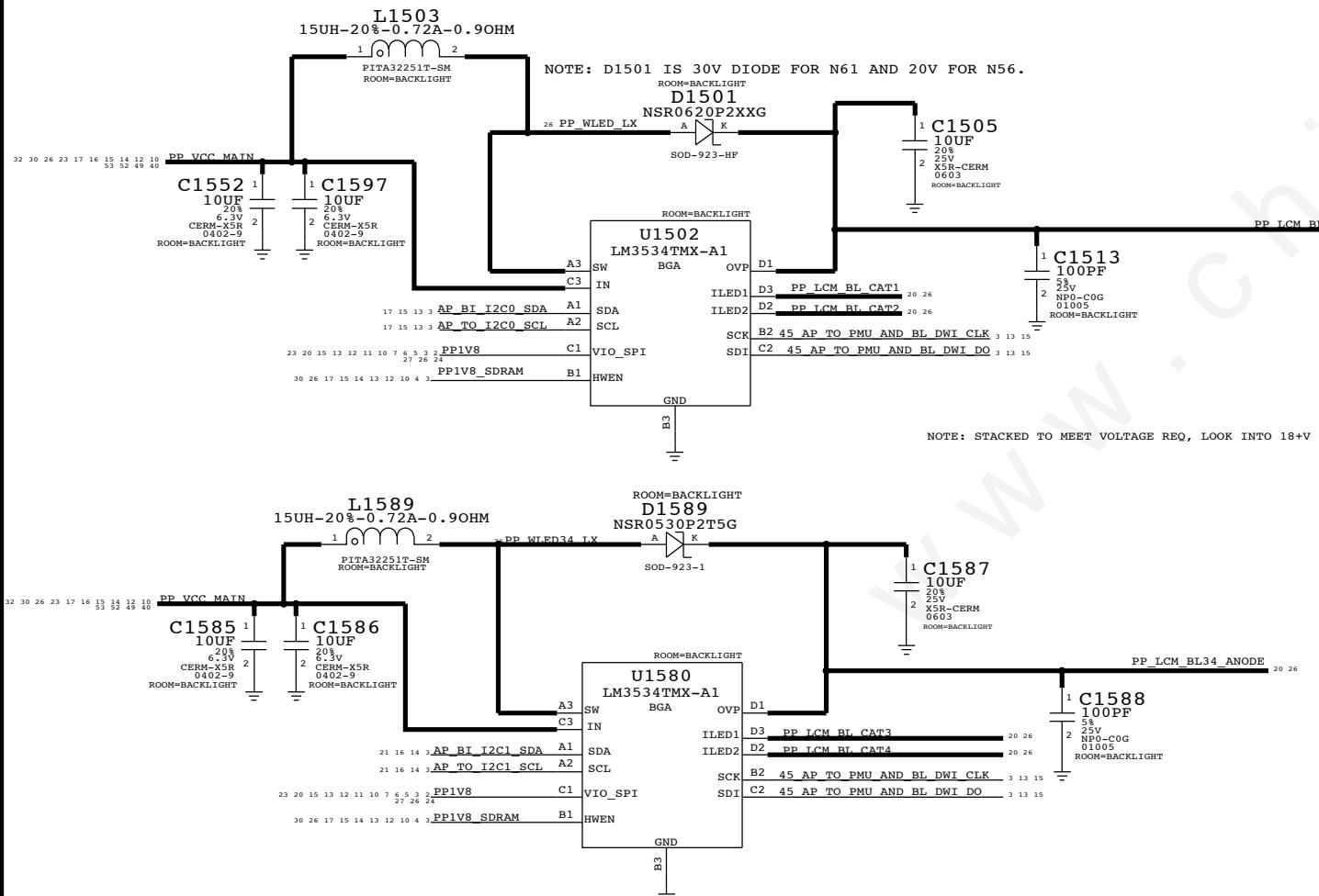


# CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

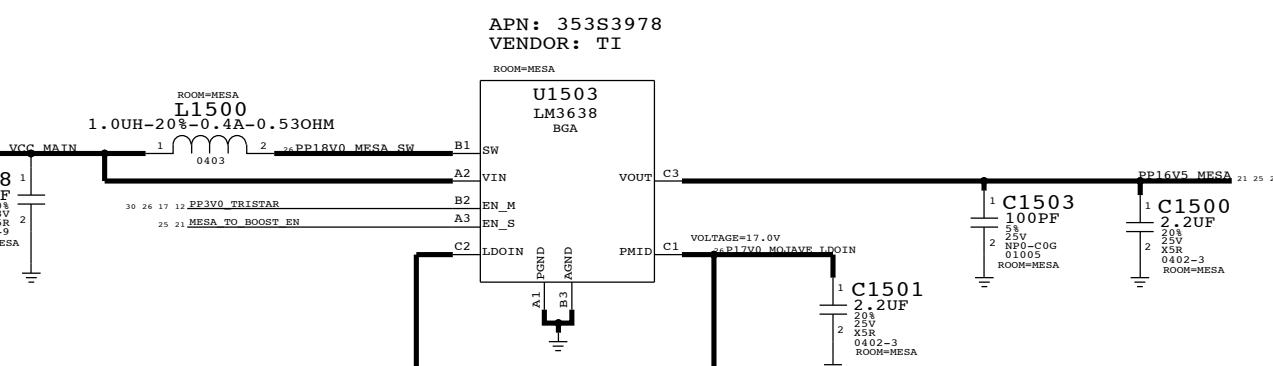
DISPLAY PMU (TI CHESTNUT, 338S1149)



## BACKLIGHT DRIVERS



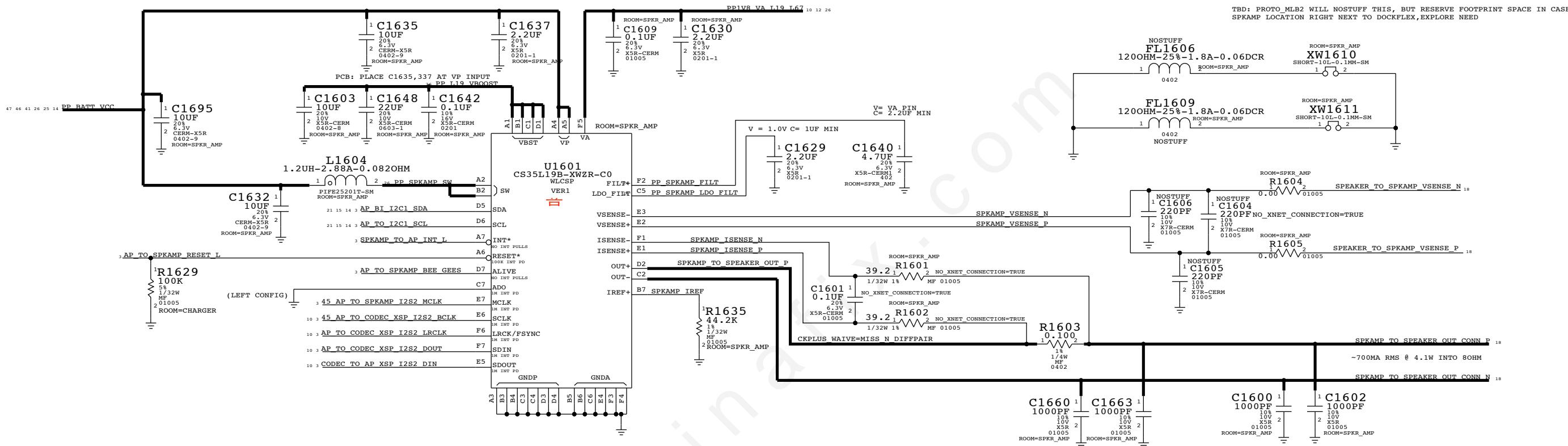
## MESA BOOST



# SPEAKER AMP, LED DRIVER

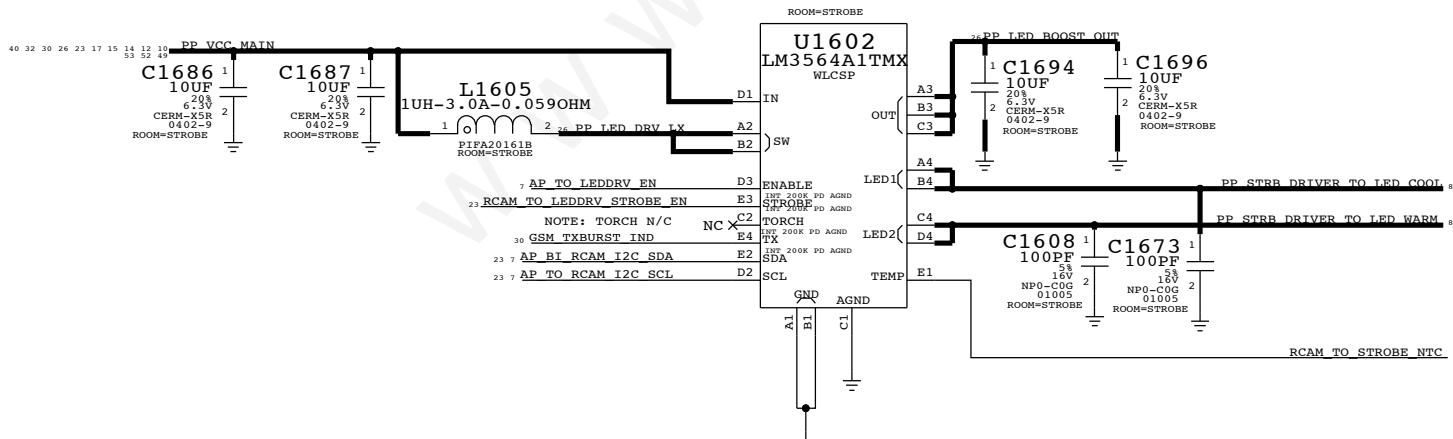
## SPEAKER AMP

I2C ADDRESS: 1000000X



## STROBE DRIVER

TI: APN 353S3899



# TRISTAR2

D

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C

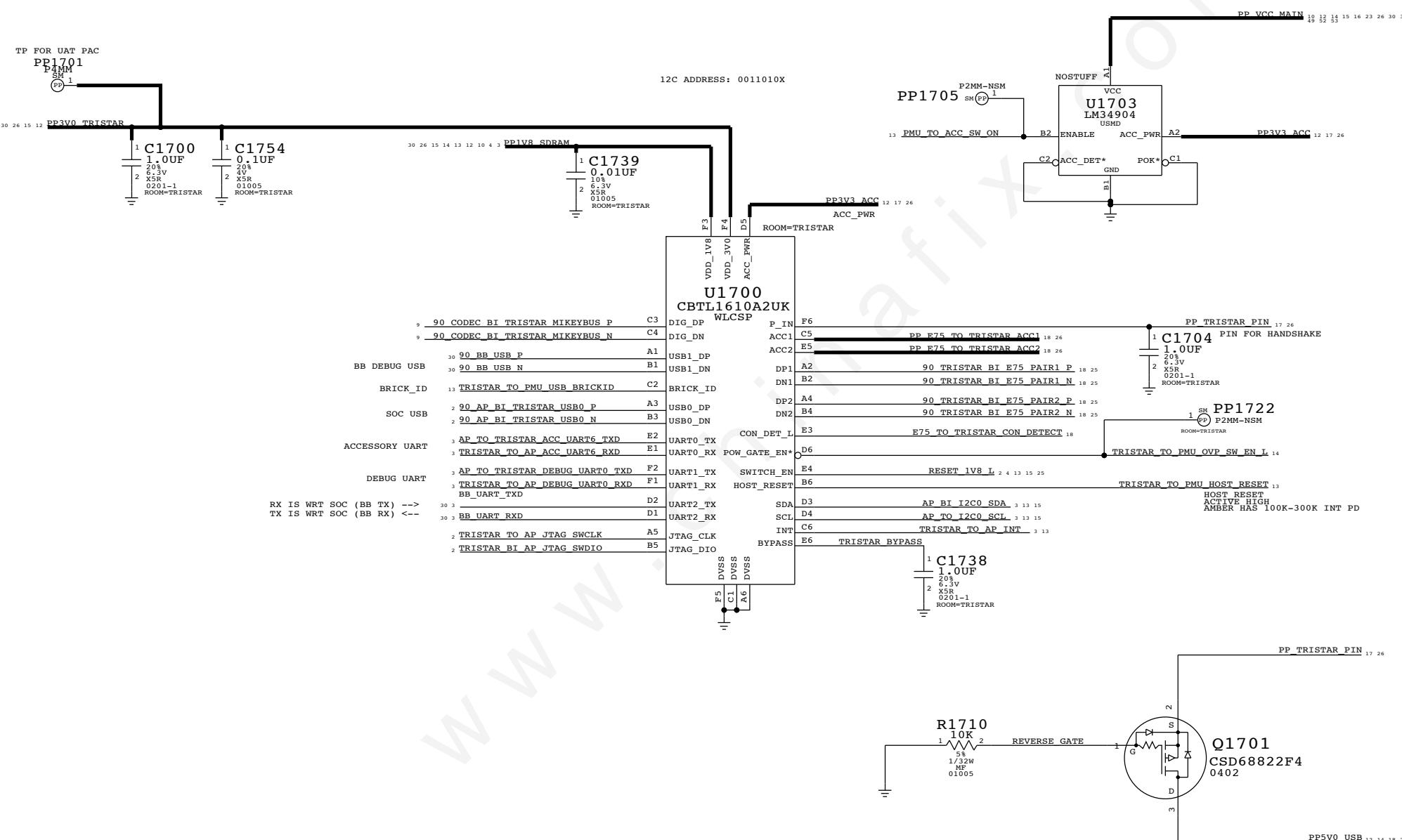
C

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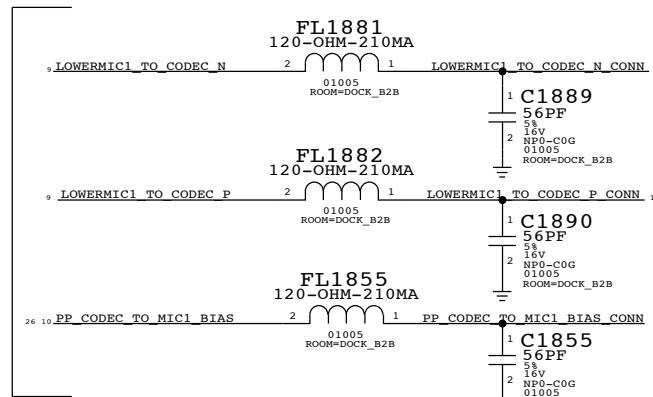
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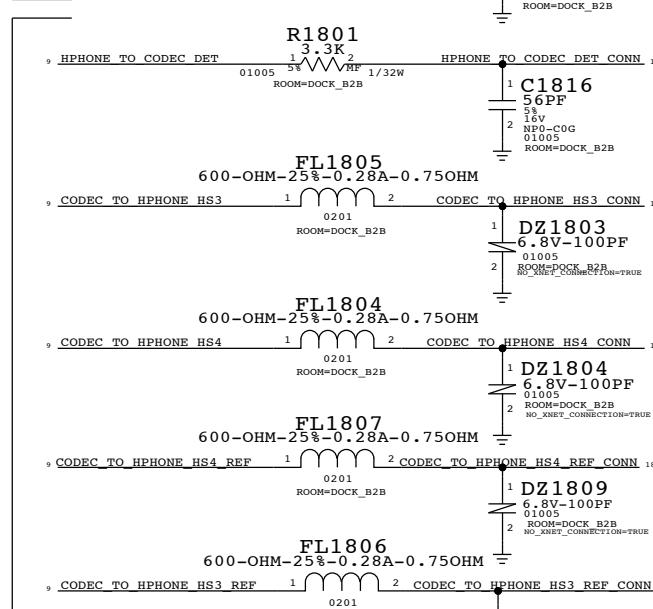
# DOCKFLEX B2B (USB VBUS, SPEAKER, ANTENNA LAT SW CTRL, MIC1 (PRIMARY MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)



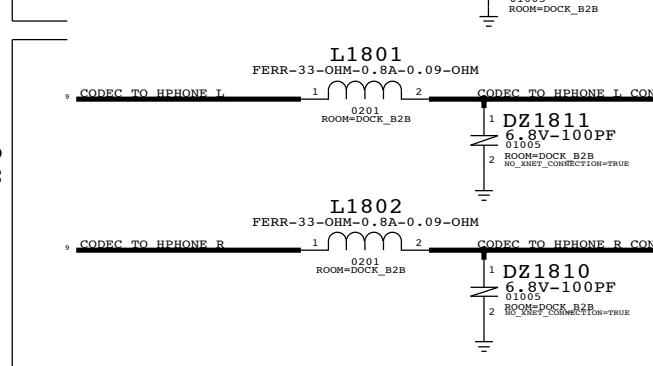
D  
LOWER MIC1  
(PRIMARY  
VOICE MIC)



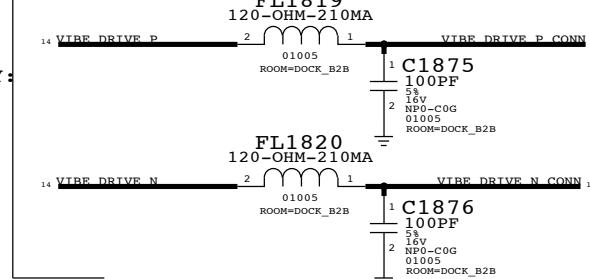
C  
HEADPHONE



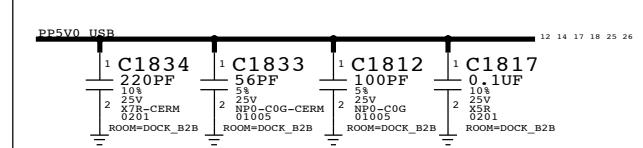
A  
CODEC TO  
HEADPHONE



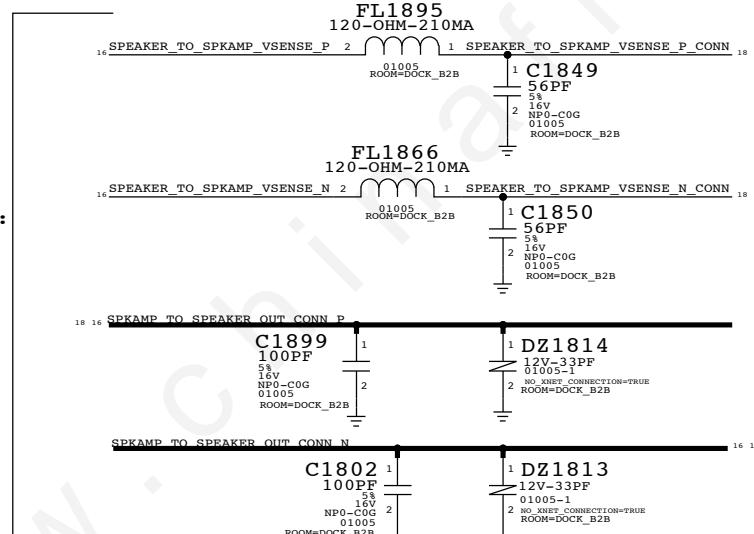
D  
ACCESSORY:  
VIBE  
DRIVE



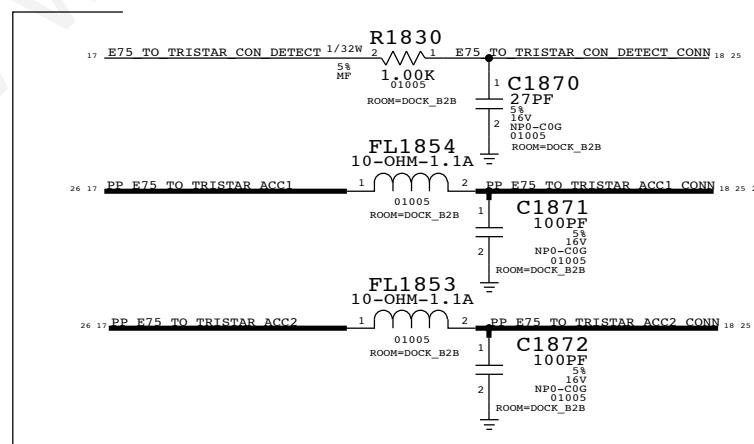
C  
USB  
VBUS



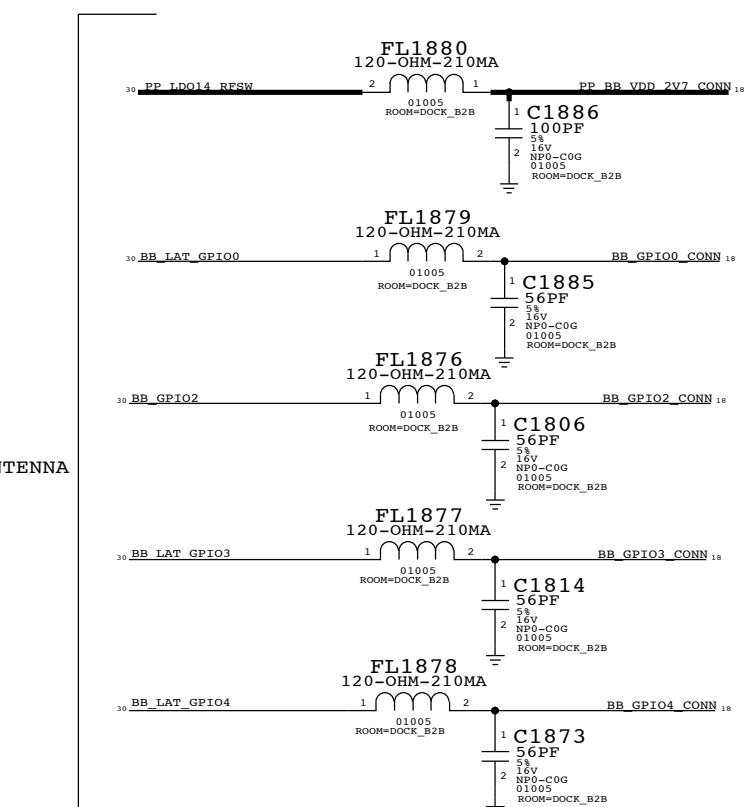
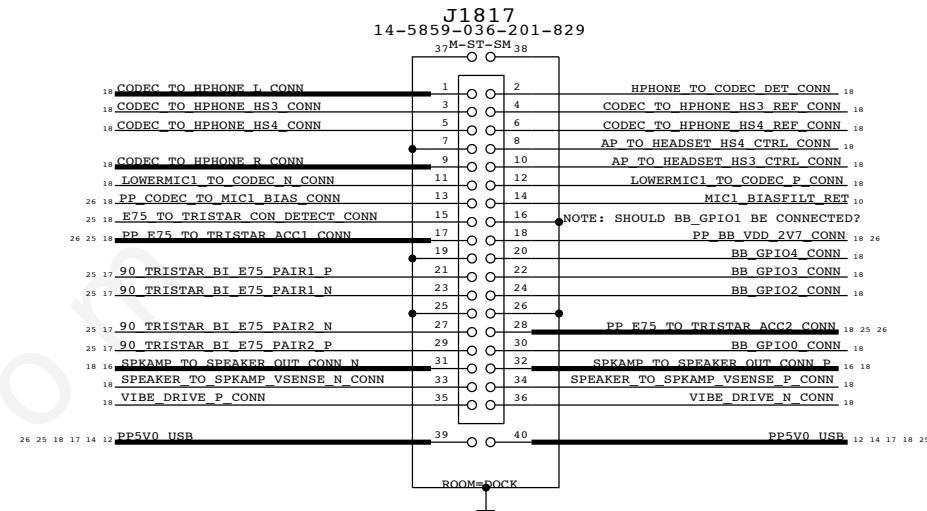
B  
SPEAKER:  
LEADS,  
VSENSE



A  
TRISTAR



MLB: 516S1282 (PLUG)



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www.chinafix.com



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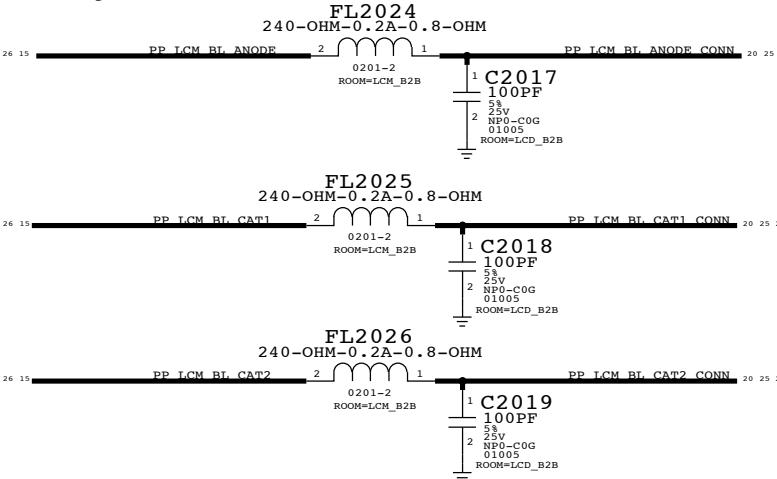
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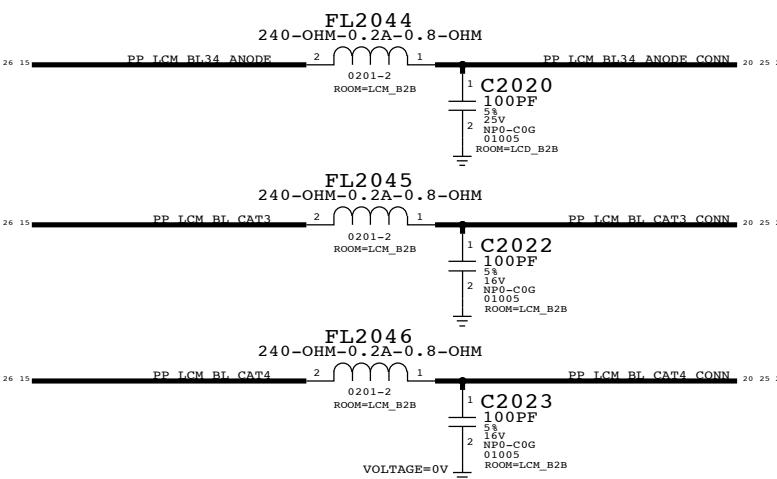
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# LCD B2B

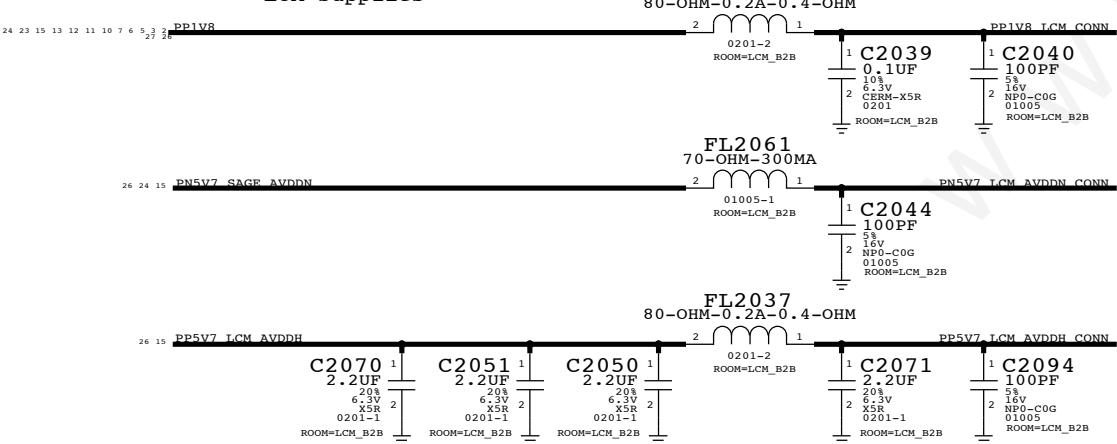
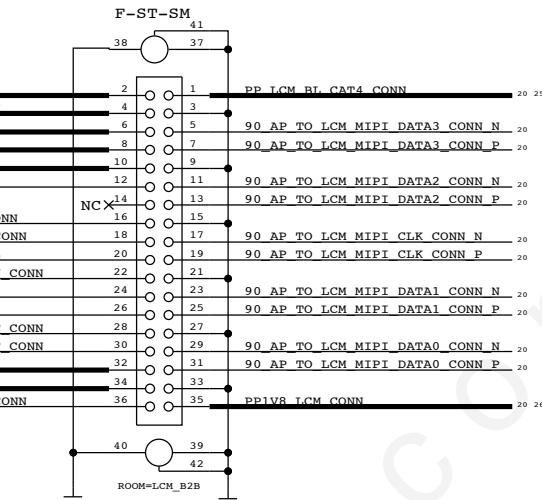
Backlight



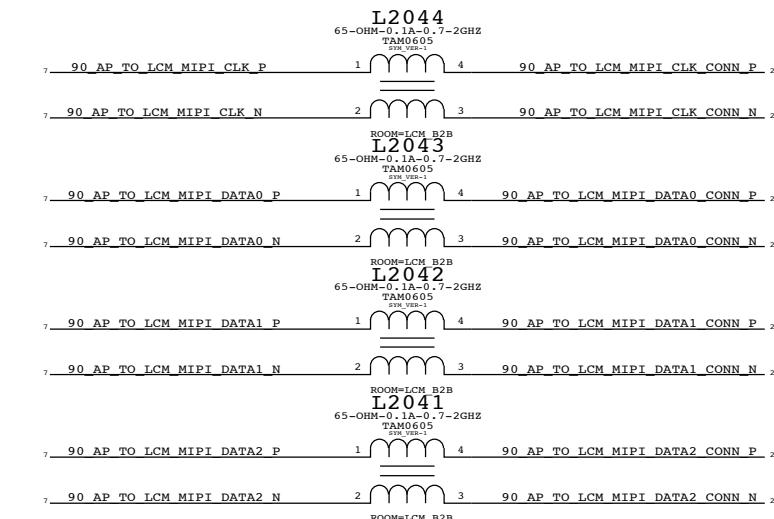
BACKLIGHT 2 (N56 ONLY)



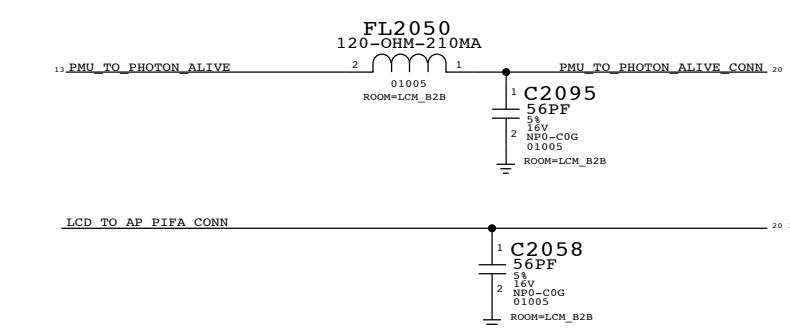
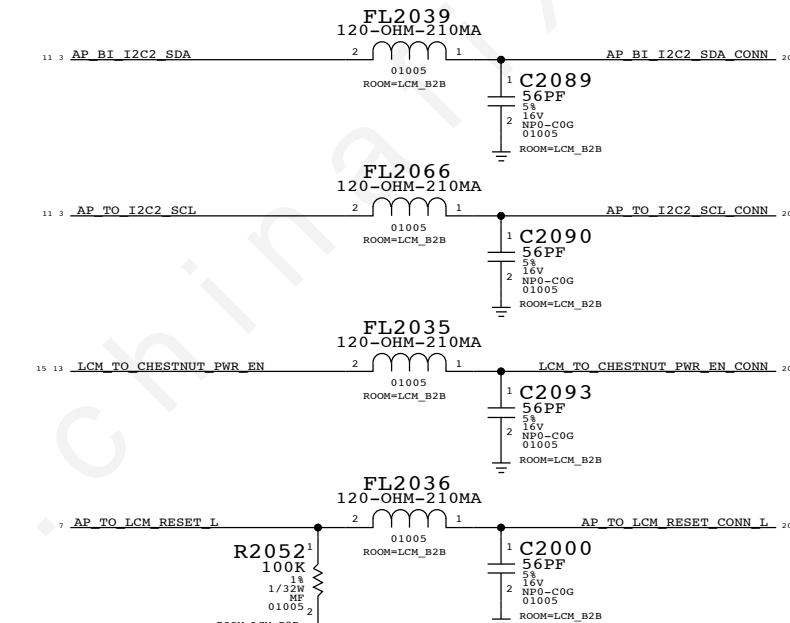
LCM Supplies

MLB: 516S1185 (RCPT)  
(516S1184 ON FLEX)J2019  
20-5857-036-001-829

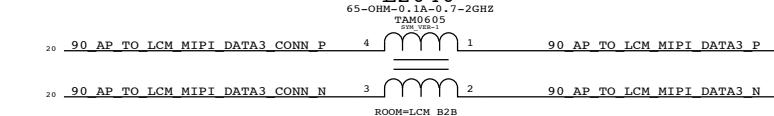
MIPI Common Mode Chokes



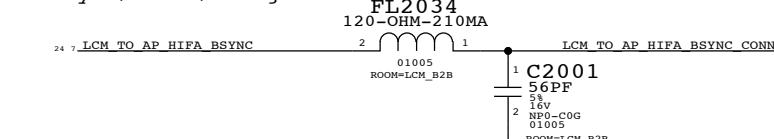
Digital Interfaces



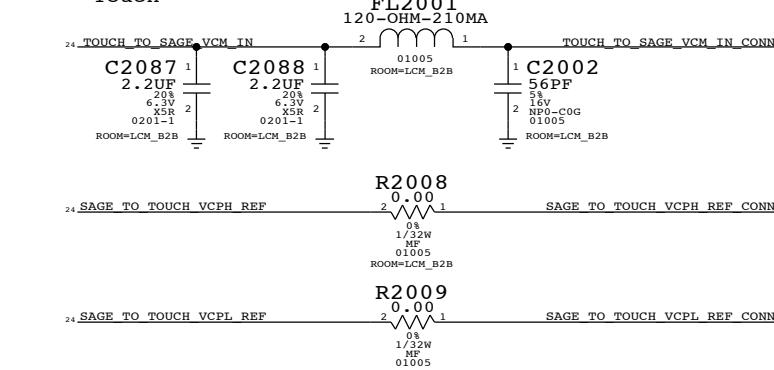
MIPI LANE 3 (N56 ONLY)



Sync/Reset/Debug



Touch

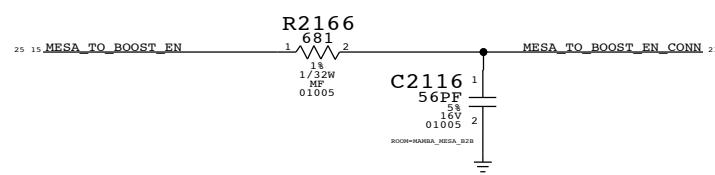
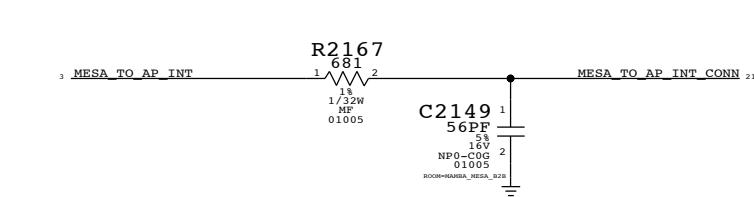


## MESA CONNECTOR

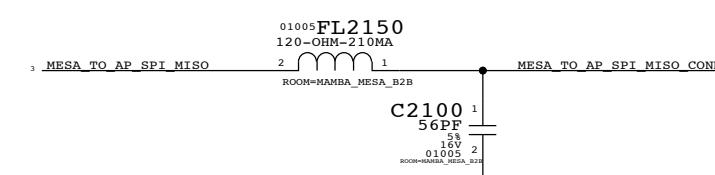
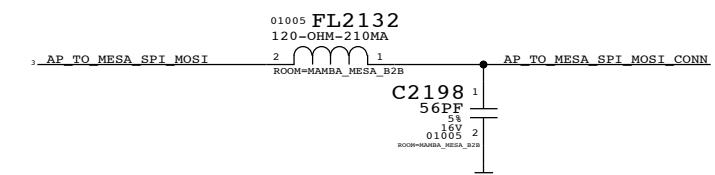


D

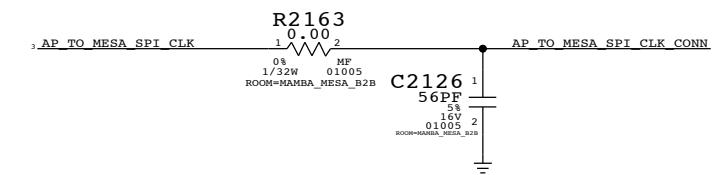
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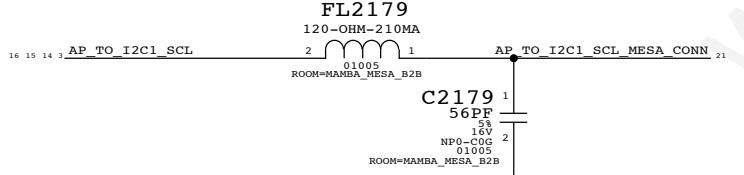
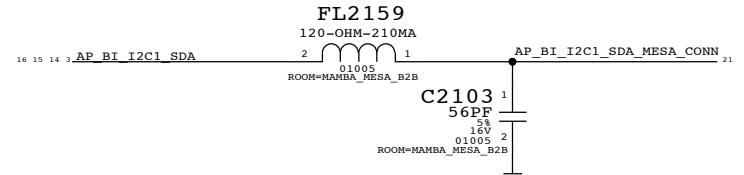
**MESA:**



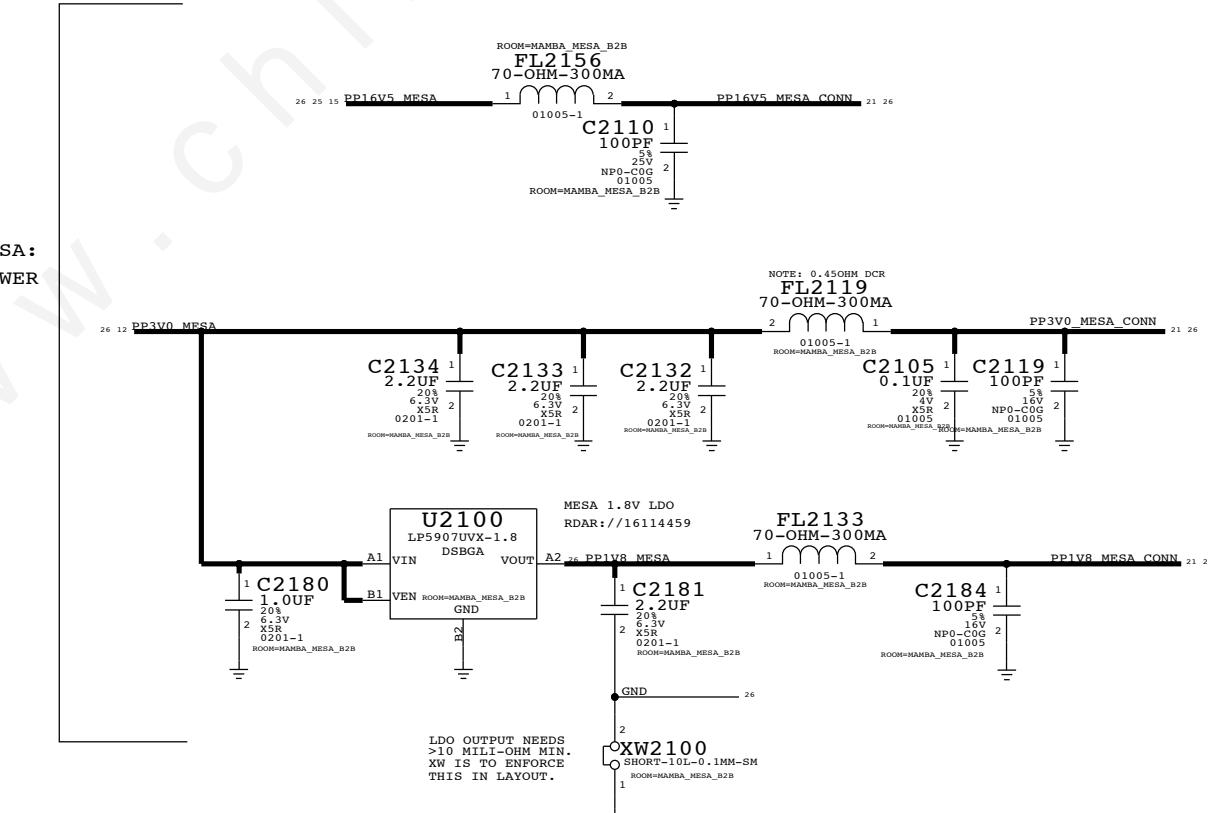
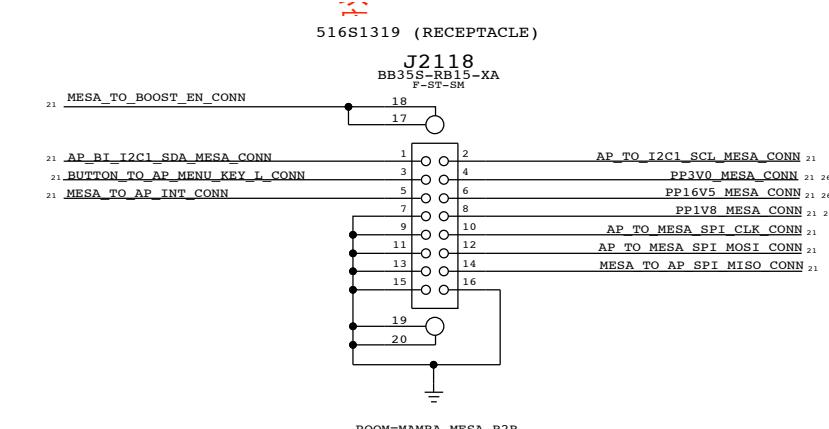
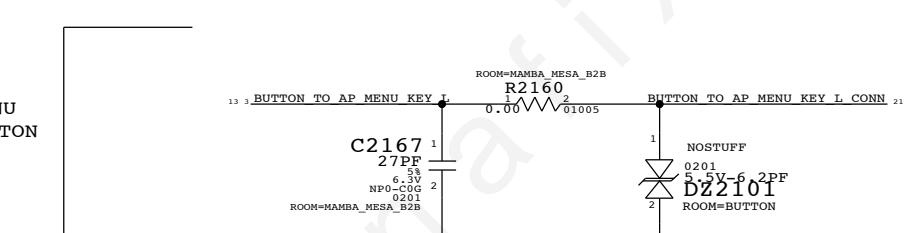
MENU  
BUTTON



MES  
POW

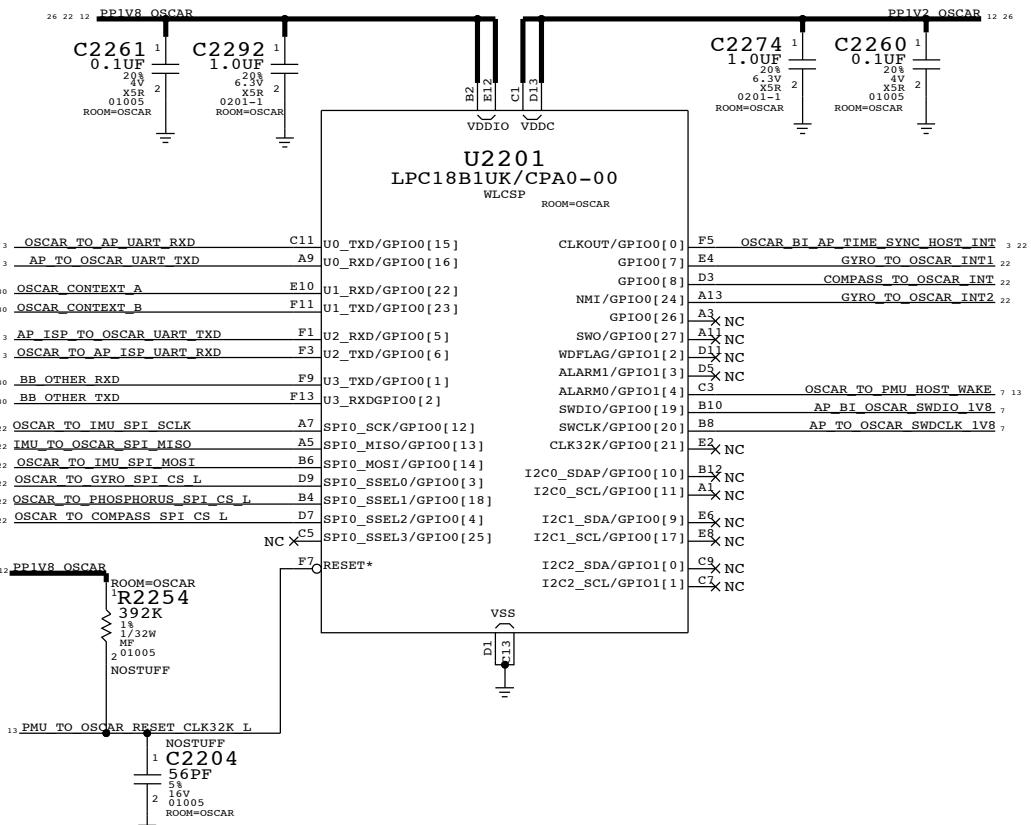


353S4

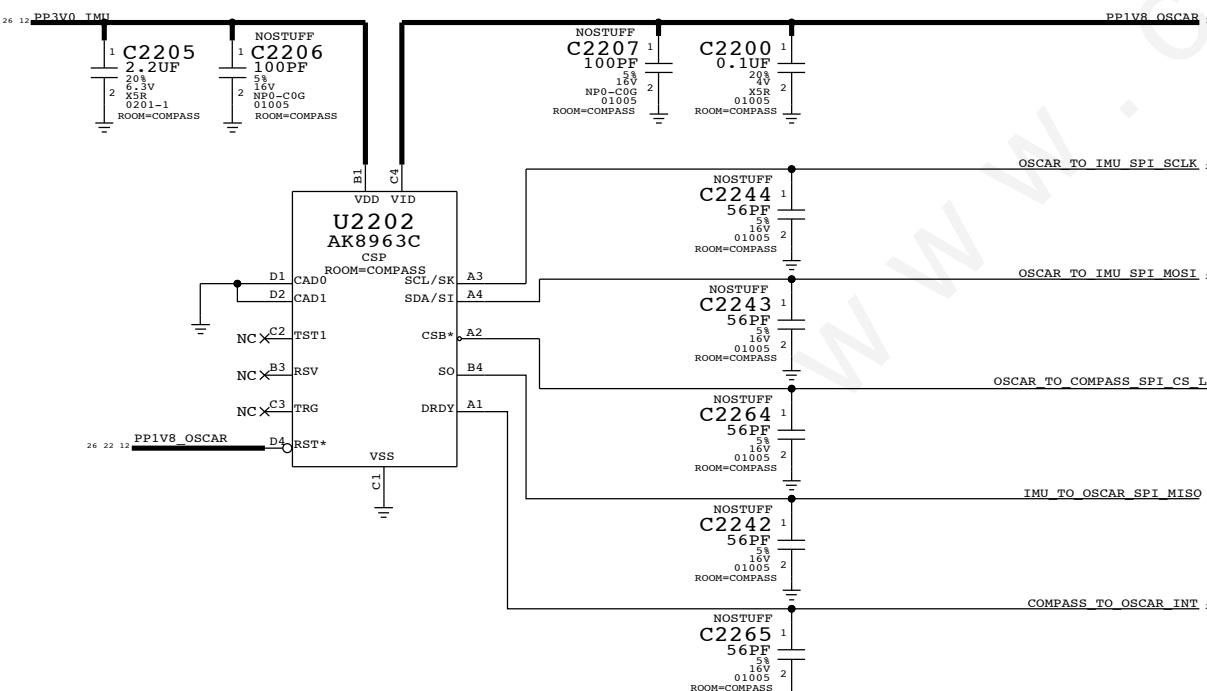


# OSCAR + SENSORS

OSCAR VDDIO = 1.8V ALWAYS ON (NEED TO WAKE HOST & RUN PLL)  
OSCAR CORE = 1.2V ALWAYS ON (NEED TO RUN IN S2RAM)

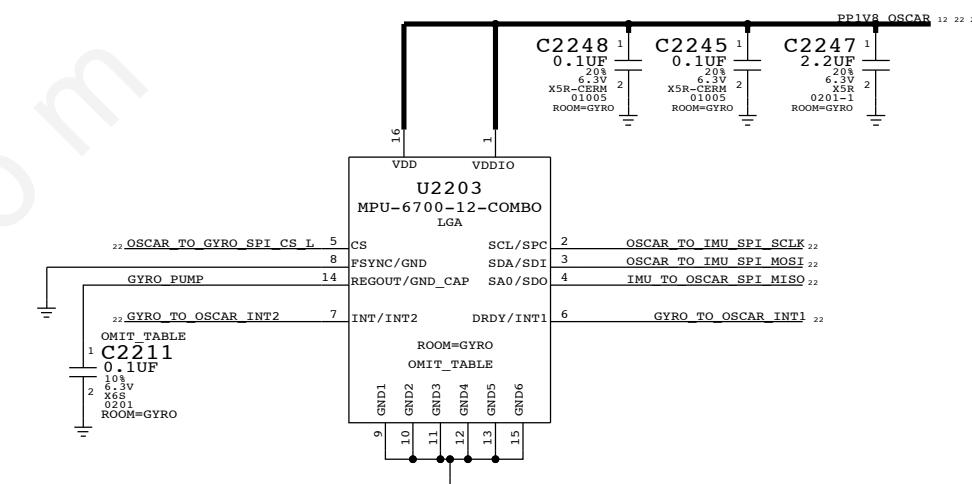


**THIS PART OUTSIDE OF SHIELD ON THE PENINSULA  
COMPASS**

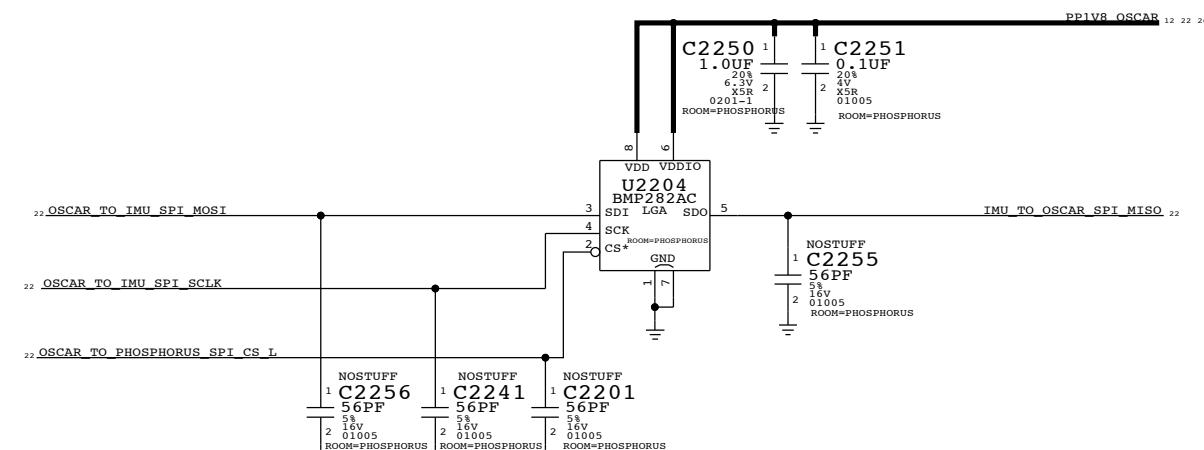


# CARBON (ACCEL GYRO COMBO)

INVENSENSE, APN 338S00017, C2211=0.1UF (132S0395)  
BOSCH, APN 338S00028, C2211=0.1UF (132S0395)  
ST, APN 338S00029, C2211=0.01UF, 25V (132S0391)



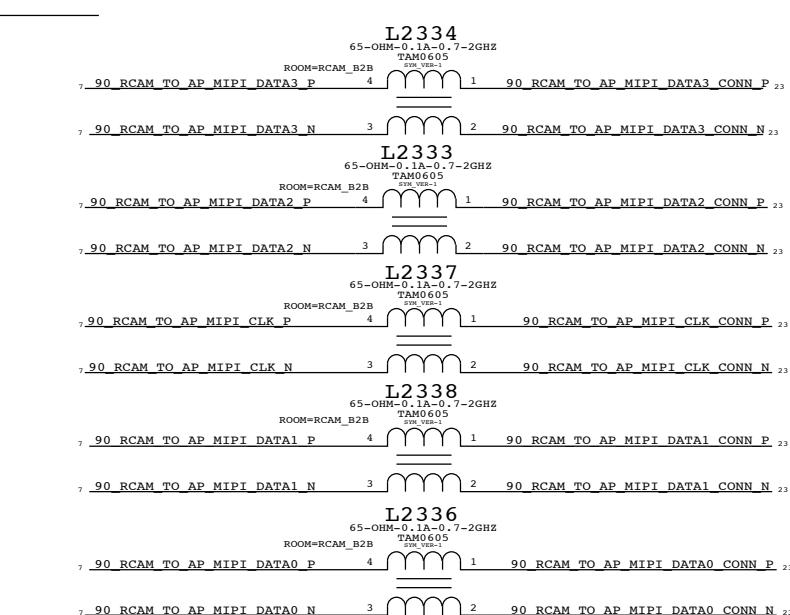
**THIS IS OUTSIDE OF SHIELD IN  
TO THE RIGHT OF THE NAND  
PHOSPHORUS**



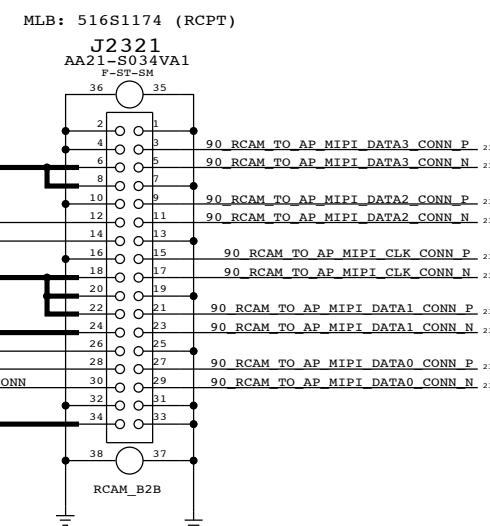
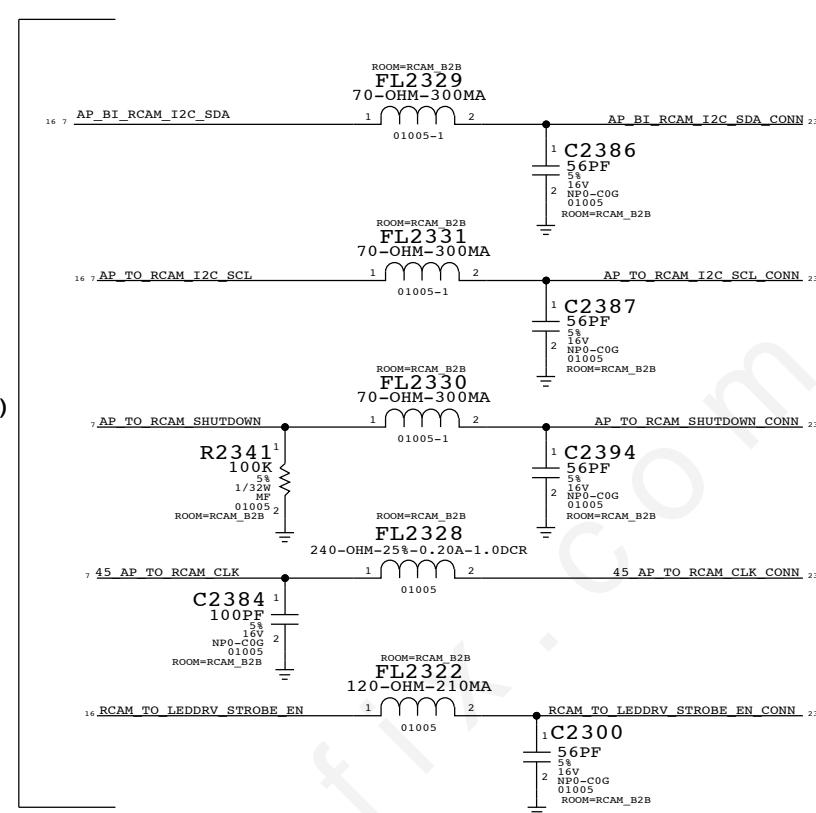
# RCAM B2B (REAR CAMERA CONNECTOR)

D

**RCAM:**  
4-LANE MIPI

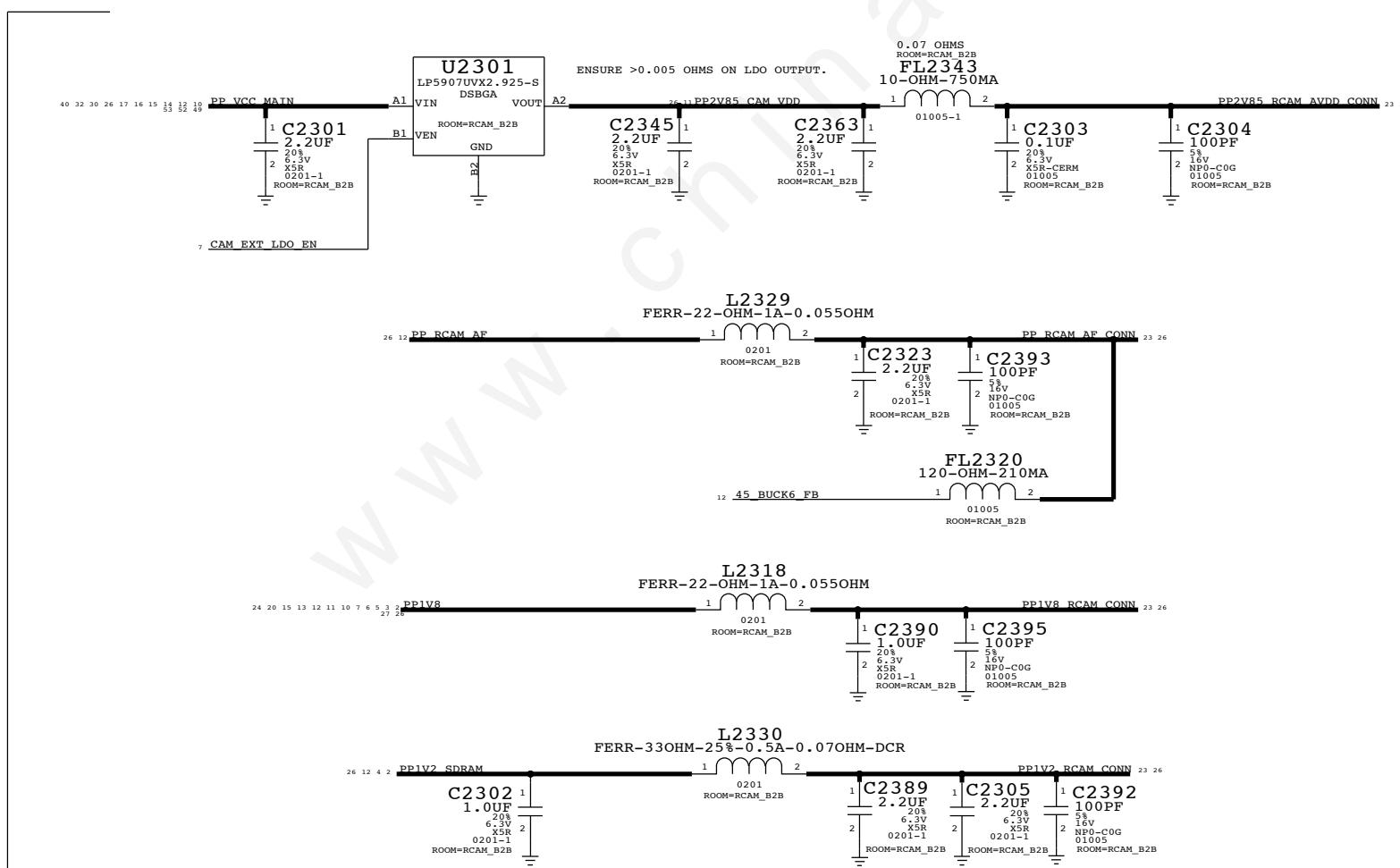


**RCAM:**  
DIGITAL I/F  
(I2C, CTRL, CLK)



C

**RCAM:**  
POWER:  
(1.8V DOVDD)  
(2.9V AVDD)  
(1.2V DVDD)  
(2V AF)



B

**RCAM:**  
POWER:  
(1.8V DOVDD)  
(2.9V AVDD)  
(1.2V DVDD)  
(2V AF)

A





# VOLTAGE PROPERTIES

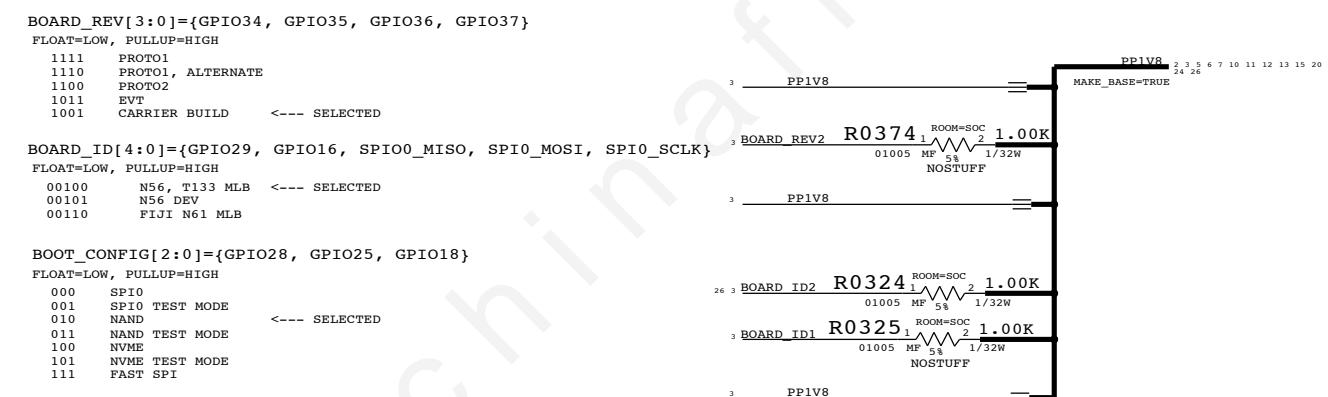
E51	VOLTAGE=3.3V	PP3V3_USB	2 12	E52	VOLTAGE=5.0V	PP_LED_DRV_LX	16
E53	VOLTAGE=1.8V	PP1V8_VA_L19	16 7 10 12 16	E54	VOLTAGE=5.0V	PP_LED_BOOST_OUT	16
E55	VOLTAGE=3.0V	PP3V0_TRISTAR	12 15 17 30	E56	VOLTAGE=2.7V	PP_BB_VDD_2V7_CONN	18
E57	VOLTAGE=3.0V	PP3V0_TMU	12 22	E58	VOLTAGE=1.8V	PP_CODEC_TO_MIC1_BIAS_CONN	18
E59	VOLTAGE=3.0V	PP1V0_NAND	6 12	E59	VOLTAGE=4.6V	PP_E75_TO_TRISTAR_ACC2	17 18
E60	VOLTAGE=3.0V	PP3V3_ACC	12 17	E60	VOLTAGE=4.6V	PP_E75_TO_TRISTAR_ACC2_CONN	18 25
E61	VOLTAGE=3.0V	PP3V0_PROX_ALS	11 12	E62	VOLTAGE=5.0V	PP_LED_DRV_LX	16
E63	VOLTAGE=3.0V	PP2V9_LDO9	12	E63	VOLTAGE=5.0V	PP_LED_BOOST_OUT	16
E64	VOLTAGE=4.6V	PP_VCC_MAIN	10 12 14 15 16 17 23 30 32	E64	VOLTAGE=2.7V	PP_BB_VDD_2V7_CONN	18
E65	VOLTAGE=1.0V	PP1V0	7 12	E65	VOLTAGE=1.8V	PP_CODEC_TO_MIC1_BIAS_CONN	18
E66	VOLTAGE=3.0V	PP3V0_PROXILED	11 12	E66	VOLTAGE=4.6V	PP_E75_TO_TRISTAR_ACC2	17 18
E67	VOLTAGE=1.8V	PP1V8_ALWAYS	3 5 12 14	E67	VOLTAGE=4.6V	PP_E75_TO_TRISTAR_ACC2_CONN	18 25
E68	VOLTAGE=3.0V	PP3V0_MESA	12 21	E68	VOLTAGE=1.8V	PP1V8_LCM_CONN	20
E69	VOLTAGE=1.1V	PP_CPU	4 12	E69	VOLTAGE=2.2V	PP_LCM_BL_ANODE_CONN	20 25
E70	VOLTAGE=1.1V	PP_GPU	4 12	E70	VOLTAGE=-5.7V	PNSV7_LCM_AVDDN_CONN	20
E71	VOLTAGE=1.2V	PP1V2_SDRAM	2 4 12 23	E71	VOLTAGE=5.7V	PNSV7_LCM_AVDDH_CONN	20
E72	VOLTAGE=1.8V	PP1V8_SDRAM	3 4 10 12 13 14 15 17 30	E72	VOLTAGE=2.95V	PP_LDO13_GPS	51
E73	VOLTAGE=1.8V	PP1V8	2 4 5 6 7 10 11 12 13 15 20 23	E73	VOLTAGE=1.8V	PP1V8_MESA	21
E74	VOLTAGE=1.8V	PP1V8_GRAPE	12 24	E74	VOLTAGE=16.5V	PP1GV5_MESA_CONN	21
E75	VOLTAGE=1.8V	PP1V8_OSCAR	12 22	E75	VOLTAGE=5.0V	PP_TRISTAR_PIN	17
E76	VOLTAGE=1.2V	PP1V2_NAND_VDDT	6	E76	VOLTAGE=1.2V	PP1V2_RCAM_CONN	23
E77	VOLTAGE=1.8V	PP_EXTMIC_BIAS_FILT_IN	10	E77	VOLTAGE=1.8V	PP1V8_RCAM_CONN	23
E78	VOLTAGE=1.8V	BOARD_ID2	3 27	E78	VOLTAGE=3.0V	PP2V85_CAM_VDD	11 23
E79	VOLTAGE=1.2V	PP1V2	2 4 5 11 12	E79	VOLTAGE=1.8V	PP2V85_RCAM_AVDD_CONN	23
E80	VOLTAGE=5.0V	PP_E75_TO_TRISTAR_ACC1_CONN	18 25	E80	VOLTAGE=1.8V	PP_CUMULUS_VDDCORE	24
E81	VOLTAGE=5.0V	PP_E75_TO_TRISTAR_ACC1	17 18	E81	VOLTAGE=0.2V	PP_CUMULUS_VDDANA	24
E82	VOLTAGE=22.0V	PP_LCM_BL_ANODE	15 20	E82	VOLTAGE=13.5V	PP_SAGE_TO_TOUCH_VCPI_CONN	24
E83	VOLTAGE=0.2V	PP_LCM_BL_CAT2	15 20	E83	VOLTAGE=-12V	PN_SAGE_TO_TOUCH_VCPI_CONN	24
E84	VOLTAGE=0.2V	PP_LCM_BL_CAT1	15 20	E84	VOLTAGE=13.5V	PE_SAGE_TO_TOUCH_VCPI	24
E85	VOLTAGE=0.2V	PP_LCM_BL_CAT2_CONN	20 25	E85	VOLTAGE=-12V	PN_SAGE_TO_TOUCH_VCPI	24
E86	VOLTAGE=0.2V	PP_LCM_BL_CAT1_CONN	20 25	E86	VOLTAGE=-12V	PN_SAGE_TO_TOUCH_VCPI	24
E87	VOLTAGE=-5.7V	PNSV7_SAGE_AVDDN	15 20 24	E87	VOLTAGE=-12V	PN_SAGE_TO_TOUCH_VCPI_FILT	24
E88	VOLTAGE=1.2V	PP1V2_OSCAR	12 22	E88	VOLTAGE=5.7V	PP_SAGE_LX	24
E89	VOLTAGE=3.0V	PP3V0_MESA_CONN	21	E89	VOLTAGE=17.0V	PP_SAGE_JY	24
E90	VOLTAGE=6V	PP6V0_LCM_BOOST	15	E90	VOLTAGE=1.8V	PP_PMI_VREF	13
E91	VOLTAGE=5.0V	PP_STRB_DRIVER_TO_LED_WARM	8 16	E91	VOLTAGE=14V	PP_SAGE_VBST_OUT	24
E92	VOLTAGE=5.0V	PP_STRB_DRIVER_TO_LED_COOL	8 16	E92	VOLTAGE=5.0V	PP_TIGRIS_VBUS_DET	14
E93	VOLTAGE=1.8V	PP_CODEC_TO_MIC1_BIAS	10 18	E93	VOLTAGE=2.5V	PP_PMI_VDD_REF	13
E94	VOLTAGE=1.8V	PP_EXTMIC_BIAS_IN	10	E94	VOLTAGE=1.8V	PP_EXTMIC_BIAS	10
E95	VOLTAGE=1.8V	PP_EXTMIC_BIAS_FILT	10	E95	VOLTAGE=1.8V	PP1V8_XTAL	2
E96	VOLTAGE=1.8V	PP_CODEC_TO_FRONTMIC3_BIAS	10 11	E96	VOLTAGE=1.8V	PP_PMI_VDD_RTC	13
E97	VOLTAGE=1.8V	PP_CODEC_TO_REARMIC2_BIAS	8 10	E97	VOLTAGE=3.80V	PP_BATT_VCC	14 16 25 41 46 47
E98	VOLTAGE=1.8V	PP_CODEC_FILT4	10	E98	VOLTAGE=1.8V	PP1V8_MESA_CONN	21
E99	VOLTAGE=2.2V	PP_CODEC_SPKR_WO	10	E99	VOLTAGE=3.0V	PP3V0_PROX_CONN	11
E100	VOLTAGE=2.5V	PP_CODEC_VCPBELL	10	E100	VOLTAGE=1.0V	PP0V95_FIXED_SOC	4 7 12
E101	VOLTAGE=2.5V	PP_CODEC_VCPBELL_F	10	E101	VOLTAGE=1.0V	PP0V95_FIXED_SOC_PCIE	7
E102	VOLTAGE=2.5V	PP_CODEC_VHP_FLYN	10	E102	VOLTAGE=1.2V	PP1V2_PLL	2
E103	VOLTAGE=0.2V	PP_CODEC_VHP_FLYC	10	E103	VOLTAGE=1.0V	PP_BUCK5_LX1	12
E104	VOLTAGE=2.5V	PP_CODEC_VHP_FLYP	10	E104	VOLTAGE=1.0V	PP_VAR_SOC	5 12
E105	VOLTAGE=1.8V	PP1V8_ECAM_CONN	11	E105	VOLTAGE=3.00V	PP_PN65_SIM_PMU	53 55
E106	VOLTAGE=3.0V	PP2V85_FCMB_AVDD_CONN	11	E106	VOLTAGE=1.8V	PP1V8_HALL_CONN	
E107	VOLTAGE=1.8V	PP_CODEC_TO_BRONNTMC3_BIAS_CONN	11	E107	VOLTAGE=1.8V	GND	21
E108	VOLTAGE=3.0V	PP3V0_ALS_CONN	11	E108	VOLTAGE=5.0V	CHARGER_LDO	14
E109	VOLTAGE=1.2V	PP1V2_ECAM_VDDTO_CONN	11	E109	VOLTAGE=5.0V	PMID_CAP	14
E110	VOLTAGE=5.0V	PP5V0_USB	12 14 17 18 25	E110	VOLTAGE=5.0V	PP_WLED_LX	15
E111	VOLTAGE=5.0V	PP5V0_USB_TO_PMU	12	E111	VOLTAGE=22.0V	PP_WLED_LX	15
E112	VOLTAGE=4.6V	PP_BUCK3_LX	12	E112	VOLTAGE=18.0V	PP1V0_MESA_SW	15
E113	VOLTAGE=4.6V	PP_BUCK4_LX	12	E113	VOLTAGE=17.0V	P17V0_Mojave_LDOIN	15
E114	VOLTAGE=4.6V	PP_BUCK2_LX	12	E114	VOLTAGE=16.5V	PP1GV5_MESA_SW	15 21 25
E115	VOLTAGE=4.6V	PP_BUCK1_LX	12	E115	VOLTAGE=8.0V	PP_SKAMP_SW	16
E116	VOLTAGE=4.6V	PP_CHESTNUT_LXP	15	E116	VOLTAGE=8.0V	PP_L19_VBOOST	16
E117	VOLTAGE=6.0V	PP_CHESTNUT_QP	15	E117	VOLTAGE=1.8V	PP_SKAMP_FILT	16
E118	VOLTAGE=6.0V	PP_CHESTNUT_CN	15	E118	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E119	VOLTAGE=5.7V	PP5V7_SAGE_AVDDH	15 24	E119	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E120	VOLTAGE=5.7V	PP5V7_LCM_AVDDH	15 20	E120	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E121	VOLTAGE=5.1V	PP5V1_GRAPE_VDDH	15 24	E121	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E122	VOLTAGE=22.0V	PP_WLED_LX	15	E122	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E123	VOLTAGE=18.0V	PP1V0_MESA_SW	15	E123	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E124	VOLTAGE=17.0V	P17V0_Mojave_LDOIN	15	E124	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E125	VOLTAGE=16.5V	PP1GV5_MESA_SW	15	E125	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E126	VOLTAGE=8.0V	PP_SKAMP_SW	16	E126	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E127	VOLTAGE=8.0V	PP_L19_VBOOST	16	E127	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E128	VOLTAGE=1.8V	PP_SKAMP_FILT	16	E128	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16
E129	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16	E129	VOLTAGE=1.8V	PP_SKAMP_LDO_FILT	16

## N56 SPECIFIC VOLTAGE PROPERTIES

E151	VOLTAGE=22.0V	PP_WLED34_LX	15
E152	VOLTAGE=22.0V	PP_LCM_BL34_ANODE	15 20
E153	VOLTAGE=22.0V	PP_LCM_BL34_ANODE_CONN	20 25
E154	VOLTAGE=0.2V	PP_LCM_BL_CAT3	15 20
E155	VOLTAGE=0.2V	PP_LCM_BL_CAT4	15 20
E156	VOLTAGE=0.2V	PP_LCM_BL_CAT4_CONN	20 25
E157	VOLTAGE=0.2V	PP_LCM_BL_CAT4_CONN	20 25
E158	VOLTAGE=12V	PN_SAGE_TO_TOUCH_VCPL_FILT	24
E159	VOLTAGE=2.0V	PP_RCAM_AF	12 23
E160	VOLTAGE=2.0V	PP_RCAM_AF_CONN	23
E161	VOLTAGE=-14.0V	PN_SAGE_VBST_OUTL	24
E162	VOLTAGE=-5.7V	PNSV7_SAGE_AVDDN_FILT	24
E163	VOLTAGE=2.0V	PP_BUCK6_LX	12

# N56 SPECIFIC

BOOTSTRAPPING (BOARD\_REV, BOARD\_ID, BOOT\_CFG, DISPLAY\_ID)



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# RADIO\_MLB HIERARCHICAL SYMBOL



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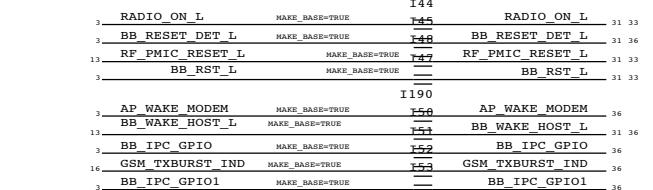
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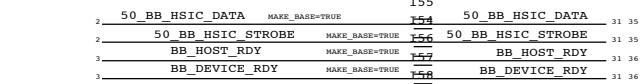
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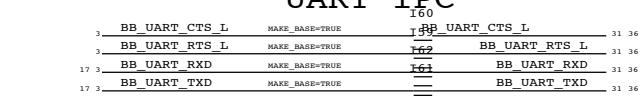
## CELLULAR HOUSE KEEPING



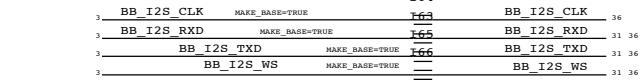
## HSIC IPC



## UART IPC



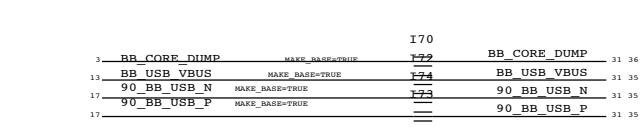
## AUDIO I2S



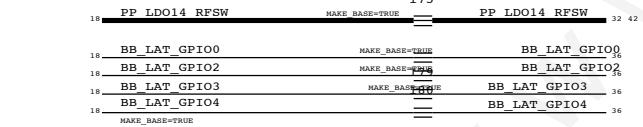
## OSCAR UART



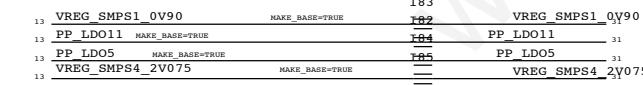
## BB DEBUG INTERFACES



## RADIO ANTENNA CONTROL



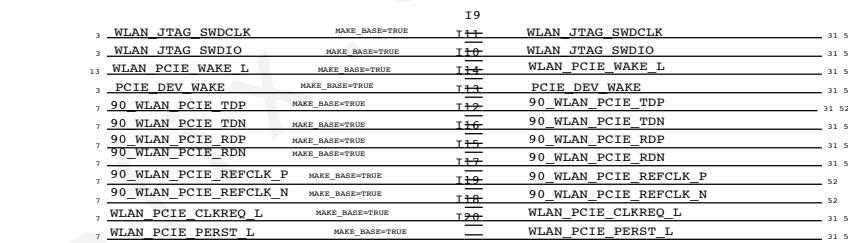
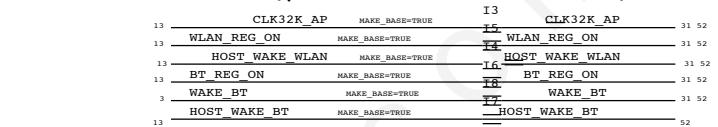
## FCT TESTING



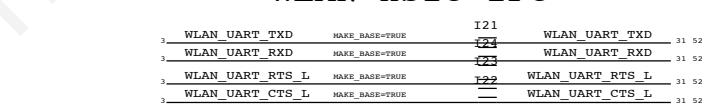
## UPPER RADIO ANTENNA CONTROL



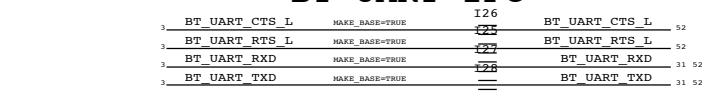
## WLAN/BT HOUSE KEEPING



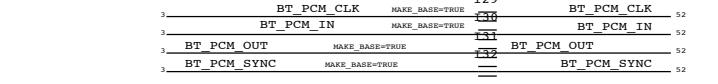
## WLAN HSIC IPC



## BT UART IPC



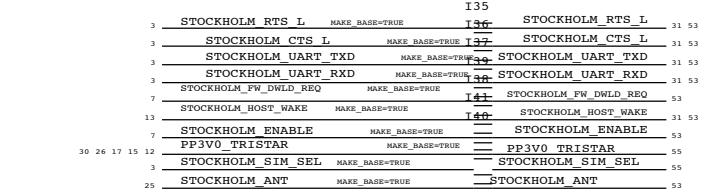
## BT AUDIO PCM



## OSCAR STATES



## STOCKHOLM



# AP INTERFACE & DEBUG CONNECTORS



## PROBE POINTS

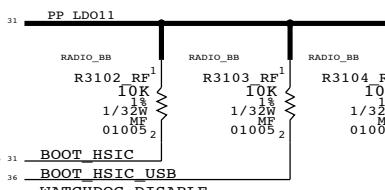
PP3105_RF P2MM-NSM SN 1 CLK32K_AP WIFI_BT	30 52	PP3111_RF P2MM-NSM SN 1 BB_COEX_UART_RXD WIFI_BT	36 52	PP3114_RF P2MM-NSM SN 1 BB_COEX_UART_TXD WIFI_BT	36 52	PP3115_RF P2MM-NSM SN 1 BT_UART_RXD WIFI_BT	30 52	PP3120_RF P2MM-NSM SN 1 BT_UART_RXD WIFI_BT	30 52	PP3124_RF P2MM-NSM SN 1 BT_UART_RXD WIFI_BT	30 52	PP3125_RF P2MM-NSM SN 1 WAKE_BT WIFI_BT	30 52	PP3128_RF P2MM-NSM SN 1 WLAN_REG_ON WIFI_BT	30 52	PP3129_RF P2MM-NSM SN 1 WLAN_REG_ON WIFI_BT	30 52	PP3132_RF P2MM-NSM SN 1 WLAN_REG_ON WIFI_BT	30 52	PP3135_RF P2MM-NSM SN 1 HOST_WAKE_WLAN WIFI_BT	30 52	PP3156_RF P2MM-NSM SN 1 WLAN_PCIE_WAKE_L WIFI_BT	30 52	PP3157_RF P2MM-NSM SN 1 WLAN_PCIE_PERST_L WIFI_BT	30 52	PP3158_RF P2MM-NSM SN 1 WLAN_PCIE_CLKREQ_L WIFI_BT	30 52	PP3159_RF P2MM-NSM SN 1 PCIE_DEV_WAKE WIFI_BT	30 52	PP3160_RF P2MM-NSM SN 1 WLAN_UART_RTS_L WIFI_BT	30 52	PP3161_RF P2MM-NSM SN 1 WLAN_UART_CTS_L WIFI_BT	30 52	PP3162_RF P2MM-NSM SN 1 WLAN_UART_RXD WIFI_BT	30 52	PP3163_RF P2MM-NSM SN 1 WLAN_UART_TXD WIFI_BT	30 52	PP3190_RF P2MM-NSM SN 1 WLAN_JTAG_SWDCLK WIFI_BT	30 52	PP3191_RF P2MM-NSM SN 1 WLAN_JTAG_SWDIO WIFI_BT	30 52	PP3121_RF P2MM-NSM SN 1 RADIO_STOCKHOLM	30 53	PP3122_RF P2MM-NSM SN 1 SIM_DEBUG	33 53	PP3123_RF P2MM-NSM SN 1 RADIO_STOCKHOLM	30 53	PP3124_RF P2MM-NSM SN 1 RADIO_STOCKHOLM	30 53	PP3125_RF P2MM-NSM SN 1 RADIO_STOCKHOLM	30 53	PP3126_RF P2MM-NSM SN 1 RADIO_STOCKHOLM	30 53	PP3127_RF P2MM-NSM SN 1 RADIO_STOCKHOLM	30 53	PP3128_RF P2MM-NSM SN 1 PP_PN65_VCC_SIM RADIO_STOCKHOLM	53	PP3174_RF P2MM-NSM SN 1 SIM_DEBUG	53 55	PP3129_RF P2MM-NSM SN 1 REF_CLK_FROM_BB SIM_DEBUG	33 53	PP3165_RF P2MM-NSM SN 1 SIM_DEBUG	35 55	PP3182_RF P2MM-NSM SN 1 SIM_DEBUG	35 55	PP3184_RF P2MM-NSM SN 1 SIM_DEBUG	35 55	PP3186_RF P2MM-NSM SN 1 SIM_DEBUG	35	PP3187_RF P2MM-NSM SN 1 SIM_DEBUG	32 34 5	PP3188_RF P2MM-NSM SN 1 SIM_DEBUG	55	PP3189_RF P2MM-NSM SN 1 SIM_DEBUG	55	PP_3178_RF P2MM-NSM SN 1 BB_SIM_RESET	31 36	PP_3179_RF P2MM-NSM SN 1 BB_SIM_CLK	31 36	PP_3180_RF P2MM-NSM SN 1 BB_SIM_DATA	31 36
--	-------	---	-------	---	-------	--	-------	--	-------	--	-------	--	-------	--	-------	--	-------	--	-------	---	-------	---	-------	--	-------	---	-------	--	-------	--	-------	--	-------	--	-------	--	-------	---	-------	--	-------	---	-------	---	-------	---	-------	---	-------	---	-------	---	-------	---	-------	--	----	---	-------	--	-------	---	-------	---	-------	---	-------	---	----	---	---------	---	----	---	----	---	-------	---	-------	--	-------

P4MM-NSM	PF	1 50 BB HSIC STROBE	30	35
P4MM-NSM	SN	1 50 BB HSIC DATA	30	35
P23116_RF	PF	1 50 BB HSIC DATA	30	35
P23101_RF	PF	1 BB DEBUG ERROR	36	
P23104_RF	PF	1 RF PMIC RESET L	30	33
P23103_RF	PF	1 PS HOLD PMIC	33	
P23127_RF	PF	1 PMIC RESOUT L	33	35
P23104_RF	PF	1 MDM_CLK	33	35
P23109_RF	PF	1 PP LD011	31	32
P23110_RF	PF	1 RADIO_ON_L	30	33
P23111_RF	PF	1 SPMI_DATA	33	35
P23112_RF	PF	1 SPMI_CLK	33	35

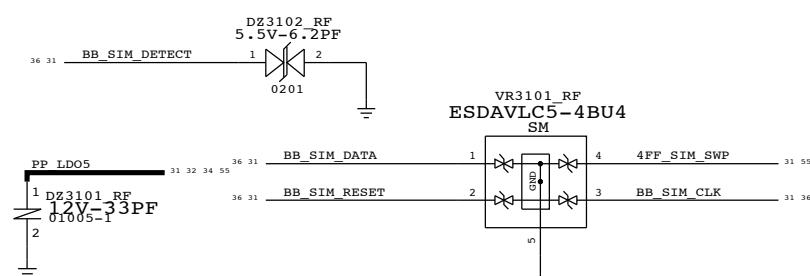
PP3130_RF	P4MM	SH 1 BB JTAG RST_L	35
	SIM_DEBUG		
PP3131_RF	P4MM	SH 1 BB JTAG TCK	35
	SIM_DEBUG		
PP3132_RF	P4MM	SH 1 BB JTAG TMS	35
	SIM_DEBUG		
PP3133_RF	P4MM	SH 1 BB JTAG TDO	35
	SIM_DEBUG		
PP3134_RF	P4MM	SH 1 BB JTAG TDI	35
	SIM_DEBUG		
PP3135_RF	P4MM	SH 1 BB JTAG TRST_L	35
	SIM_DEBUG		
PP3136_RF	P4MM	SH 1 BB DEBUG_STATUS	36
	SIM_DEBUG		
PP3137_RF	P4MM	SH 1 BB CORE_DUMP	30 36
16 38 39	SIM_DEBUG		
PP3138_RF	P4MM	SH 1 BB USB_VBUS	30 35
	SIM_DEBUG		
PP3139_RF	P4MM	SH 1 90_BB_USB_N	30 35
	SIM_DEBUG		
PP3140_RF	P4MM	SH 1 90_BB_USB_P	30 35
	SIM_DEBUG		

P3141_1_RF	P4MM	PP3140_1_RF	P4MM
SM 1	BB_UART_TXD	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	RFFE1_CLK
30 36		36 40	
P3142_1_RF	P4MM	PP3171_1_RF	P4MM
SM 1	BB_UART_RXD	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	RFFE1_DATA
30 36		36 40	
P3143_1_RF	P4MM	PP3172_1_RF	P4MM
SM 1	BB_UART_RTS_L	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	RFFE2_CLK
30 36		36 46	
P3144_1_RF	P4MM	PP3173_1_RF	P4MM
SM 1	BB_UART_CTS_L	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	RFFE2_DATA
30 36		36 46	
P3145_1_RF	P4MM	PP3175_1_RF	P4MM
SM 1	BB_HOST_RDY	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	BB_I2S_WS
30 36		30 36	
P3146_1_RF	P4MM	PP3176_1_RF	P4MM
SM 1	BB_DEVICE_RDY	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	BB_I2S_RXD
30 36		30 36	
P3147_1_RF	P4MM	PP3177_1_RF	P4MM
SM 1	BB_GPS_SYNC	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	BB_I2S_TXD
30 36		30 36	
P3148_1_RF	P4MM	PP3178_1_RF	P4MM
SM 1	BB_WAKE_HOST_L	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	BB_OTHER_TXD
30 36		30 36	
P3149_1_RF	P4MM	PP3179_1_RF	P4MM
SM 1	BB_RESET_DET_L	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	BB_OTHER_RXD
30 36		30 36	
P3150_1_RF	P4MM	PP3182_1_RF	P4MM
SM 1	BB_RST_L	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	RFFE2_CLK_BUFFER
30 33		36 54	
P3151_1_RF	P4MM	PP3185_1_RF	P4MM
SM 1	BOOT_HSIC	SM 1	RF_DEBUG
(P)	SIM_DEBUG	(P)	RFFE2_DATA_BUFFER
31 36		36 54	

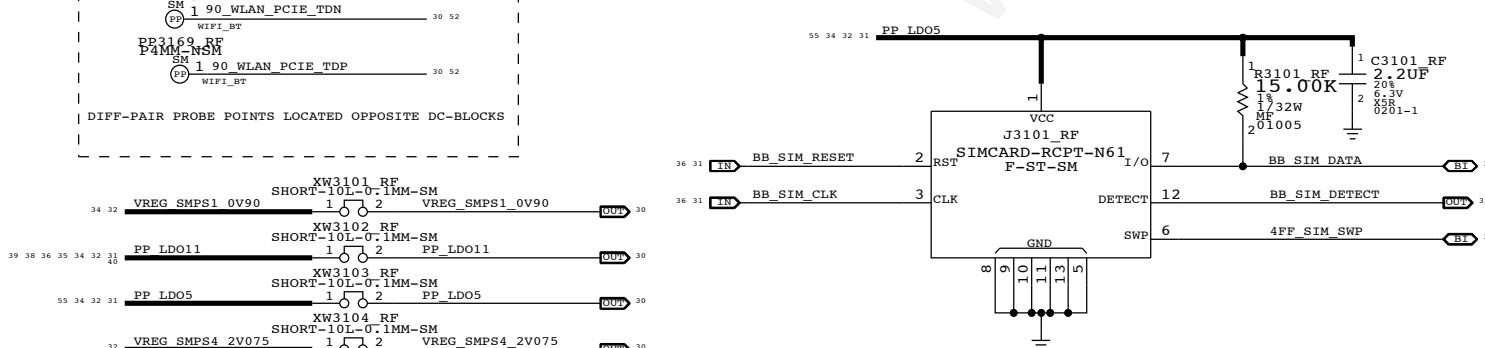
PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y3301_RF	KDS 19.2MHZ XTEL
197S0598	197S0593	ALTERNATE	Y3301_RF	AVX 19.2MHZ XTEL
138S00005	138S00003	ALTERNATE	C3216_RF	15UF CAPACITOR
138S0739	138S0706	ALTERNATE	C4207_RF	1.0UF CAPACITOR
138S0945	138S0706	ALTERNATE	C4207_RF	1.0UF CAPACITOR
138S1103	138S0719	ALTERNATE	C4007_RF	4.7UF CAPACITOR
339S0231	339S0228	ALTERNATE	U5201_RF	CORONA MODULE USI
339S0242	339S0228	ALTERNATE	U5201_RF	CORONA MODULE TDK
155S00024	155S0950	ALTERNATE	F_TRI_RF	TRIPLEXER BIN2



## SIM CARD ESD PROTECTION



## STM CARD CONNECTOR



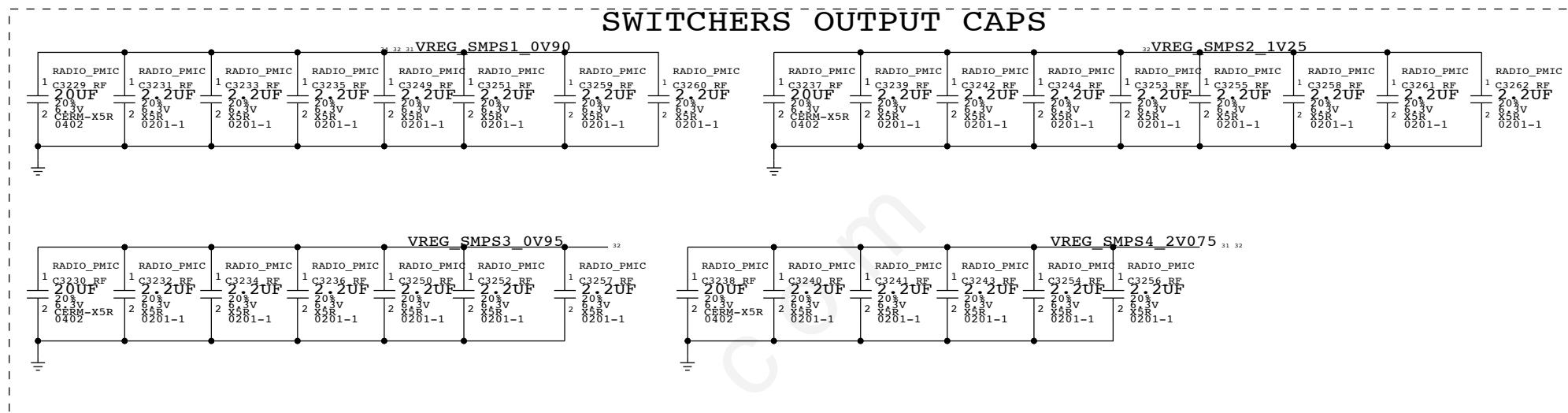
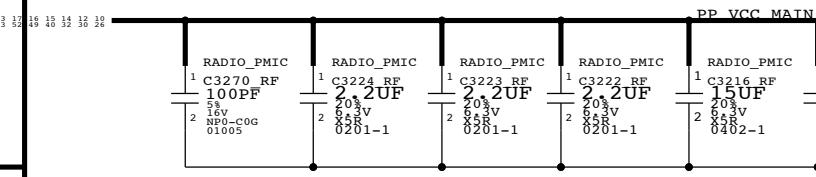
CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

# BASEBAND PMU (1 OF 2)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

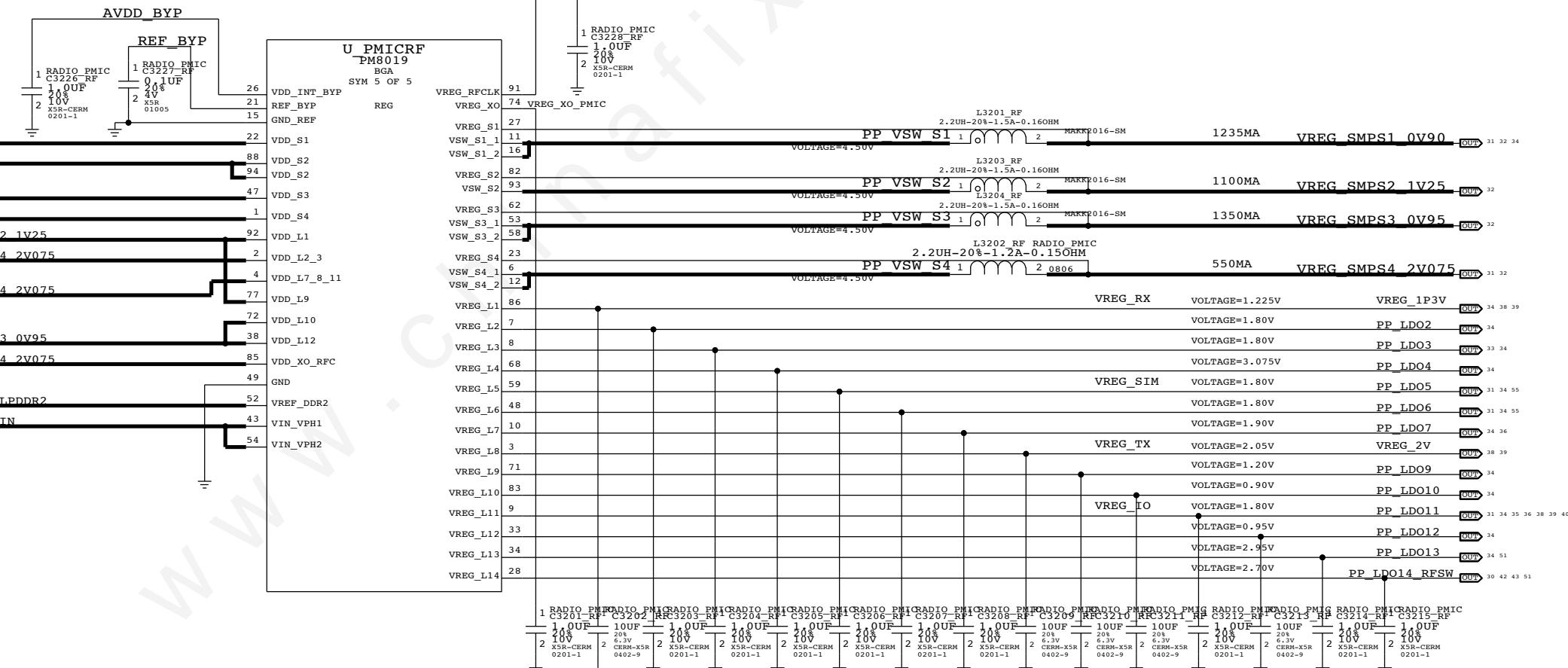
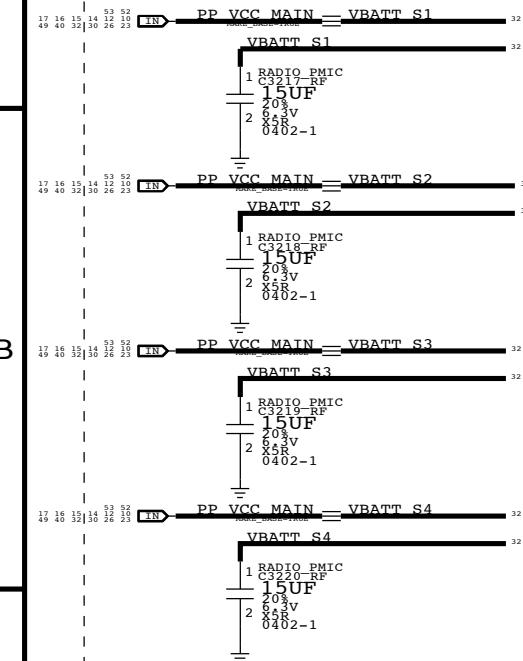


D



C

## SWITCHERS BULK CAPS



A

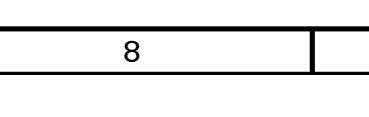
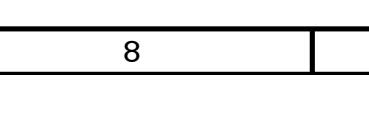
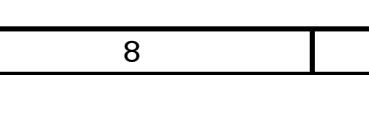
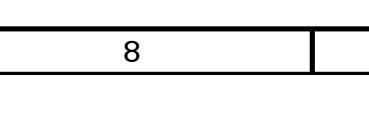
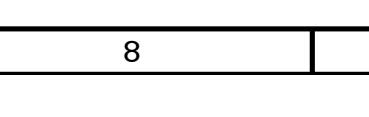
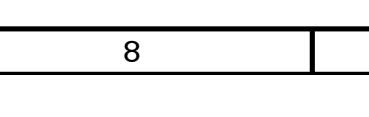
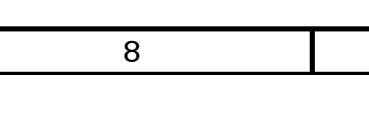
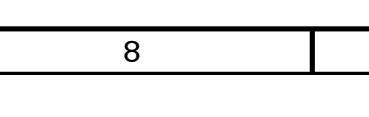
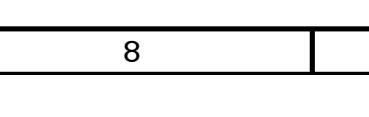
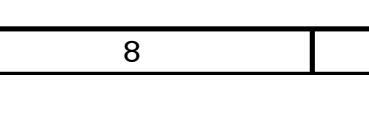
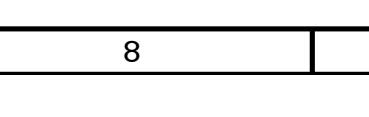
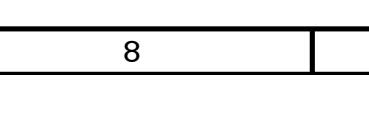
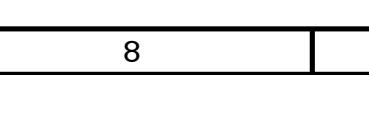
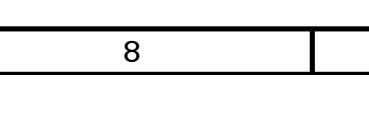
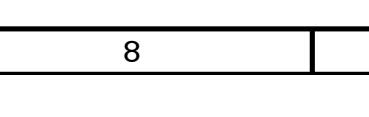
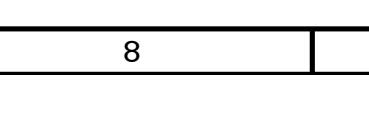
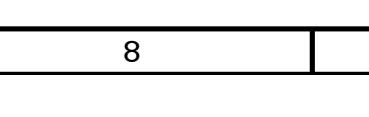
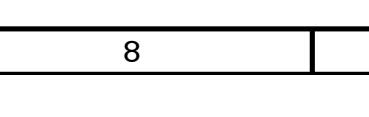
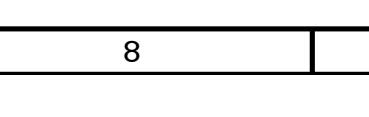
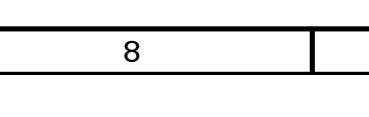
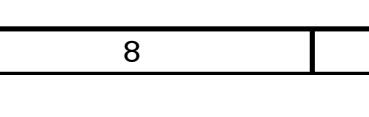
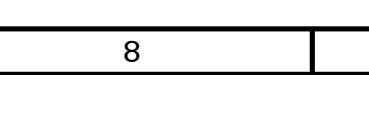
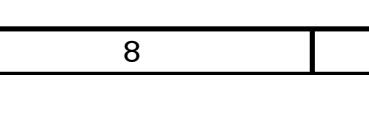
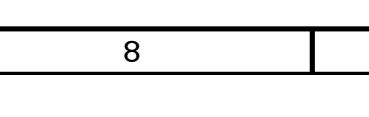
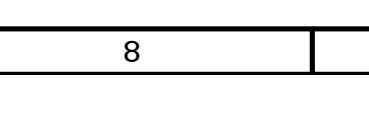
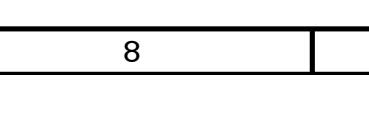
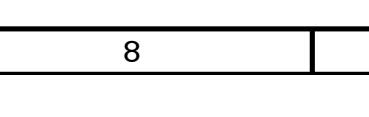
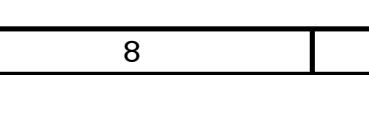
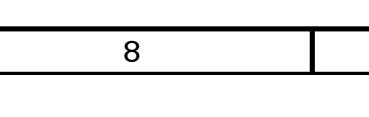
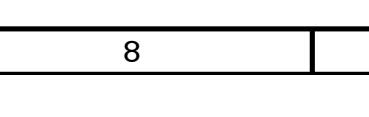
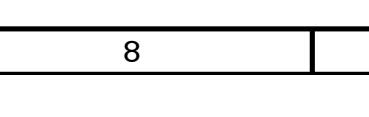
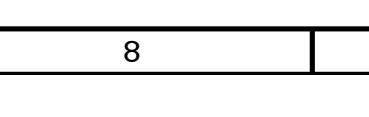
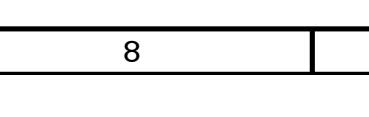
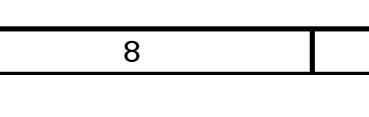
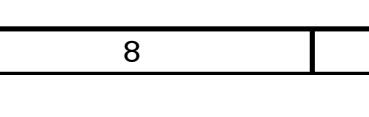
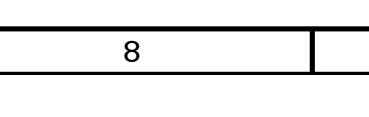
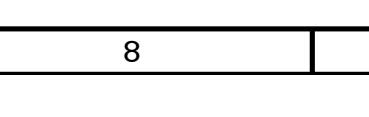
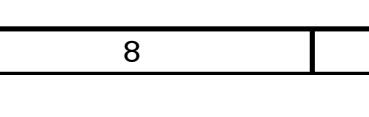
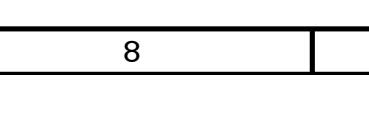
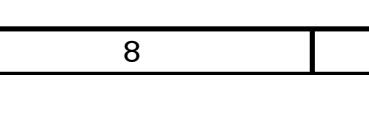
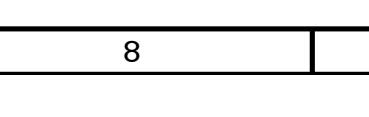
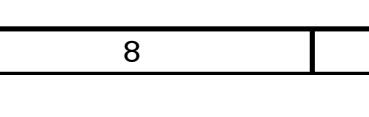
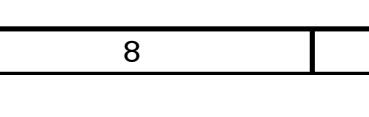
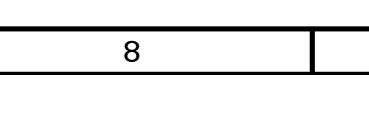
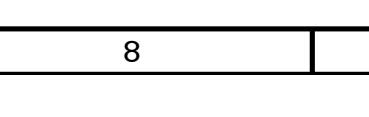
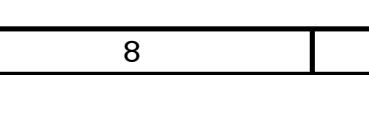
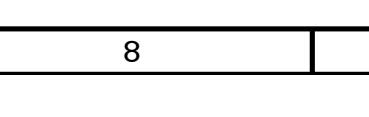
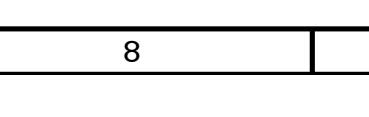
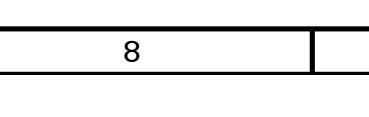
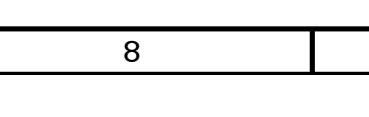
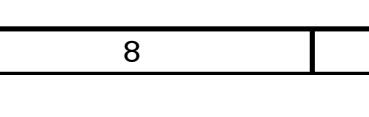
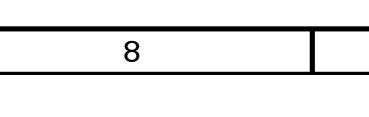
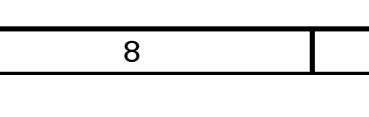
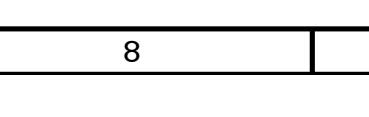
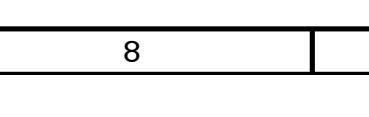
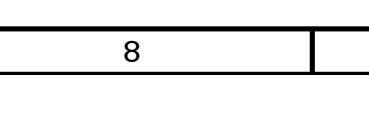
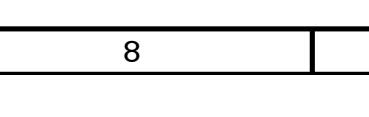
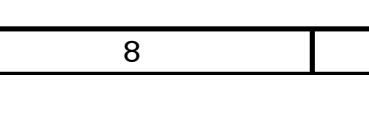
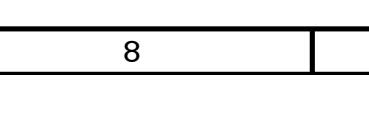
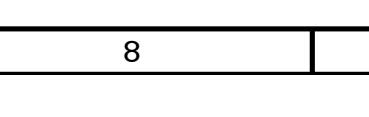
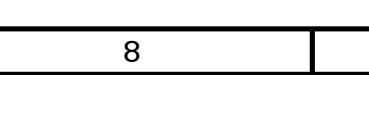
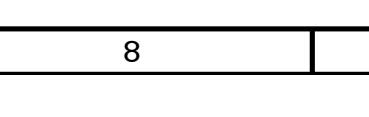
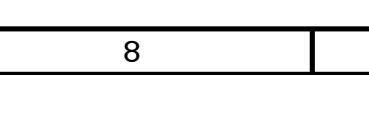
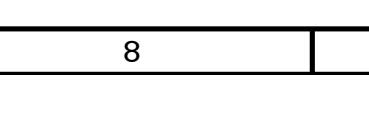
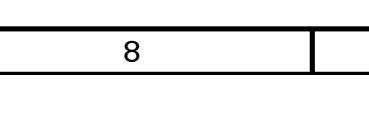
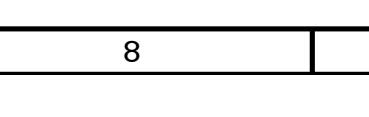
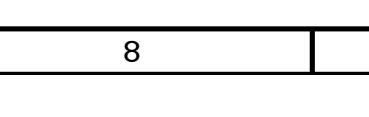
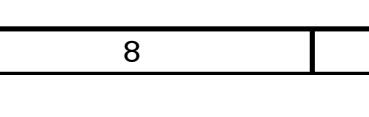
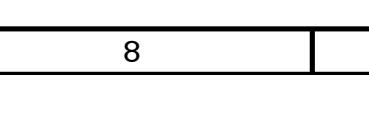
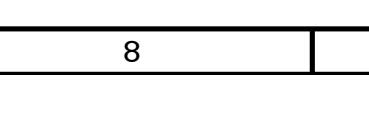
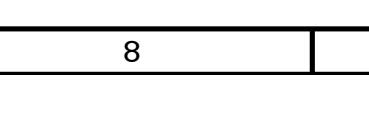
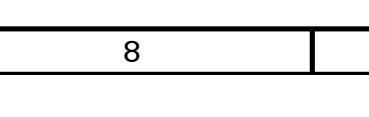
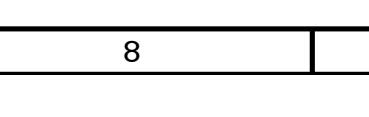
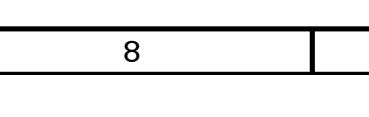
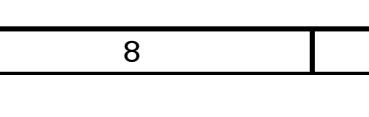
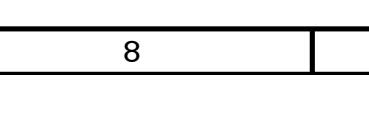
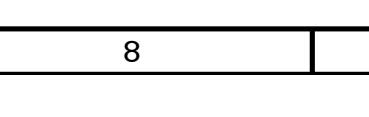
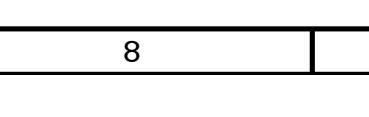
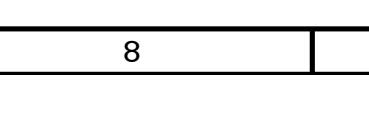
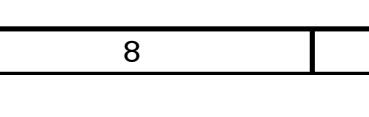
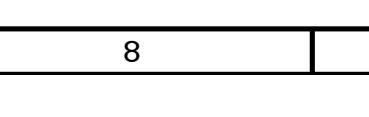
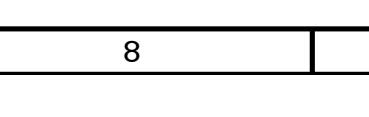
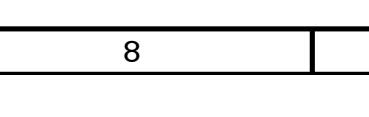
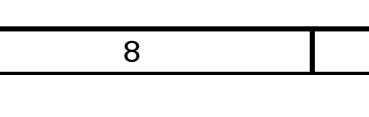
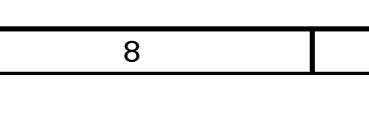
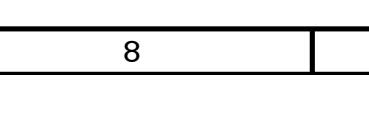
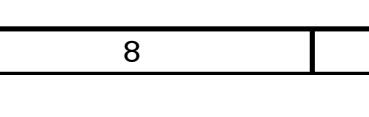
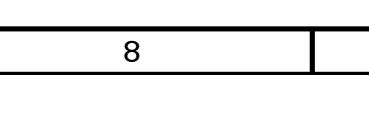
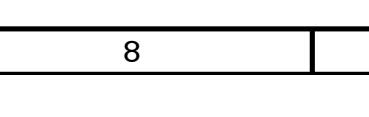
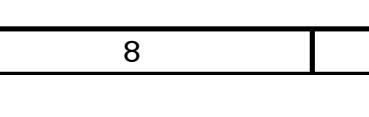
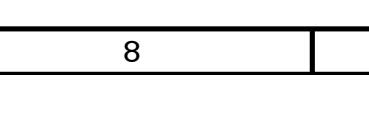
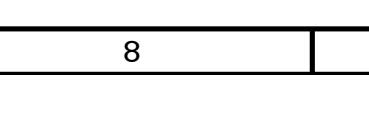
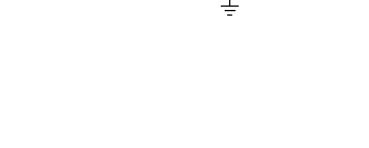
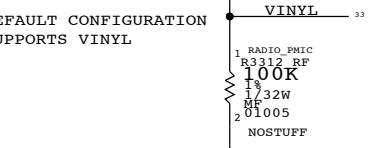
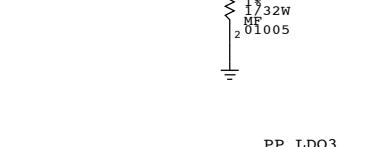
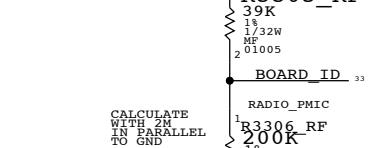
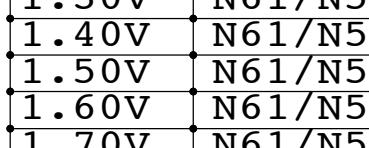
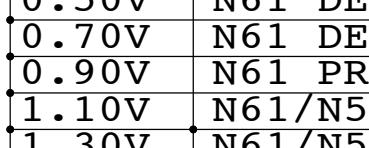
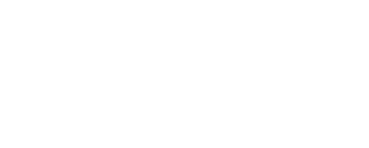
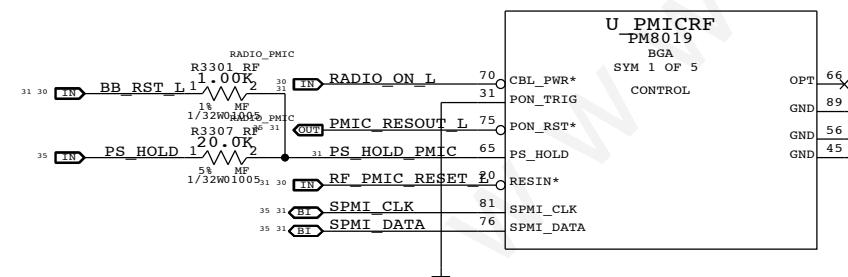
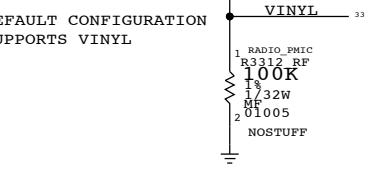
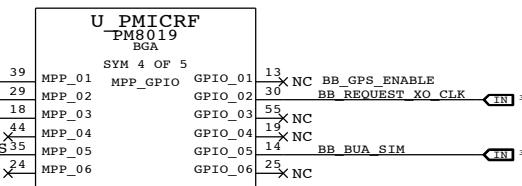
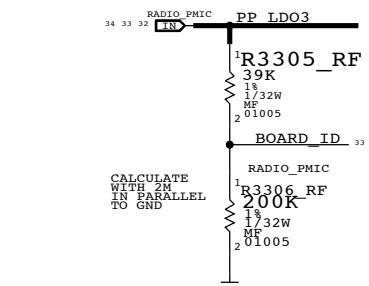
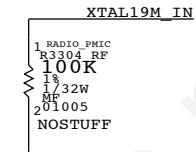
# BASEBAND PMU (2 OF 2)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C401  
R411  
L400  
U404



BOARD	REVISION
0.00V	N61 PROTO_MLB1
0.50V	N61 DEV3
0.70V	N61 DEV4
0.90V	N61 PROTO_MLB2
1.10V	N61/N56 PROTO1
1.30V	N61/N56 PROTO2
1.40V	N61/N56 EVT1
1.50V	N61/N56 EVT2 (CARRIER)
1.60V	N61/N56 DVT
1.70V	N61/N56 PVT



# BASEBAND (1 OF 3)

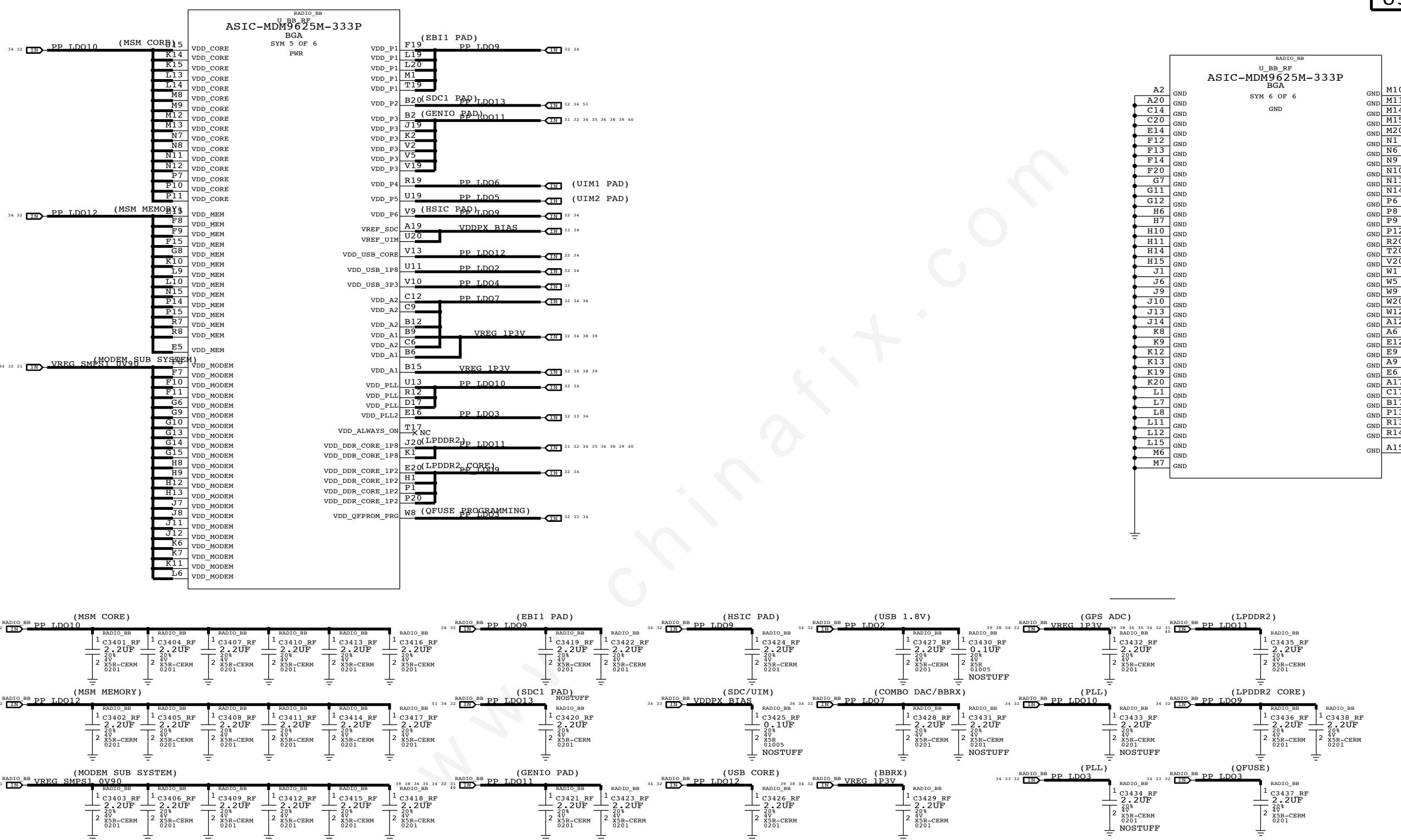
CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST

C538

R500

L500

U502



# BASEBAND (2 OF 3)

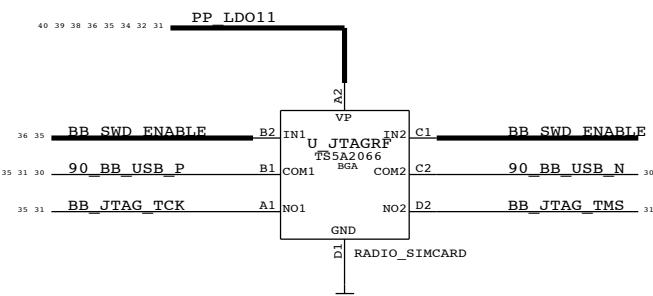
CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C600  
R606  
L600  
U602



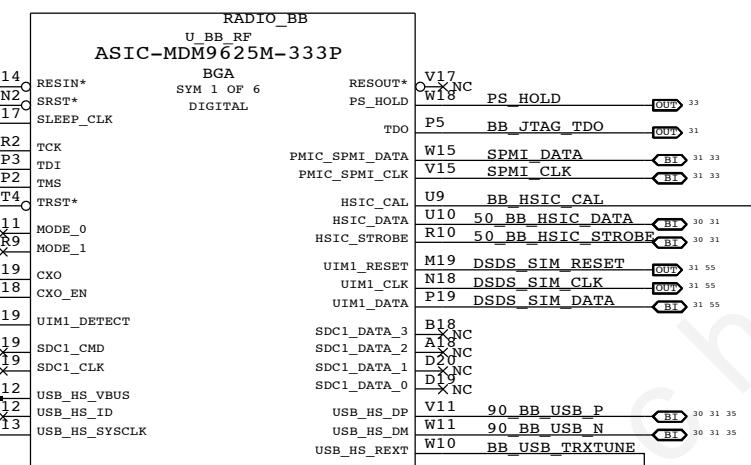
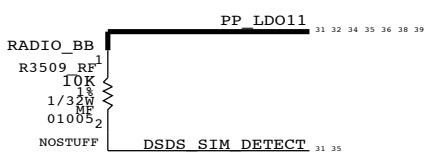
D

D



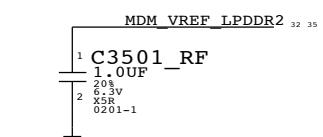
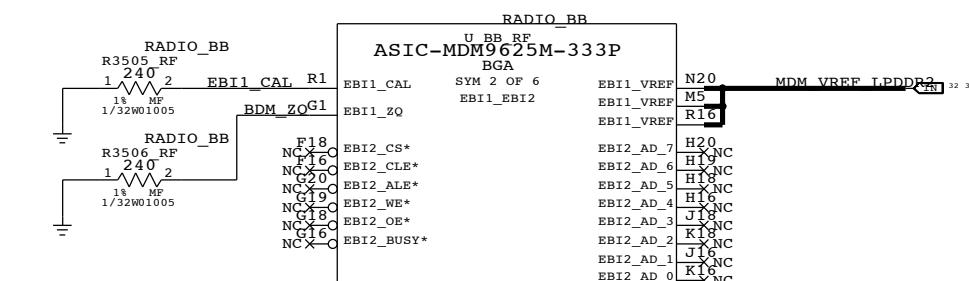
C

C



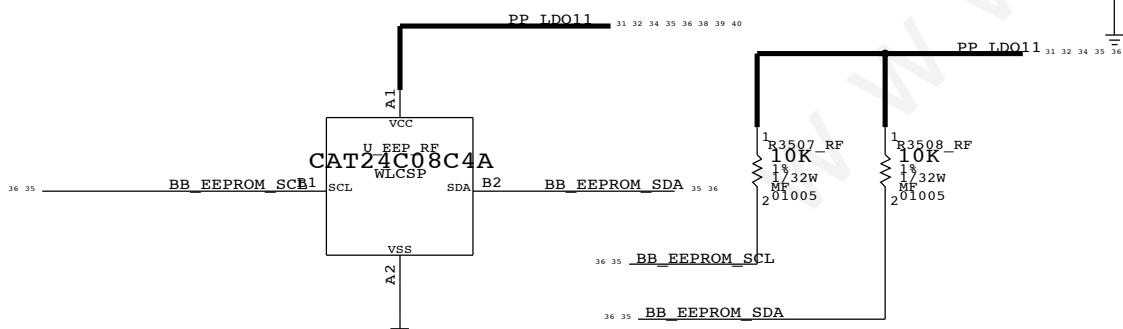
B

B



A

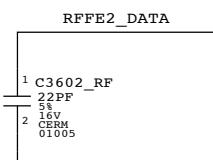
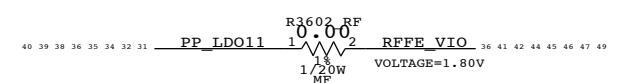
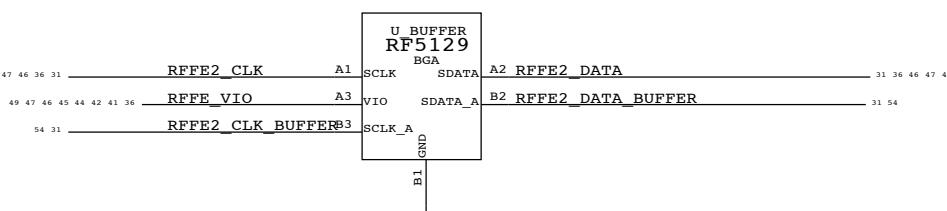
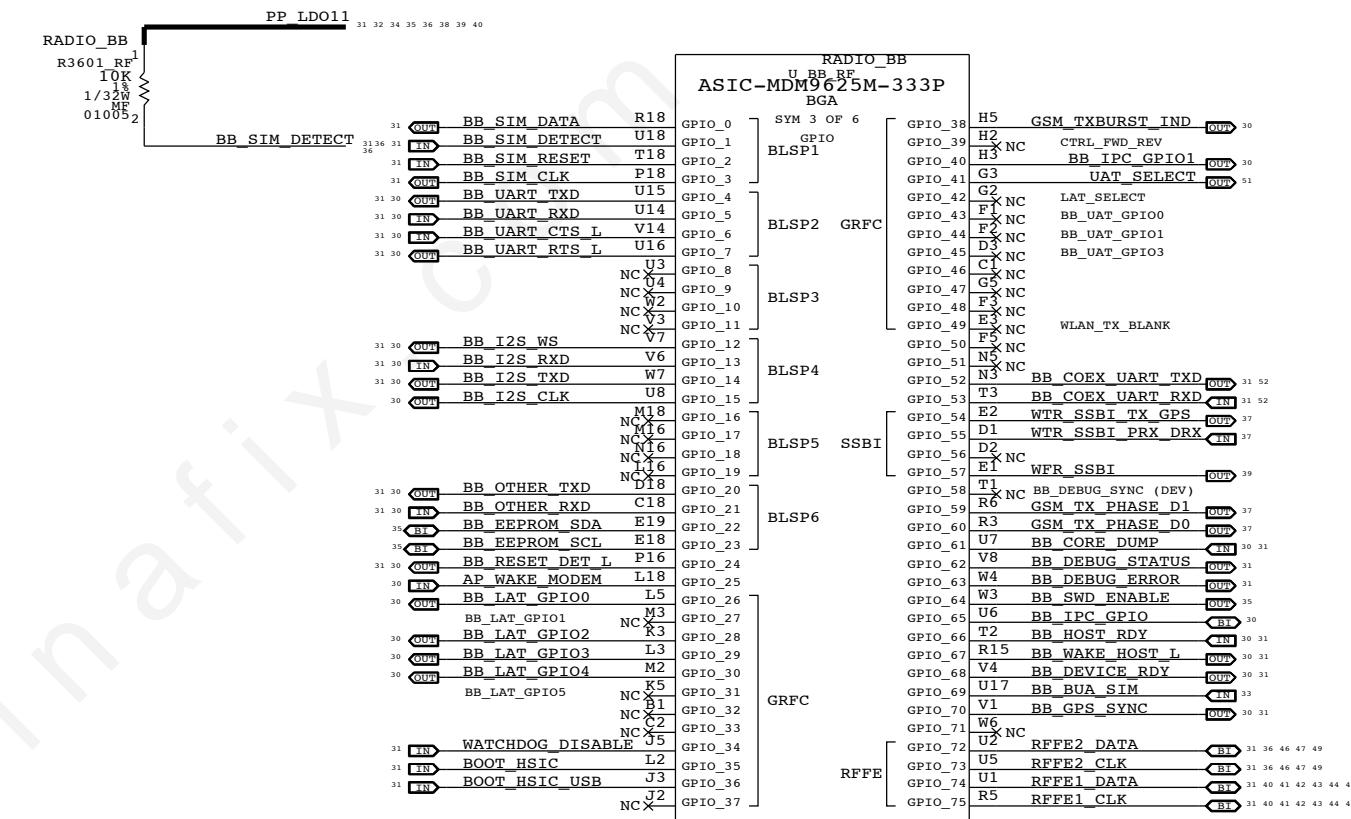
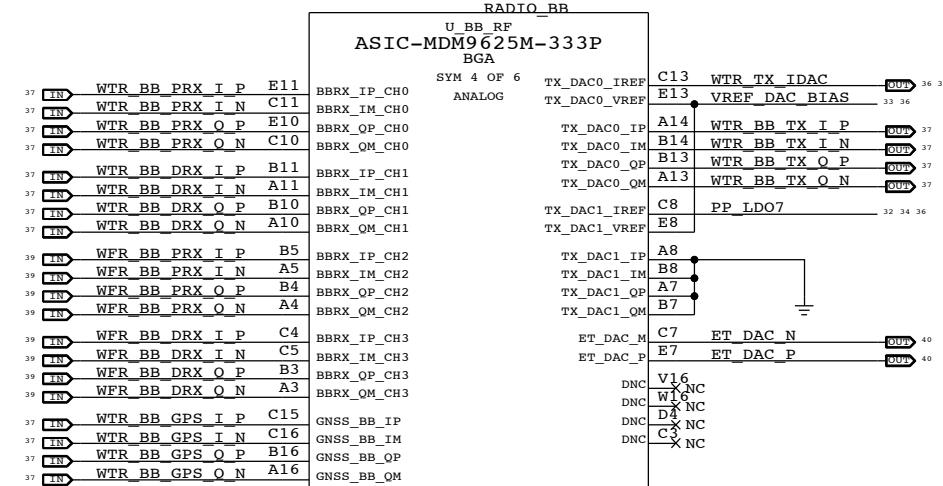
A



# BASEBAND (3 OF 3)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C704  
R700  
L700  
U702



MOBILE DATA MODEM (2 OF 2)

# WTR TRANSCEIVER (1 OF 2)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C802  
R802  
L800  
U803



D

D

C

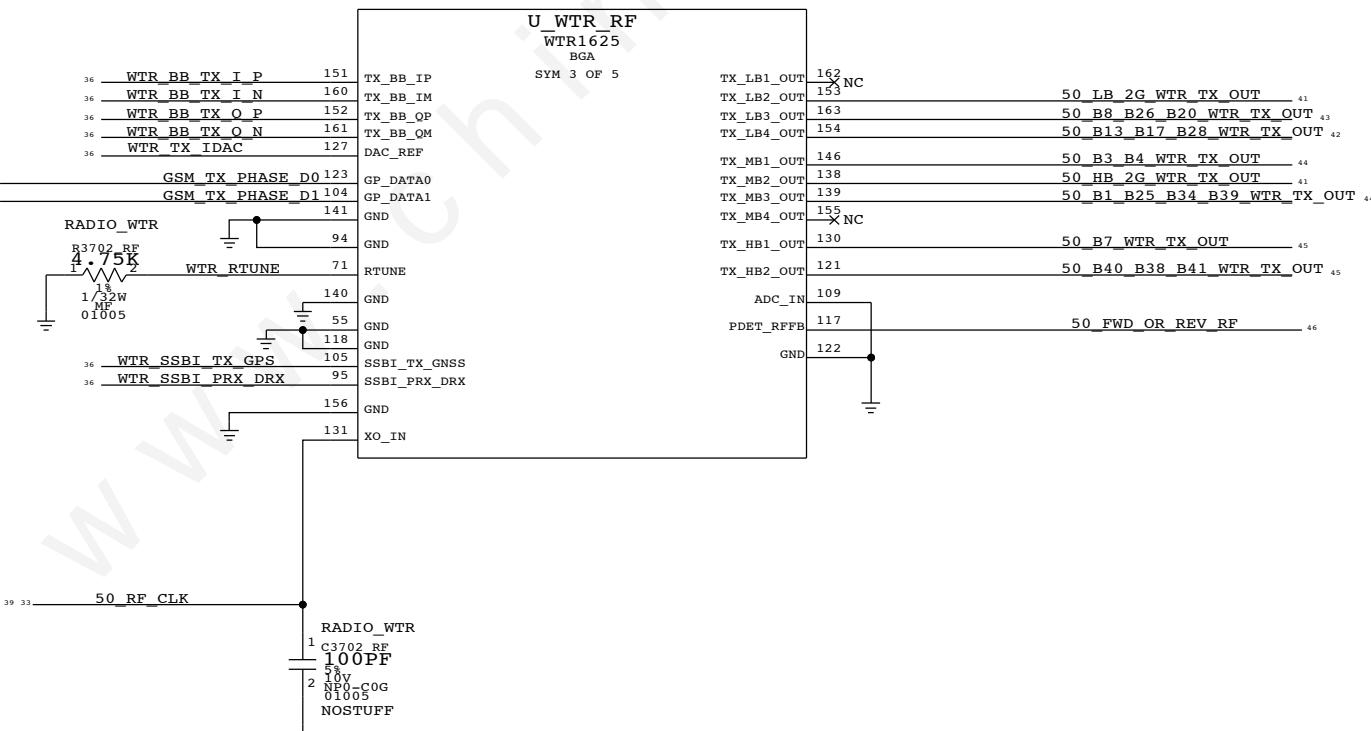
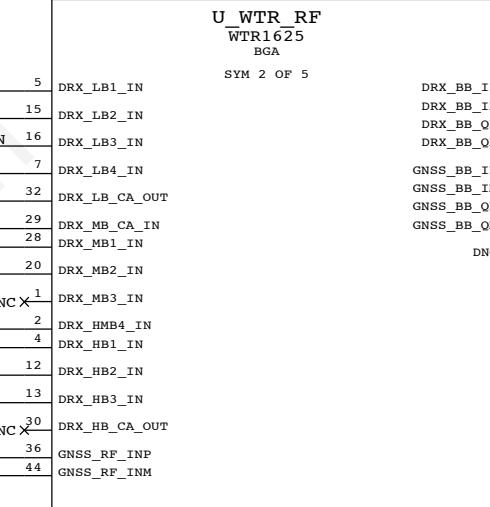
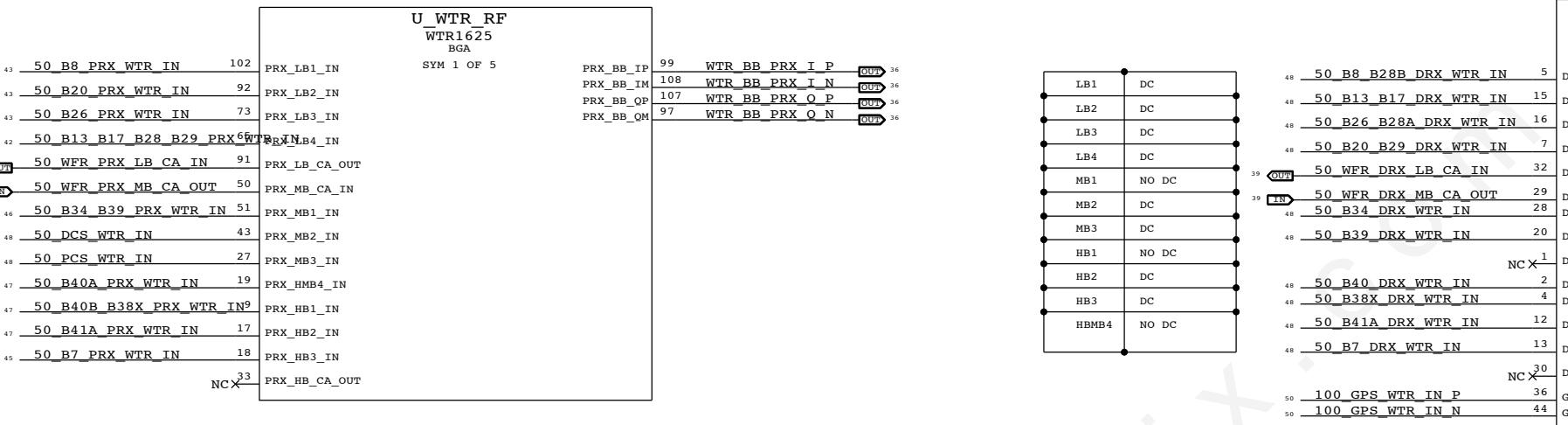
C

B

B

A

A



# WTR TRANSCEIVER (2 OF 2)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C934

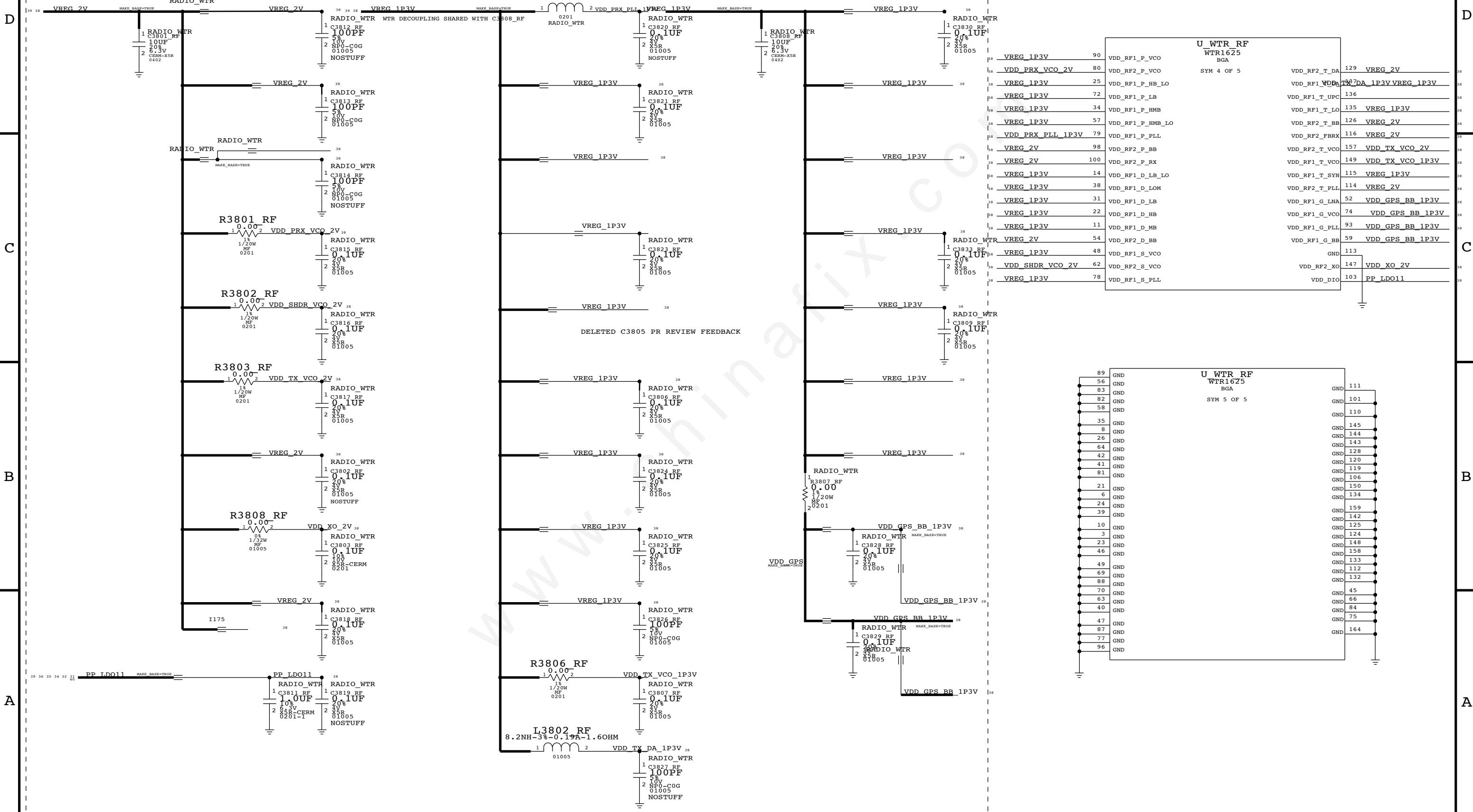
R926

L3802 RF

U902



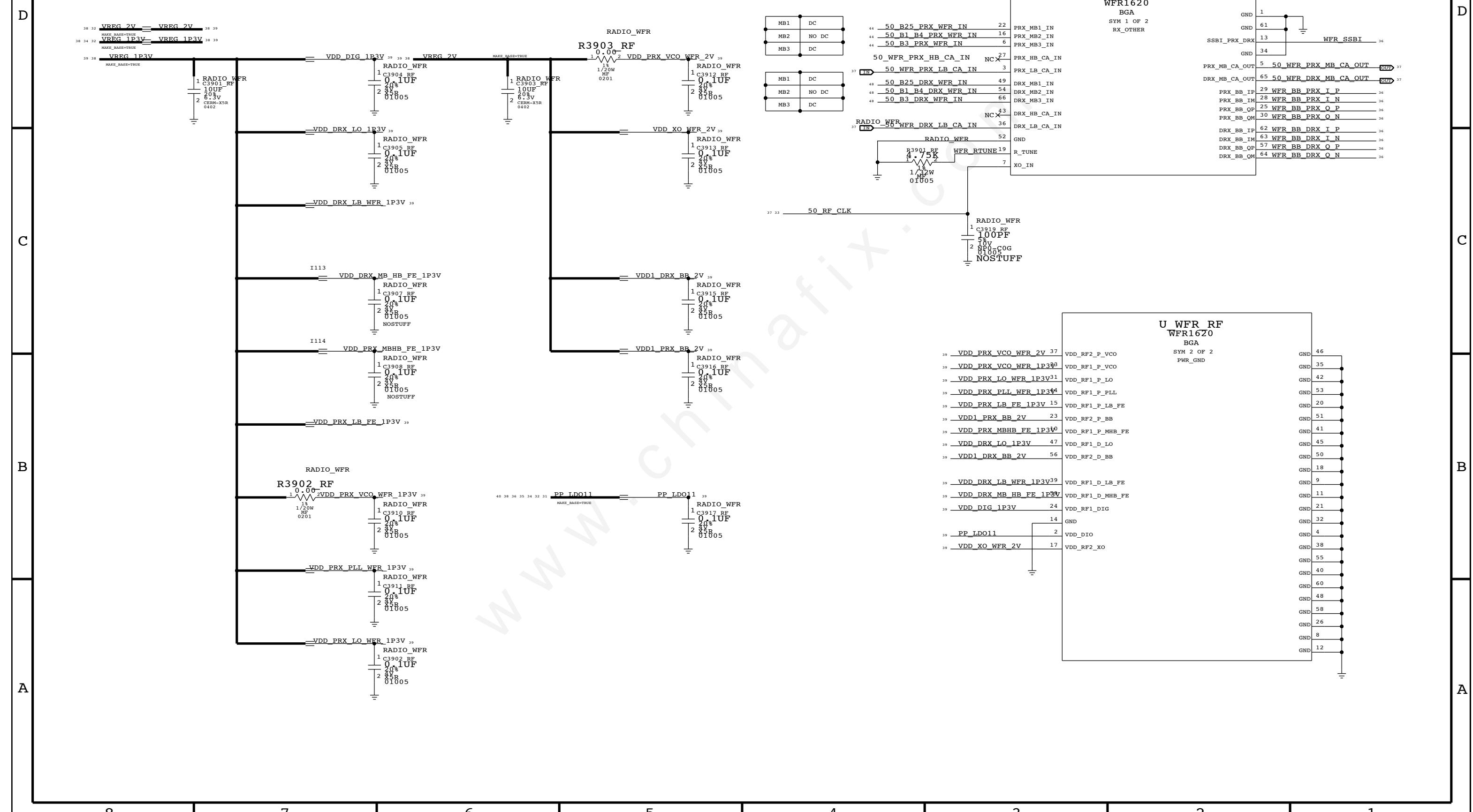
## WTR DECOUPLING CAPS



# WFR TRANSCEIVER

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C1019  
R1016  
L1000  
U1002



# QFE DCDC

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C1110  
R1102  
L1104  
U1101



D

D

C

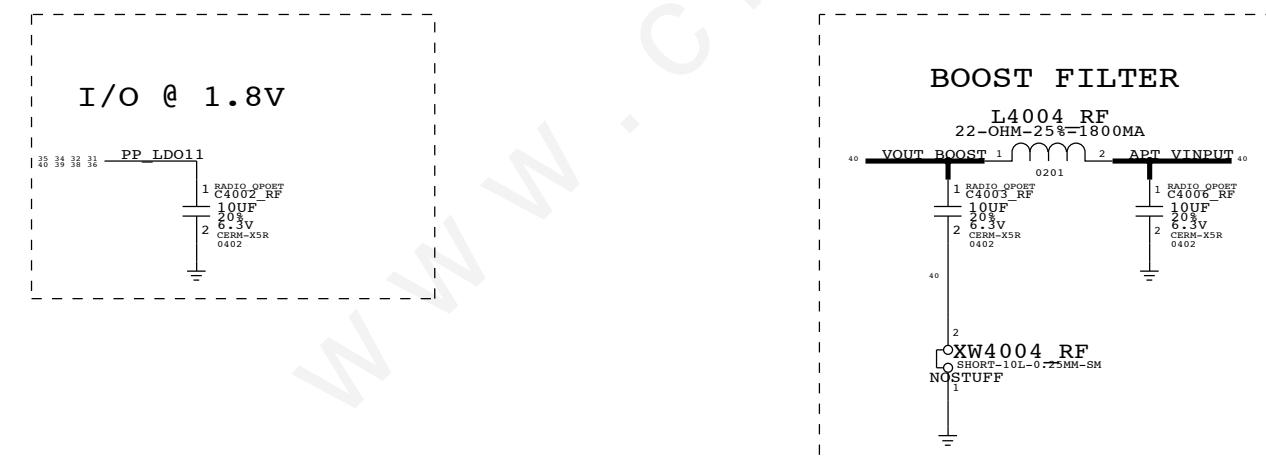
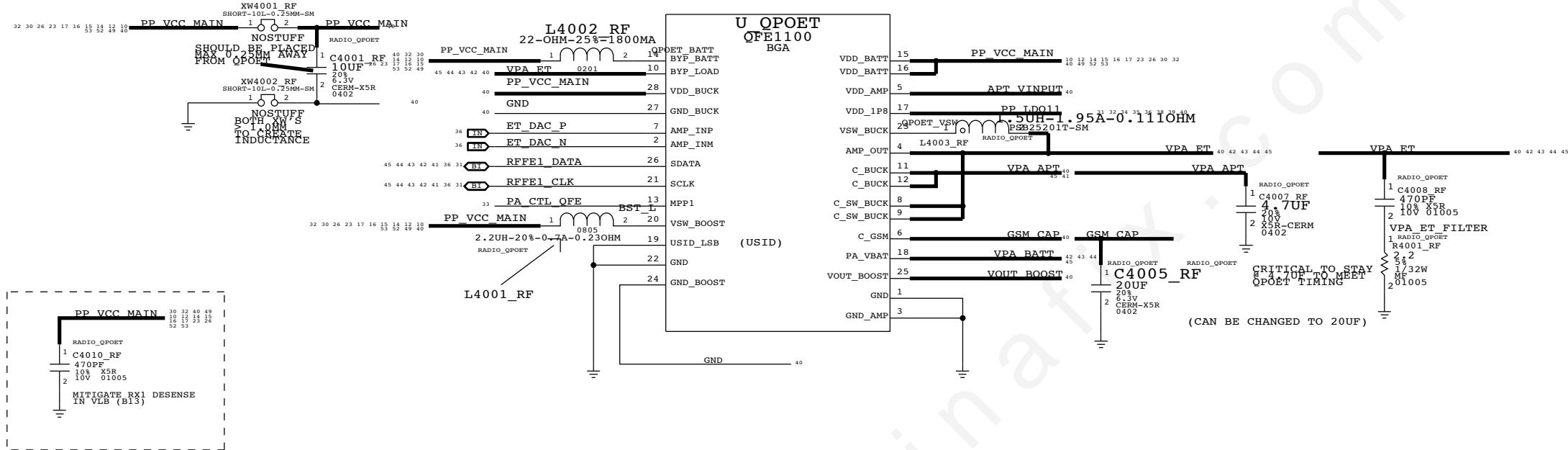
C

B

B

A

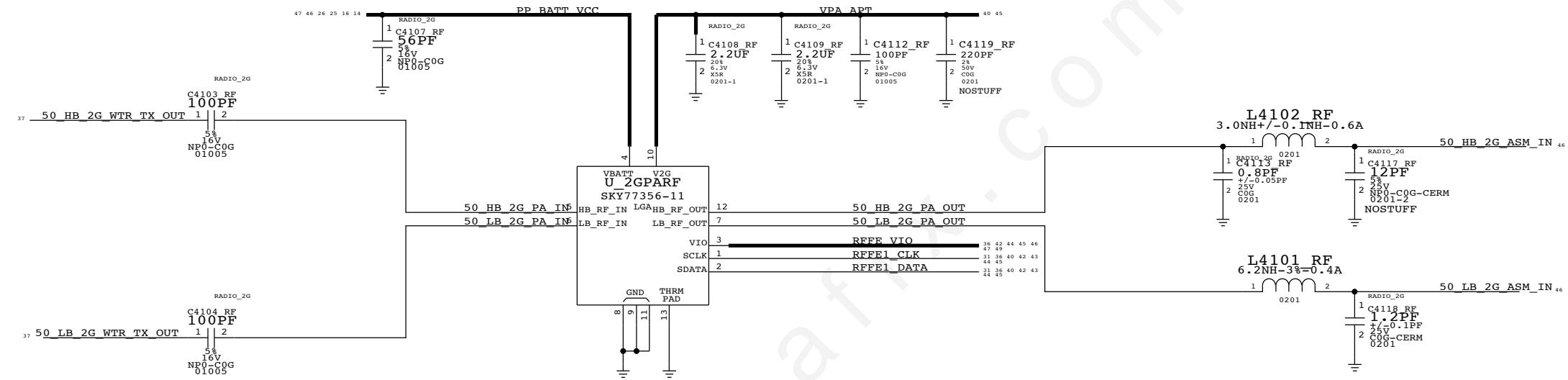
A



# 2G PA

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

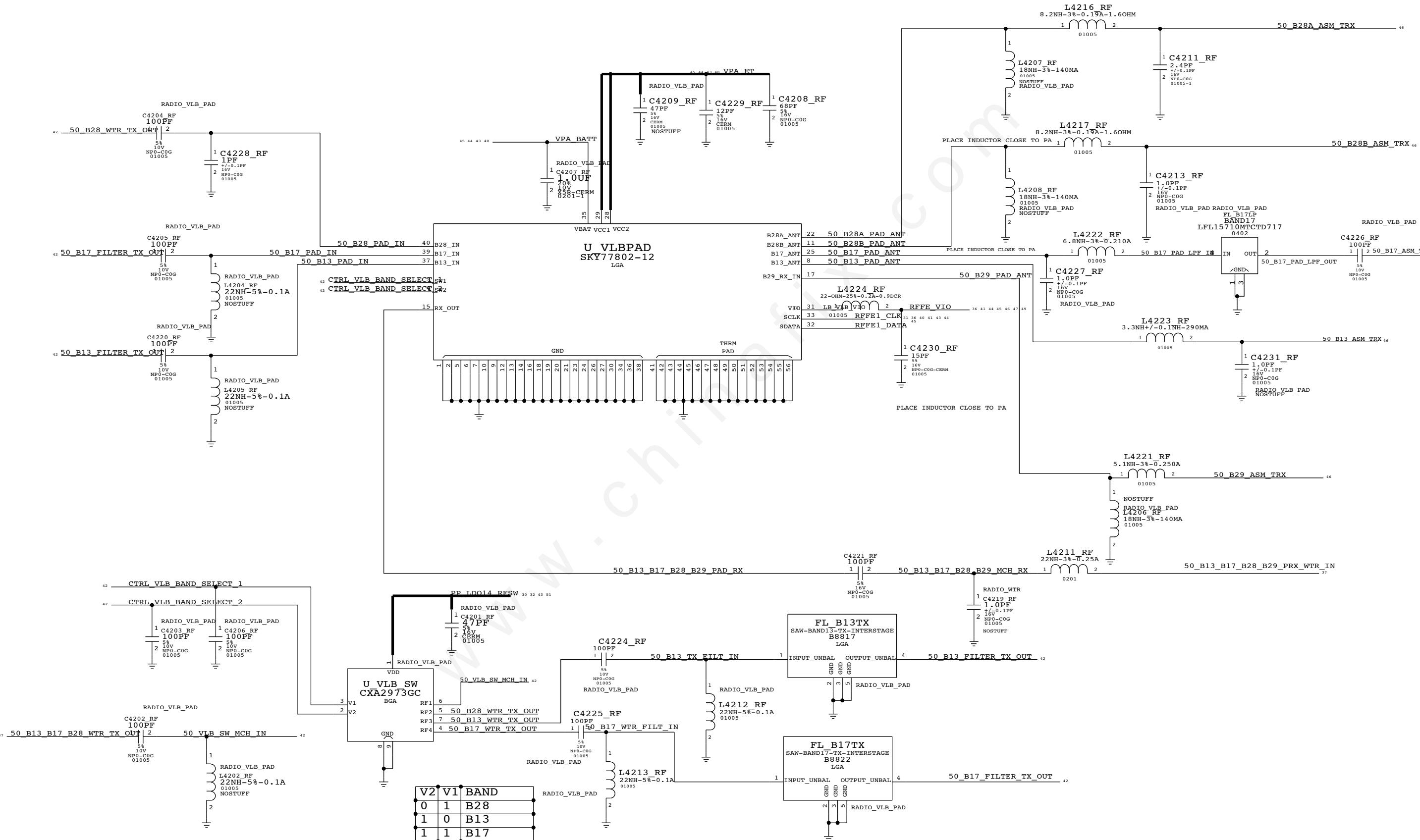
C1208  
R1200  
L1204  
U1201



# VERY LOW BAND PAD (B13, B17, B28)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

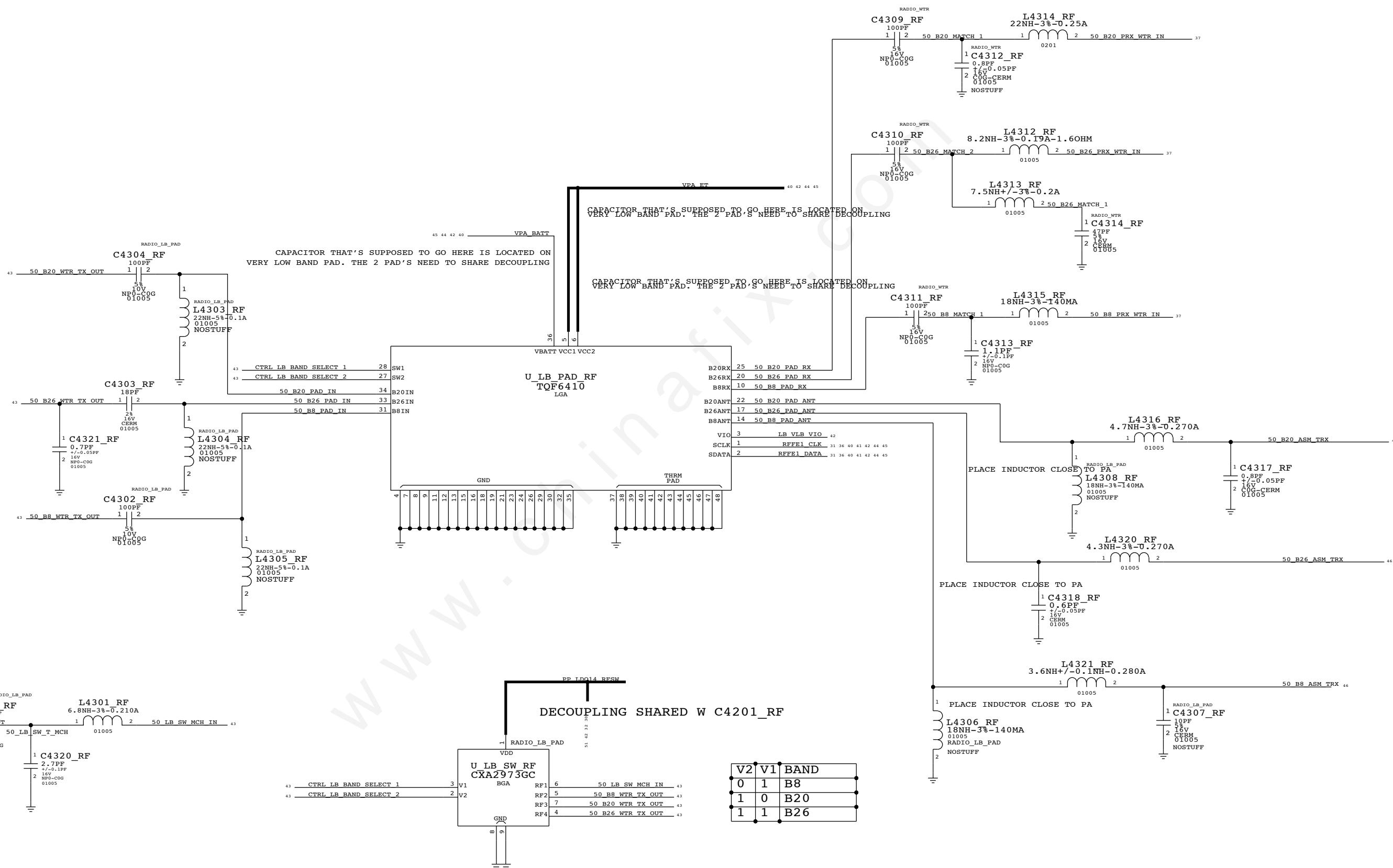
C1332  
R1300  
L4215 RF  
U1304



# LOW BAND PAD (B8, B26, B20)

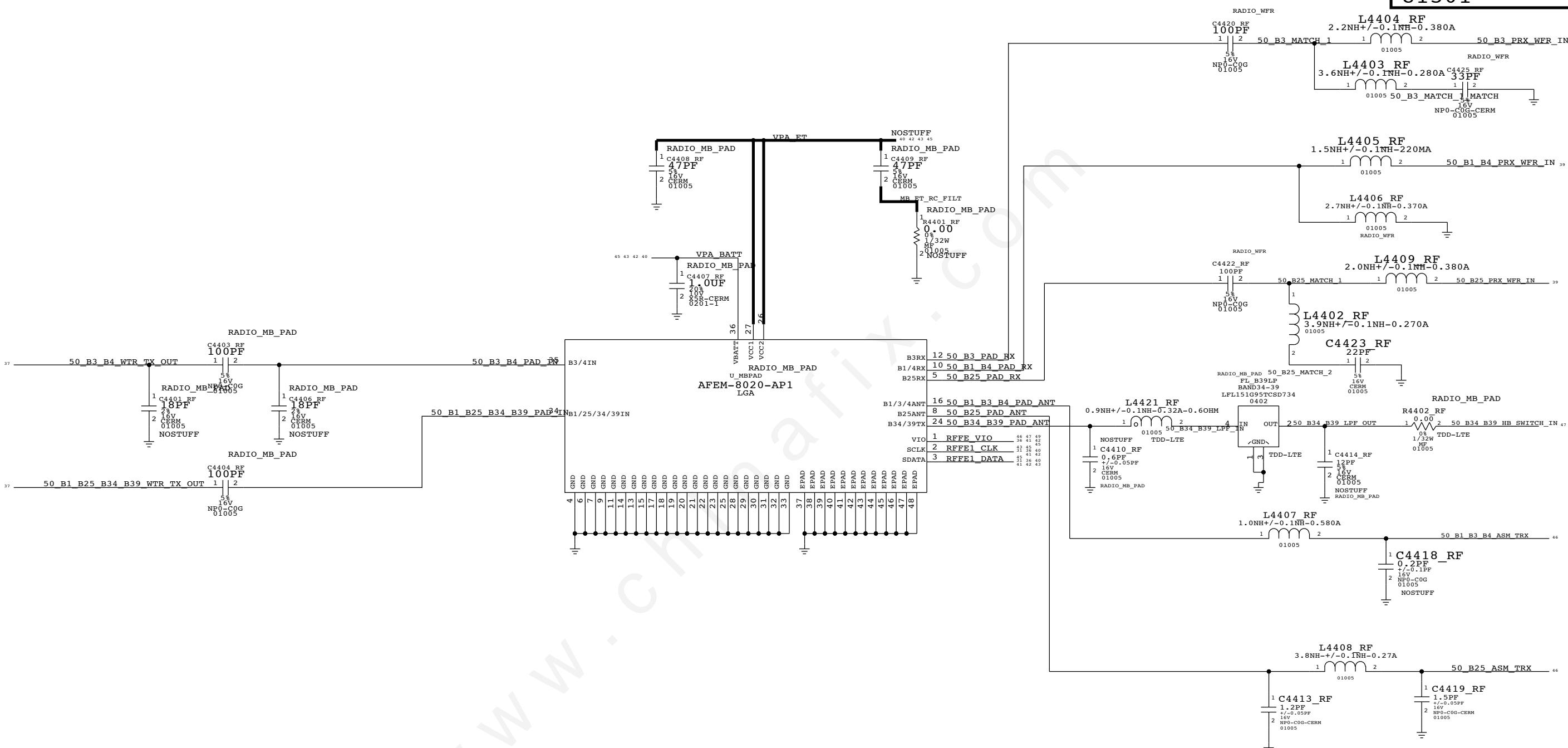
CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST

C4318 RF  
R1400  
L4322 RF  
U1402



# MID BAND PAD (B1, B25, B3, B4, B34, B39)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.



# HIGH BAND PAD (B7, B38, B40, B41, XGP)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C4533	RF
R1600	
L1616	
U1601	



D

D

C

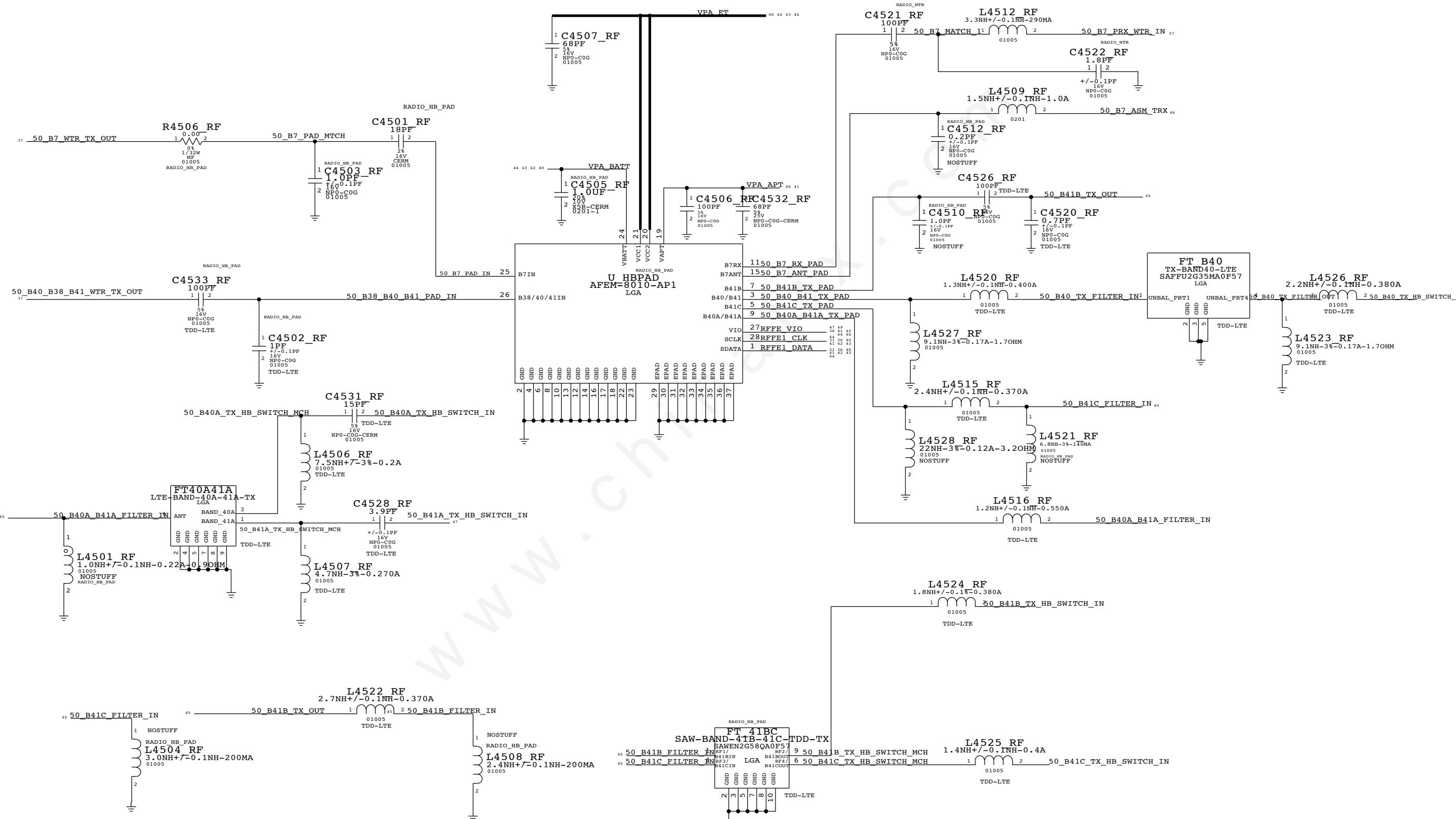
C

B

B

A

A



# ANTENNA SWITCH

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C1702
R1700
L4608 RF
U1702



D

D

C

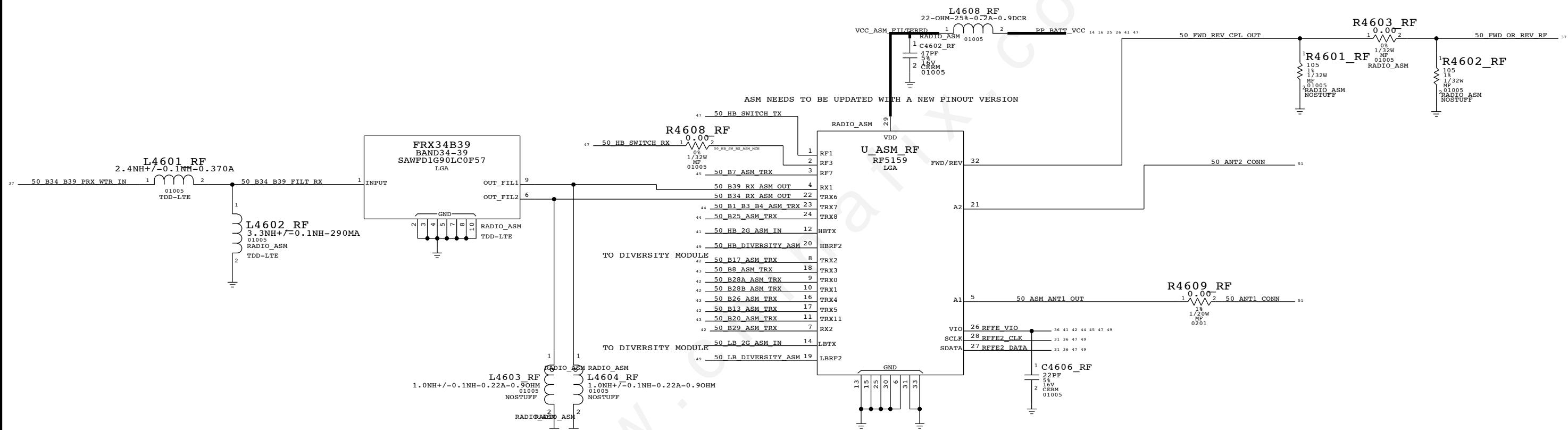
C

B

B

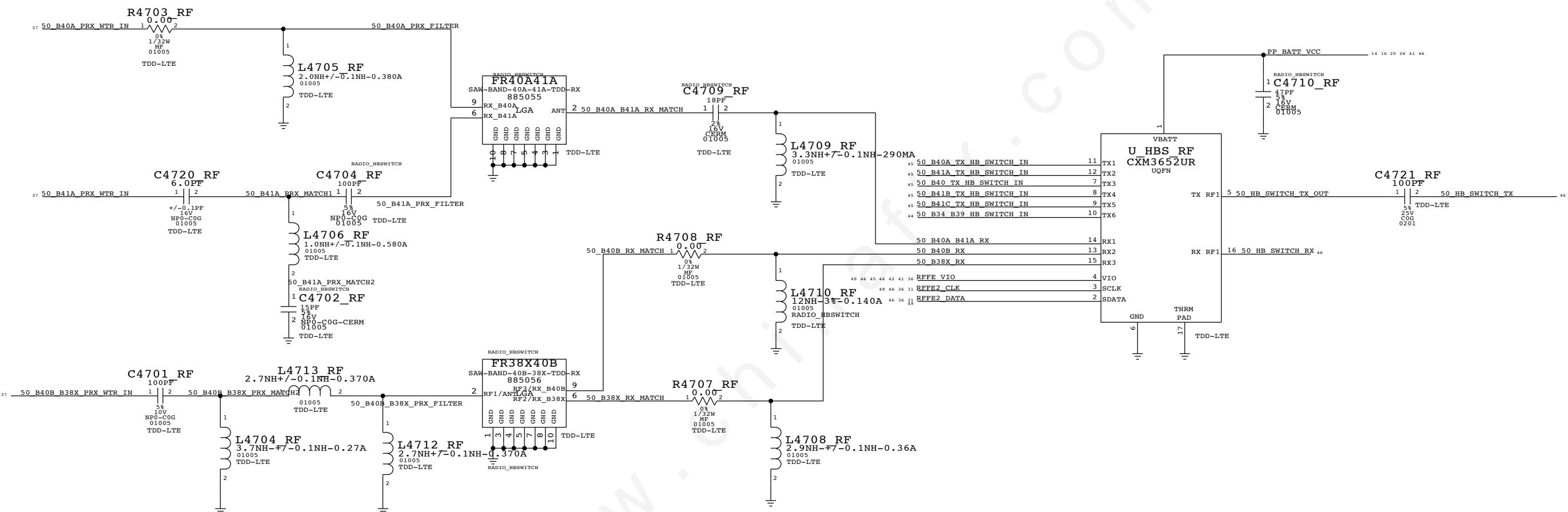
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A



# HIGH BAND SWITCH

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.



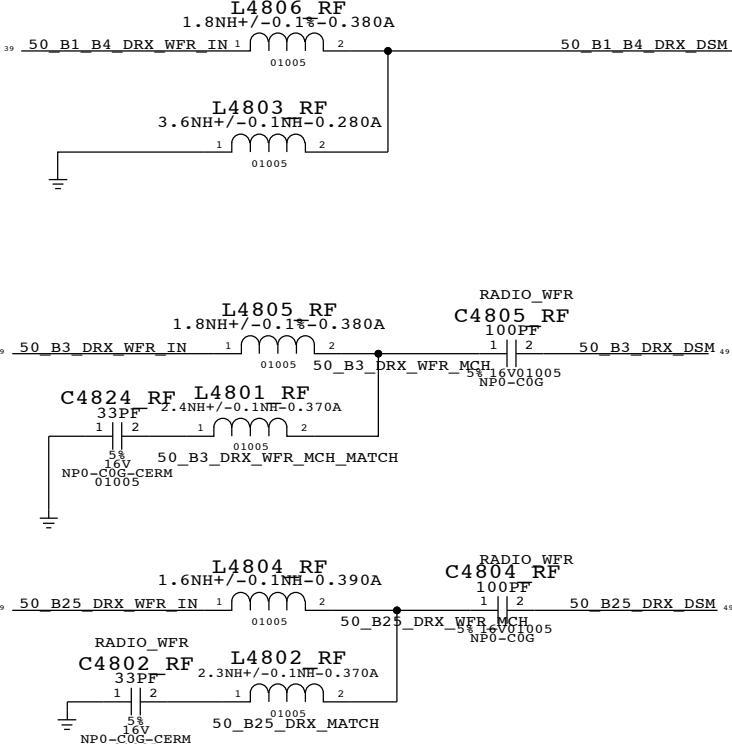
# RX DIVERSITY (1)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

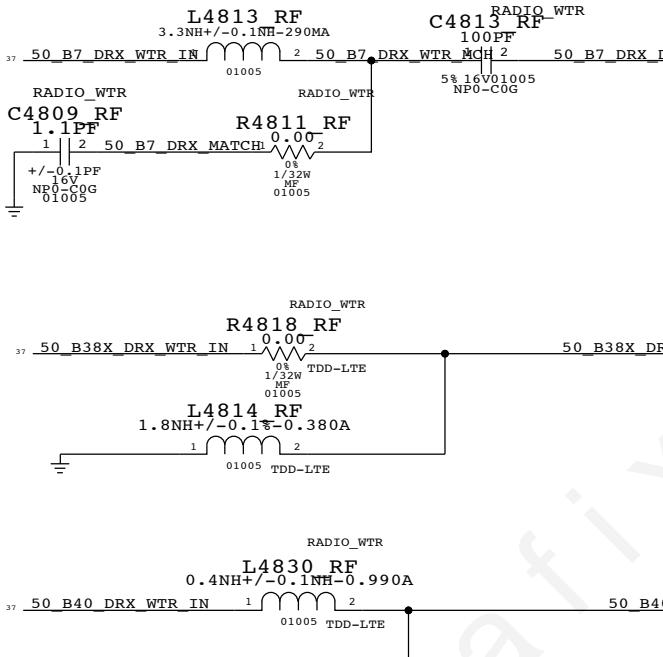
C4826 RF
R1800
L1829
U1801



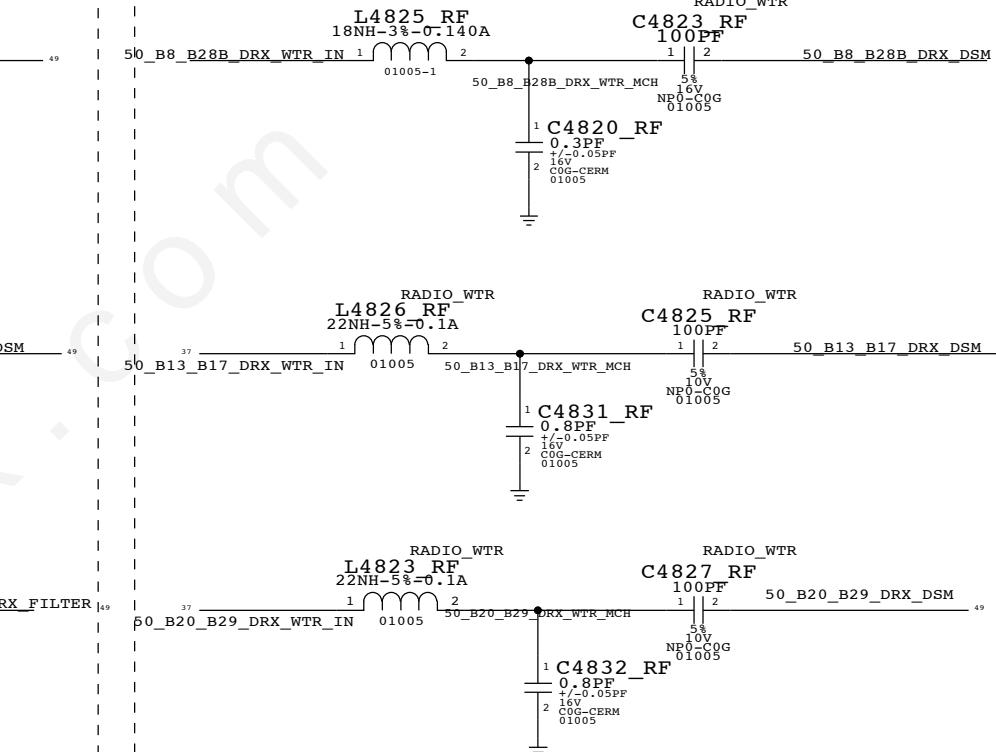
## MIDBAND MIDBAND DIVERSITY - WFR



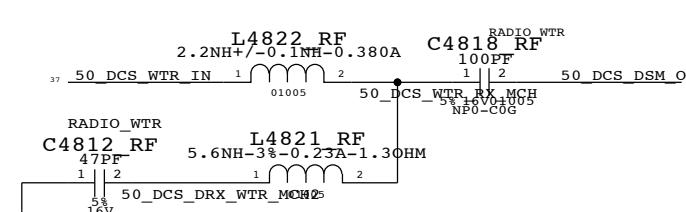
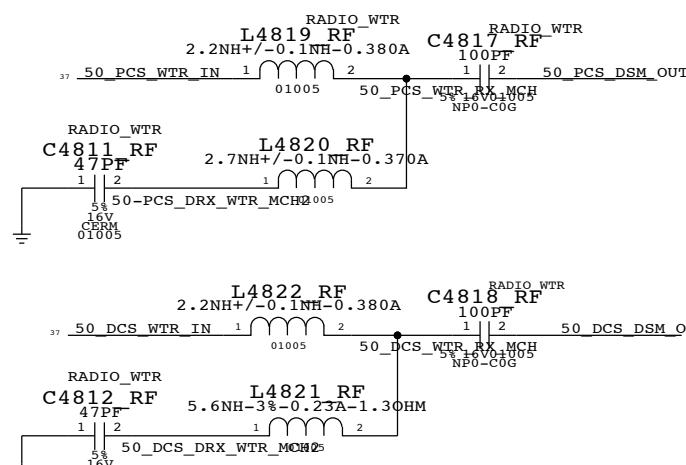
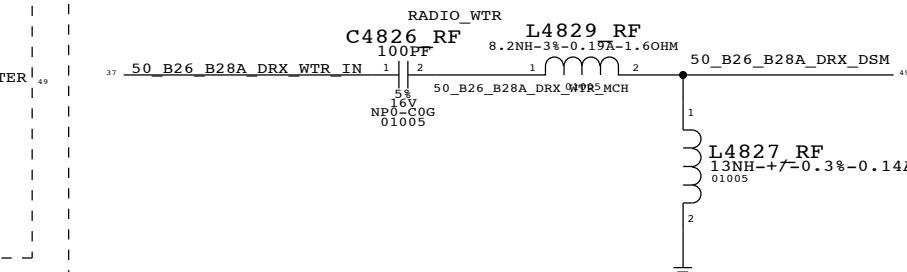
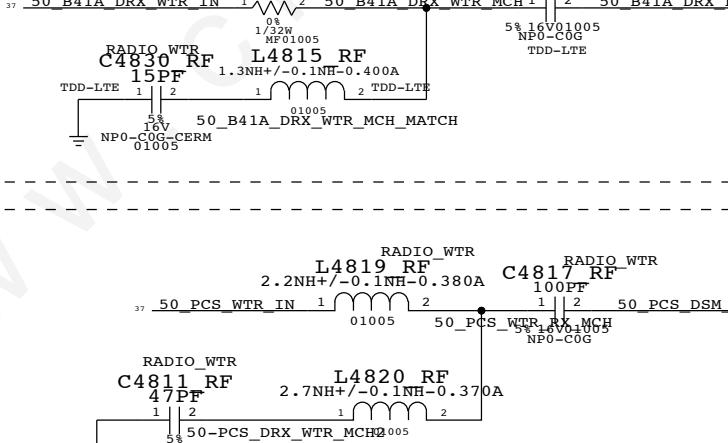
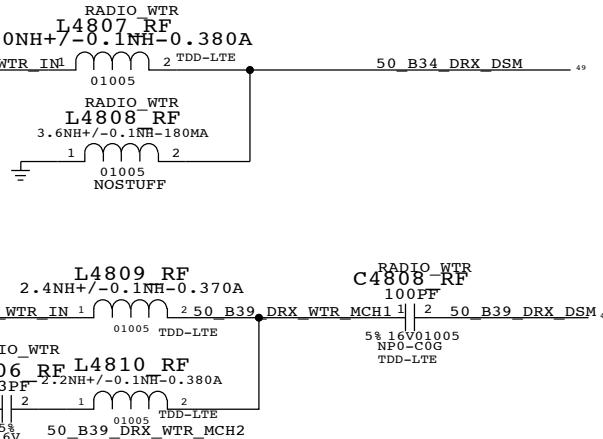
## HIGHBAND DIVERSITY - WTR



## LOWBAND DIVERSITY - WTR



## MIDBAND DIVERSITY - WTR



D

D

C

C

B

B

A

A

# RX DIVERSITY (2)

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C1900  
R1900  
L1900  
U1901



D

D

C

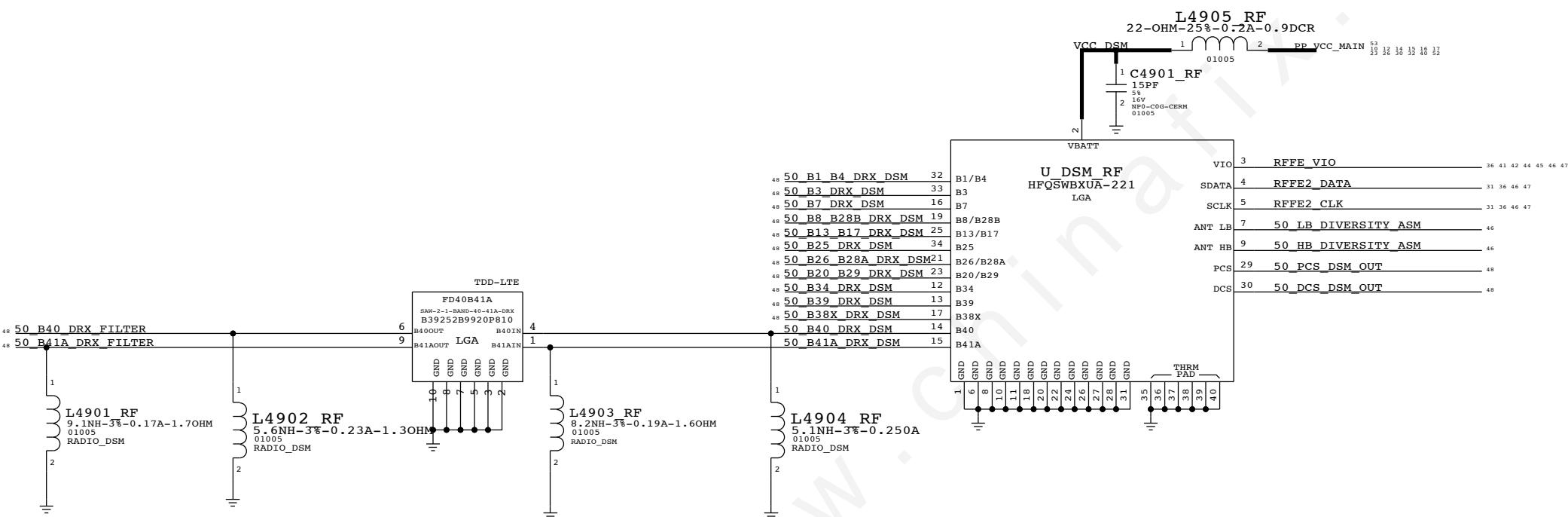
C

B

B

A

A



# GPS

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C1900
R1900
L1900
U1901



D

D

C

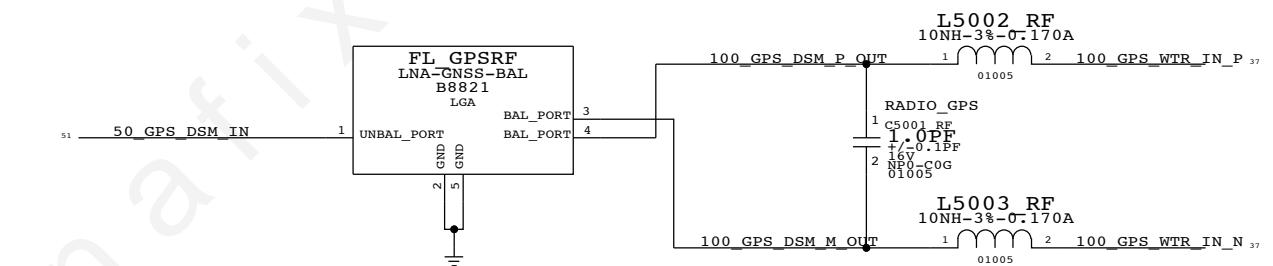
C

B

B

A

A

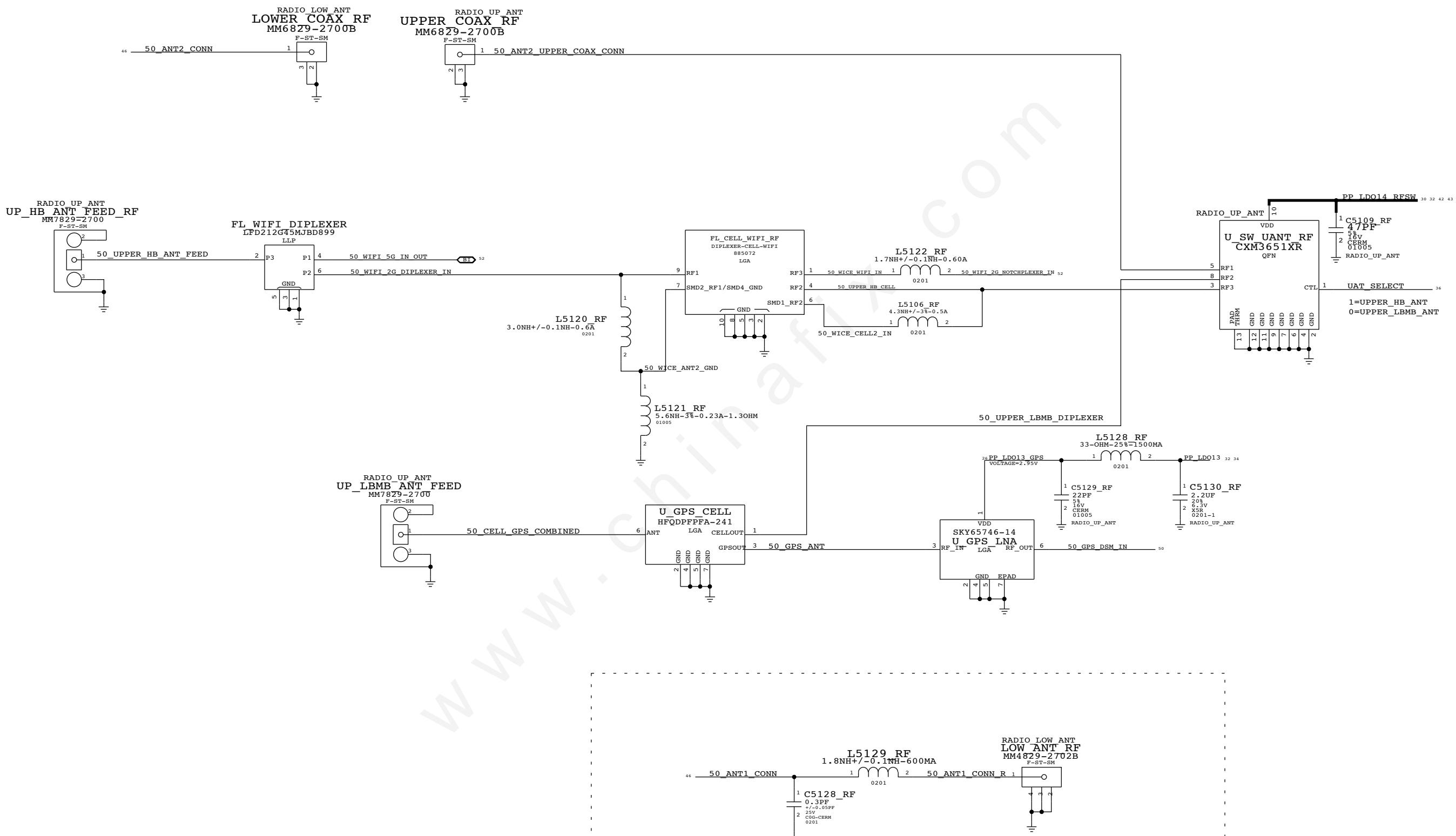


# ANTENNA FEED'S

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

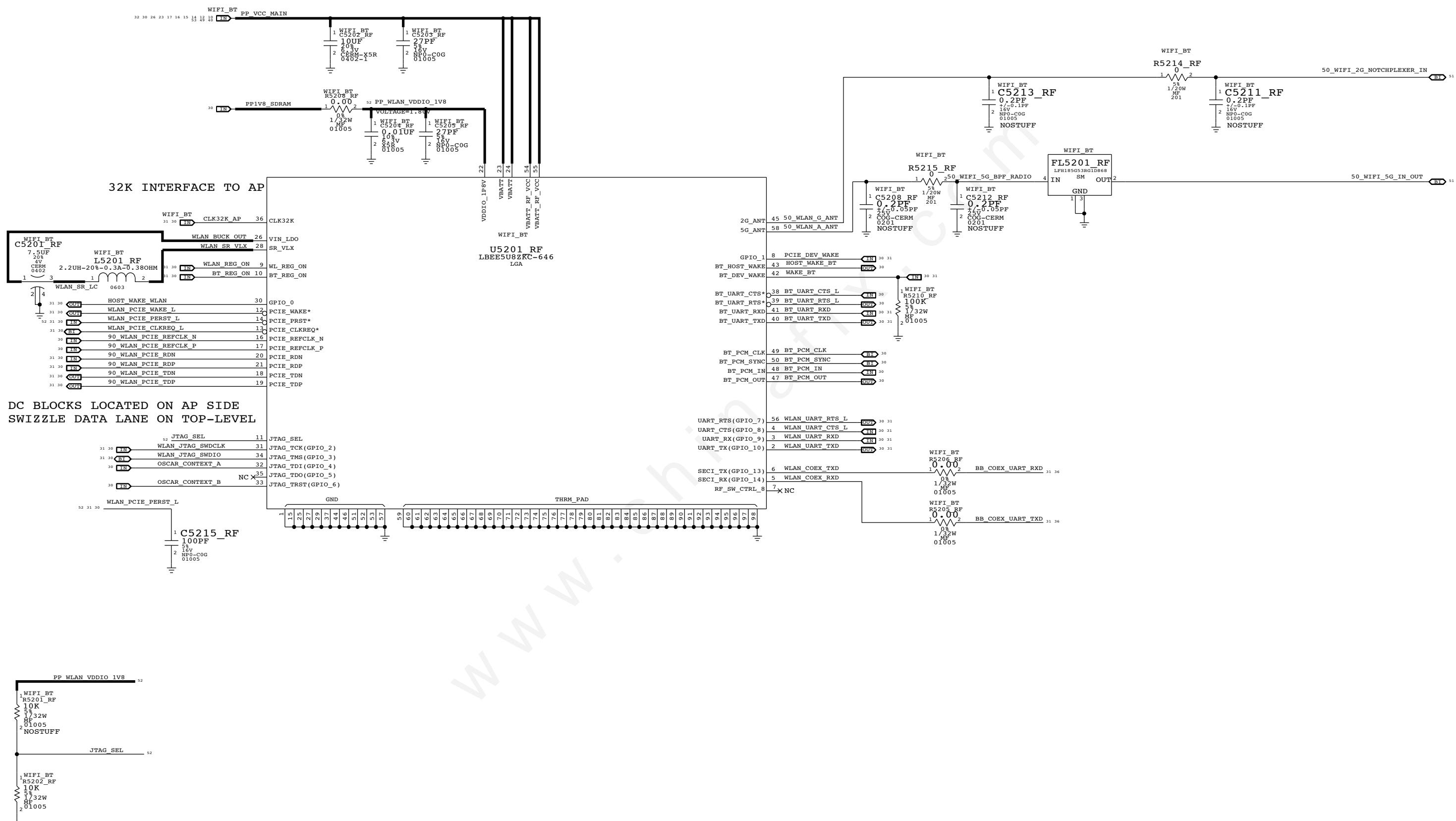


TEST & COAX CONNECTOR FOR LOWER SECTION OF MLB



# WLAN / BT

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.



MODULE BOOT-STRAPPED TO PCIE INTERNALLY

# STOCKHOLM

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

C2101  
R2100  
L2102  
U2100



D

D

C

C

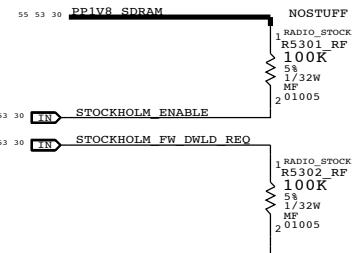
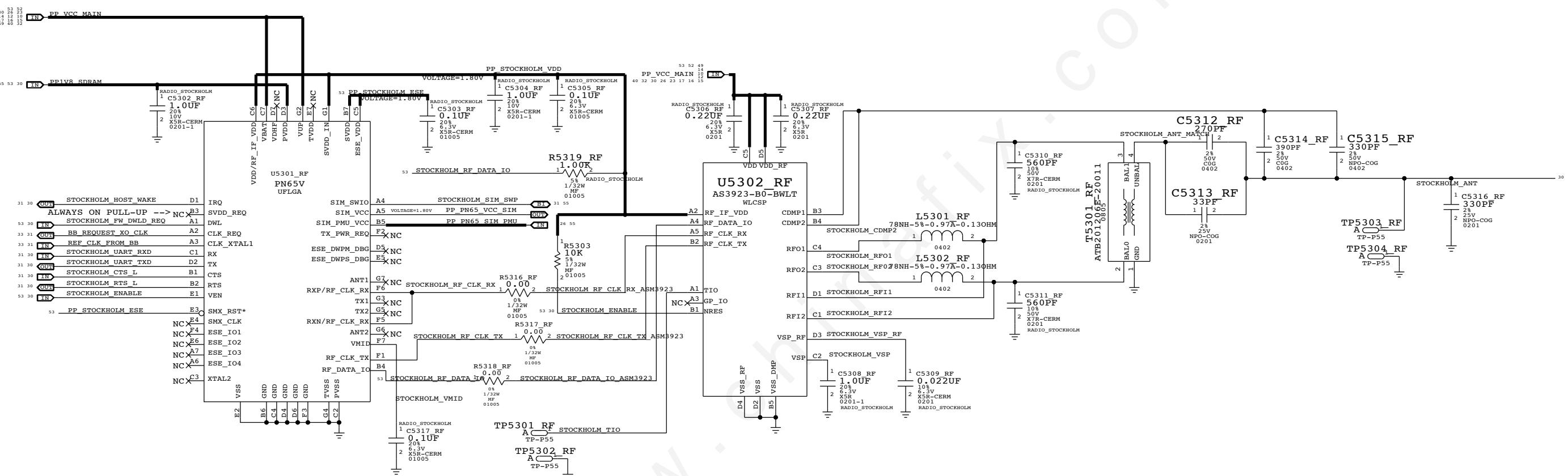
B

B

A

A

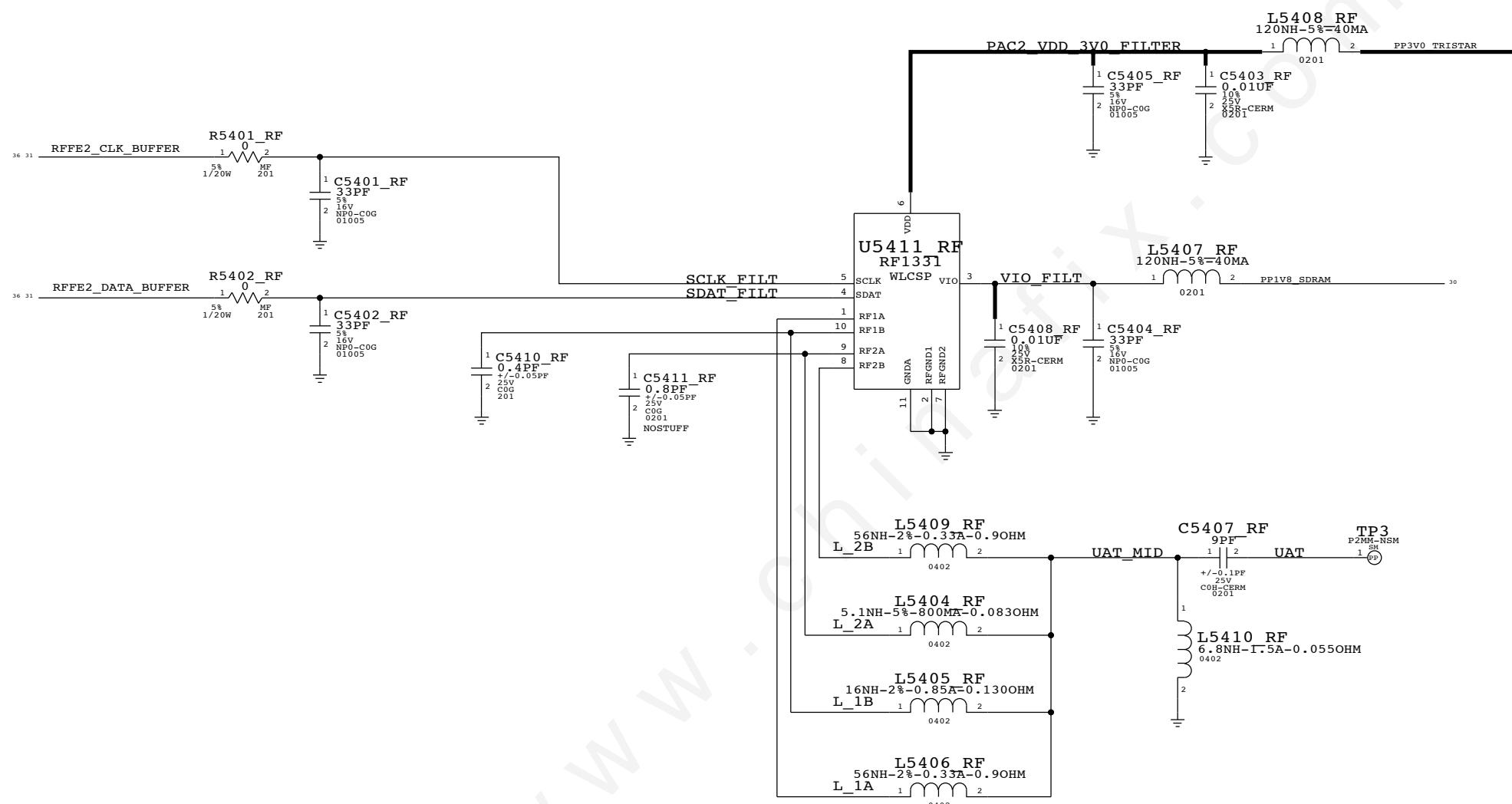
REMOVING BULK CAP 4.7UF 0402 -->  
BECAUSE OF OTHER BULK CAPS IN LAYOUT



# ON-BOARD JUMPER FLEX

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

## UAT JUMPER



# DSDS

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSES ONLY - NOT A CHANGE REQUEST.

