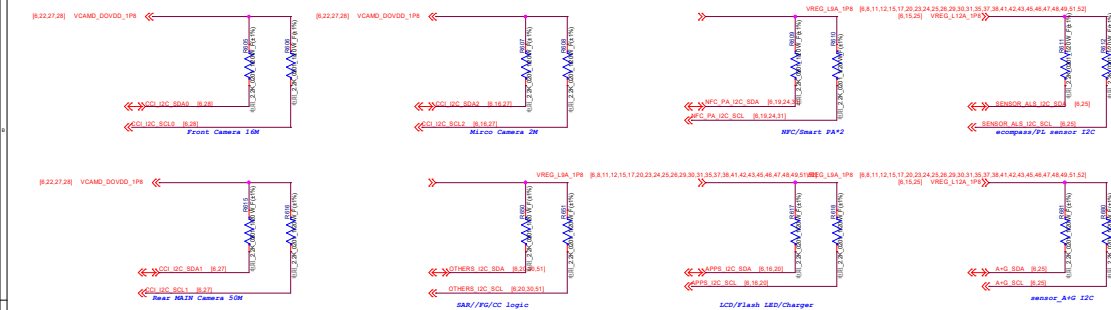




★ I2C_PULL_UP



Schematic design notice of "06_SM6375_GPIO"

Note 06-1: NO external Pull-up on the GPIOs (GPIO_117,GPIO_119,GPIO_120, GPIO_103, GPIO_106, GPIO_113, GPIO_51,GPIO_50)

Note 06-2: FAST_BOOT_SEL [2: 0]

BOOT_CONFIG[3:1]/ FAST_BOOT_SEL[2:0]	SM6375 FAST BOOT OPTIONS
0b000	UFS HS G1 -> SD -> eDL (eDL path - USB only)
0b001	SD -> UFS HS G1 -> eDL (eDL path USB only)
0b010	USB -> eDL (eDL path - SD then USB)
0b011	eMMC -> SD -> eDL (eDL path - USB only)
0b100	SD -> eMMC -> eDL (eDL path USB only)
0b101	UFS HS G1 -> eDL (eDL path - USB only)
0b110	UFS HS G1 -> SD -> eDL (eDL path - USB only)
0b111	eMMC -> eDL (eDL path - USB only)

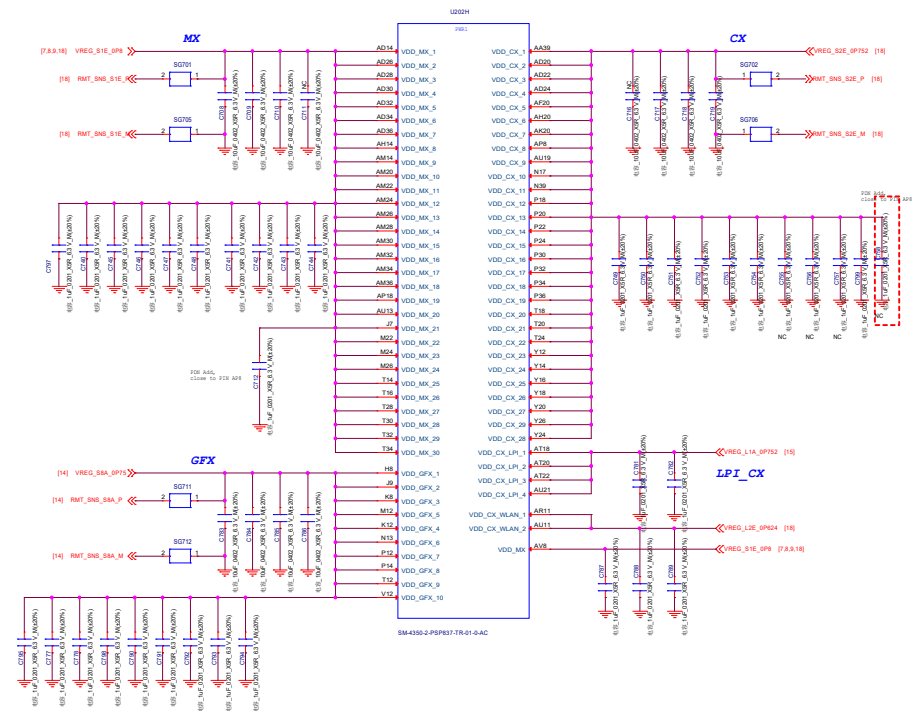
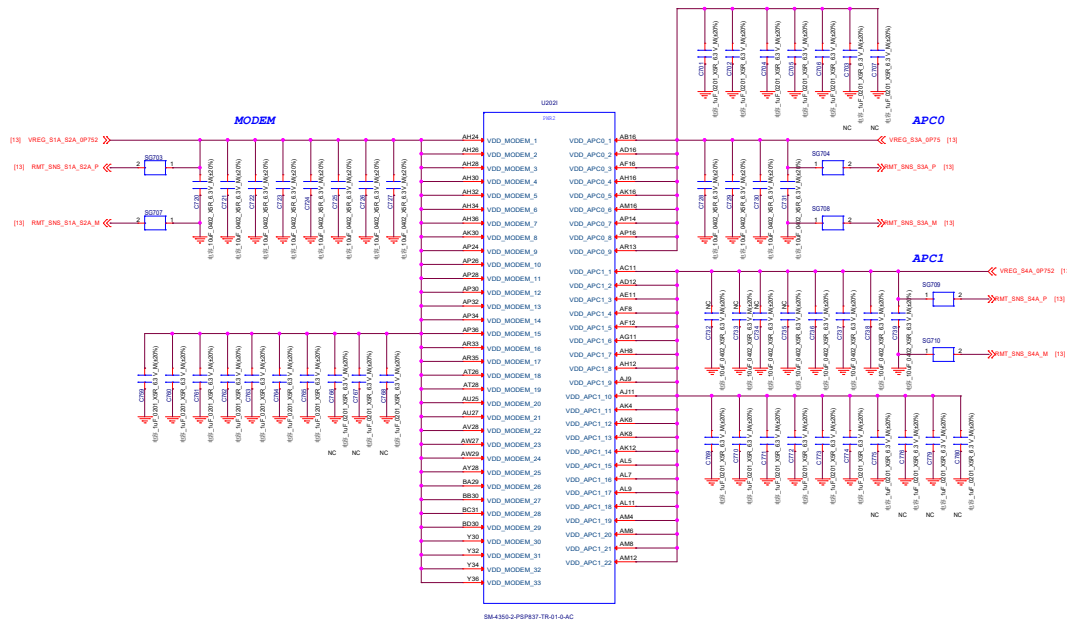
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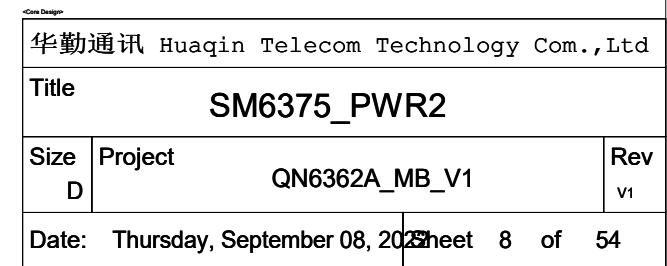
Title
SM6375_GPIO

Size D	Project QN6362A_MB_V1	Rev V1
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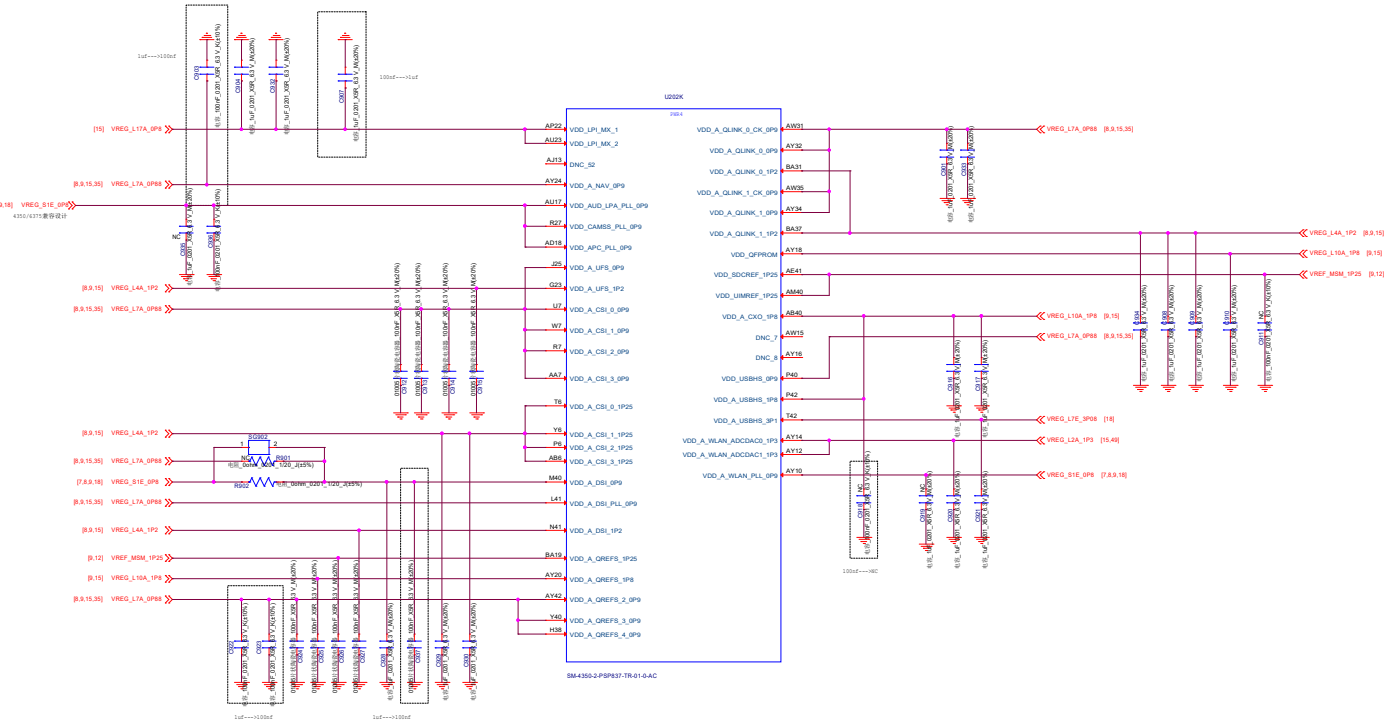
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BR1510, CS16, L20367/C3151 NC
BR15176, CS16, NC/C3151 NC



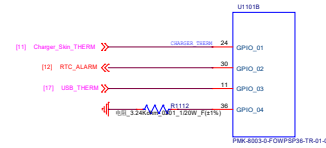
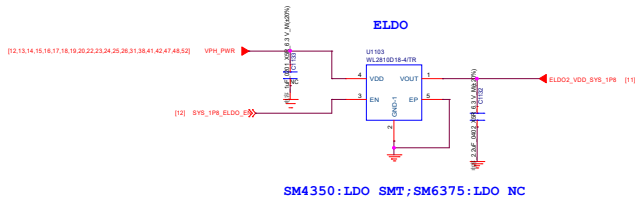
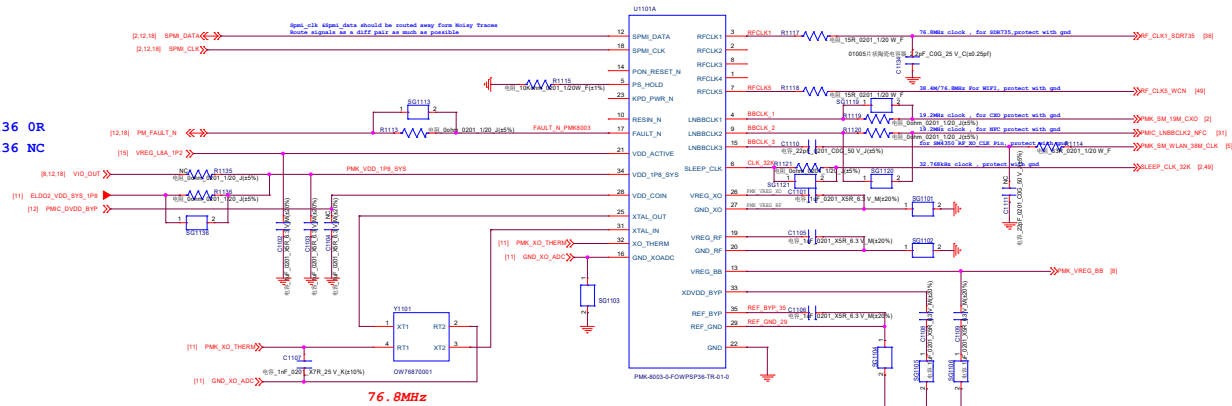
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Title SM6375_PWR3		
Size D	Project QN6362A_MB_V1	Rev v1
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PMK8003

SM4350 R1135 NC//R1136 OR
SM6375 R1135 OR//R1136 NC



3.7.1 Option hardware controls

The GPIO pin insensitive drivers for boot up behavior of the PMIC. The configuration for various options is shown in Table 3-20 and Table 3-21.

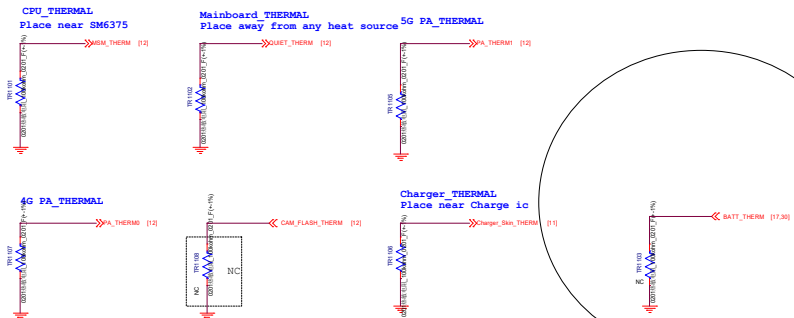
Table 3-20 Option pin state for SM7225, SM6350, and SM4350

Resistor value (Ω) (%) accuracy	Boot options
5200	No SD card boot
8700	SD card boot

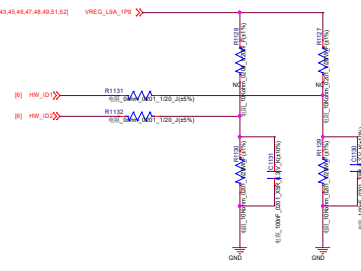
Table 3-21 Option pin state for SM7225, SM6350-AC

PMIC003 Pin name	Resistor value (Ω) (%) accuracy
GPIO_4	5200

NTC



HW ID



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Title PMK8003_CLK/NTC		
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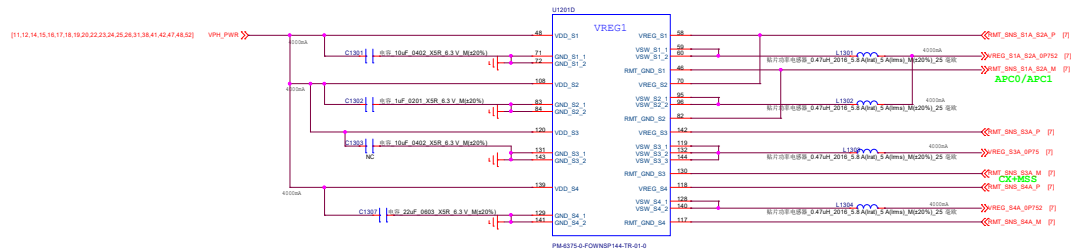


Table 3-6 PM6375 SMPS regulator summary for SM6375/SM4350

Regulator	Circuit type	I _{LOAD} (mA)	Default on booting voltage (V)	Specified range (V)	Programmab le range (V)	Default on	Expected use (SM6375/SM4350)
S1A_S2A	FTS510	6000	0		0.32~1.352	N	Modem
S3A	FTS510	4500	0.828		0.32~1.352	Y	APC0
S4A	FTS510	4500	0		0.32~1.352	N	APC1
S3A_S4A	FTS510	9000	0		0.32~1.352	N	APC1
S5A	HFS510	4000	0.952		0.32~2.04	Y	LV Sub Reg
S6A	HFS510	2000	1.352		0.32~2.04	Y	MV Sub Reg
S7A	HFS510	2500	2.04		0.32~2.04	Y	HV Sub Reg
S8A	FTS510	4500	0		0.32~1.352	N	GFX

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PM6375_BUCKS		
Size	Project	Rev
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REF 1401-1499

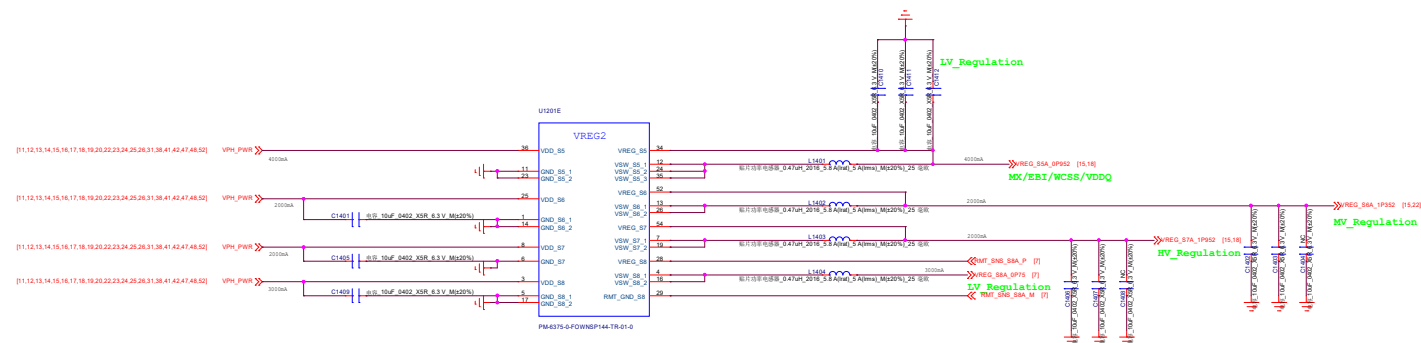


Table 3-6 PM6375 SNPS regulator summary for SM6375/SM4350

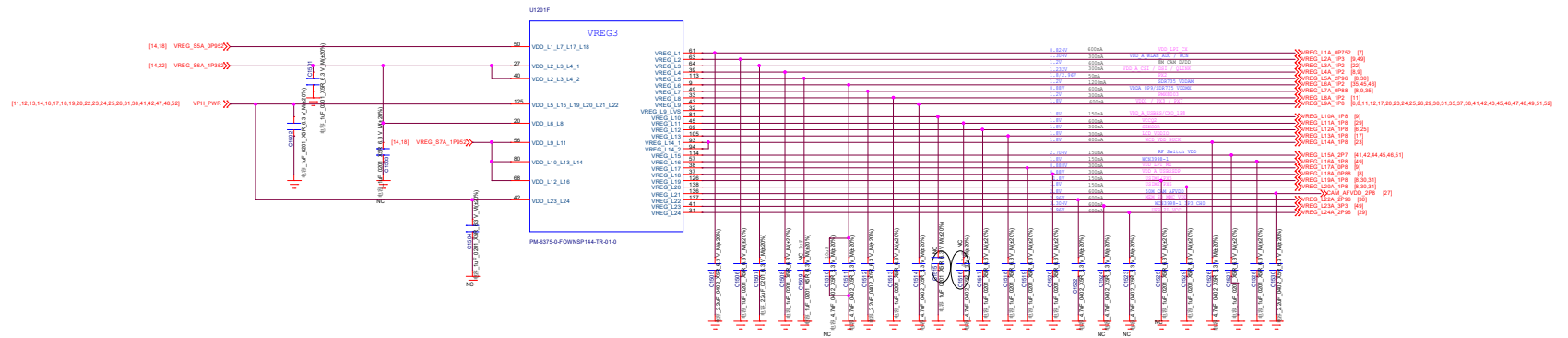
Regulator	Circuit type	I _{rated} (mA)	Default on booting voltage (V)	Specified range (V)	Programmable range (V)	Default on	Expected use (SM6378/SM4380)
S1A_S2A	FTS510	6000	0	0.32-1.352		N	Modem
S3A	FTS510	4500	0.828	0.32-1.352		Y	APC0
S4A	FTS510	4500	0	0.32-1.352		N	APC1
S3A_S4A	FTS510	9000	0	0.32-1.352		N	APC1
S5A	HFSS10	4000	0.952	0.32-2.04		Y	LV Sub Reg
S6A	HFSS10	2000	1.352	0.32-2.04		Y	MV Sub Reg
S7A	HFSS10	2500	2.04	0.32-2.04		Y	HV Sub Reg
S8A	FTS510	4500	0	0.32-1.352		N	BFX

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Title	PM6375_LDOS
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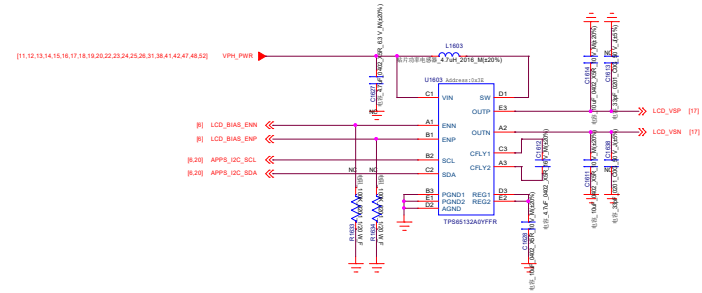
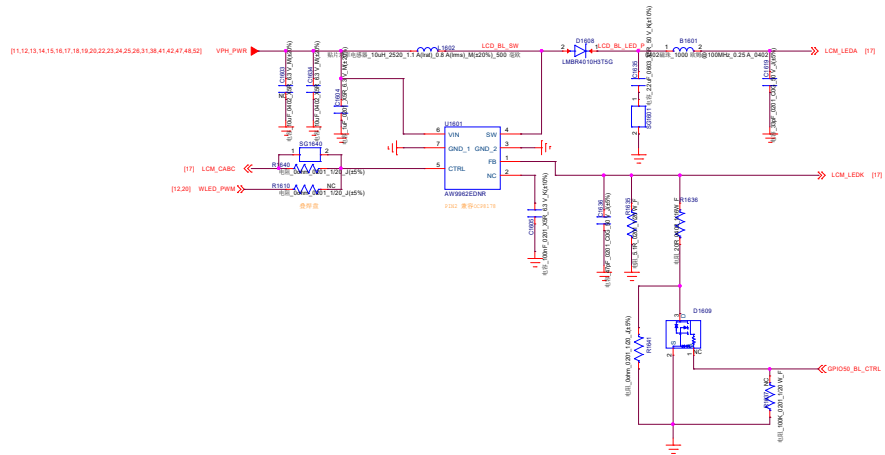
Size D	Project QN6362A_MB_V1	Rev v1
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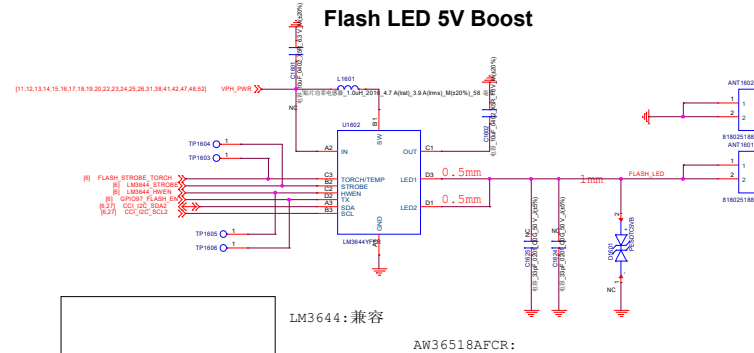


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Title PM6375_CNTRL/GPIO/BOB/FLASH			
Size D	Project QN6362A_MB_V1	Rev V1	
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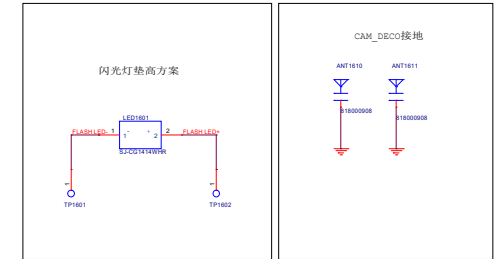
LCD LED Driver



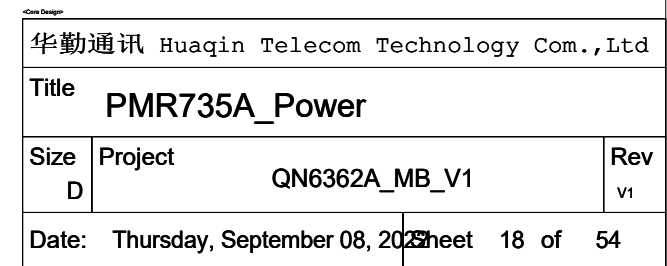
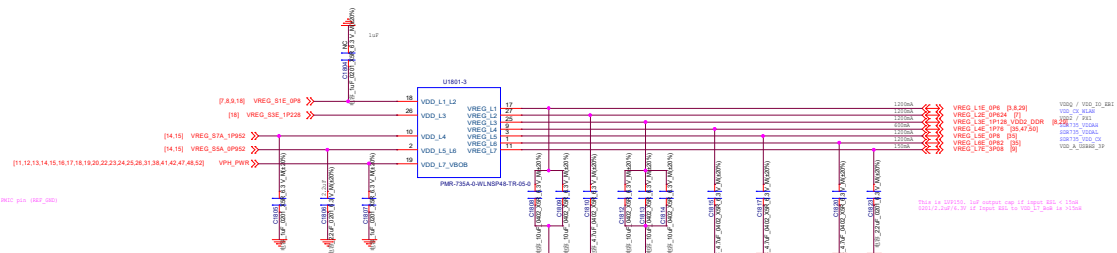
Rear CAM FLASH



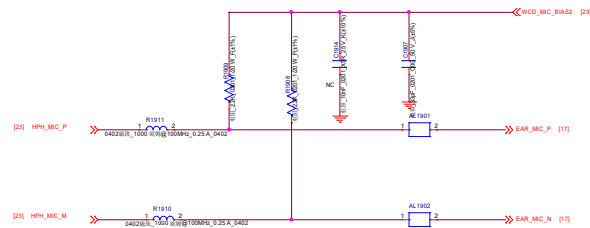
闪光灯料号 主供: HQ1211049000
二供: HQ11210822000



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Title 16.Amoled/Flash_LED/RGB Deriver		
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i2C Slave Address / Write / Read (7-bit mode)		
AW87390FCR	0X58	
FS1599SN	0X34	

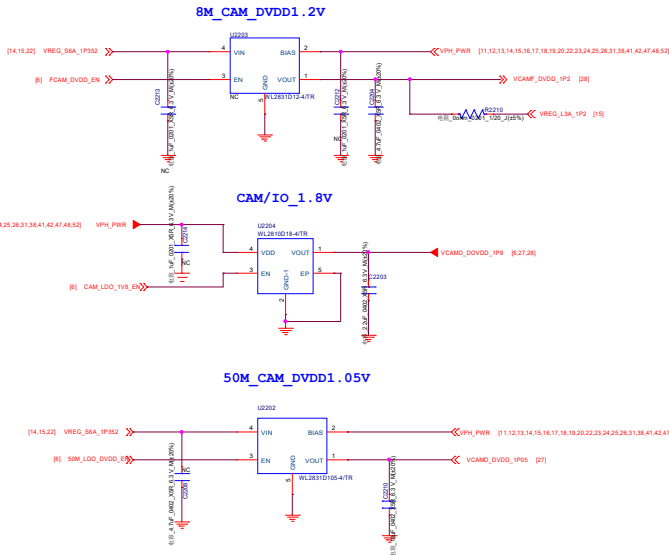
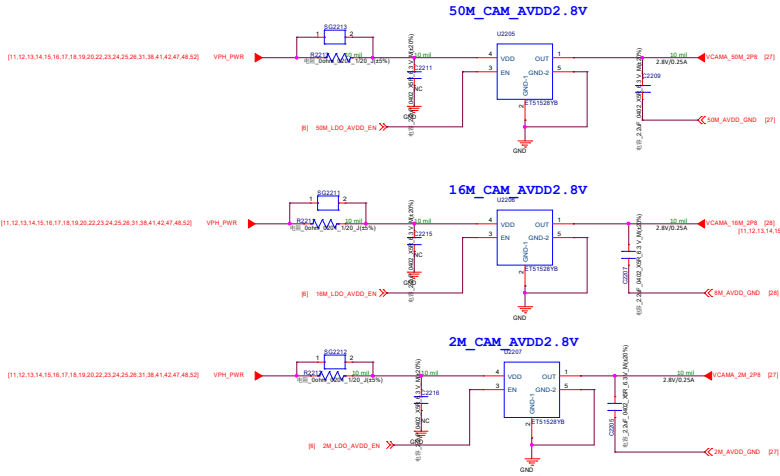


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HQ11150937000, LDO_1.05V_500 mA WL2831D105-4/TR
 HQ11150936000, LDO_1.2V_500 mA
 HQ11154597000, LDO_1.1_500 mA
 HQ11152324000, LDO_3.1V_1000 mA

CAMERA 供电电压分布

	AVDD	DVDD	DOVDD	AFVDD
FCAM13M	NA	NA	NA	NA
FCAM16M	2.8V	1.1V	1.8V	NA
FCAM8M	2.8V	1.2V	1.8V	NA
RCAM50MAF	2.8V	1.05V	1.8V	2.8V
RCAM50M0IS	NA	NA	NA	NA
微距2M	2.8V	NA	1.8V	NA
广角8M	NA	NA	NA	NA
景深2M	NA	NA	NA	NA

Note 22-1:

I2C Slave Address / Write / Read (7-bit mode)		
ET5907MV	0X35	Read:0X6B / Write:0X6A
FAN53870UC00X	0X35	Read:0X6B / Write:0X6A
WL2868C-20/TR	0X2F	Read:0X5F / Write:0X5E

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Title Camera_LDO		
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[illegible]

i2C Slave Address / Write / Read (7-bit mode)		
AW87390FCR	0X59	
FS1599SN	0X35	

[illegible]

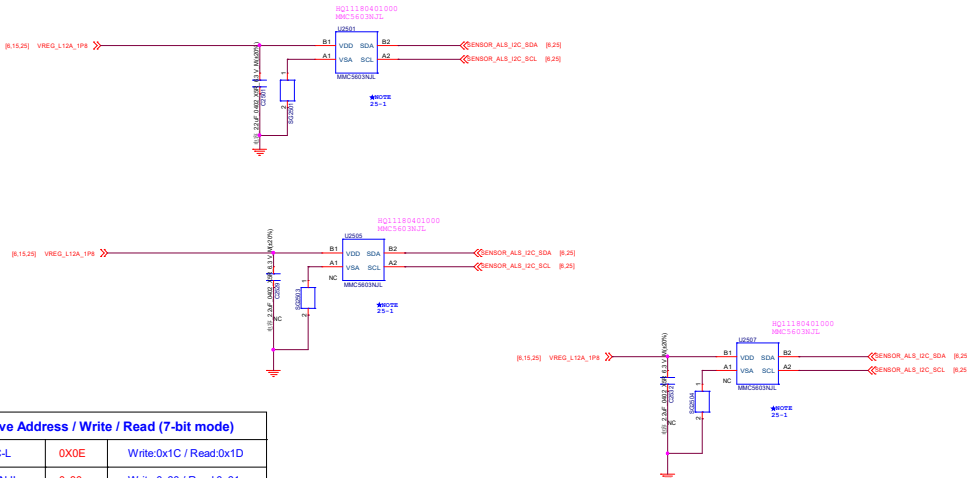
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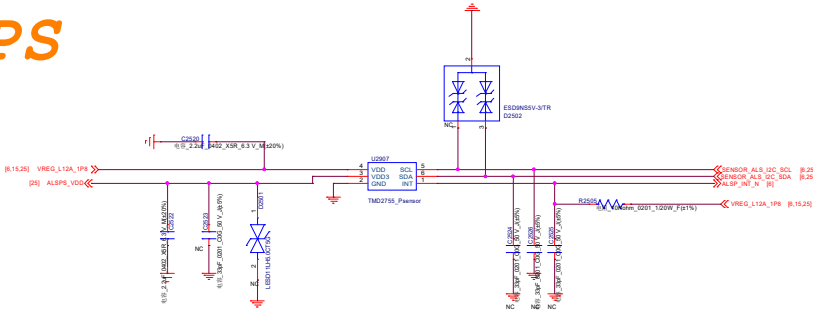
eCOMPASS



Note 25-1:

I2C Slave Address / Write / Read (7-bit mode)		
AK09919C-L	0x0E	Write:0x1C / Read:0x1D
MMC5603NJL	0x30	Write:0x60 / Read:0x61

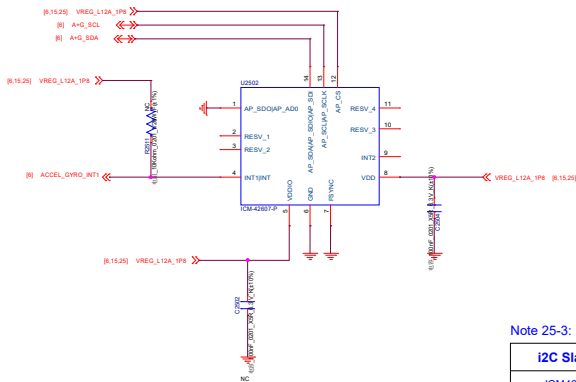
ALS+PS



Note 25-2:

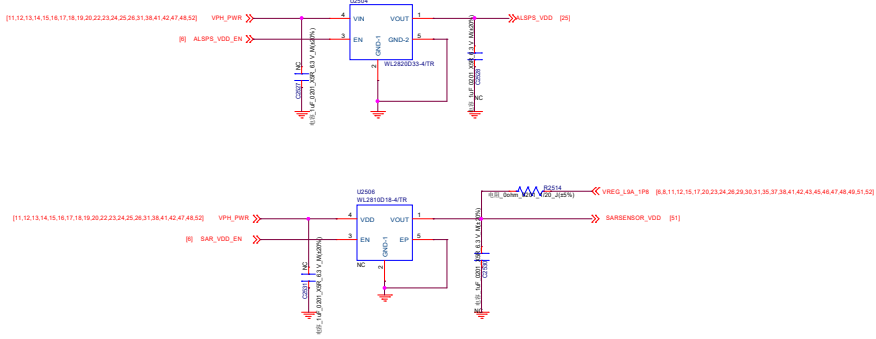
I2C Slave Address / Write / Read (7-bit mode)		
STK33562	0x46	Write:0x8C / Read:0x8D

Accelerate+Gyroscope



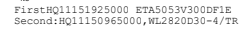
Note 25-3:

I2C Slave Address / Write / Read		
ICM42607	AD0=	Write:0xD0 / Read:0xD1



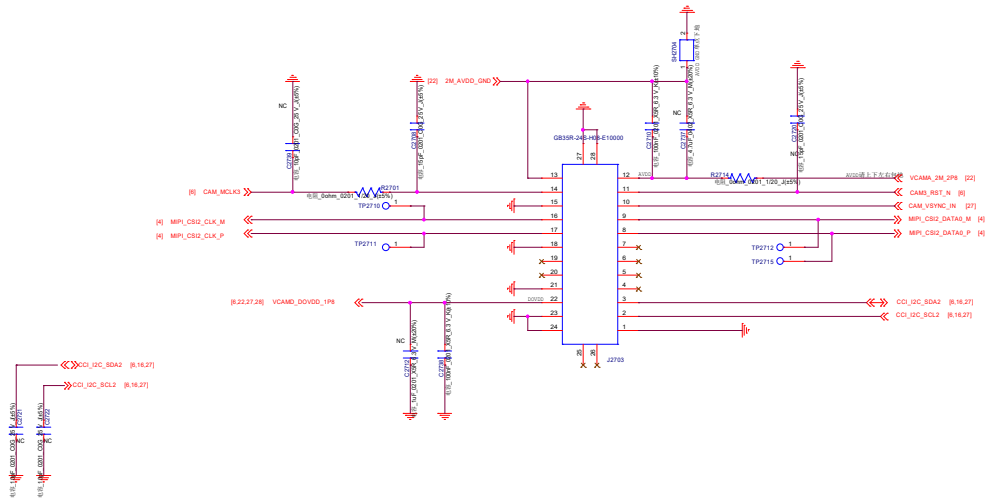
*Core Design		
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Title		SENSOR
Size D	Project	QN6362A_MB_V1
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VREG_L3A_1P8



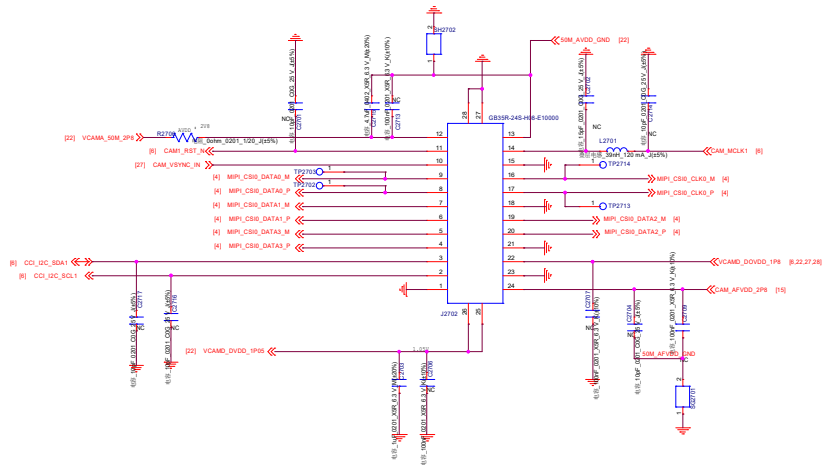
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Title AMOLED/TP/FPS/KEY					
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Mirco Camera 2M



Main Camera 50M OIS

★NOTE:



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Title

REAR_CAM

Size
D

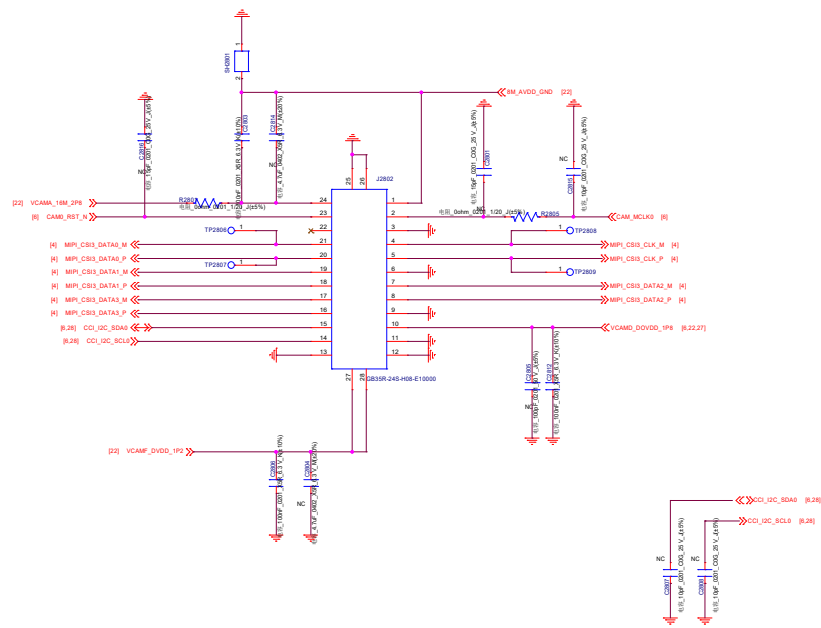
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QN6362A_MB_V1

Rev
V1

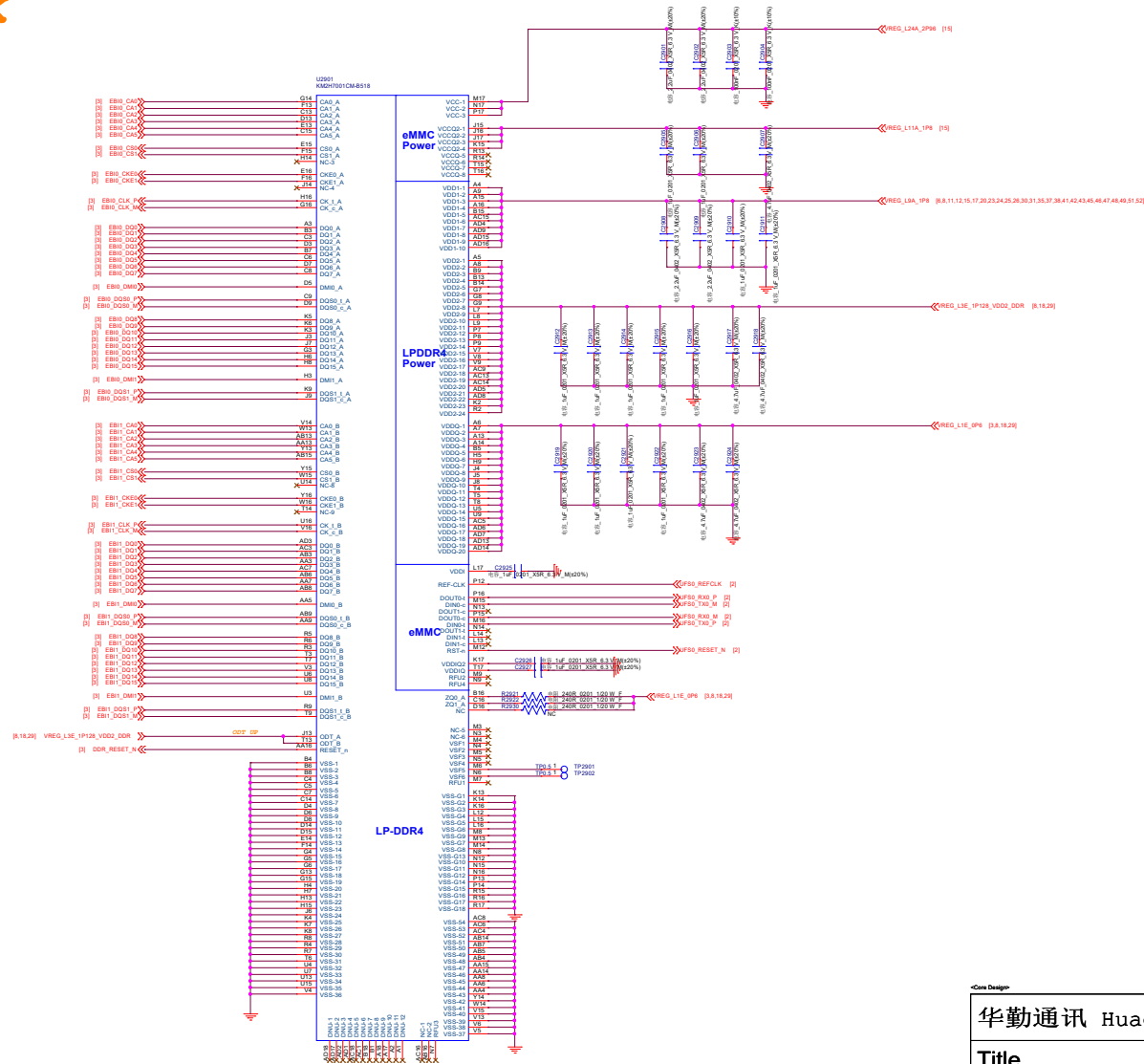
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Front CAM 8M



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Title FRONT_CAM		
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UFS+LPDDR4x



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Title		
UFS+LPDDR4x		
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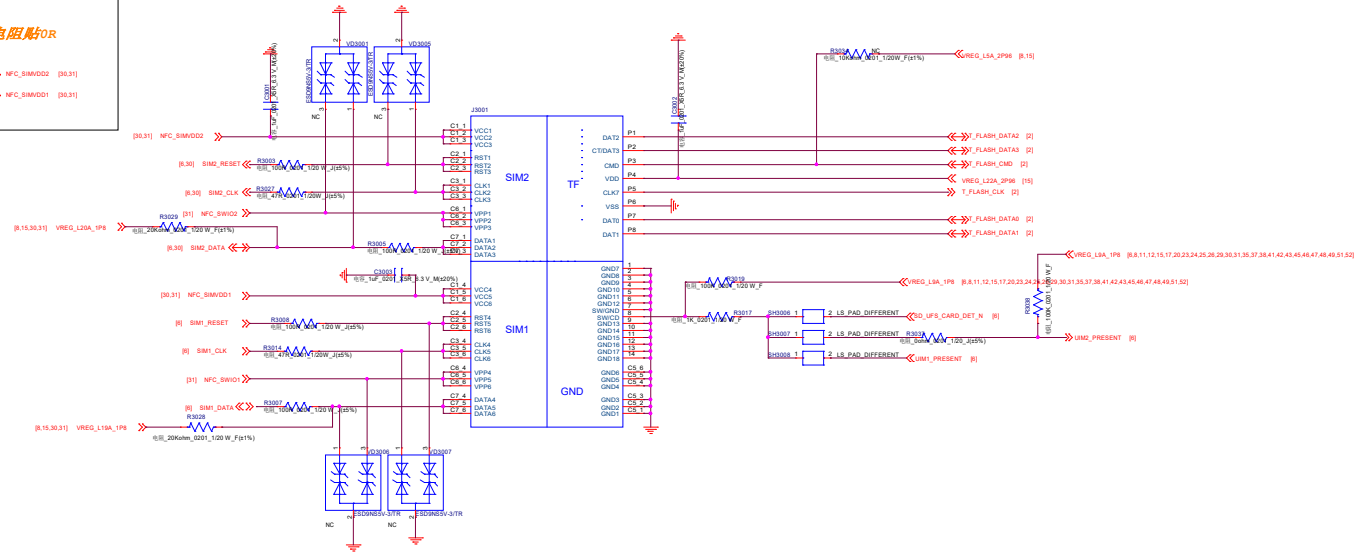
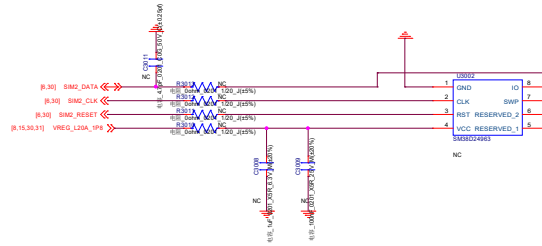
3IN2 SIM1&SIM2&SD

NFC配置电阻NC//无NFC配置电阻贴0R

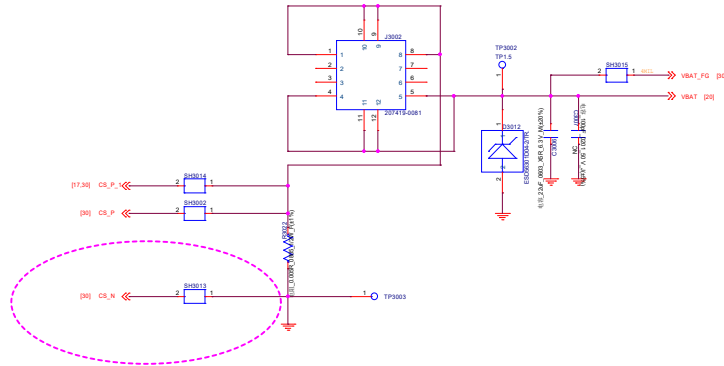
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E-SIM

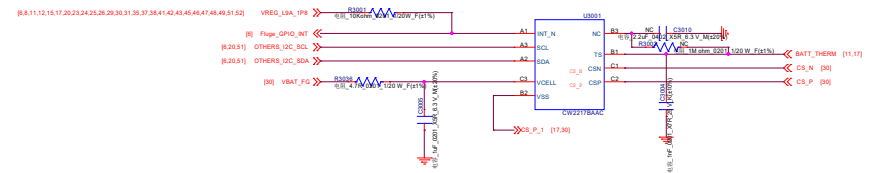


BATT CON



Note 30-1:

I2C Slave Address / Write / Read (7-bit mode)		
CW2217B	0X64	Read:0XC9 / Write:0XC8
SM5602	0X71	Read:0XE3/ Write:0XE2



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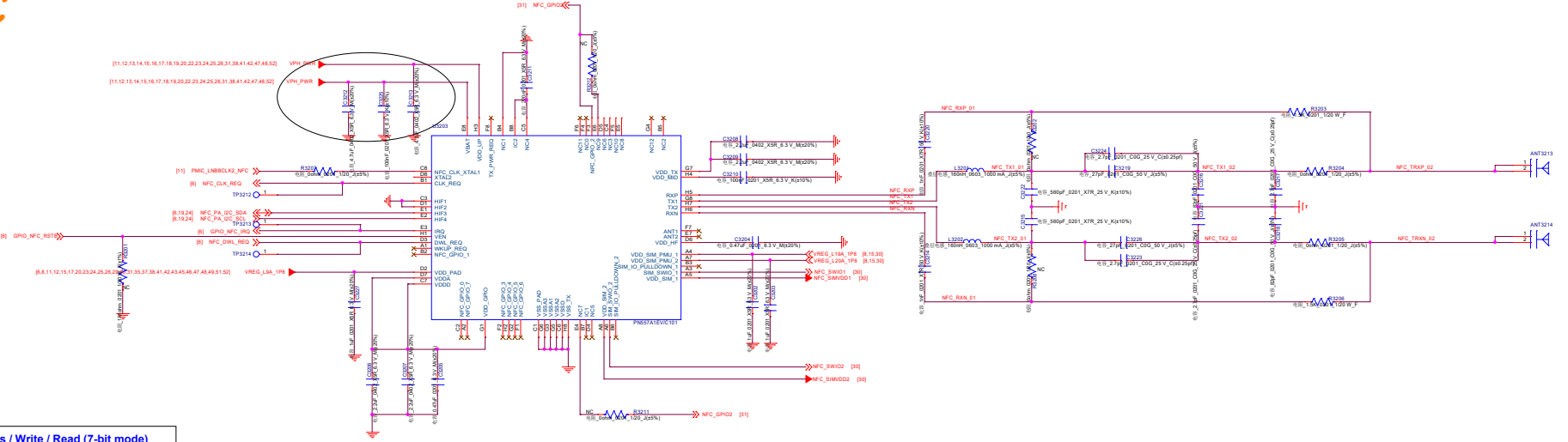
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Title SIM/SD/BATT

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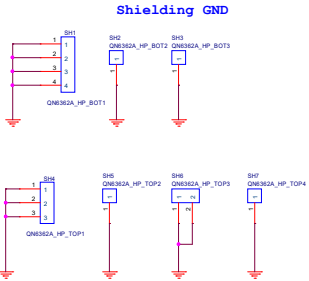
NFC



Note 20-4:

I2C Slave Address / Write / Read (7-bit mode)		
PN557	0X28	
SN110P	0X7	

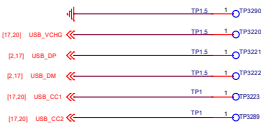
KEYPAD



UART



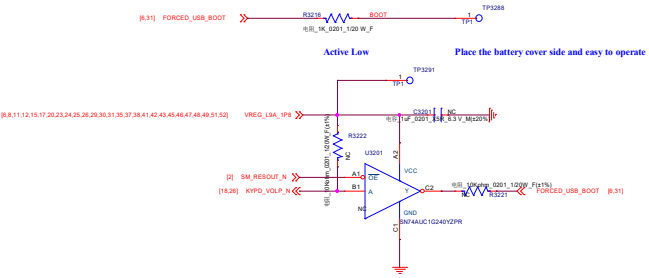
VCHG



JTAG

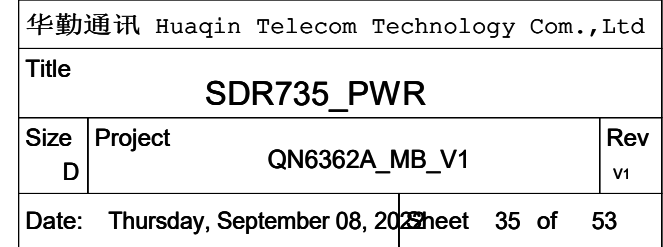


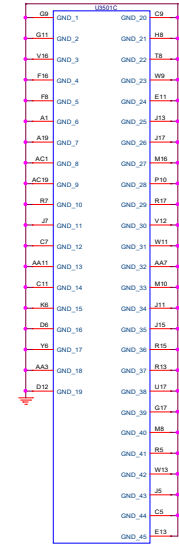
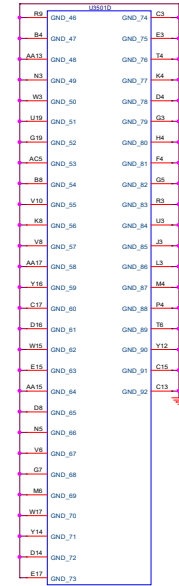
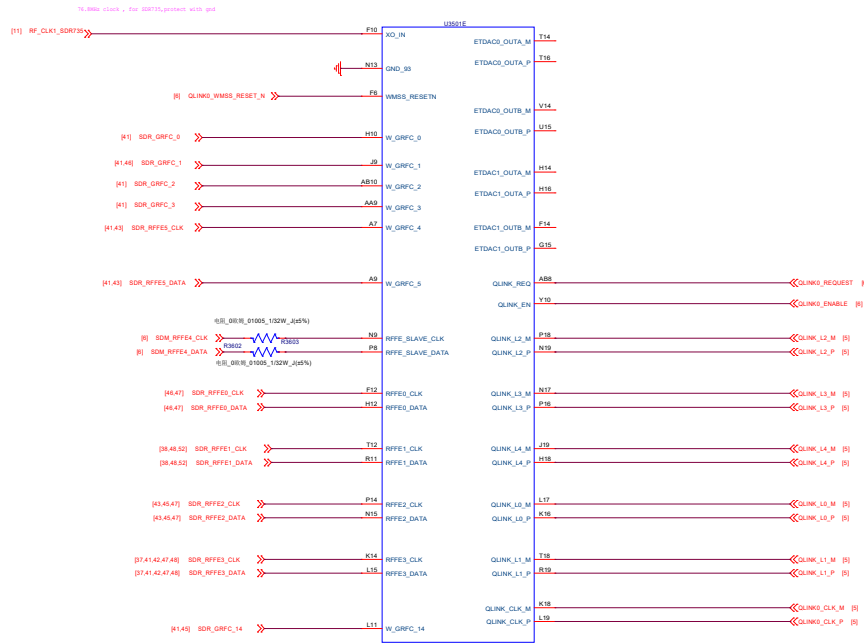
FORCE USB BOOT



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Title		
TEST_POINT/SHIELD		
Size	Project	Rev
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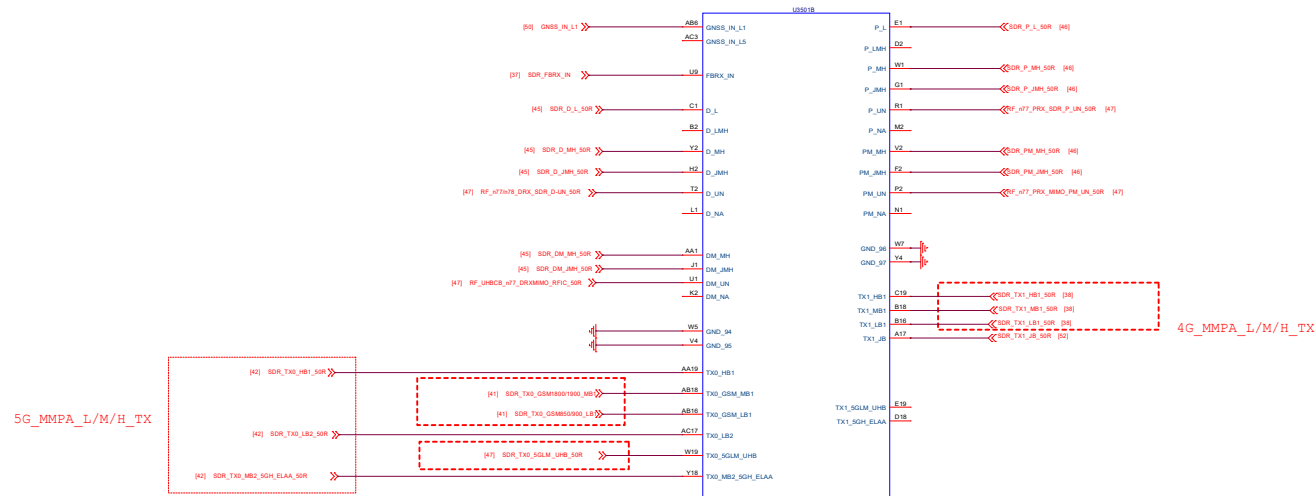
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Title Reserve_NC_BB		
Size D	Project QN6362A_MB_V1	Rev V1
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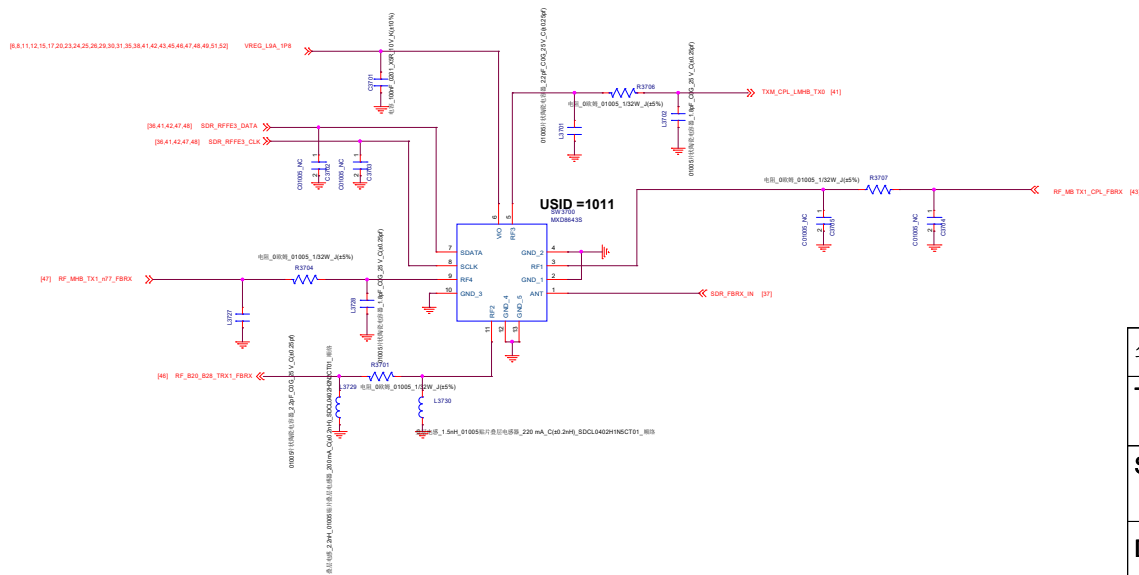


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Title SDR735_WMSS		
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SDR735 RF Port

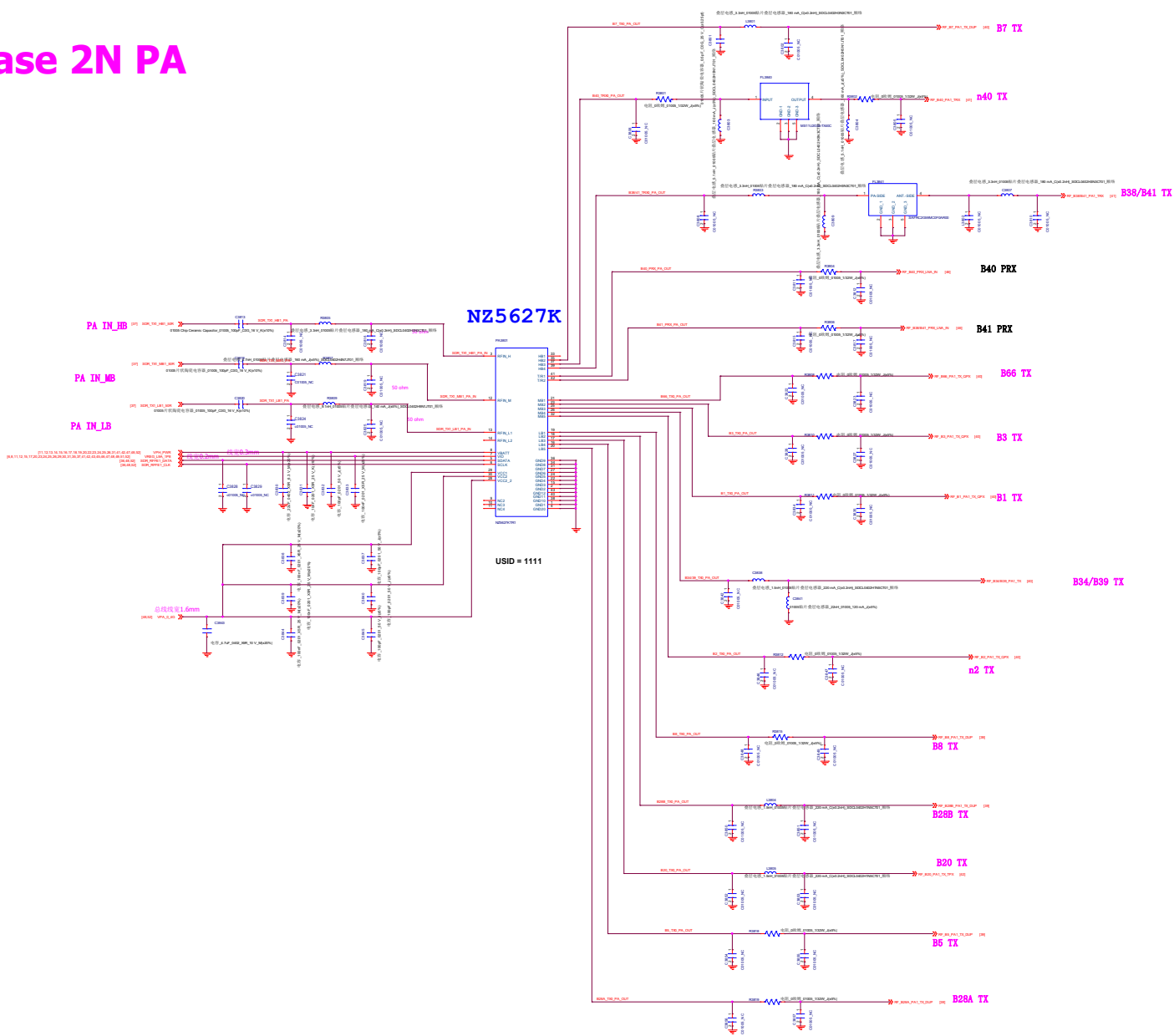


FBRX CPL SP4T



华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title <div>SDR735_TX_RX_FBRX</div>			
Size D	Project <div>QN6362A_MB_V1</div>		Rev V1
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Phase 2N PA

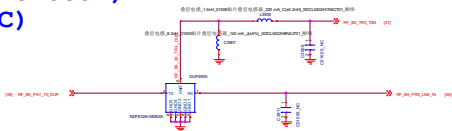


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Title		5G_PA0_NZ5627K#1	
Size	Project	QN6362A_MB_V1	Rev
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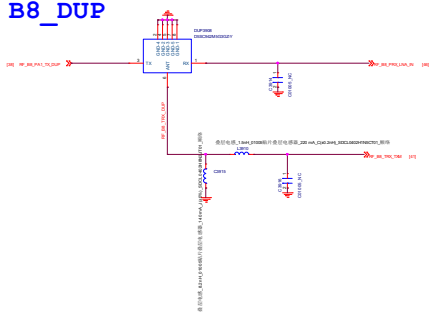
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位号	Description	Code	Vendor	SKU
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	B5_DUP	SFX836EY202	Wiso.	PRC

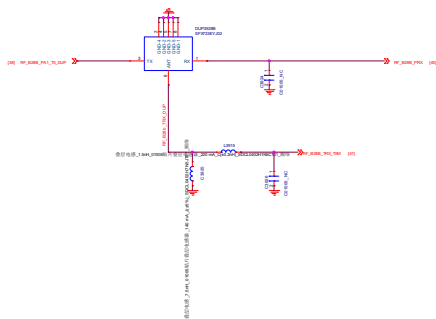
B26_DUP (LA&EU&JP)
B5_DUP (PRC)



B8_DUP

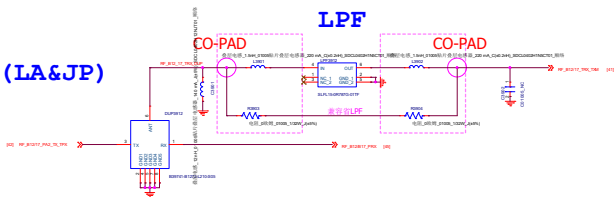


B28B_DUP (LA&PRC&JP)

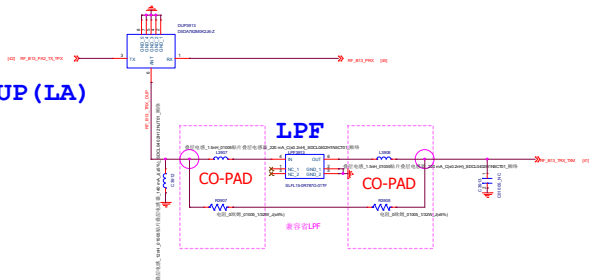


LB TRX(P5N PA)

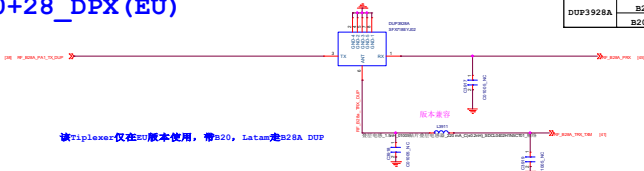
B12/17_DUP (LA&JP)



B13_DUP (LA)



B28A_DUP (LA&PRC&JP)
B20+28_DFX (EU)

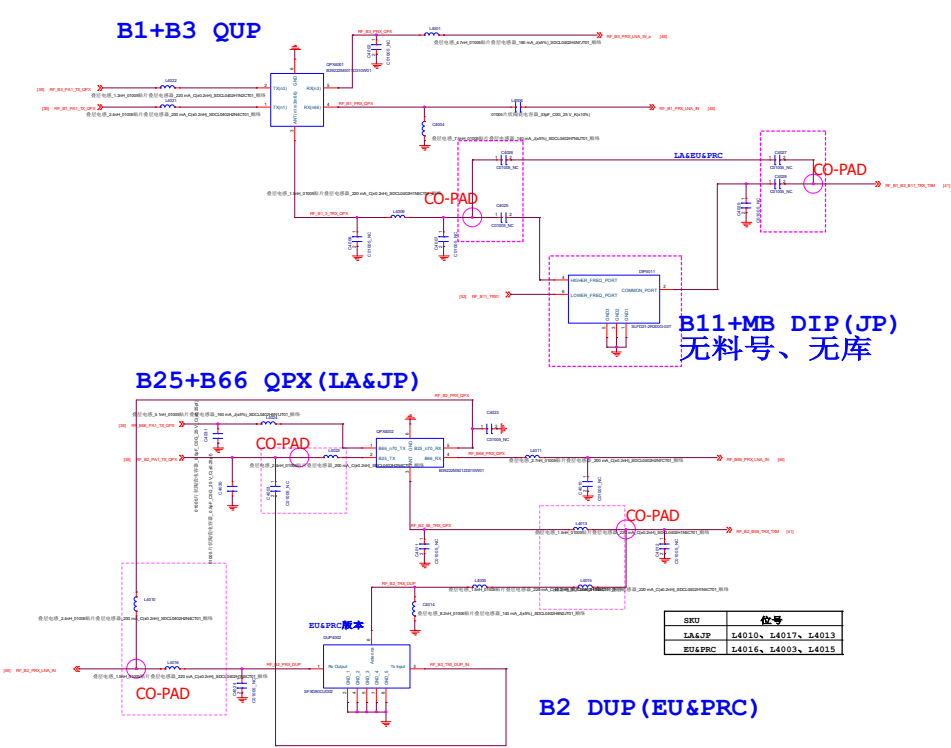


位号	Description	Code	Vendor	SKU
DUP3928A	B28A_DFX	NDP028-0718XA	华勤	LA&PRC
	B20+28_DFX	B1295	RF360	EU

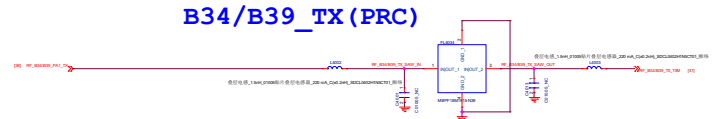
该Diplexer仅在20版本使用，带c20，Latan是28A_DUP

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Title LB TRX0			
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MB TRX(NZ5627K)



B34/B39_TX (PRC)

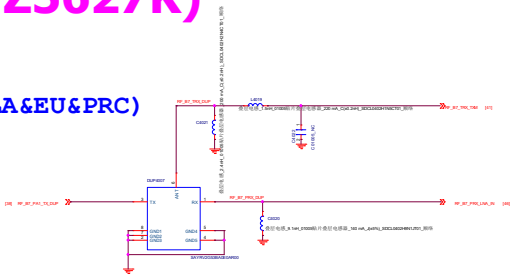


B34/B39_RX (PRC)



HB TRX(NZ5627K)

B7 DUP (LA&EU&PRC)



华勤通讯 Huaqin Telecom Technology Com.,Ltd			
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ANT0:
LMB_TRX0/NR_TRX1
0.6~2.2G

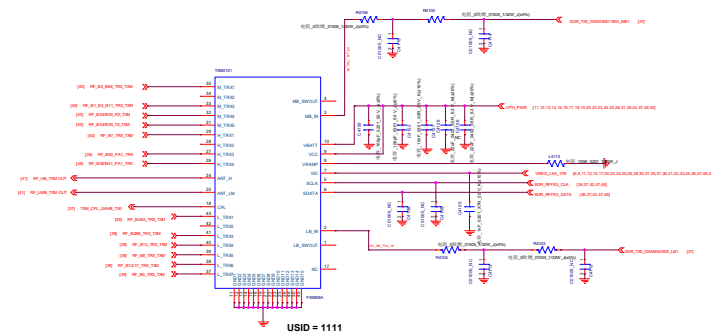
LM+H合路器#1

DPDT #1_TRX0

L+M(H)合路器#2

DPDT #2_TRX1

ANT1:
LMHB_TRX1/NR_TRX0
0.6~2.69G



LA&EU&JP

CO-PAD

SPDT_SRS #2(PRC)

LA&EU&JP

SKU	位号
LA&EU&JP	R4151, R4152
PRC	I4109a, R4112

ANT4:
MHB_DRX_MIMO
1.7~2.7G

SP4T_SRS(PRC)

USID = 1001

SPDT_SRS #3(PRC)

SKU	位号
LA&EU&JP	R4114, R4115
PRC	I4110, R4113

CO-PAD

ANT6:
MHB_PRX_MIMO
1.7~2.7G

SPDT_SRS #1(PRC)

SKU	位号
LA&EU	C4153, C4154
PRC	C4138, R4111

LA&EU&JP

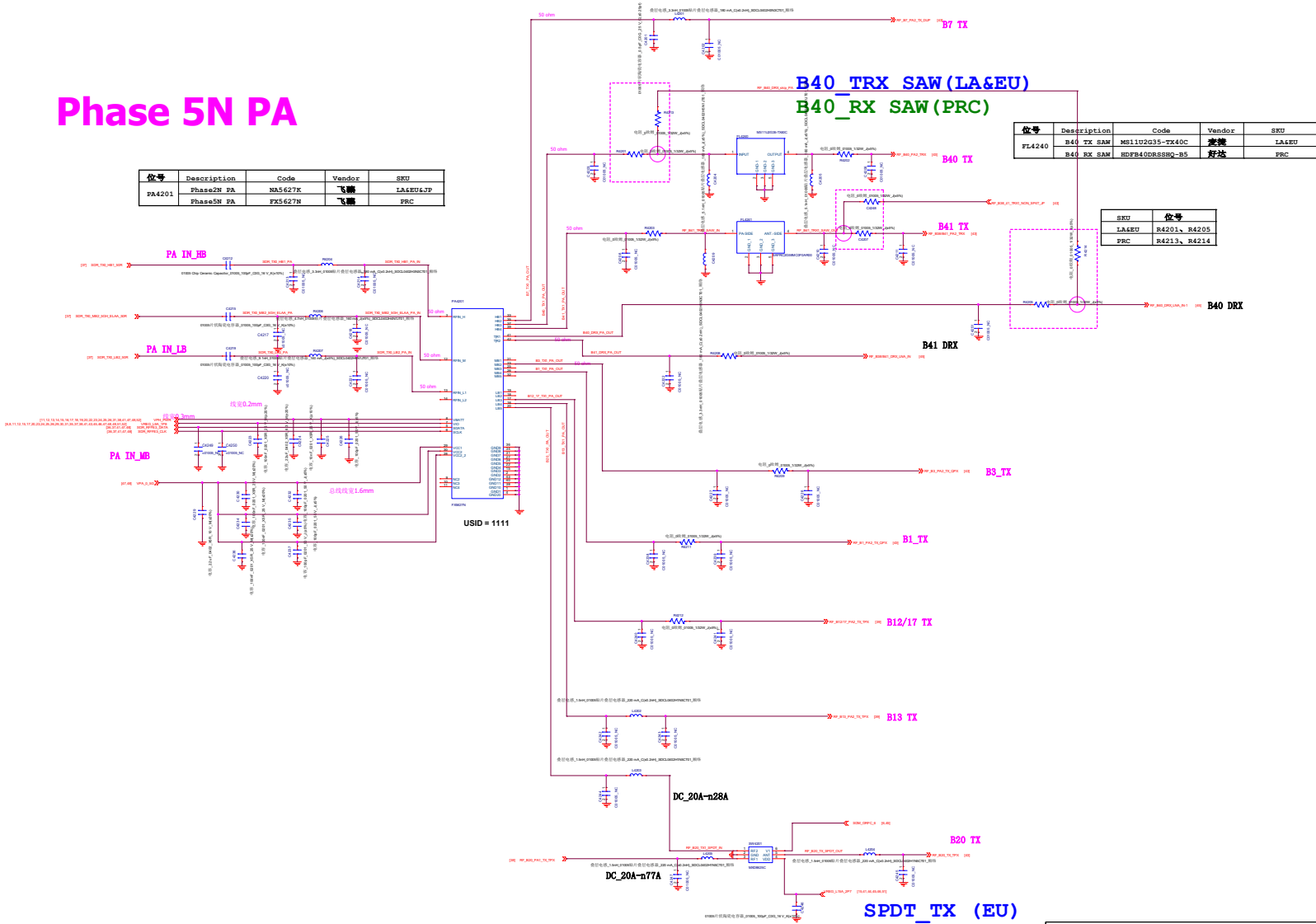
MHB+n77合路器

ANT5:
HB_TRX0/NR_TRX1
2.3~2.69G, 3.3~4.2G

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Phase 5N PA

位号	Description	Code	Vendor	SKU
PA4201	Phase2N PA	NA5627K	飞箭	LA&EU
	Phase5N PA	FX5627N	飞箭	PRC



位号	Description	Code	Vendor	SKU
FL4240	B40 TX SAW	MS1102035-TX40C	凌通	LA&EU
	B40 RX SAW	MDPB40DRSSBQ-B5	好亮	PRC

SKU	位号
LA&EU	R4201、R4205
PRC	R4213、R4214

HB

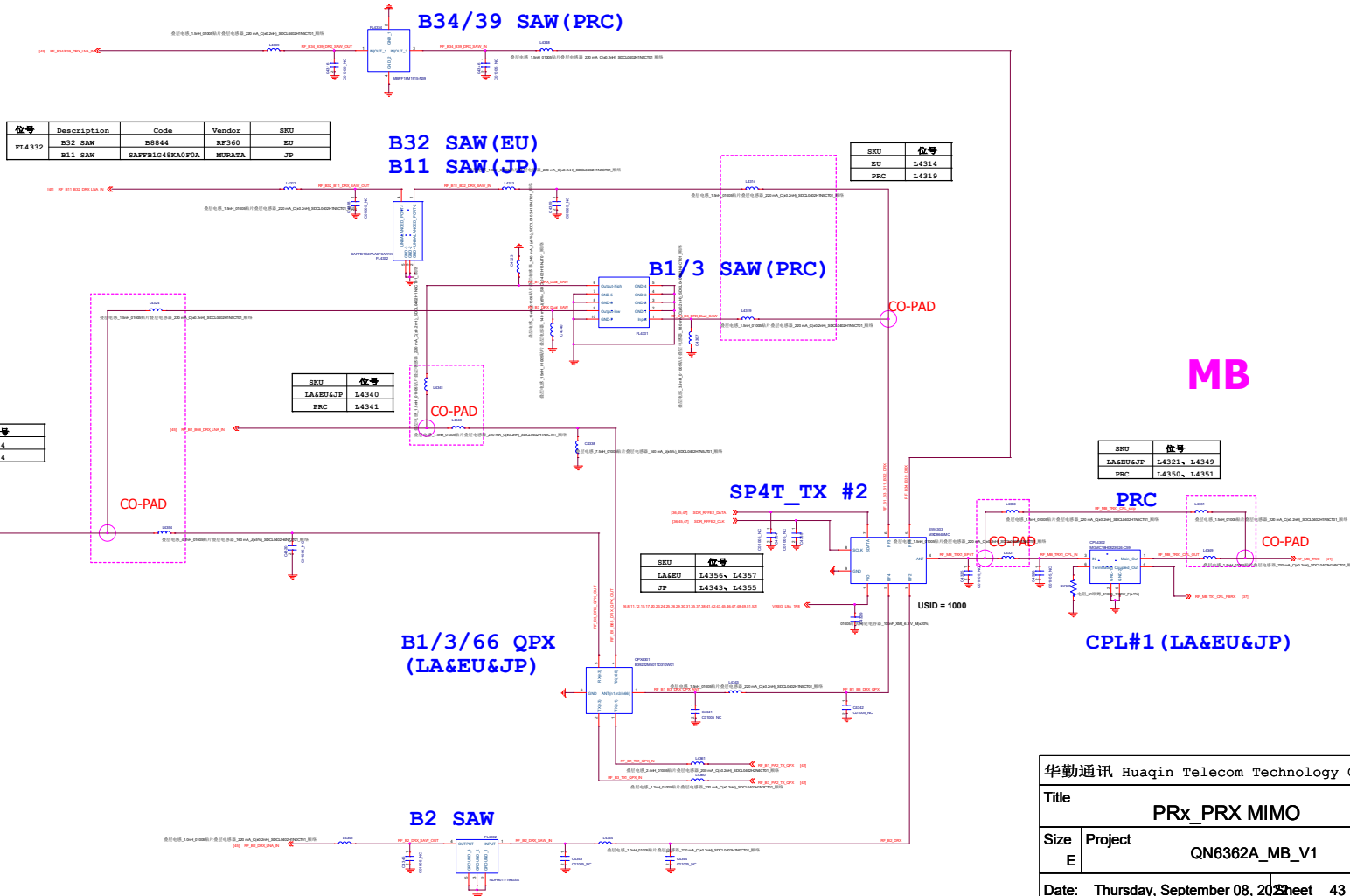
B7 DUP (LA&EU)

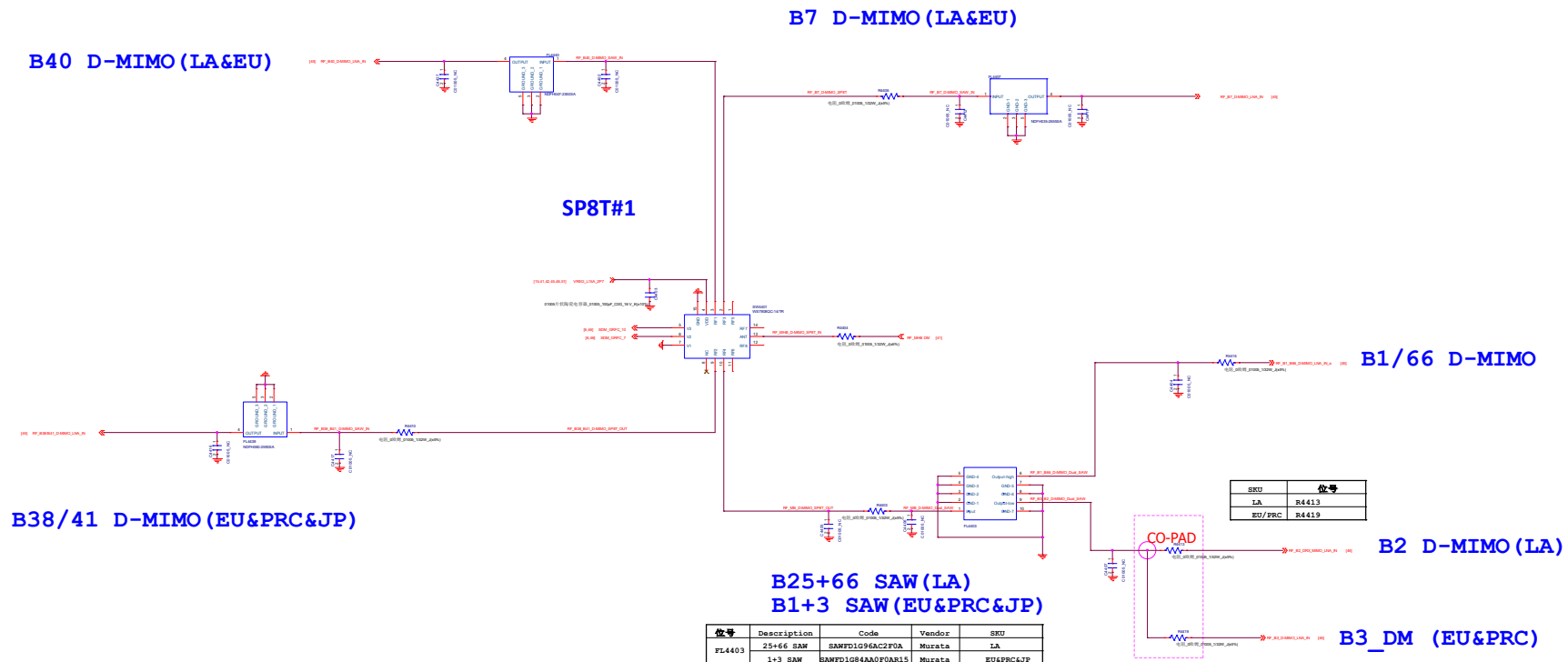
LB

20+28 TPX (EU)

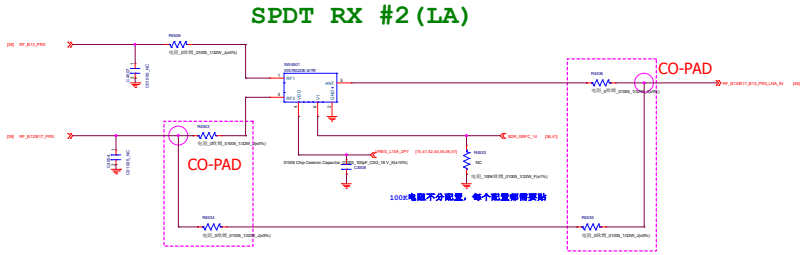
该器件only用EU版本

SP4T_TX #1 (LA&EU&PRC)

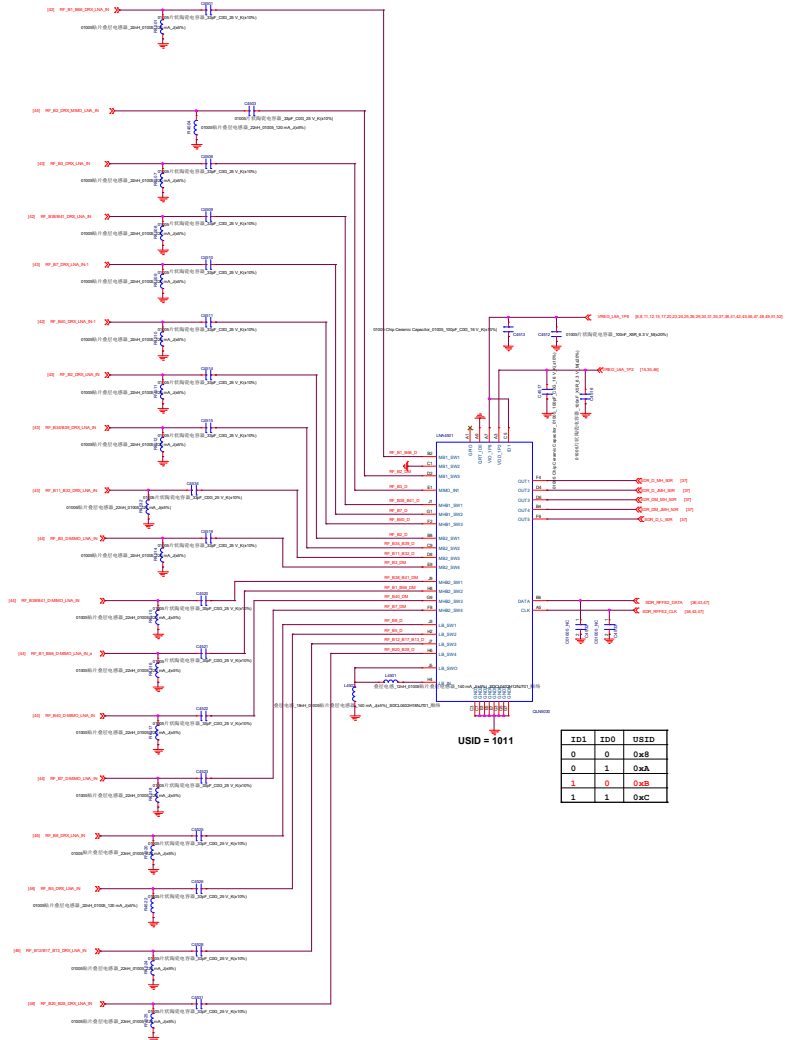
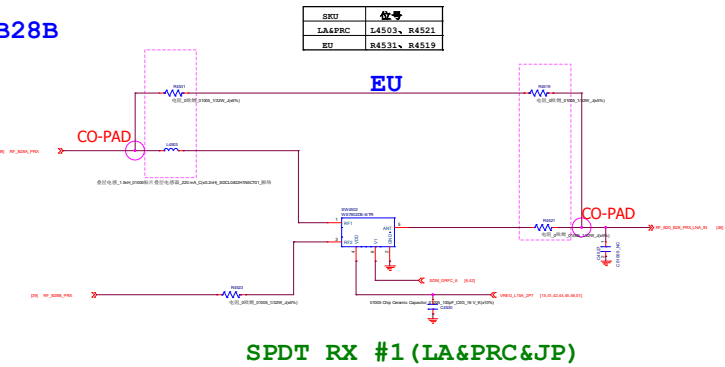




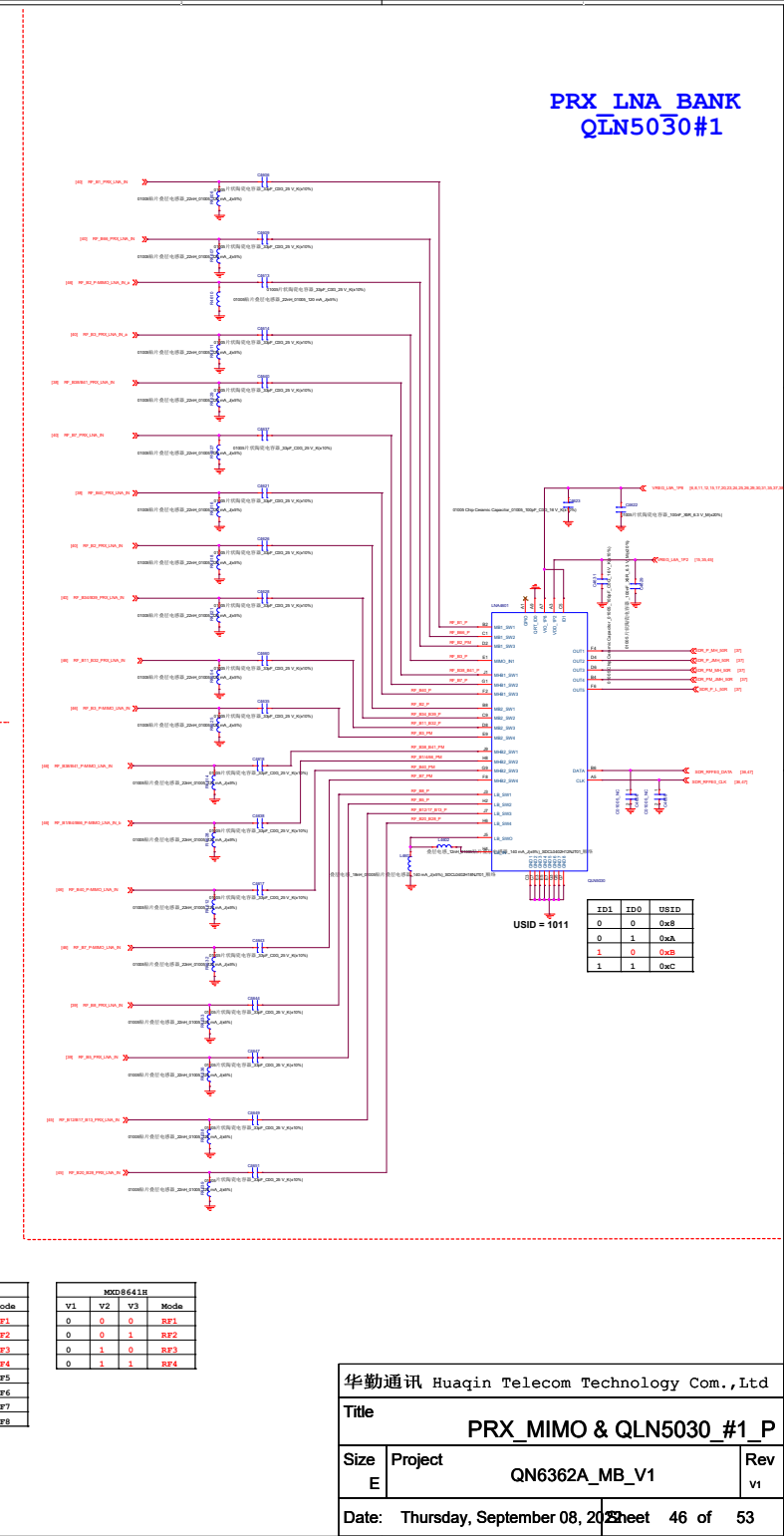
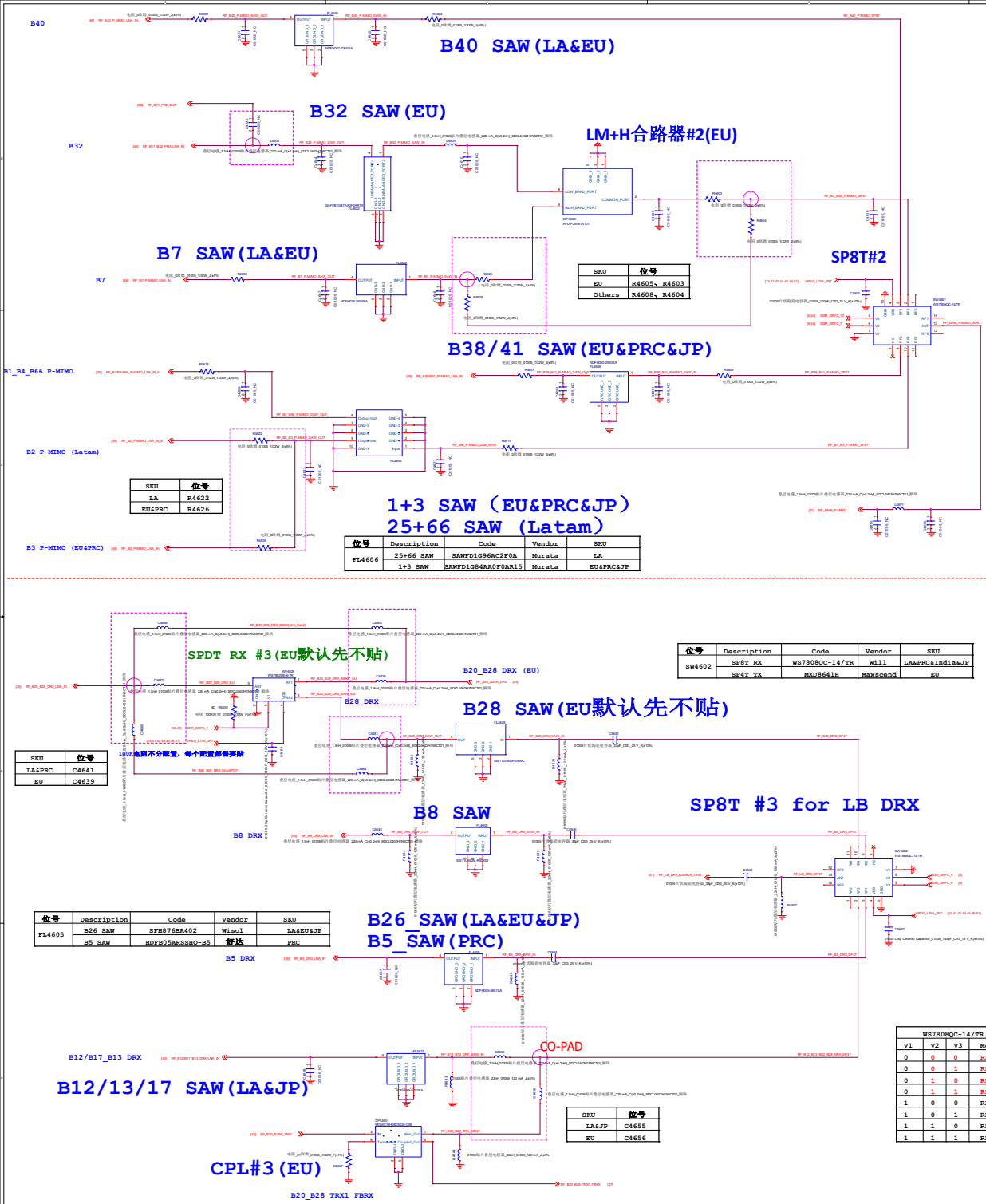
SPDT for B12_B13



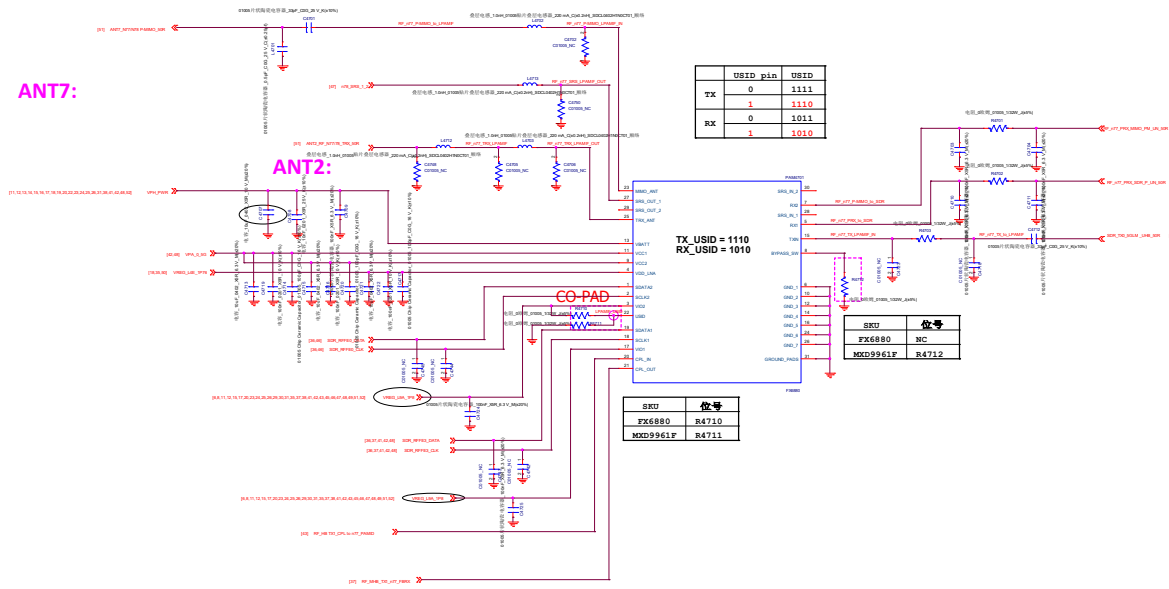
SP2T for B28A/B28B



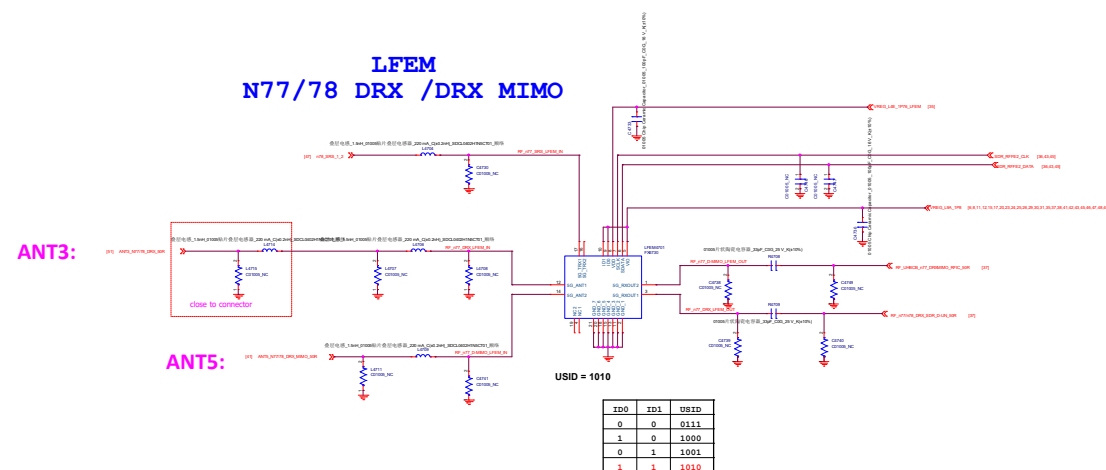
ID1	ID0	USID
0	0	0x8
0	1	0xA
1	0	0xB
1	1	0xC



n77/n78_LPAMIF FX6880



LFEM N77/78 DRX /DRX MIMO



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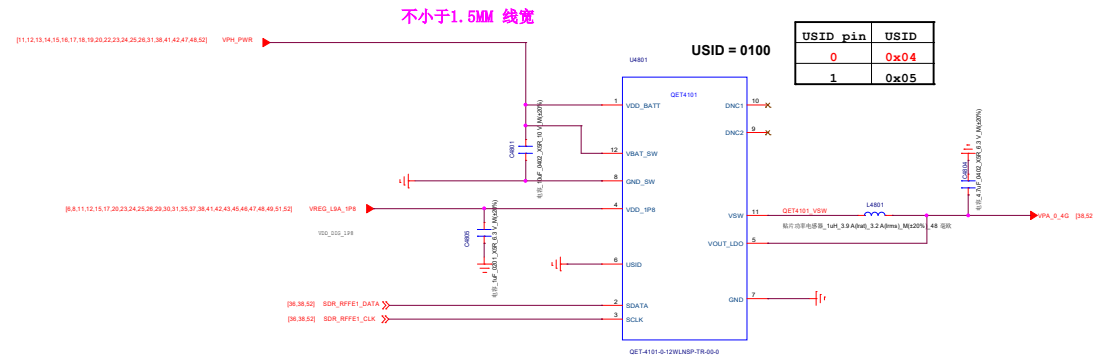
Title
N77/N78/B42_LPAF_FX6880

Size
E Project
QN6362A_MB_V1

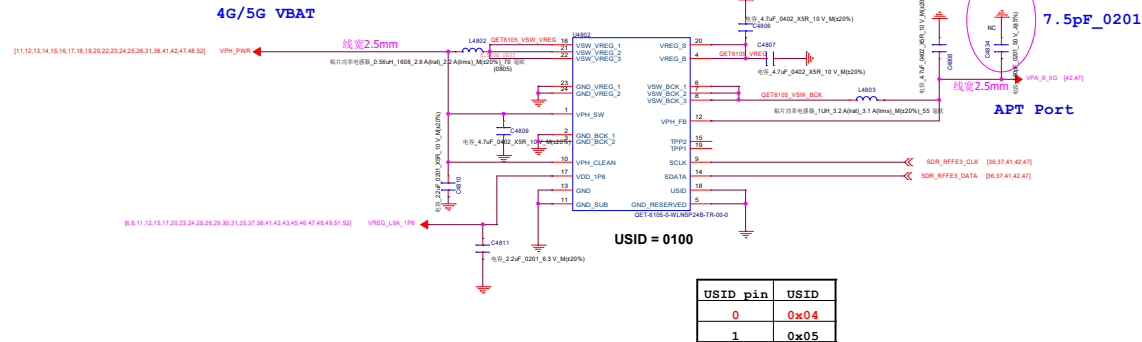
Rev
v1

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QET4101 for 3/4/5G PA#1 & B11 PA



QET6105_1 for 5G PA#2+N77 LPAF



USID pin	USID
0	0x04
1	0x05

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Title <div style="text-align: center; font-size: 1.2em;">QFE6105_QET4101</div>			
Size <div style="text-align: center;">D</div>	Project <div style="text-align: center; font-size: 1.1em;">QN6362A_MB_V1</div>		Rev <div style="text-align: center;">V1</div>
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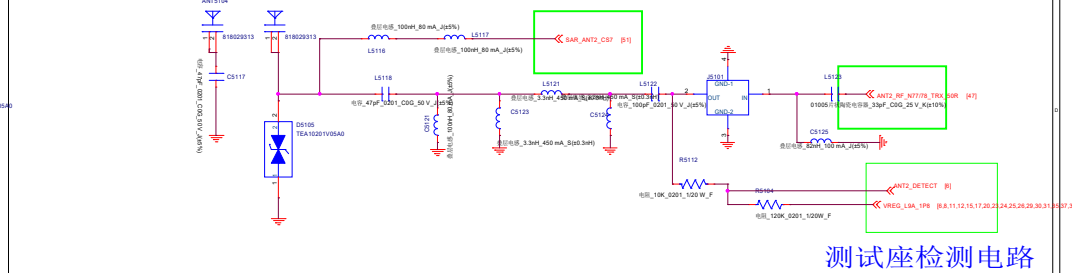
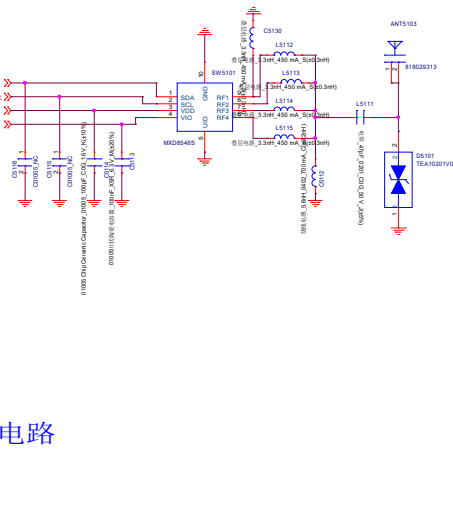
GPS



WIFI 2.4G/BT

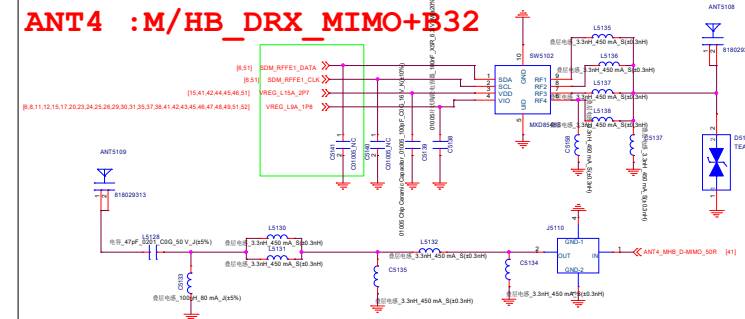
华勤通讯 Huaqin Telecom Technology Com.,Ltd		
Title GPS/WIFI_2.4G_5G/BT		
Size D	Project QN6362A_MB_V1	Rev V1
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ANT2:B42+N77/78 TRX0

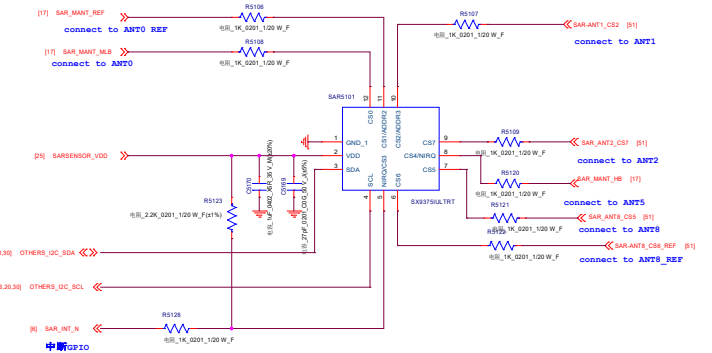


测试座检测电路

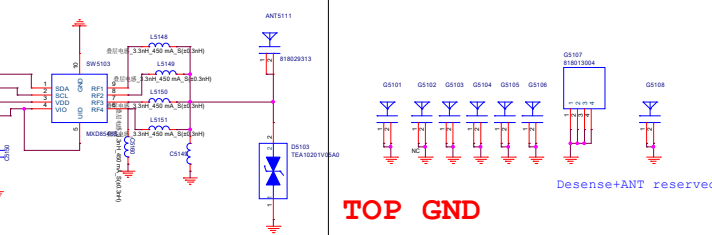
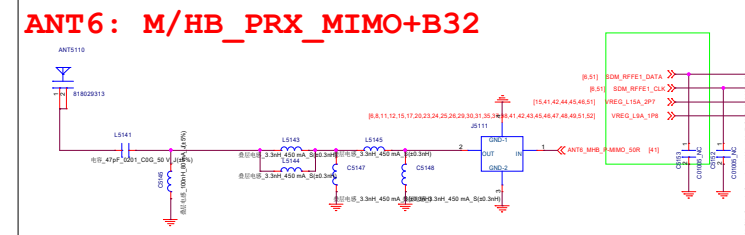
ANT4 :M/HB DRX MIMO+B32



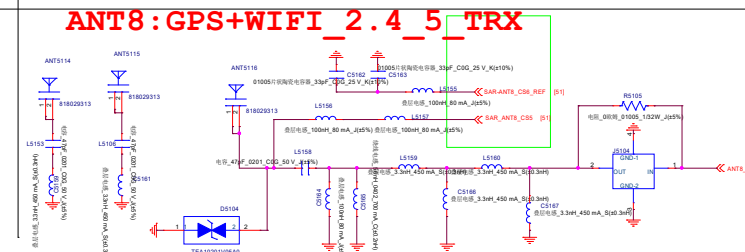
SAR sensor



ANT6: M/HB PRX MIMO+B32



ANT7:N77/78 PRX MIMO



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V2.0

2021/11/09: U4301 change to QLN5030, add L4359/L4360;

2021/11/09: U5202 change to QLN5030, add L4418/L4419;

11/08 1. DEL L5137, 2. 更换 LNA4902 GPS LNA 为AW5005DNRZ

11/09 1. 修改L5201/L5205封装 0603.

2021/11/09: U4304/U4702/U4116 封装由TDK 变更为村田。

2021/11/12: 删除电感L4342, 增加电阻R4320

QLN5030 V3.0 test

2021/11/20: 删除L4407

2021/11/20: 删除L4731,C4706,C4705

2021/11/24: U4304/U4702/U4116 封装更改2520;

2021/11/24: C4320/C4321/C4304/C4306/C4315/C4363/C4311

NFC V3.0

2021/11/30: 删除C5222/C5224/C5225/L5206/L5203

2021/11/30: C5217/C5204/C5205 封装0402

2021/12/8: RF_B3_PRX2_LNA_IN和RF_B1B66_PRX2_LNA_IN port 交换;

2021/12/8: add C4431, PRC co-pad兼容设计;

V4.0

2021/12/8: add R5401,R5402;

V5.0

2021/01/18: L5036 0402变为0201

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TitleRF_Changelist		
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