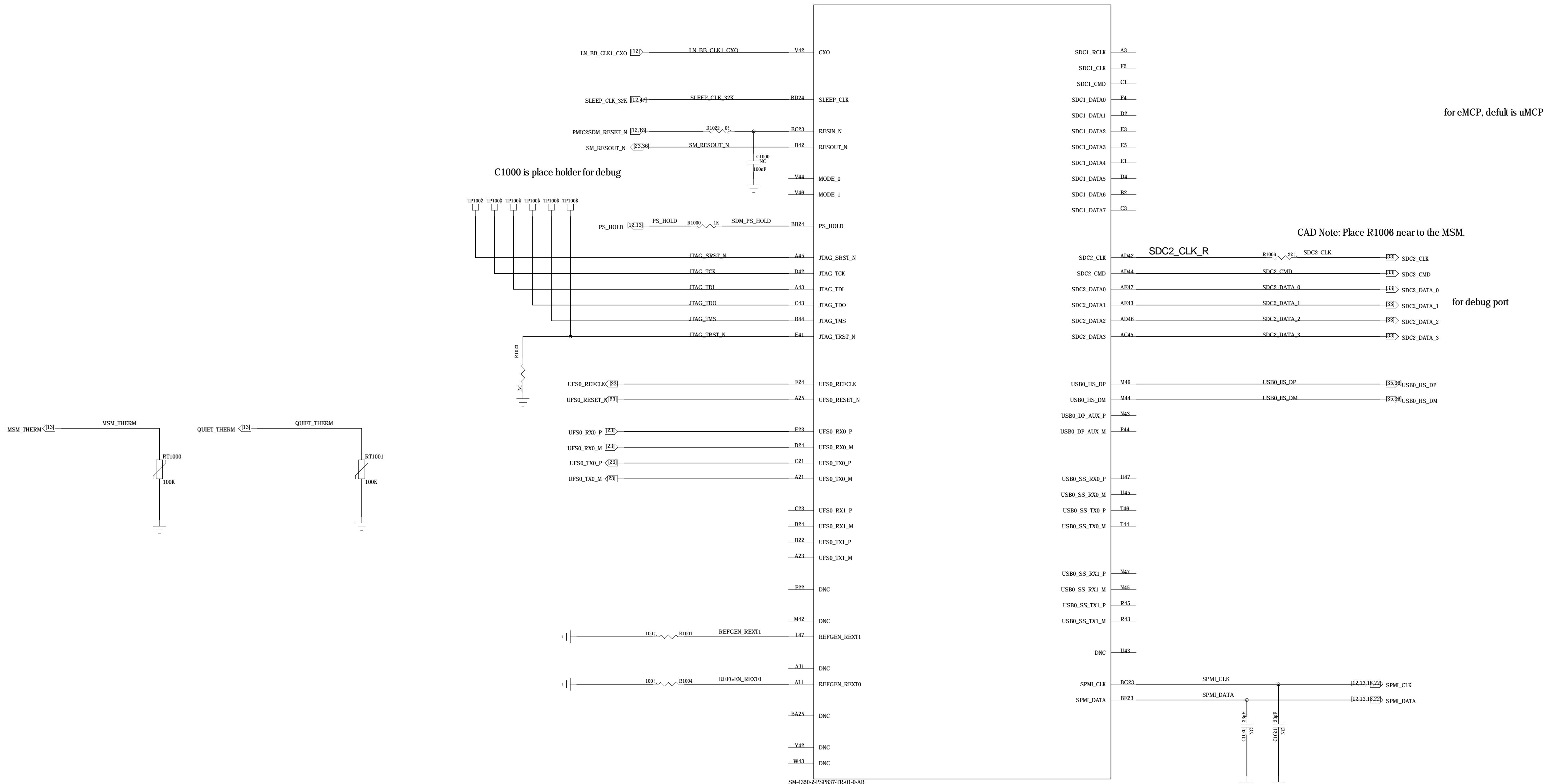


REVISION RECORD			
ltr	ECO NO:	APPROVED:	DATE:

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U1000-K



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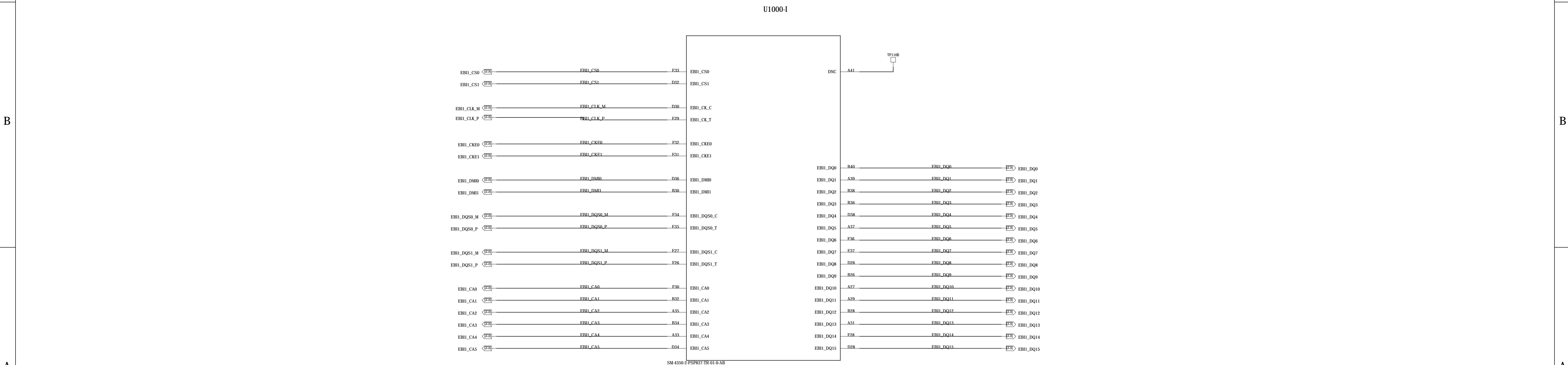
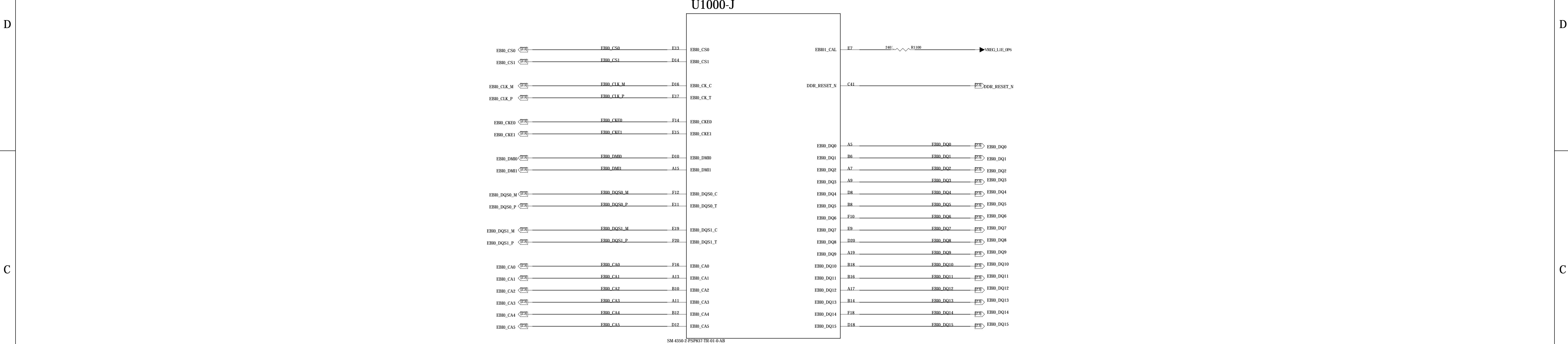
B

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A

COMPANY: <Company Name>	
TITLE: SM4350	
DRAWN: weiye.feng	DATED: 20210820
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>
CODE: P720AE	SIZE: D
DRAWING NO: P720AE	REV: V1.0
SCALE: <Scale>	SHEET: 4F 51

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



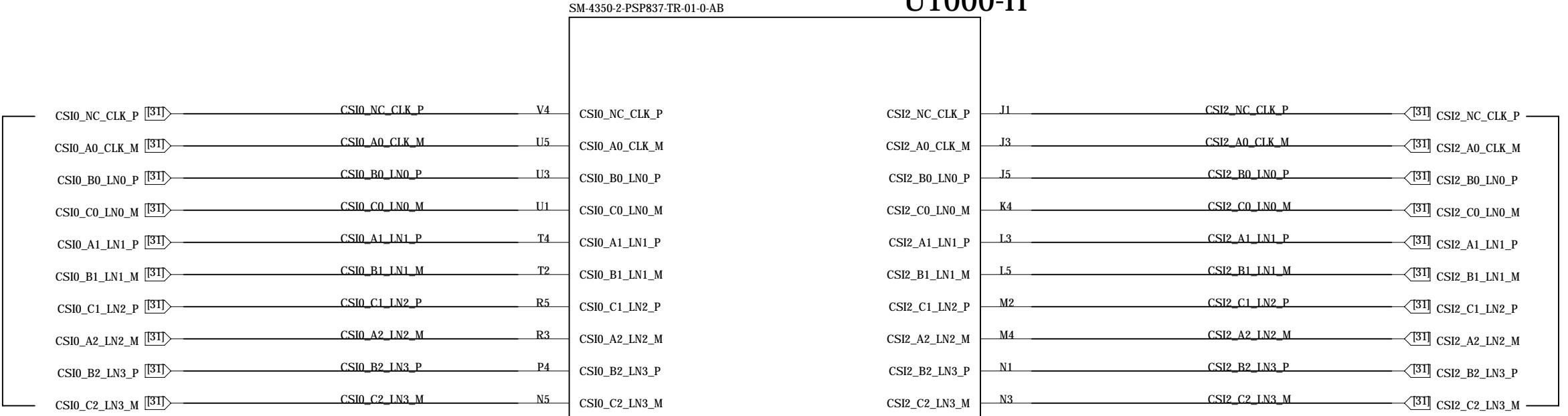
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TITLE: SM4350				
CODE: P720AE	SIZE: D	DRAWING NO: P720AE	REV: V1.0	
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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REAR CAMERA #0

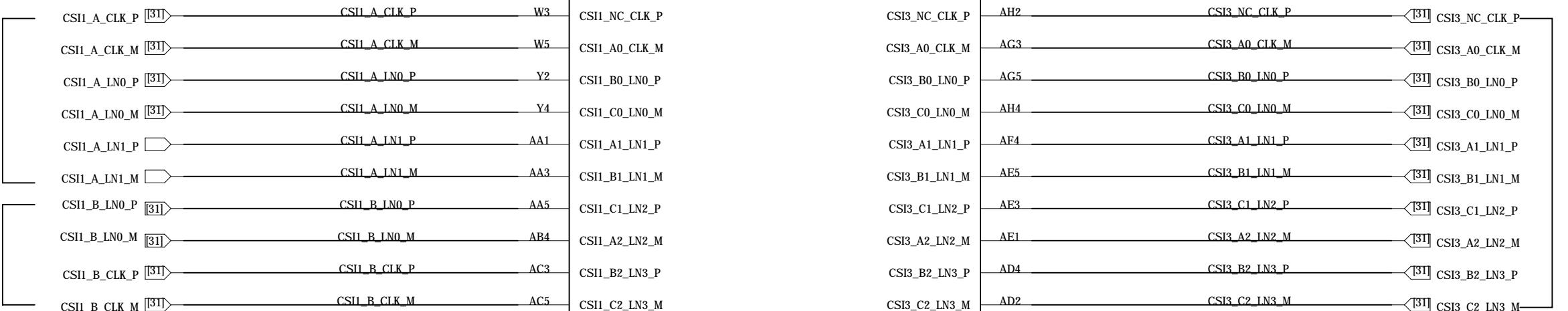


REAR CAMERA #3 (U.WIDE)

C

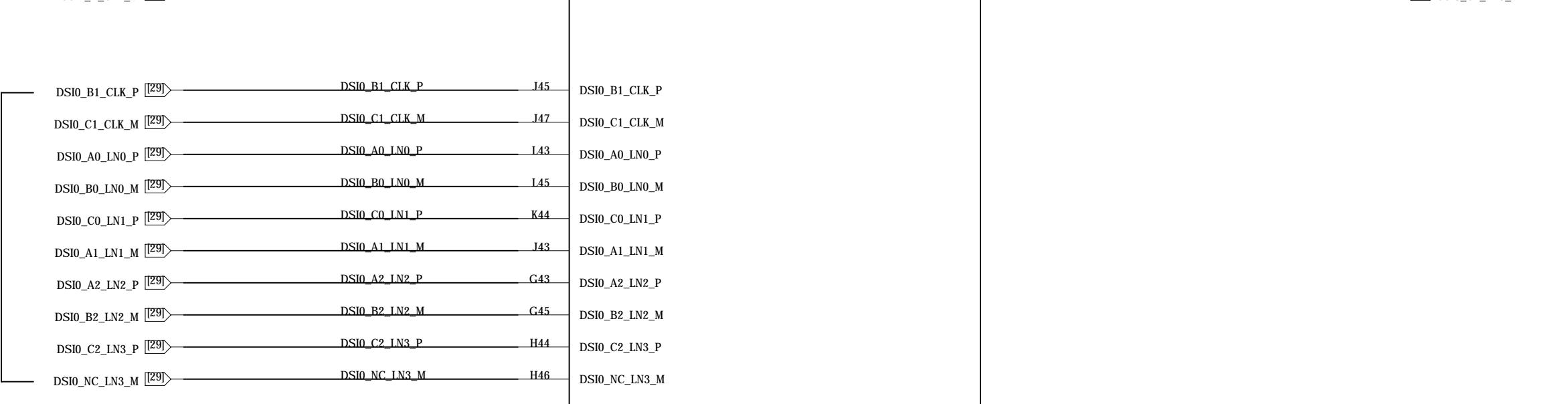
C

REAR CAMERA #2 (MACRO)



FRONT CAMERA

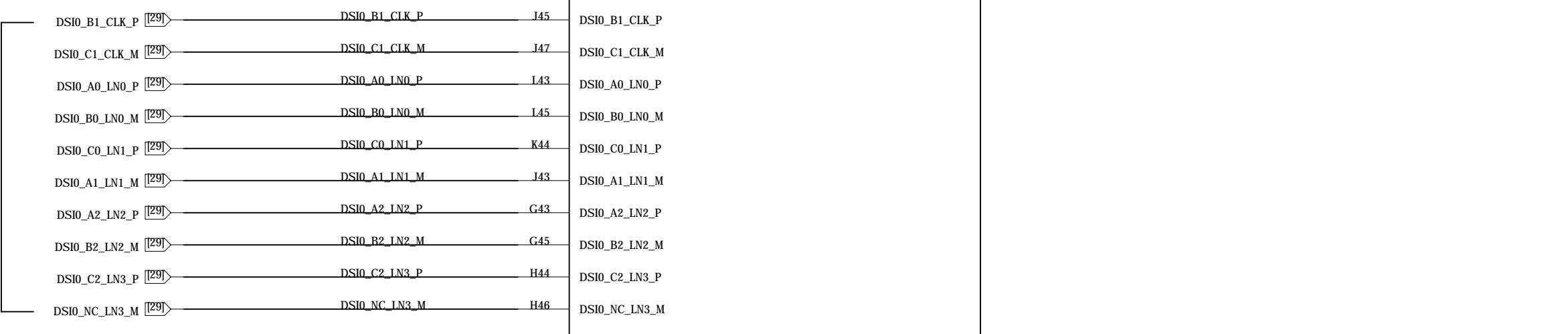
REAR CAMERA #1 (DEPTH)



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DSI #0



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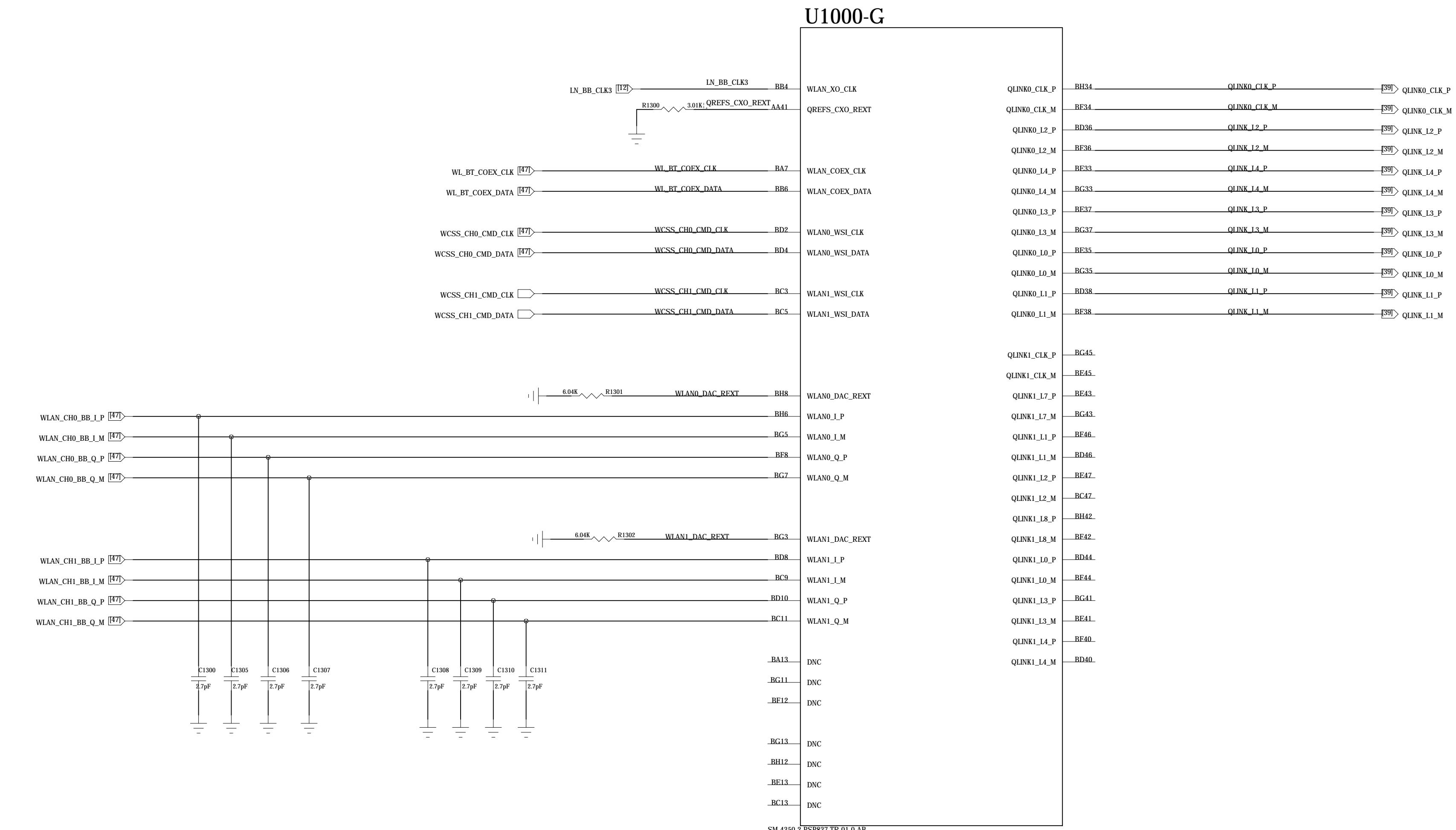
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TITLE:	SM4350		
P720AE			
DRAWN:	weiye.feng	DATED:	20210820
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
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P720AE	D	P720AE	V1.0
SCALE:	<Scale>	SHEET:	8f 51

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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CAD NOTE: Keep the resistors on the MSM REXT pins near to MSM and away from noisy signals.



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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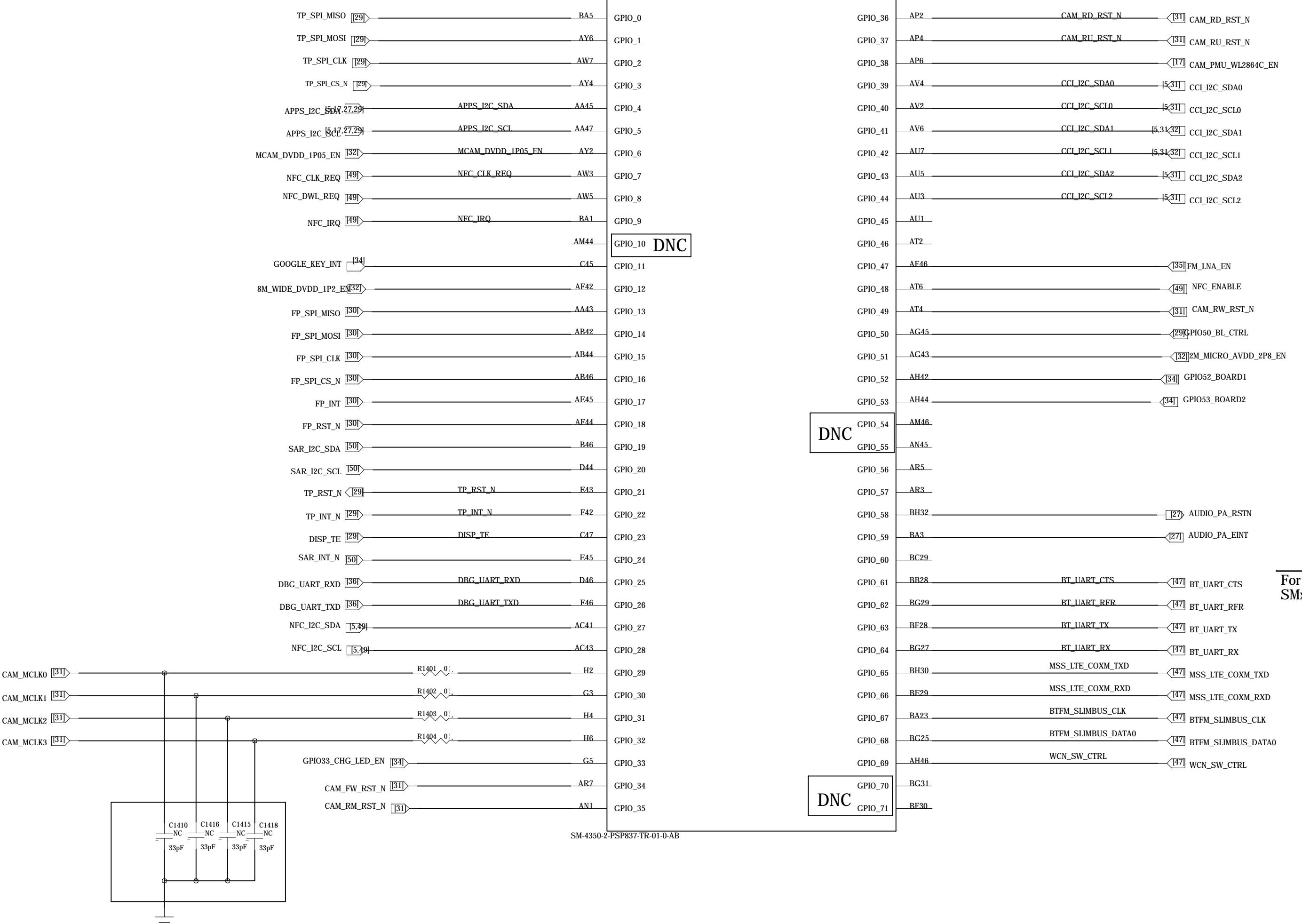
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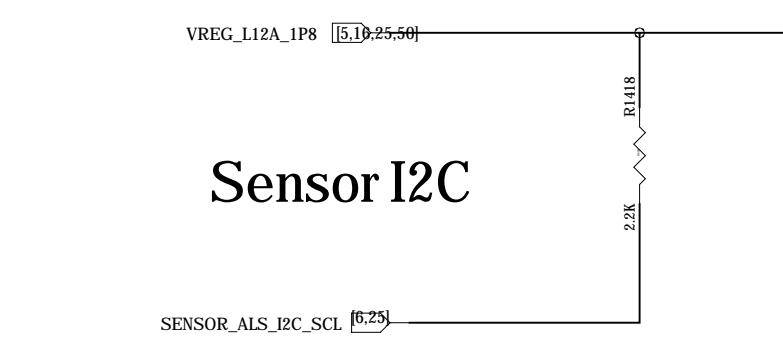
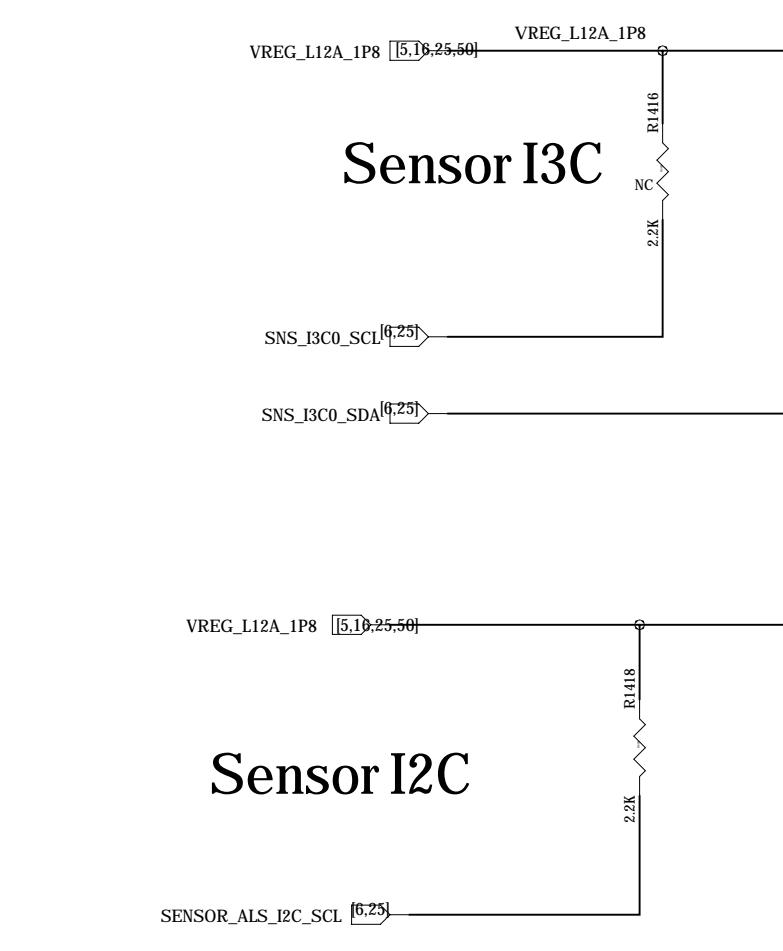
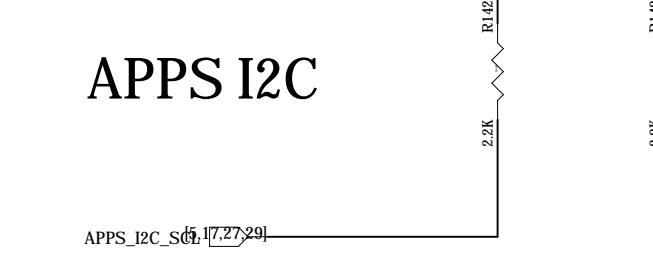
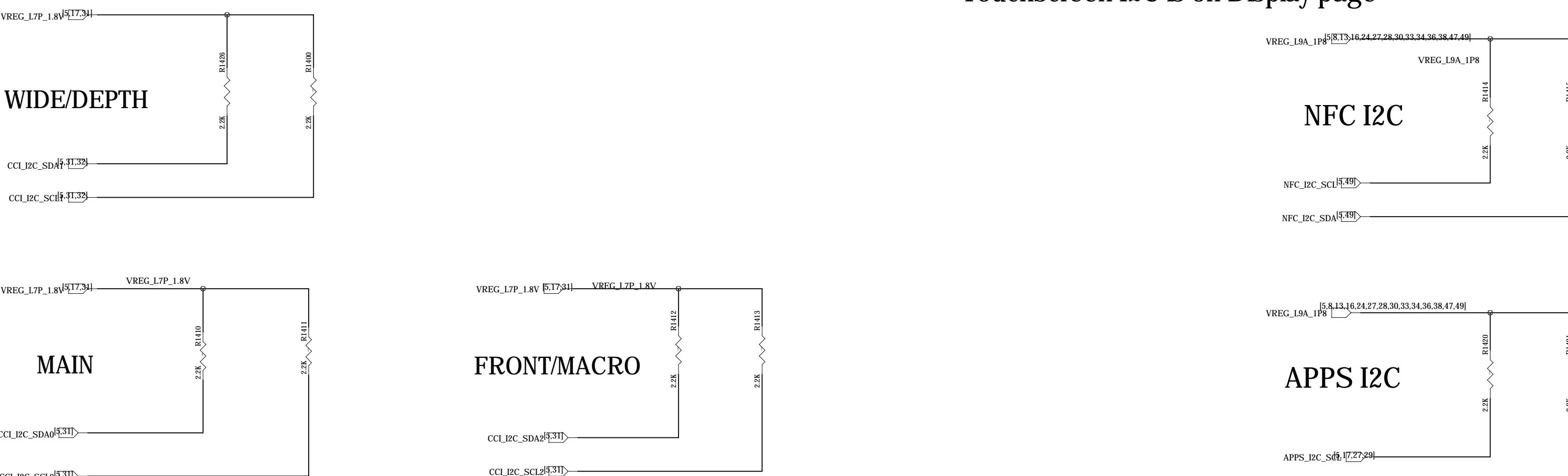
U1000-F



For SMxxxx only When the design of WSA8815 is used
SMxxxx is Pin2Pin compatible chip with SM6350

for test chip boot_config[1]

Touchscreen I2C is on Display page



COMPANY:	<Company Name>		
TITLE:	SM4350		
DRAWN:	weiye.feng	DATED:	20210820
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
SCALE:	<Scale>	DRAWING NO:	V1.0
REV:		CODE:	D
		P720AE	P720AE

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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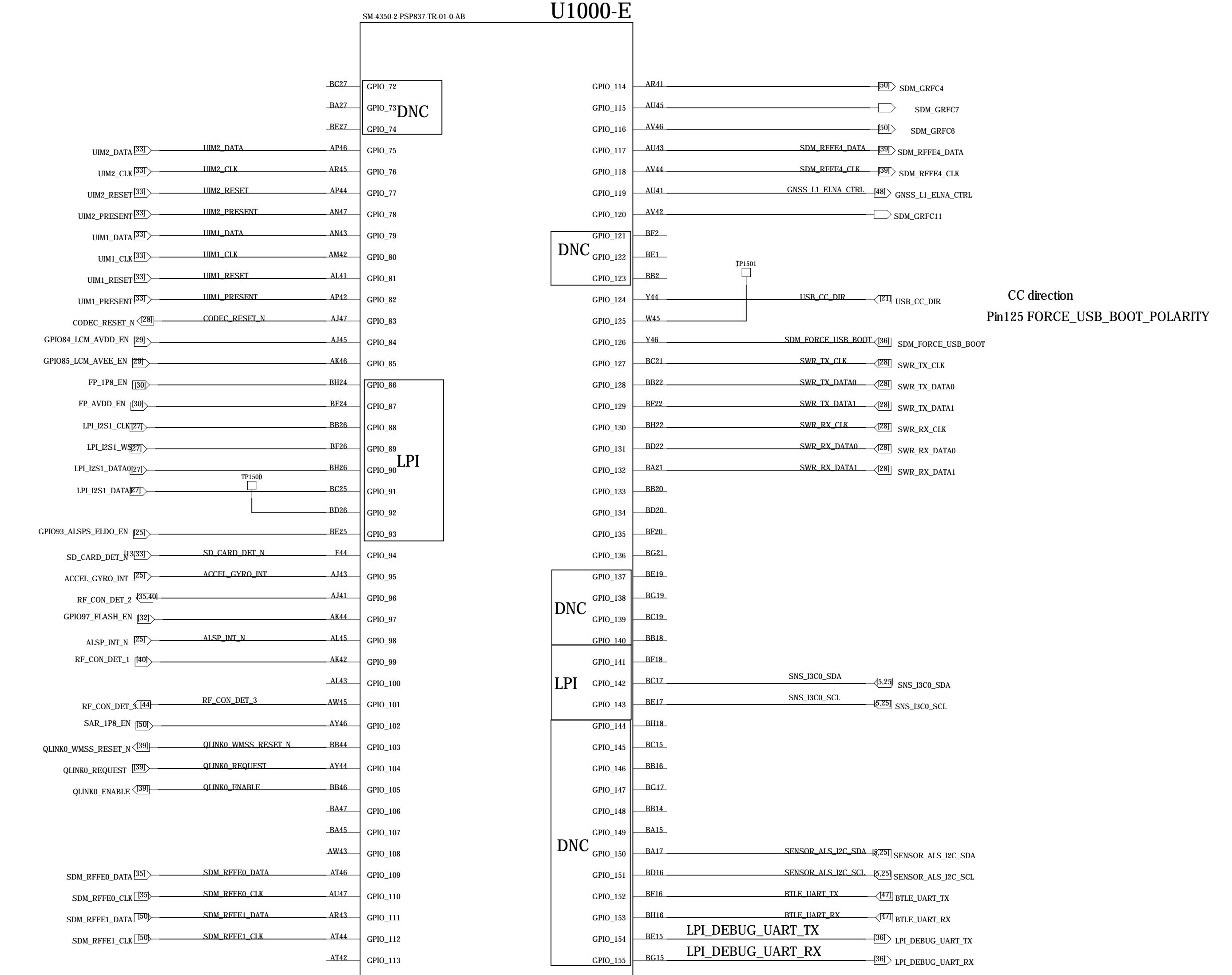
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For SMxxx only When the design of WSA8815 is used



COMPANY: <Company Name>	
TITLE: SM4350	
DRAWN: weiye.feng	DATED: 20210820
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>
SCALE: <Scale>	REV: V1.0
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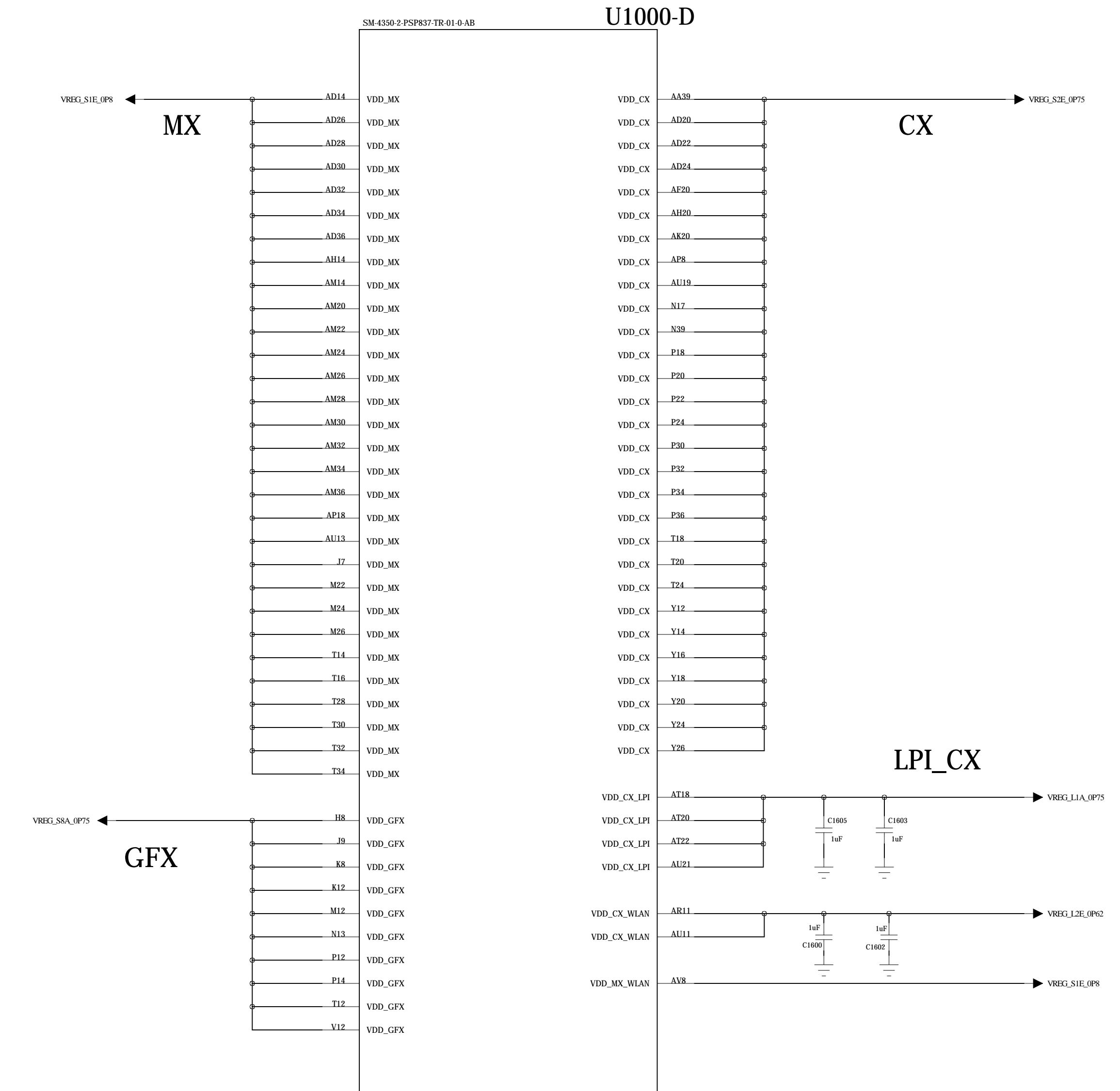
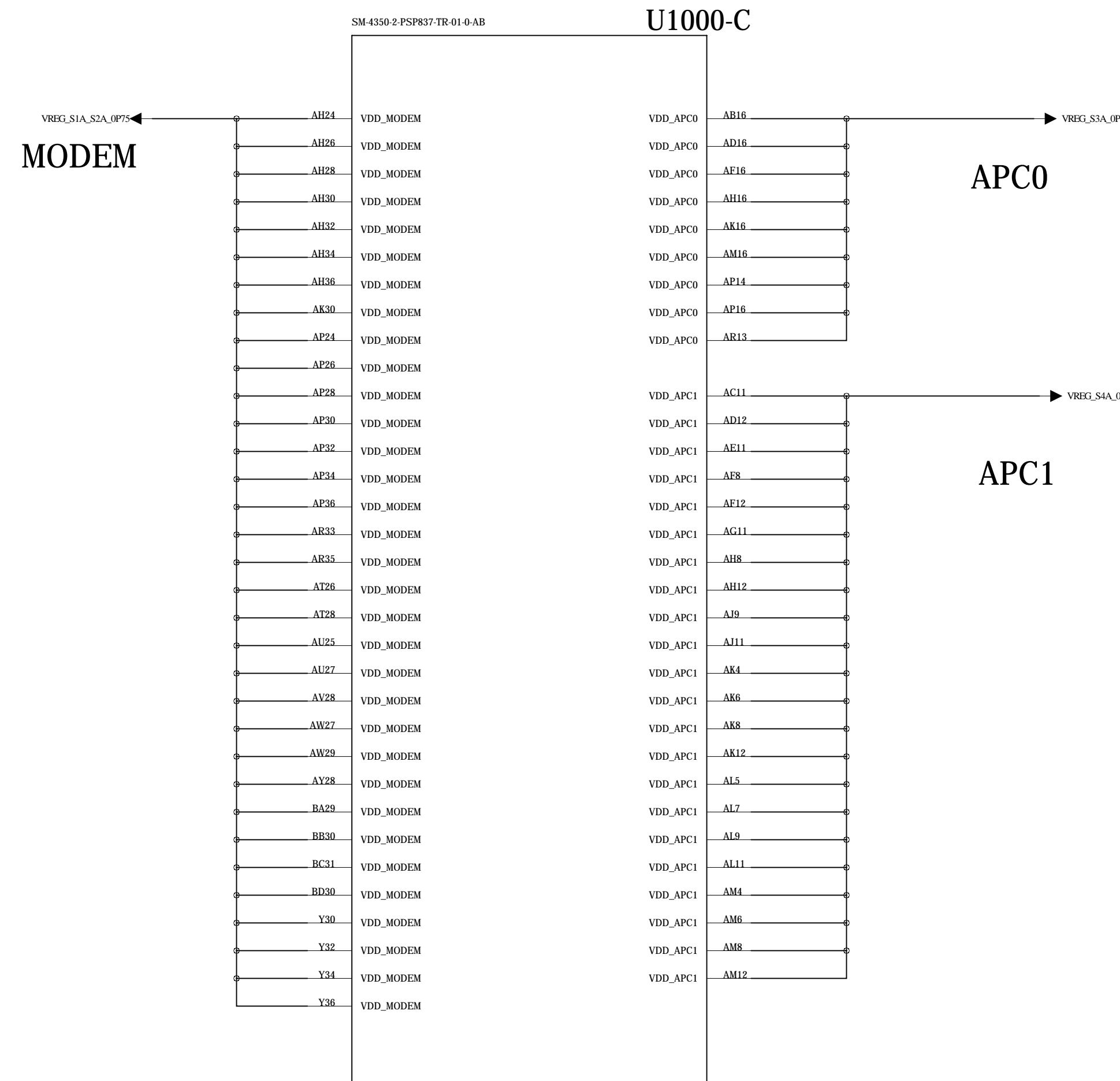
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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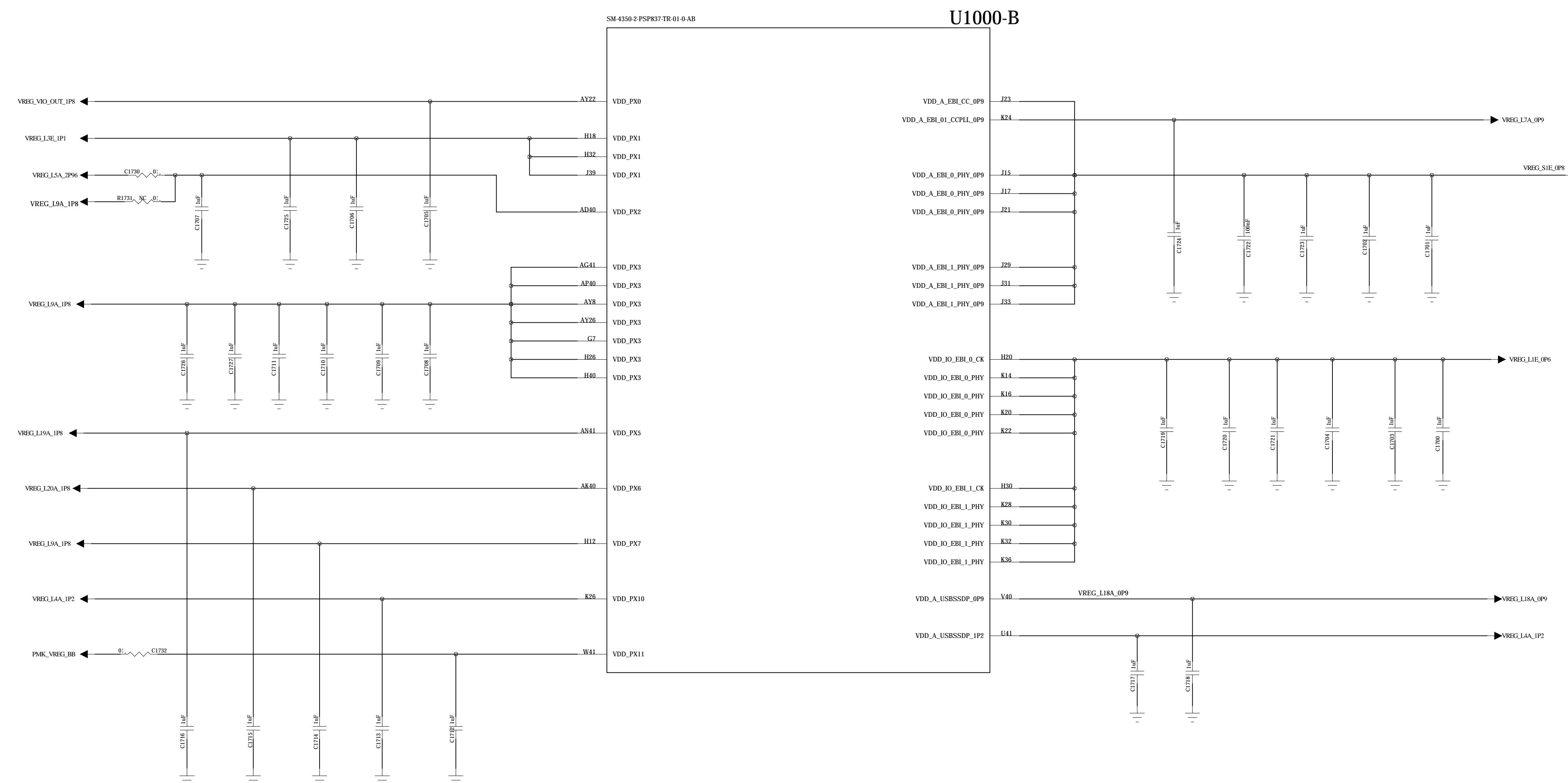
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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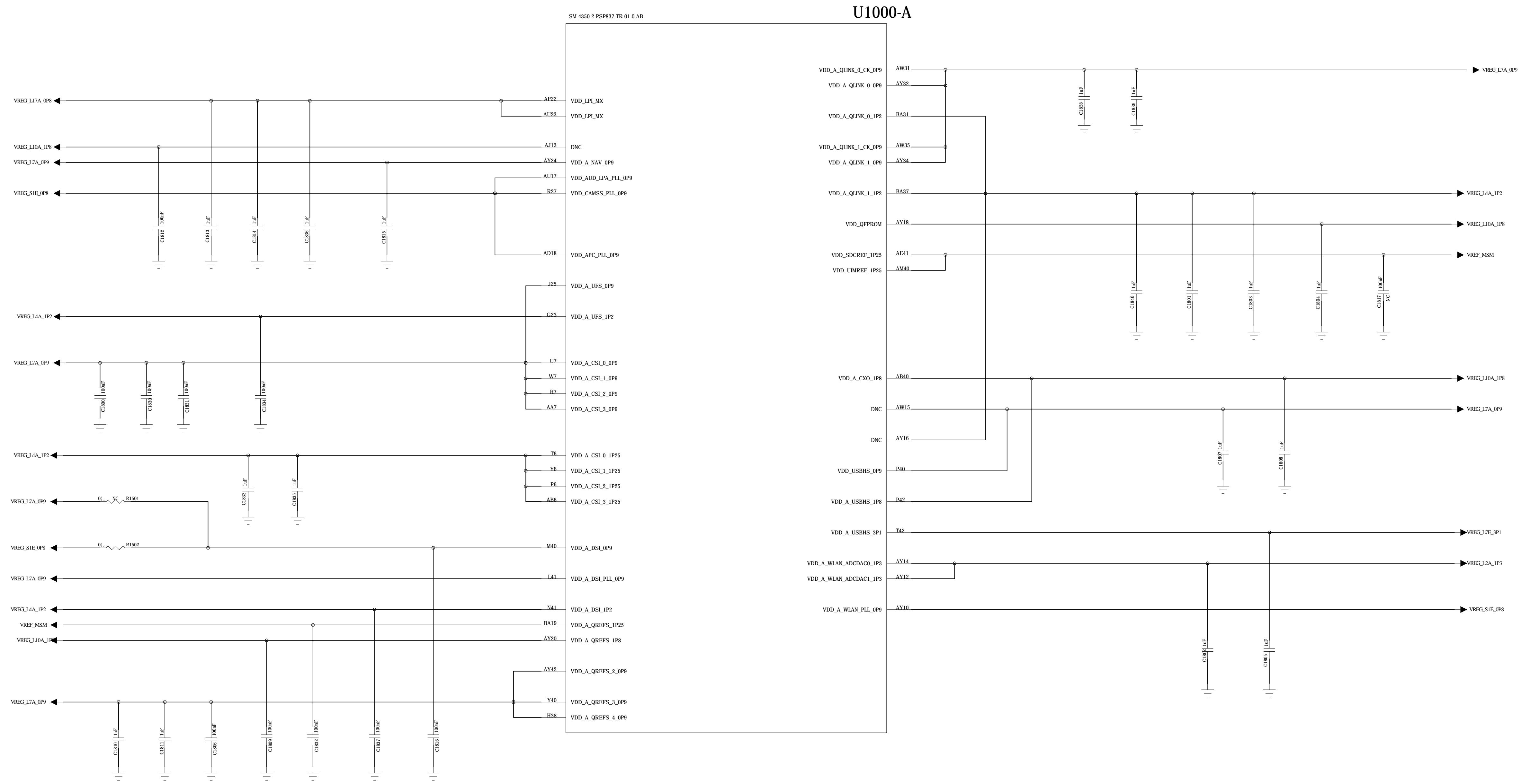
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DRAWN: weiye.feng	DATED: 20210820	CODE: P720AE	SIZE: D
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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		TITLE: SM4350	
DRAWN: weiyefeng	DATED: 20210820		
CHECKED: <Checked By>	DATED: <Checked Date>	CODE: P720AE	SIZE: D
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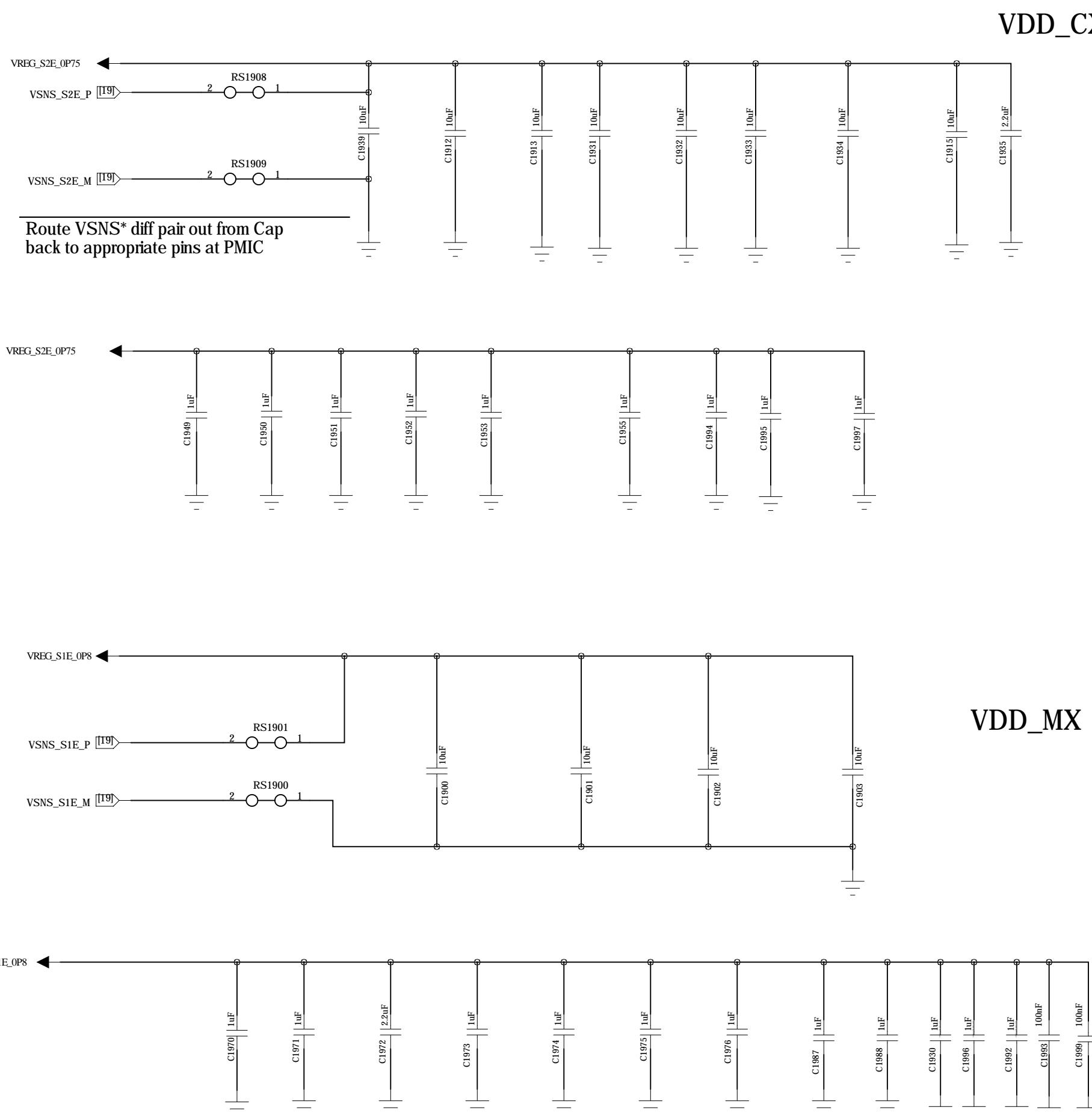
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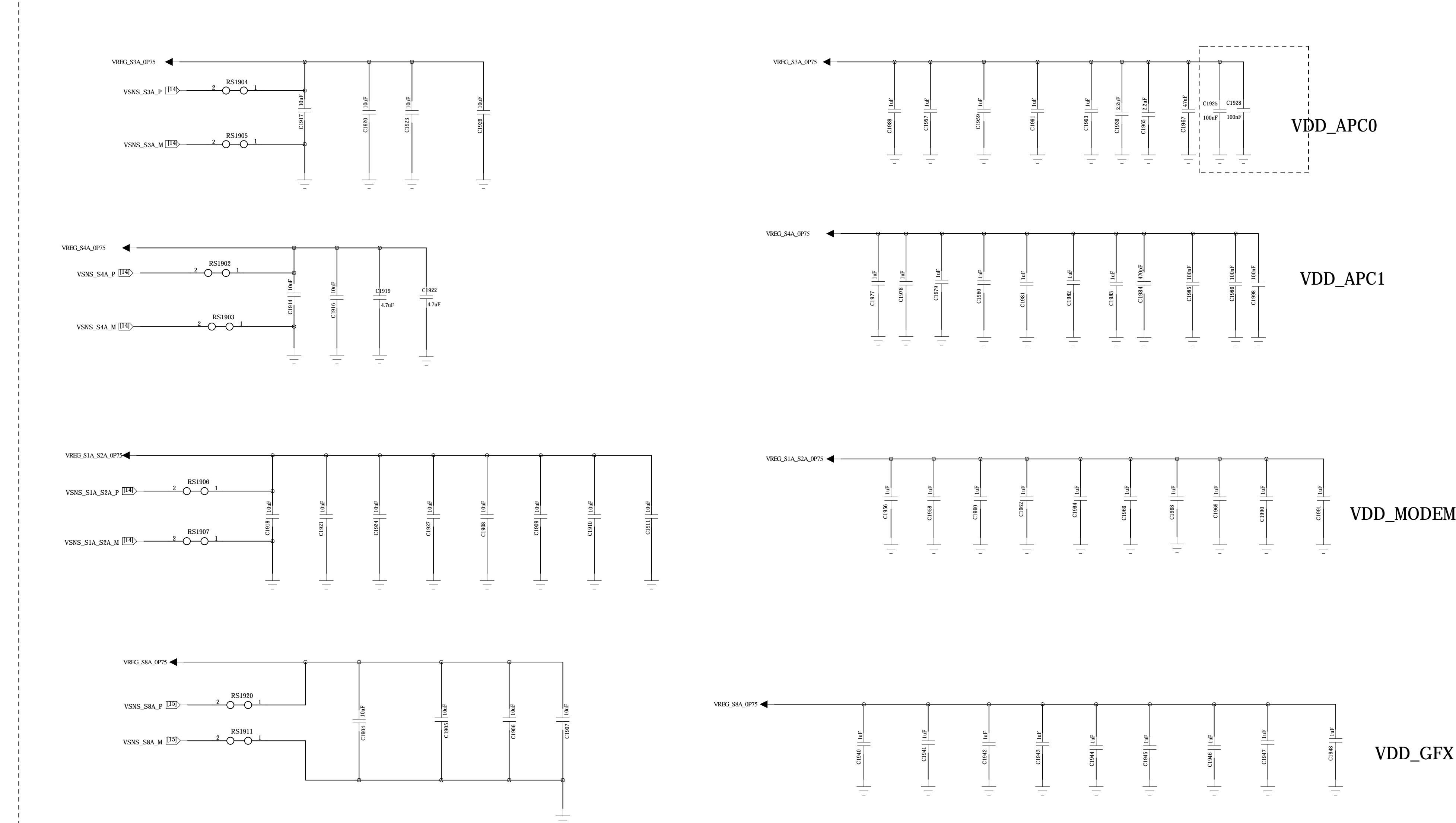
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PMR735A FTS Bucks



PM6375 FTS Bucks



COMPANY: <Company Name>	TITLE: SM4350		
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CHECKED: <Checked By>	DATED: <Checked Date>	CODE: P720AE	SIZE: D
QUALITY CONTROL: <QC By>	DATED: <QC Date>		
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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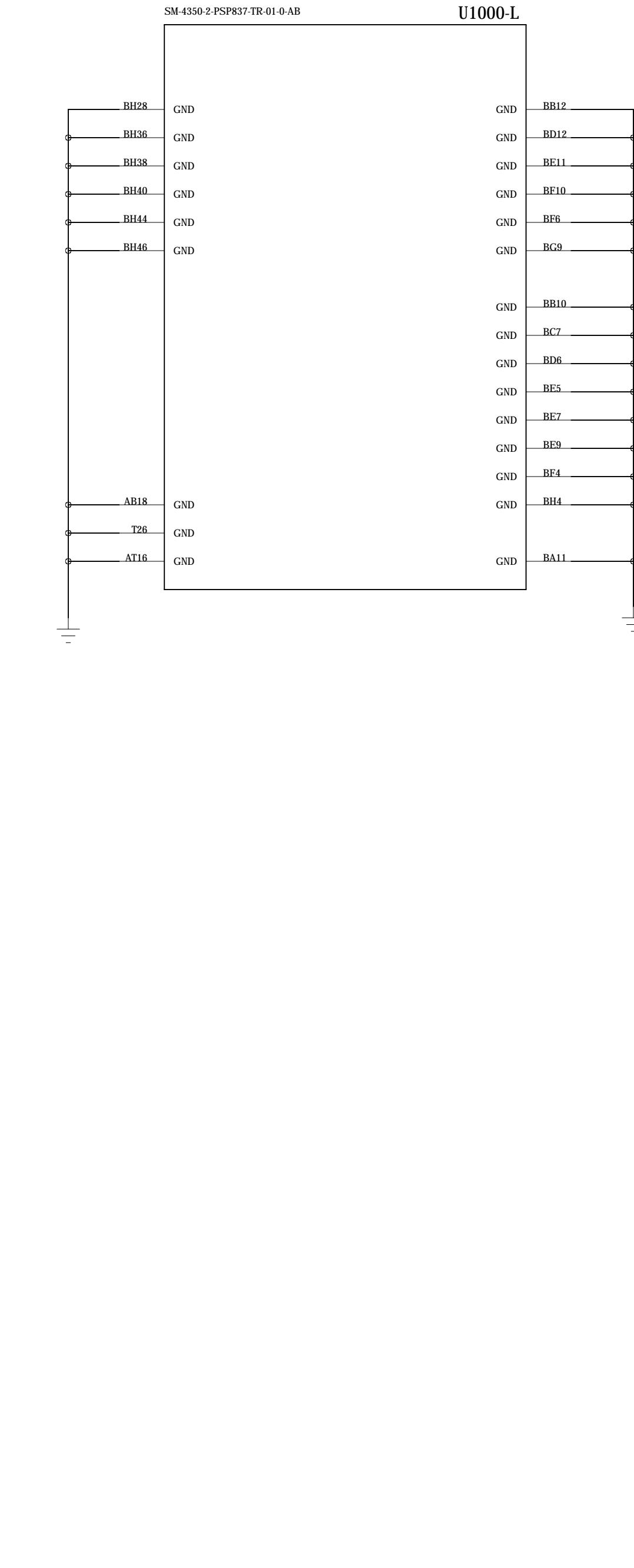
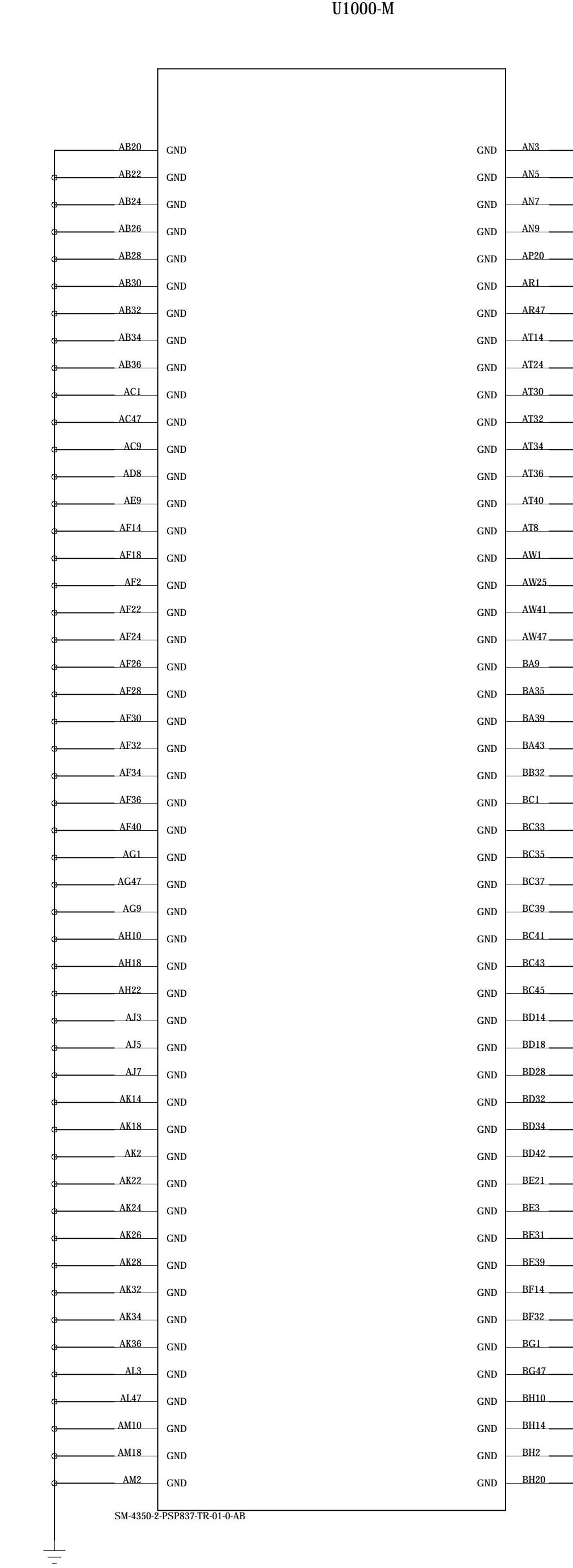
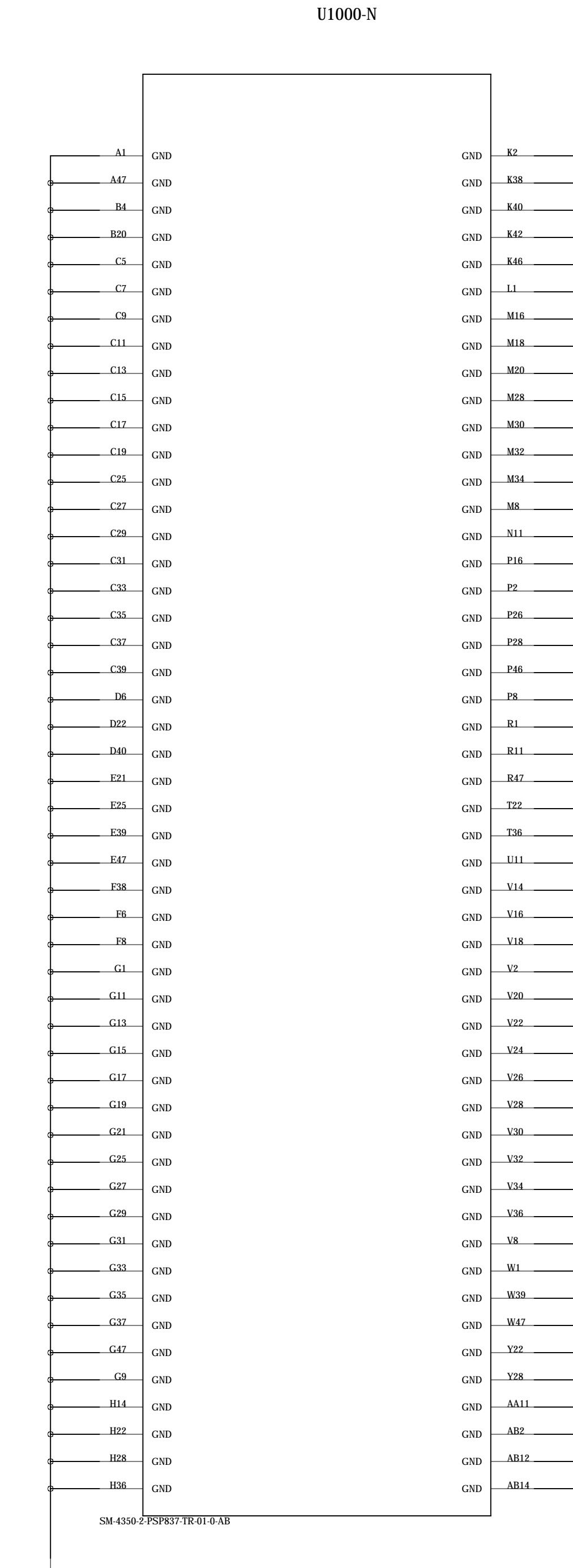
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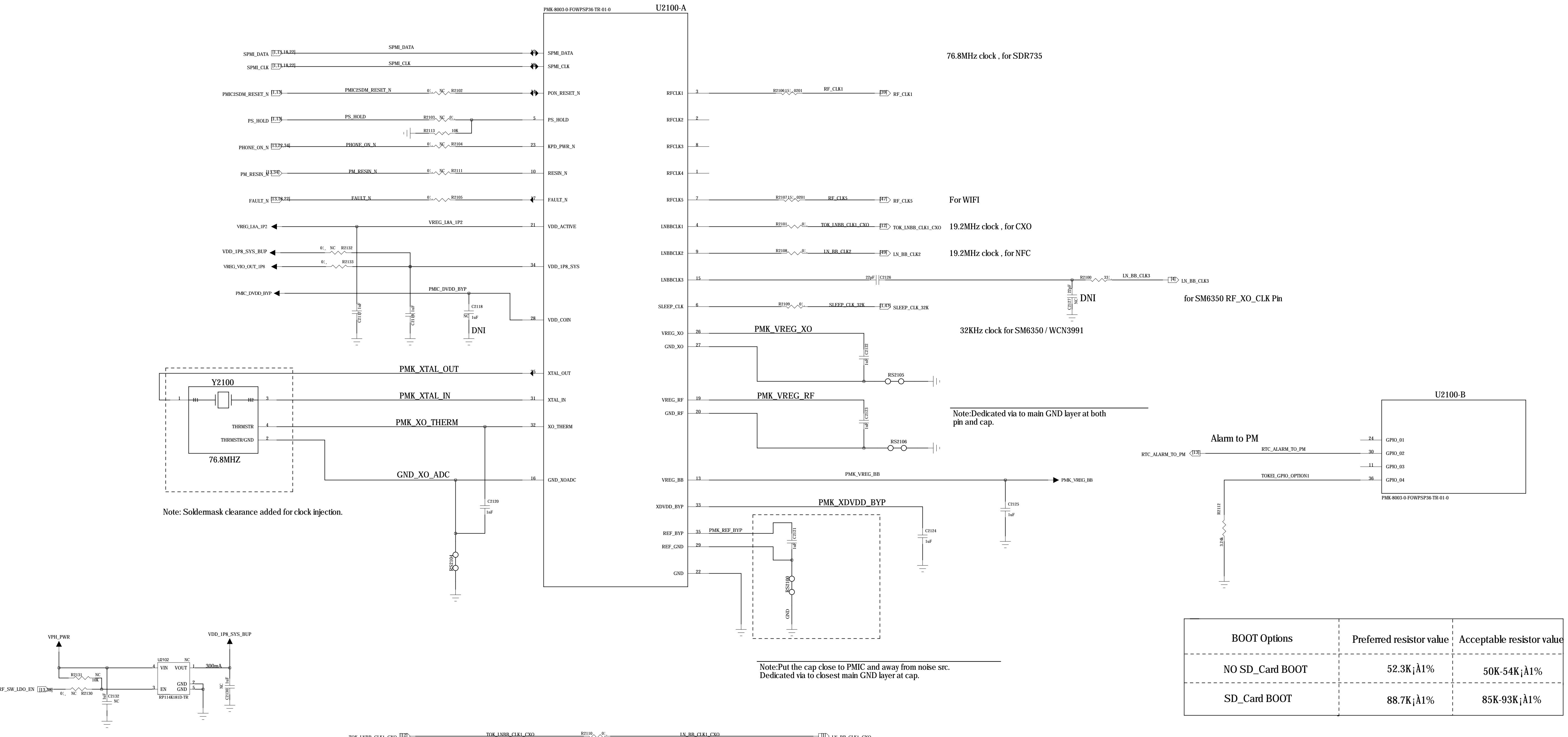
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COMPANY: <Company Name>	
TITLE: SM4350	
DRAWN: weiye.feng	DATED: 20210820
CHECKED: <Checked By>	DATED: <Checked Date>
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RELEASED: <Released By>	DATED: <Release Date>
CODE: P720AE	SIZE: D
DRAWING NO: V1.0	
REV: dd	SCALE: <Scale>

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Note: SPMI_CLK & SPMI_DATA should be routed away from noisy traces
Route signals as a diff pair as much as possible



BOOT Options	Preferred resistor value	Acceptable resistor value
NO SD_Card BOOT	52.3K ±1%	50K-54K ±1%
SD_Card BOOT	88.7K ±1%	85K-93K ±1%

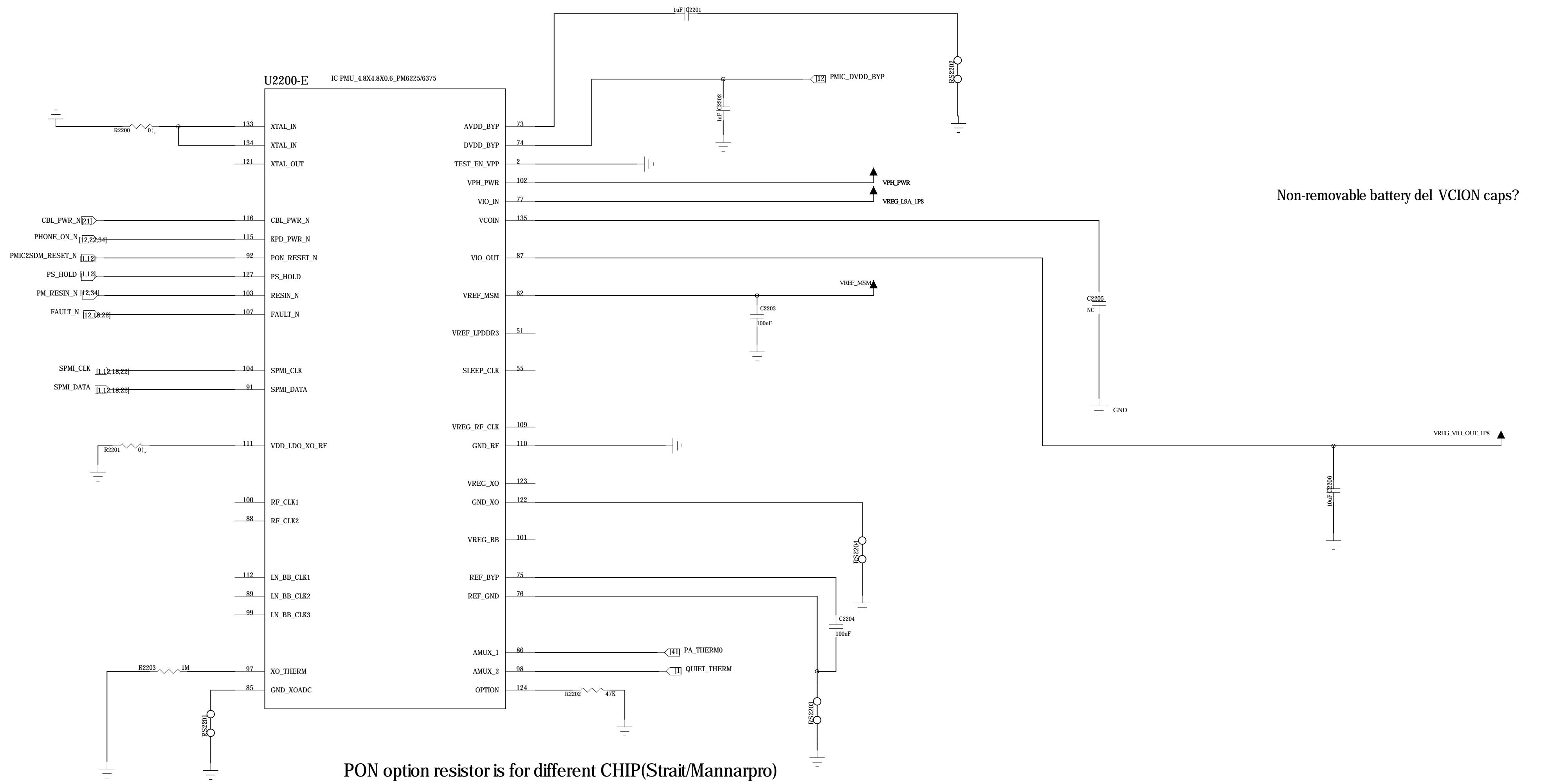
Note: Mount R1915 for SM6350, DNI R1915 for Test Chip.

COMPANY: <Company Name>			
TITLE: SM4350			
CODE: P720AE	SIZE: D	DRAWING NO: P720AE	REV: V1.0
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REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

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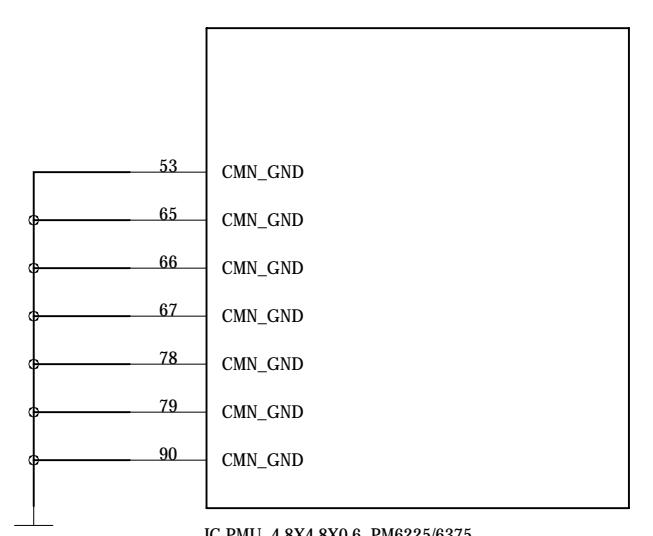
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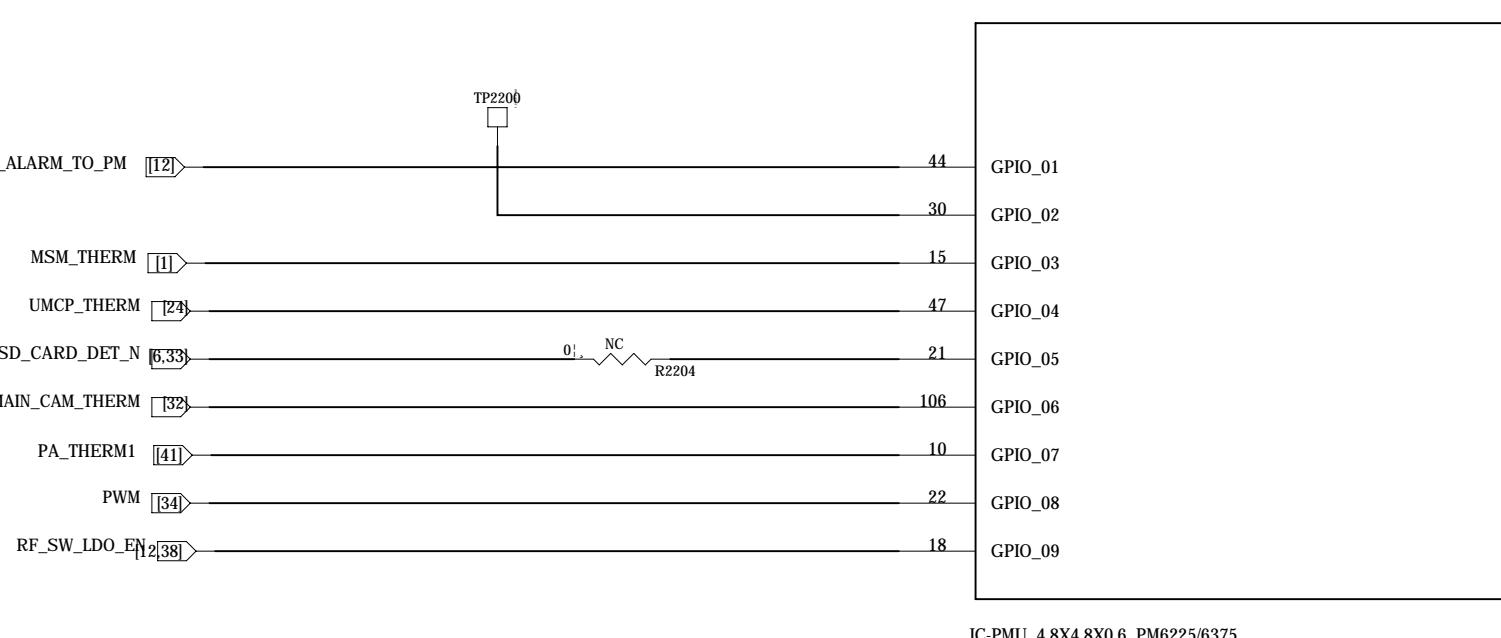
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U2200-F



U2200-D



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COMPANY: <Company Name>			
TITLE: SM4350			
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CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: P720AE	REV: V1.0
QUALITY CONTROL: <QC By>	DATED: <QC Date>		
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REVISION RECORD			
ltr	ECO NO.	APPROVED	DATE

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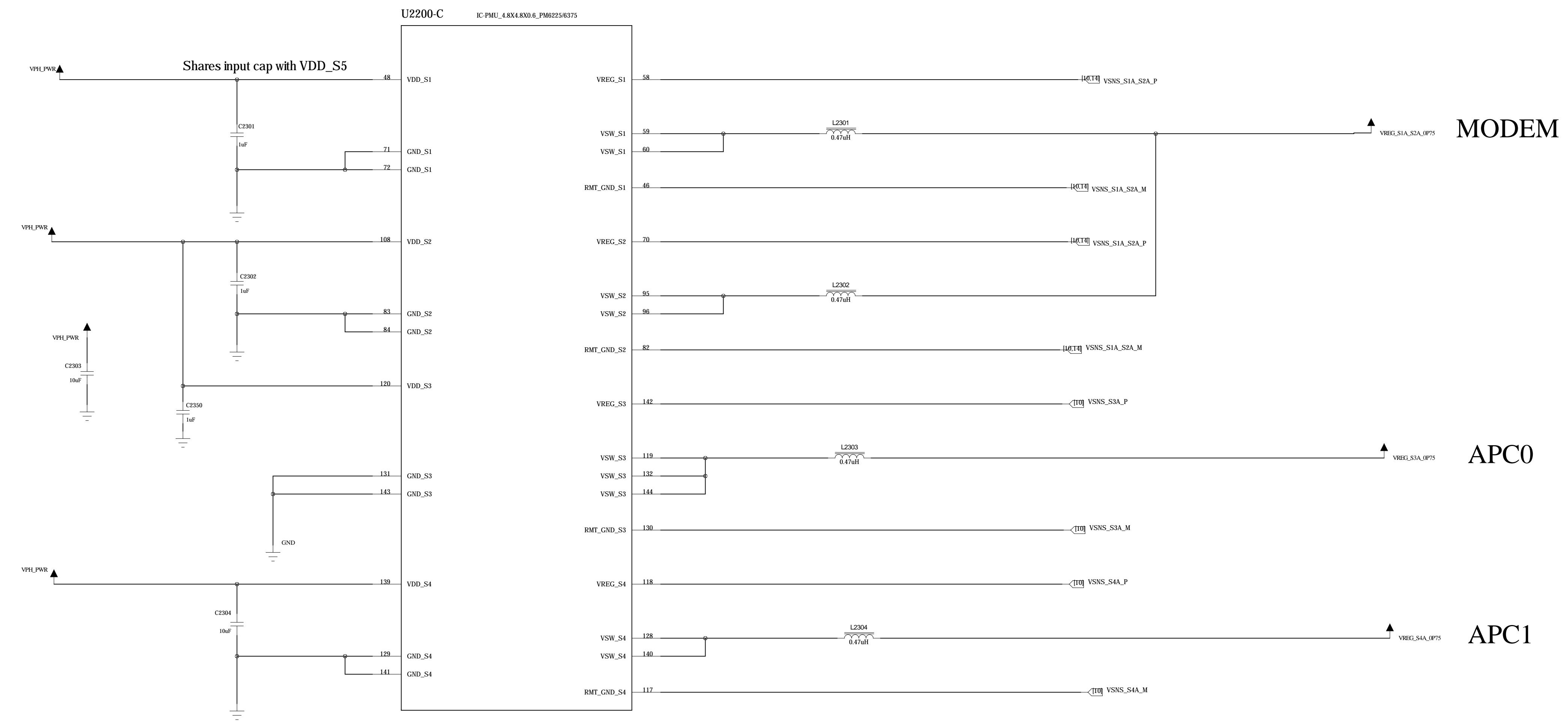
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REVISION RECORD			
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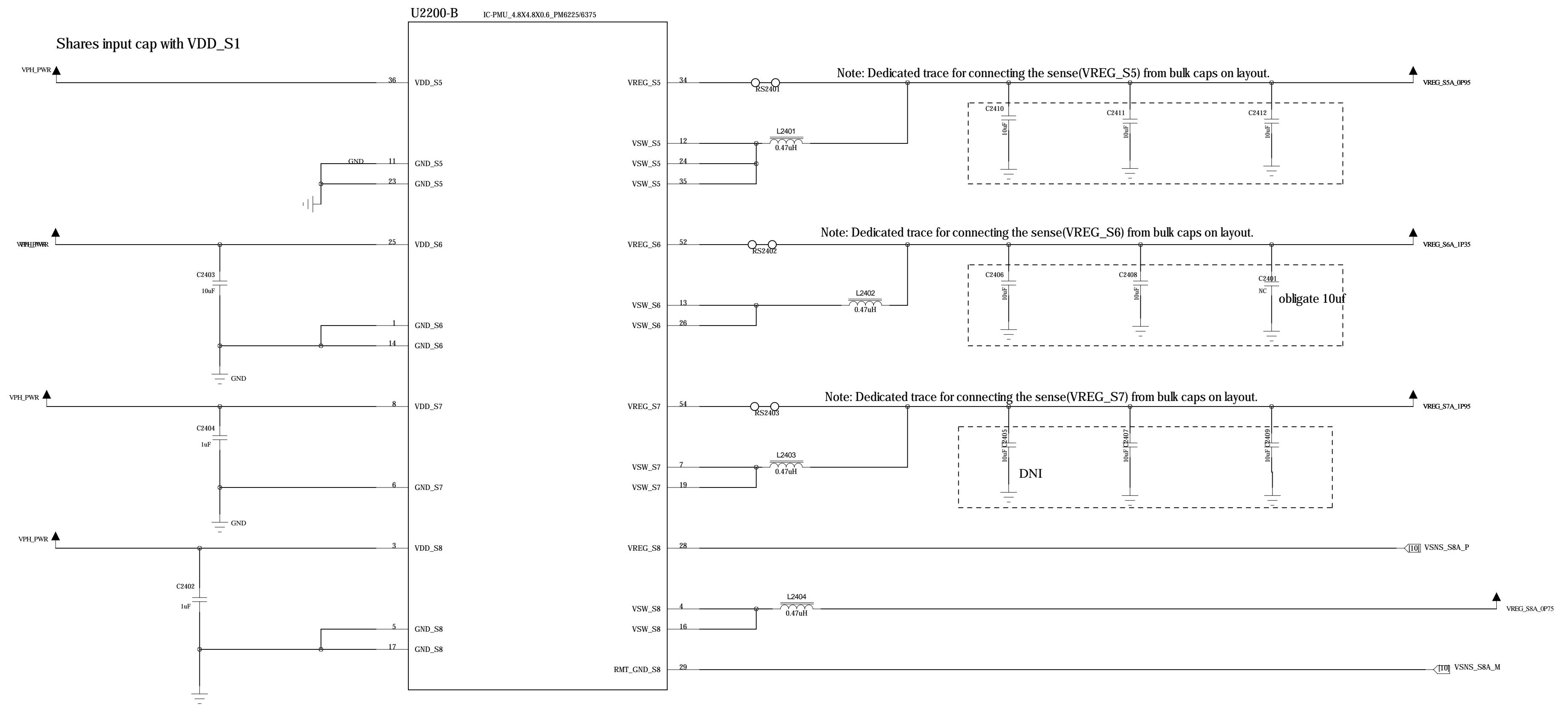
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COMPANY: <Company Name>	
TITLE: SM4350	
DRAWN: weiye.feng	DATED: 20210820
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>
CODE: P720AE	SIZE: D
DRAWING NO: P720AE	REV: V1.0
SCALE: <Scale>	SCALE: <Scale>
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REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

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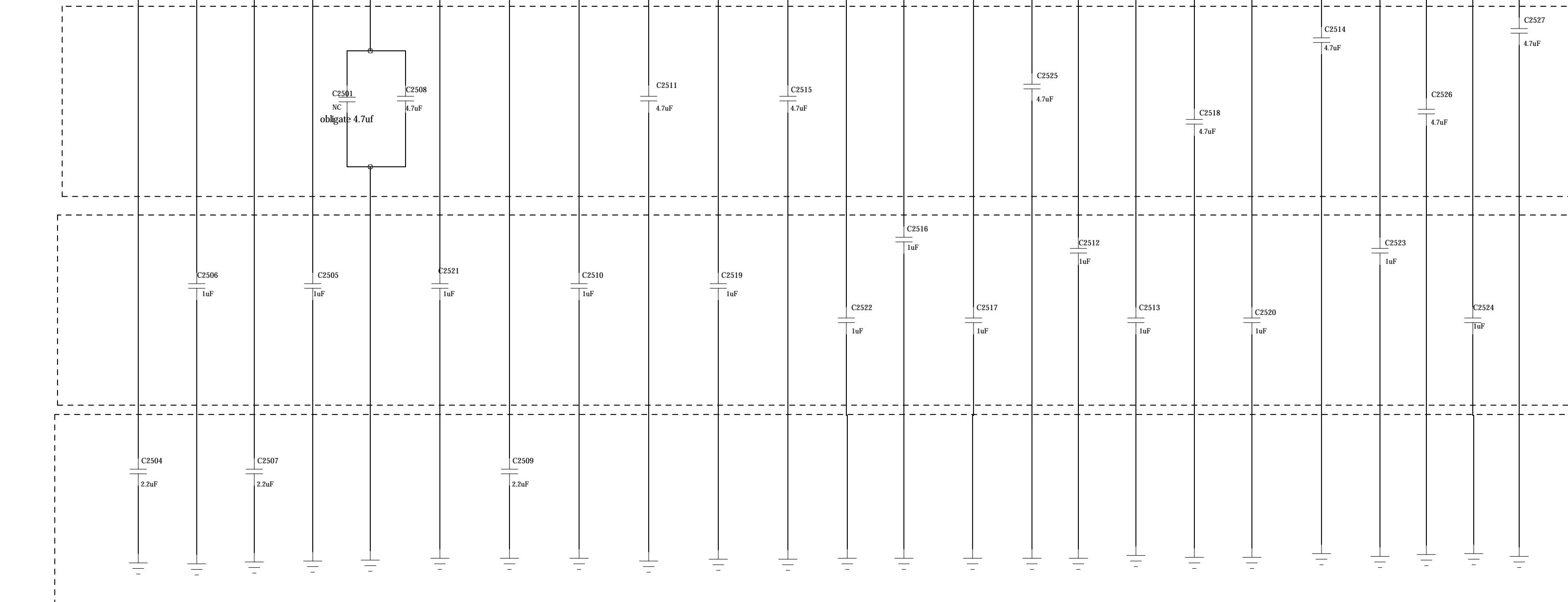
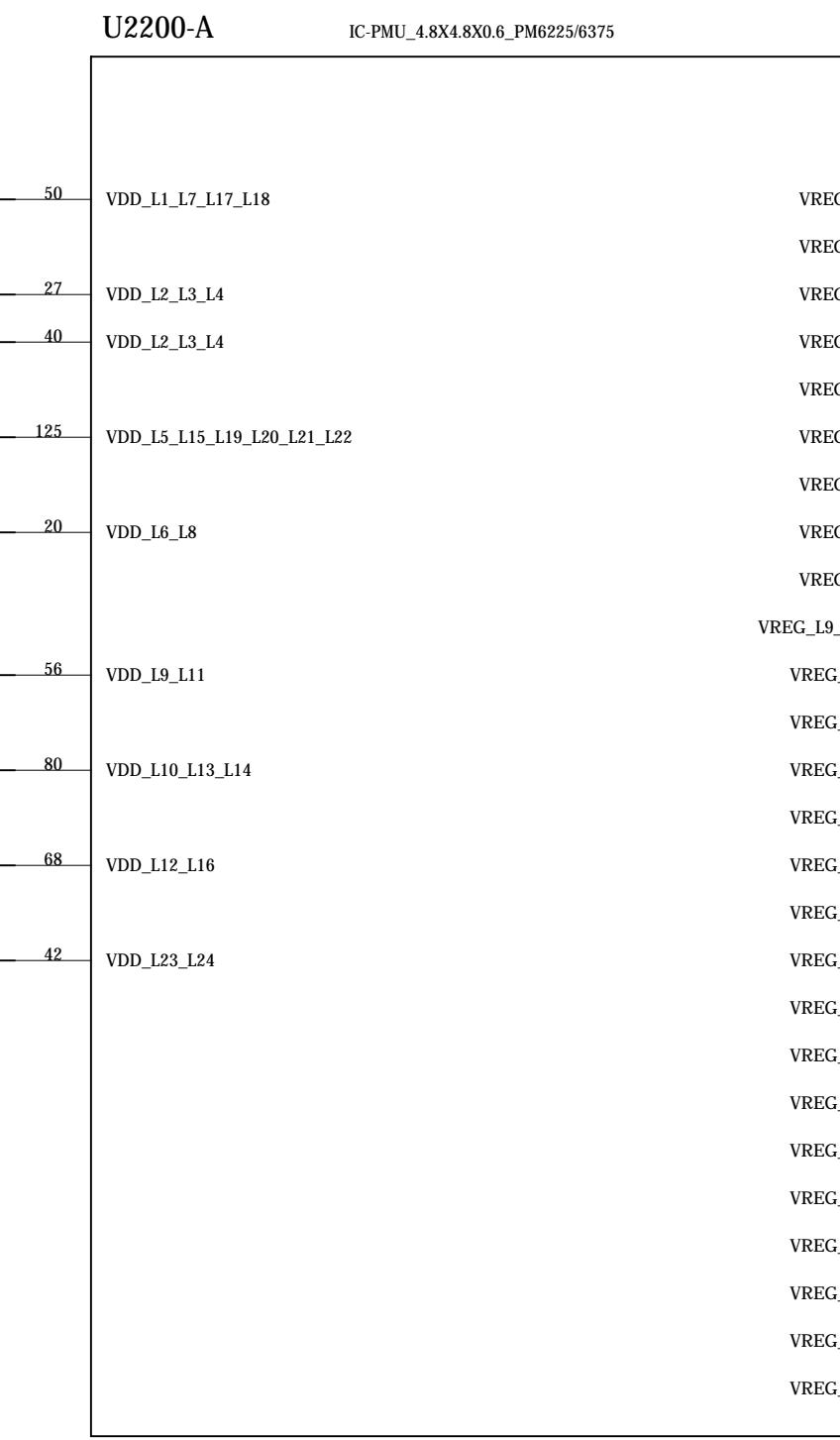
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Note: Place LDO input caps near to PMIC



VDD_LPI_CX
 VDD_A_WLAN_ADC / WCN
 NOT USED
 VDD_A_CSI / DS1 / QLINK
 PX2
 SDR735_VDDAM
 VDDA_0P9/SDR735_VDDMX
 PX11
 VDD1 / PX3 / PX7
 VDD_A_USBHS/CXO_1P8
 VCCQ2
 SENSOR
 LCD_VDDIO
 WCD9375_VDD_BUCK
 RF switch
 WCN3998-1
 VDD_LPI_MX
 VDD_A_USBSSDP
 UIM_0 / SN110U/VDD_PX5
 UIM_1 / SN110U/VDD_PX6
 WCN3998-1_3P3_CH NOT USED
 SDCARD
 WCN3998-1_3P3_CH0
 UFS_2L_VCC

4.7uF

1.0uF

2.2uF

COMPANY: <Company Name>			
TITLE: SM4350			
DRAWN: weiye.feng	DATED: 20210820		
CHECKED: <Checked By>	DATED: <Checked Date>		
QUALITY CONTROL: <QC By>	DATED: <QC Date>		
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CODE: P720AE	SIZE: D	DRAWING NO.: P720AE	REV.: V1.0
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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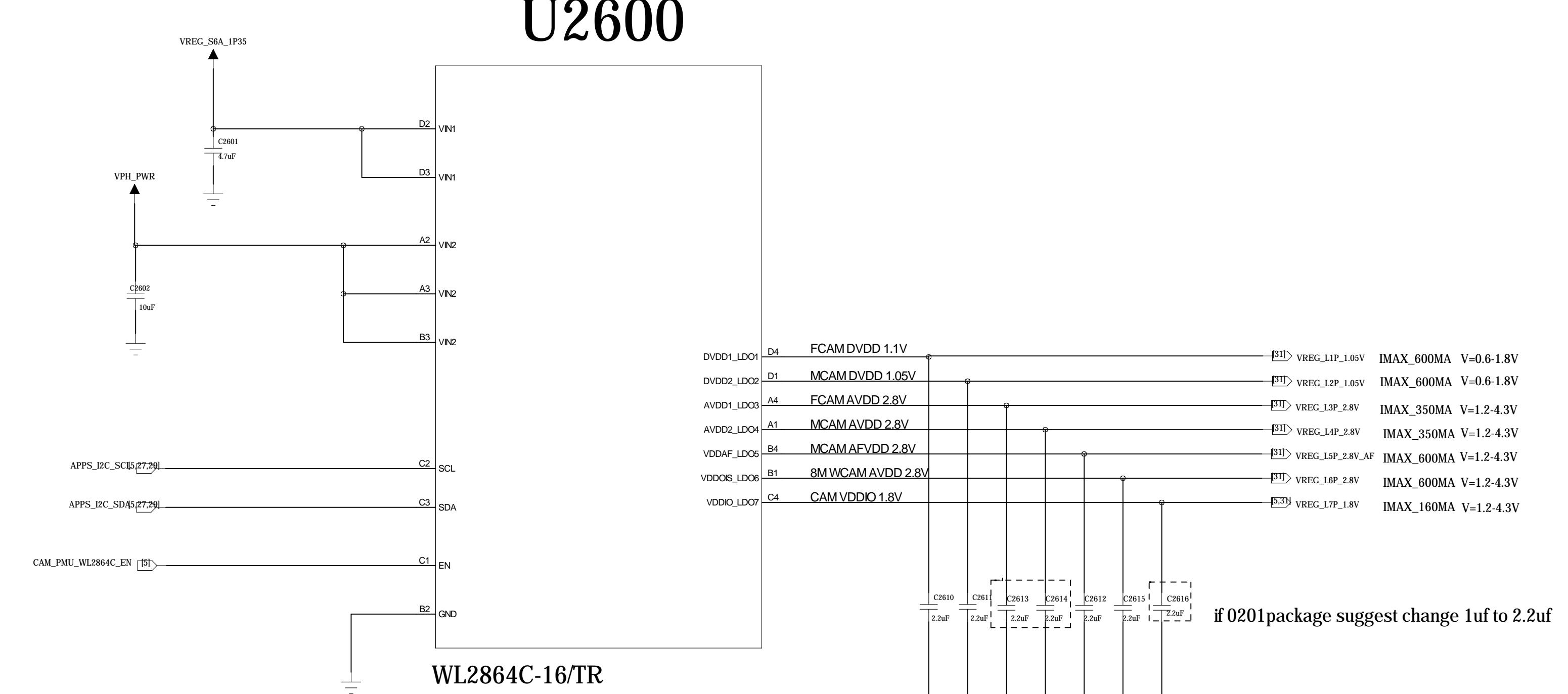
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COMPANY: <Company Name>	
TITLE: SM4350	
DRAWN:	weiye.feng
DATED:	20210820
CHECKED:	<Checked By>
DATED:	<Checked Date>
QUALITY CONTR:	<QC By>
DATED:	<QC Date>
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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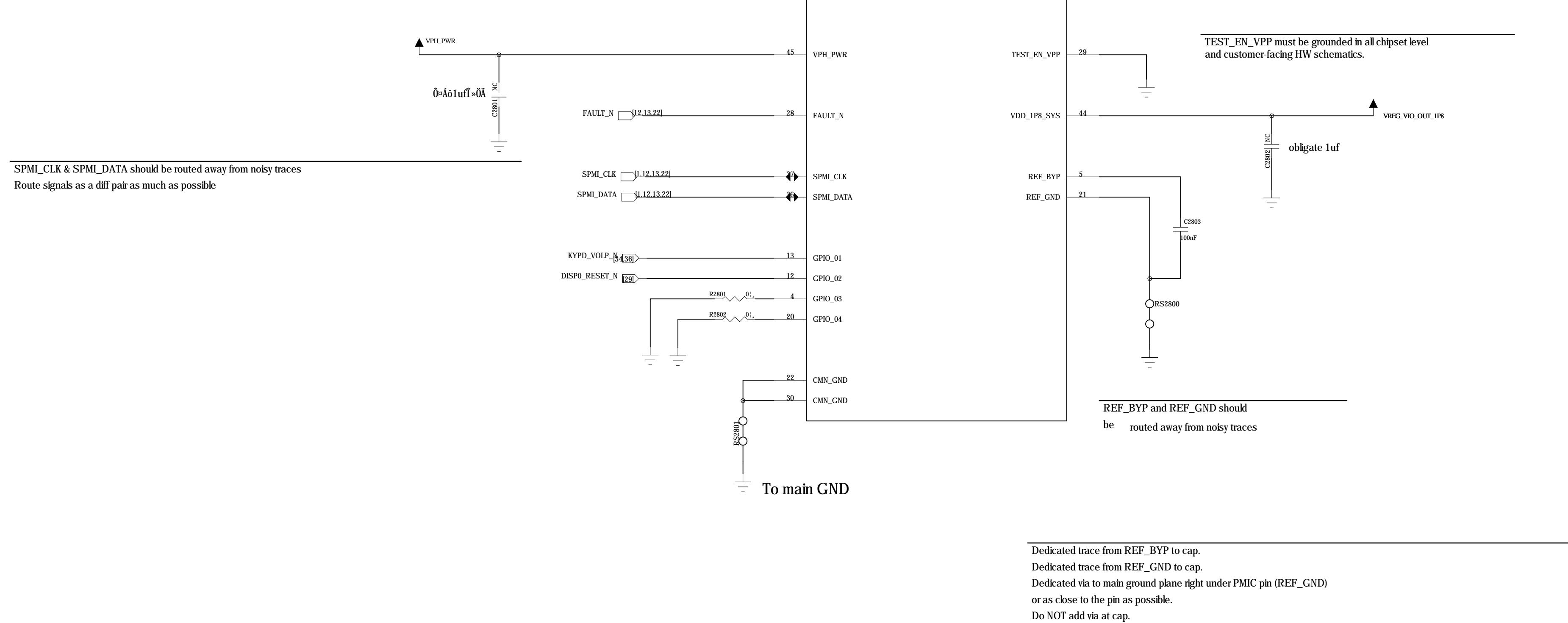
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DRAWN:	wejye.feng	DATED:	20210820
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

SM4350

CODE:	SIZE:	DRAWING NO:	REV:
P720AE	D	P720AE	V1.0
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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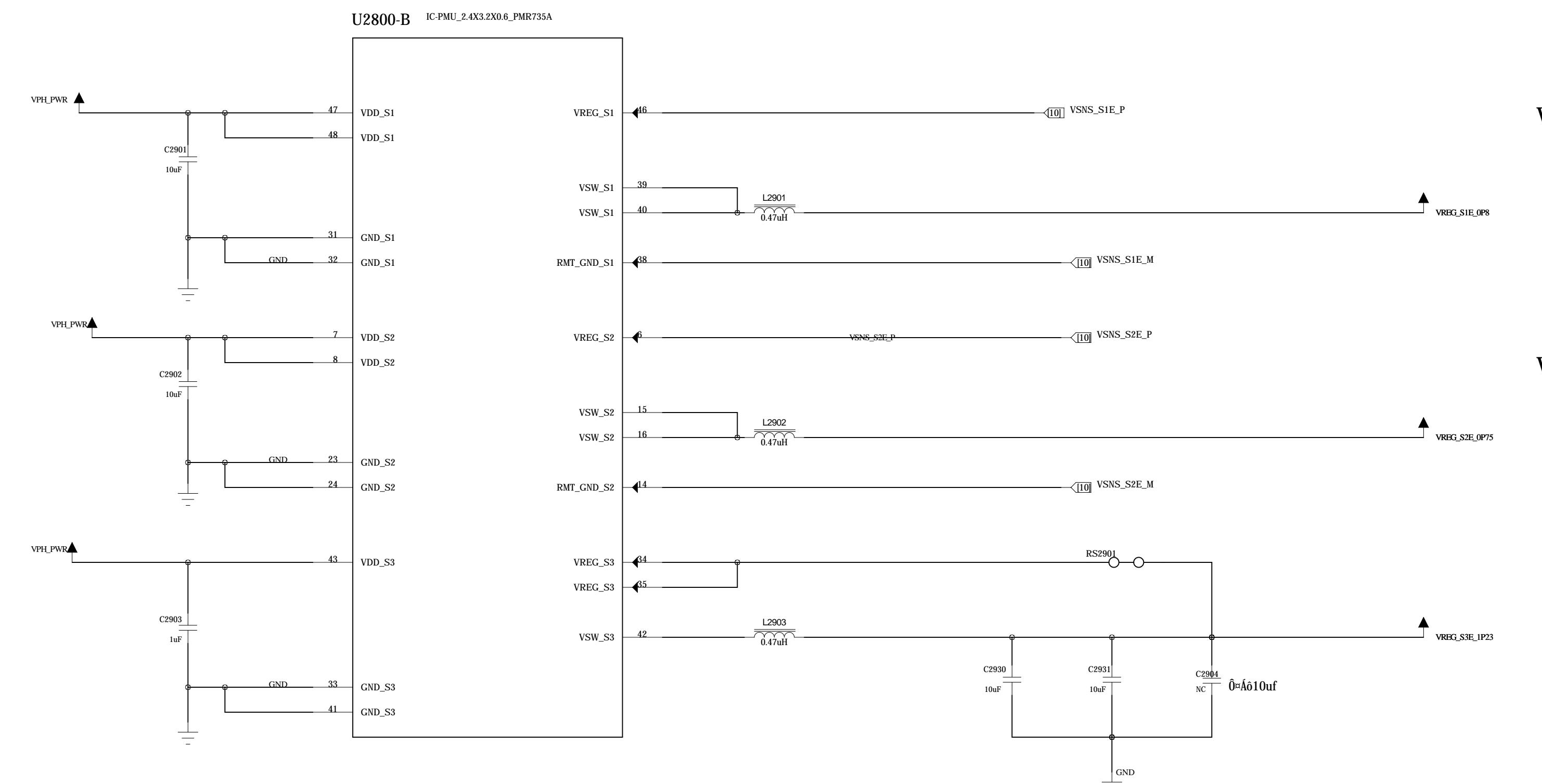
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Place Cin as close to PMIC as possible to achieve this requirement.

Loop inductance from (VIN_Sx to Cin + Cin to GND_Sx) must be < 3nH

COMPANY:	<Company Name>		
TITLE:	SM4350		
DRAWN:	weiye.feng	DATED:	20210820
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>
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P720AE	D	P720AE	V1.0
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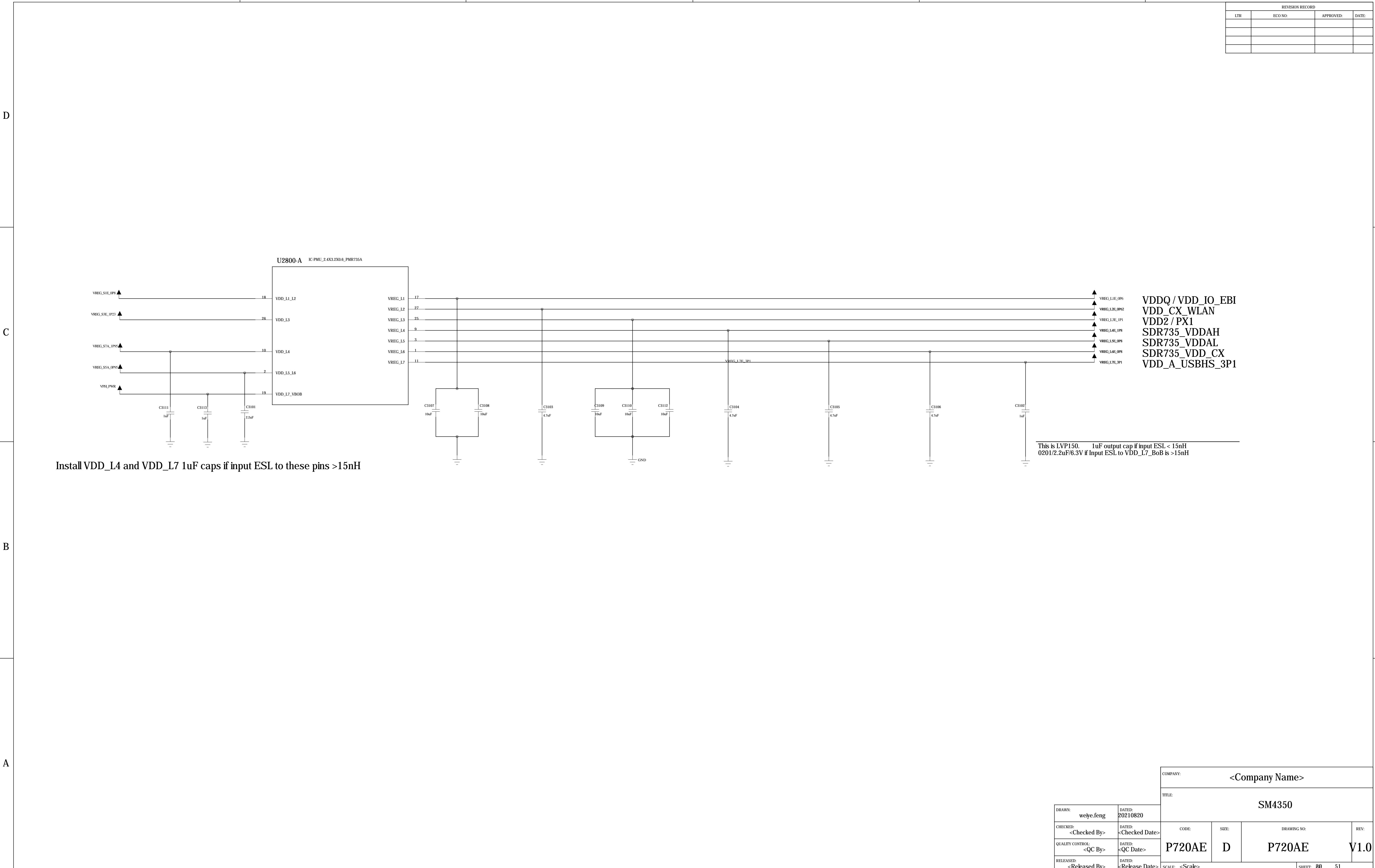
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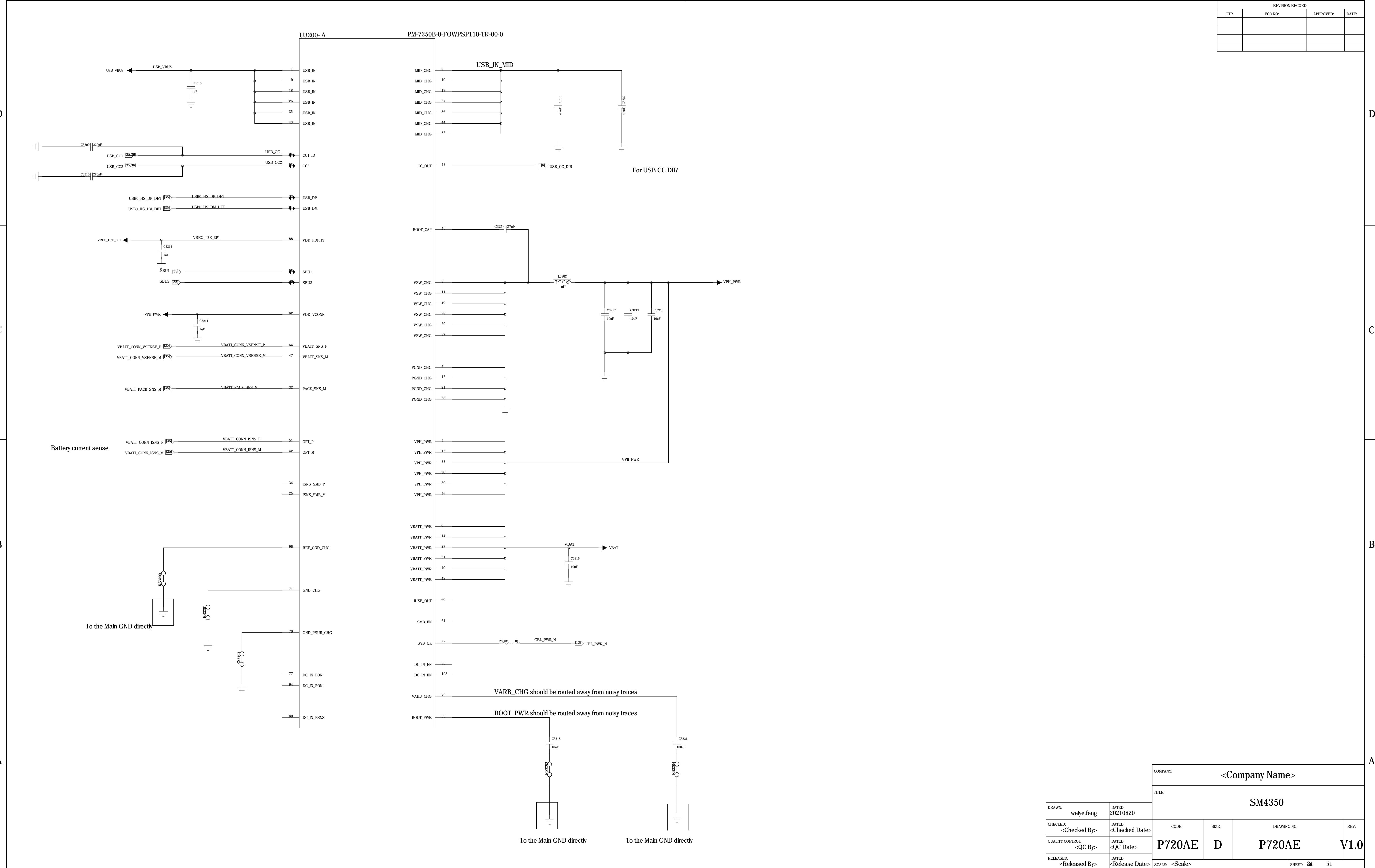
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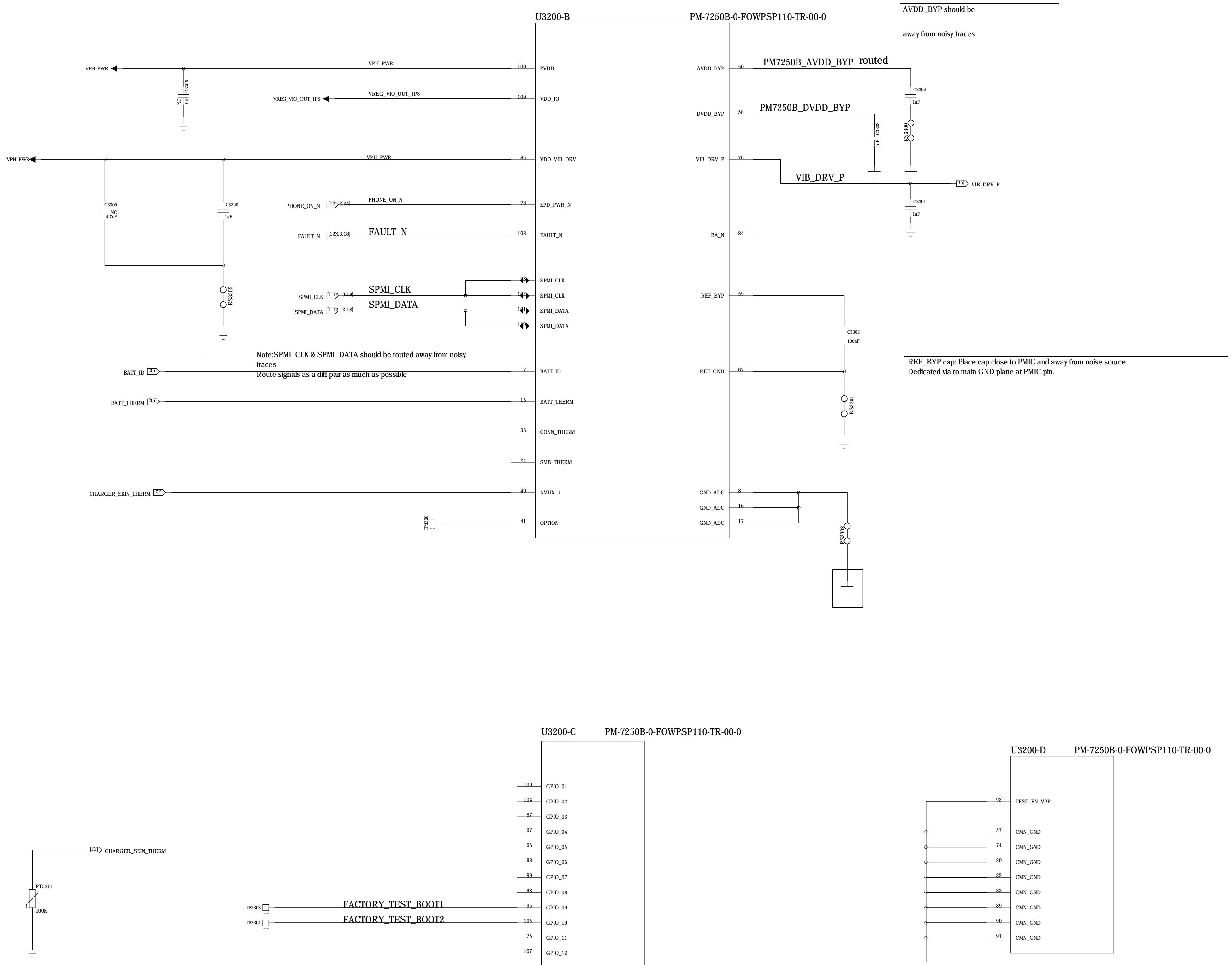
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REVISION RECORD			
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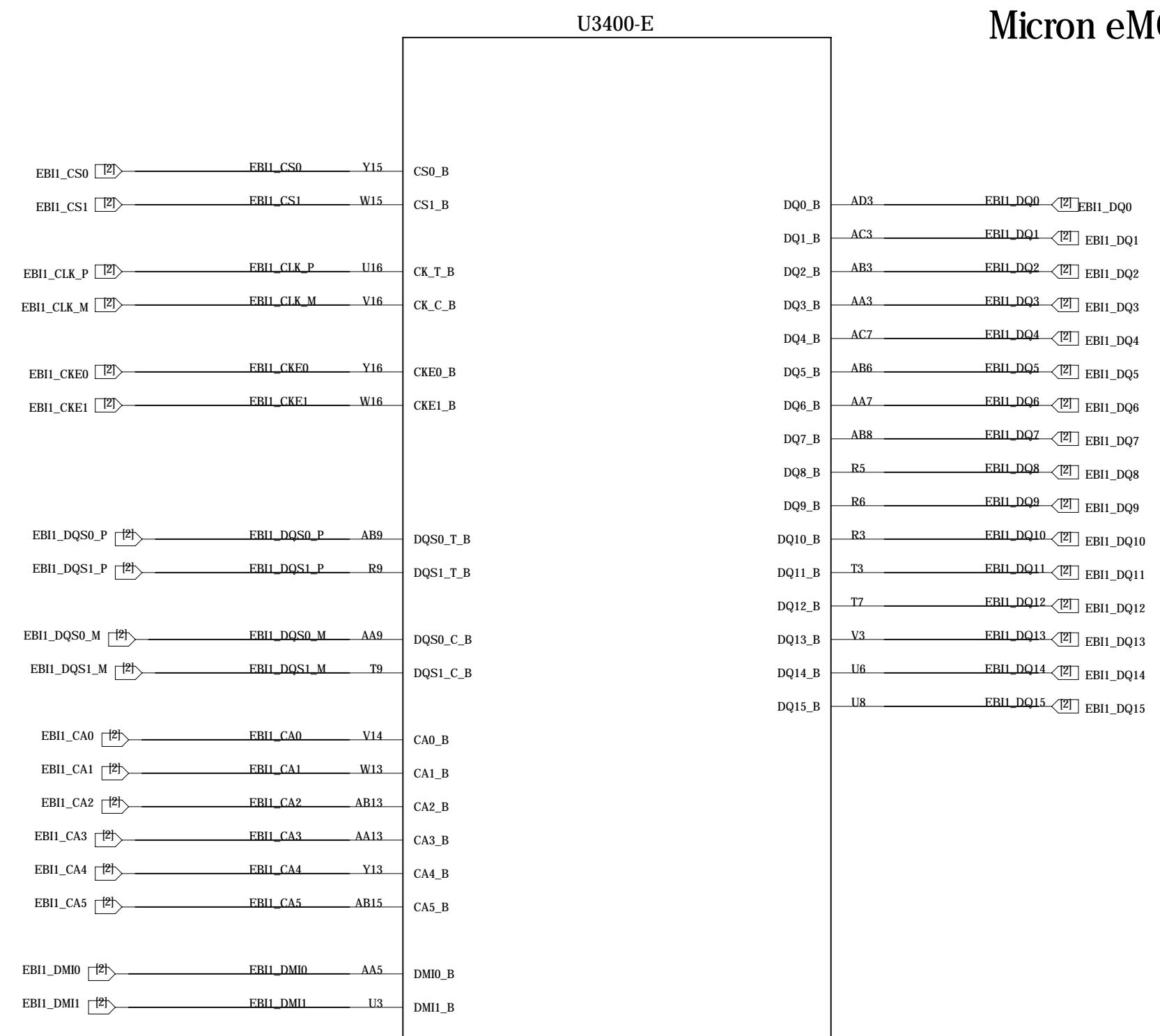


COMPANY: <Company Name>			
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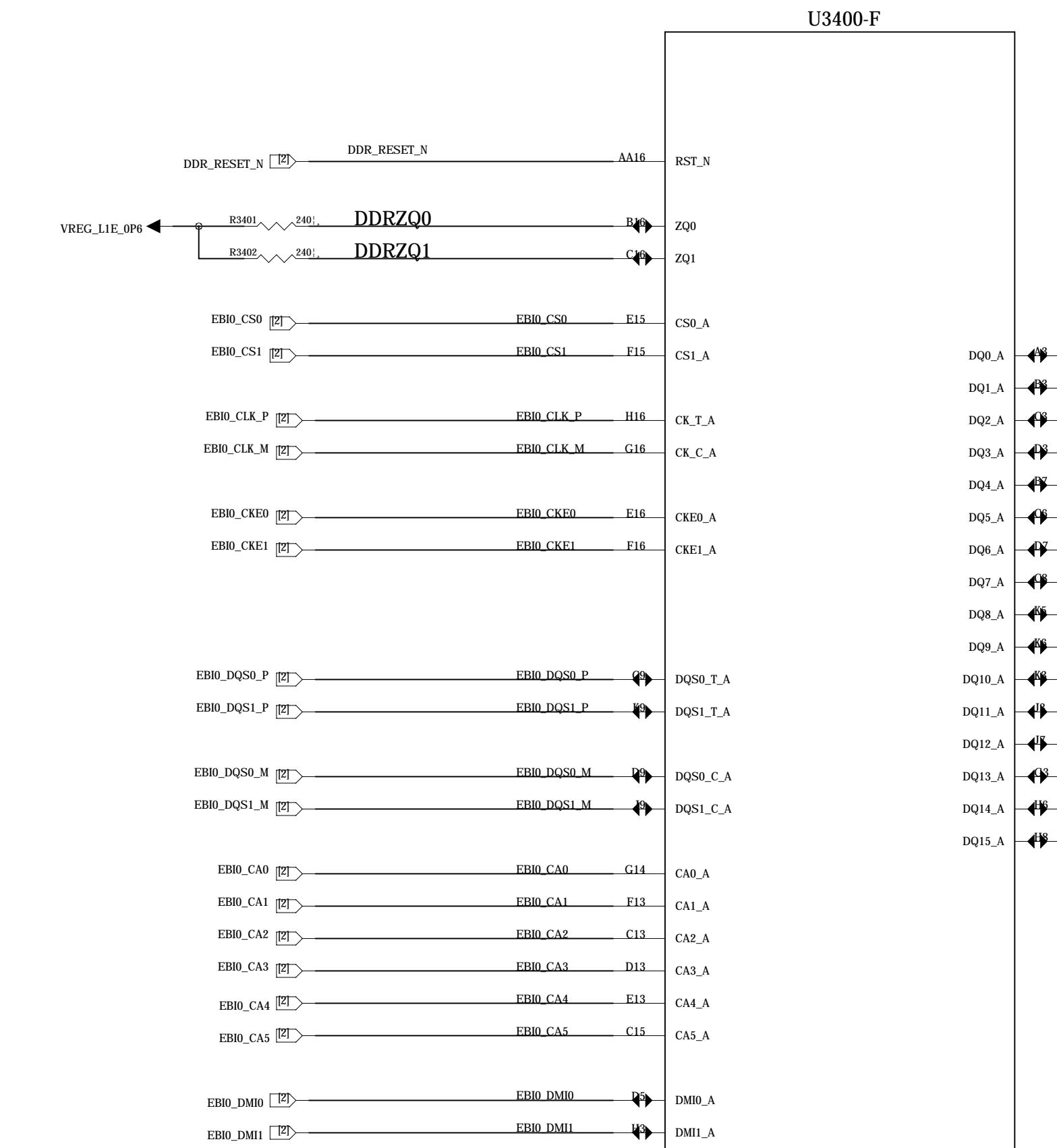
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SKHynix uMCP MCN
271-94865-0000(default)
Micron eMCP MCN 271-94866-0000

REVISION RECORD			
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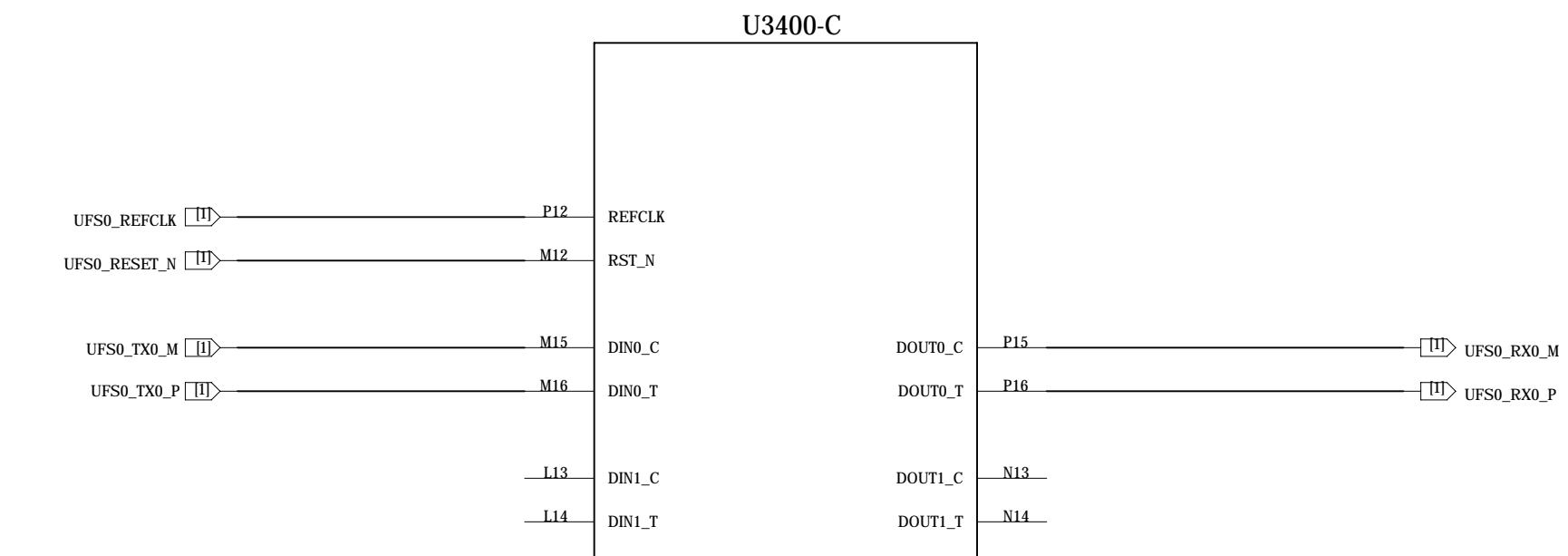
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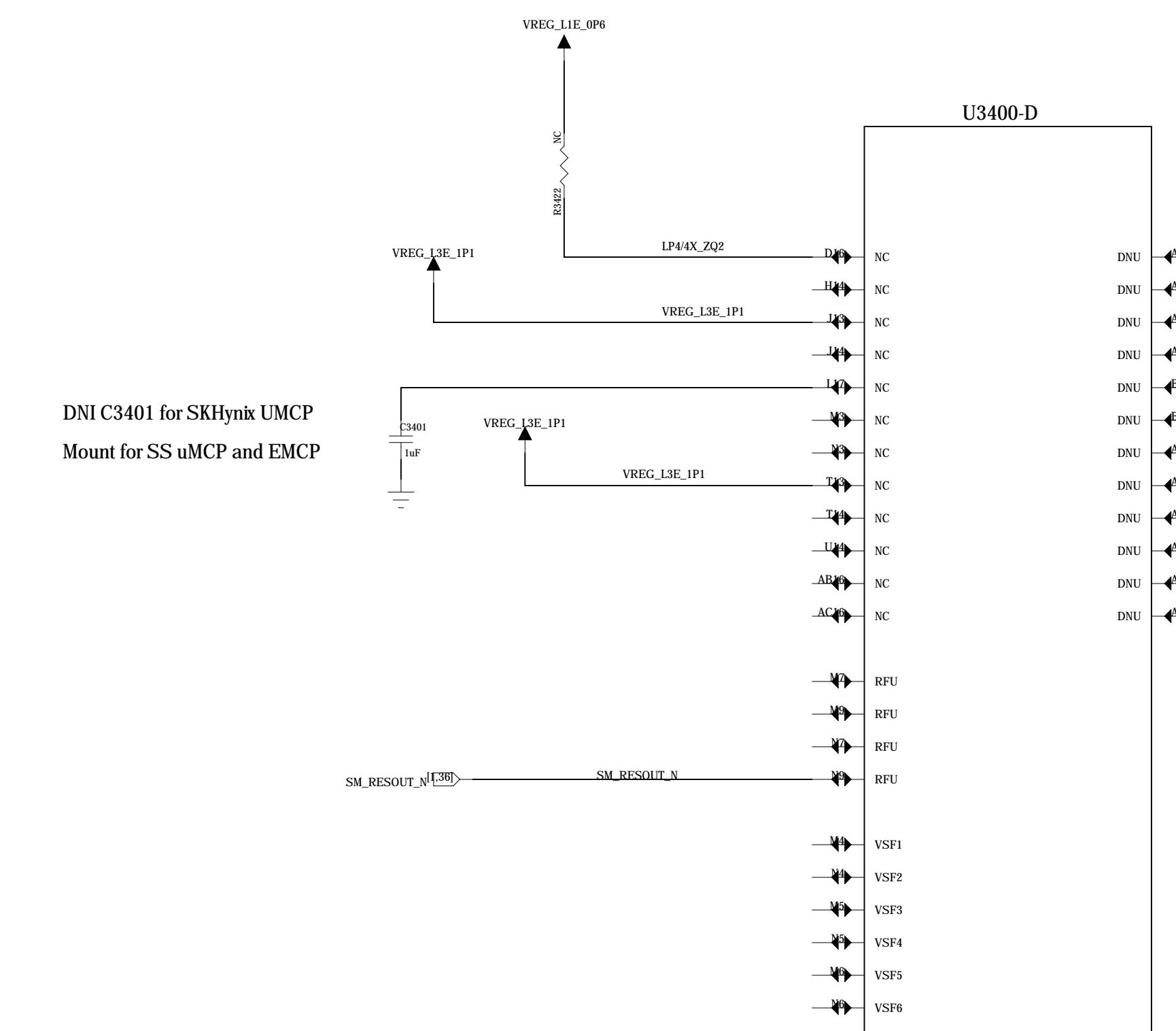
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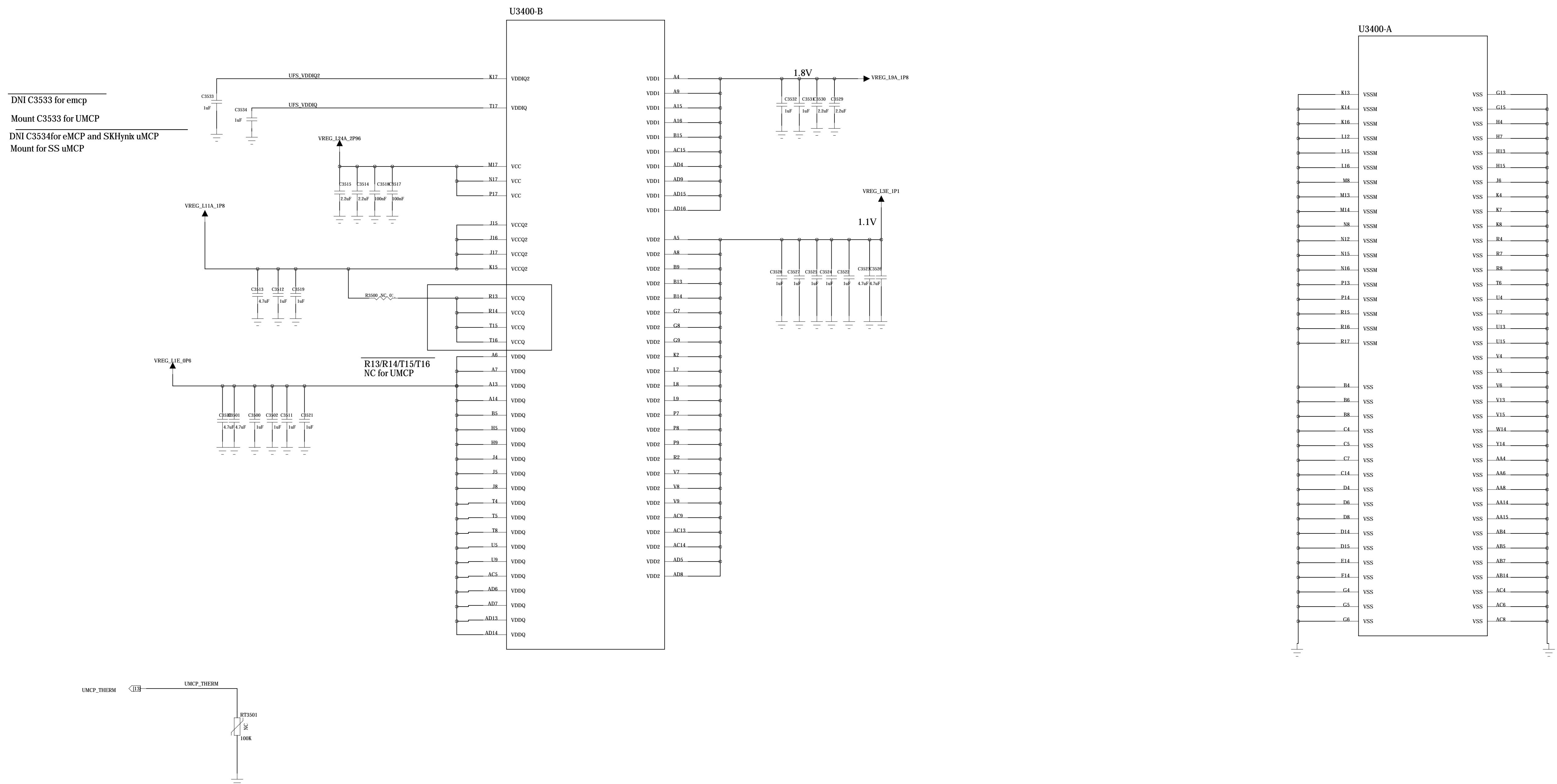
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CHECKED:	<Checked By>	DATED:	<Checked Date>
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RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY:	<Company Name>		
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P720AE	D	P720AE	V1.0

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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COMPANY: <Company Name>	TITLE: SM4350		
DRAWN: weiye.feng	DATED: 20210820	CODE: P720AE	SIZE: D
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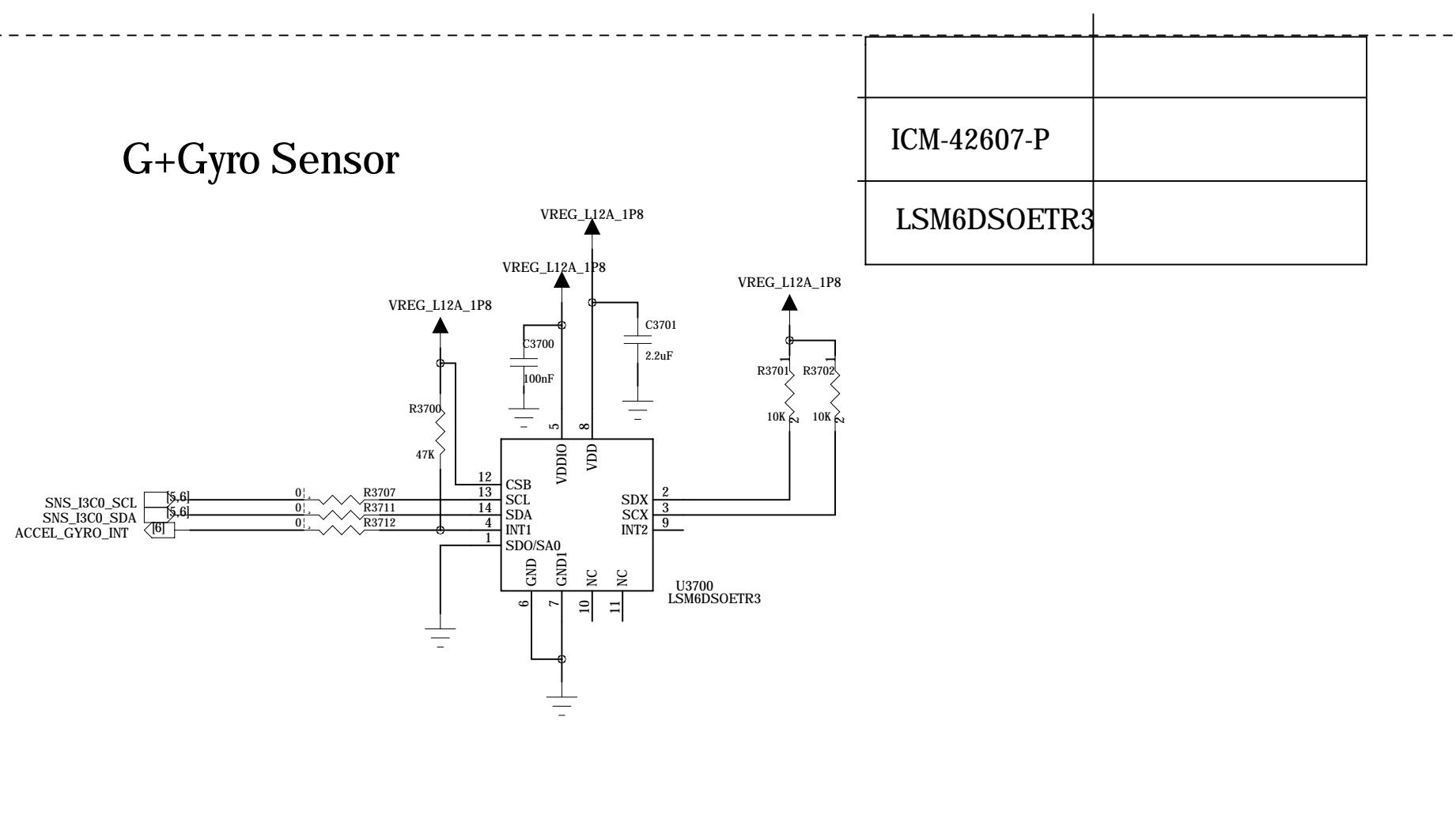
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REVISION RECORD			
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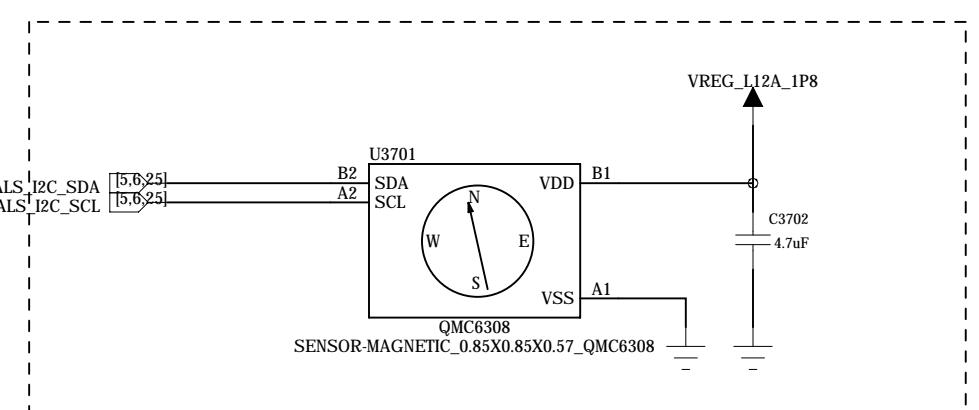
E-Compass



QMC6308	0x2C//58H(W)/59(R)

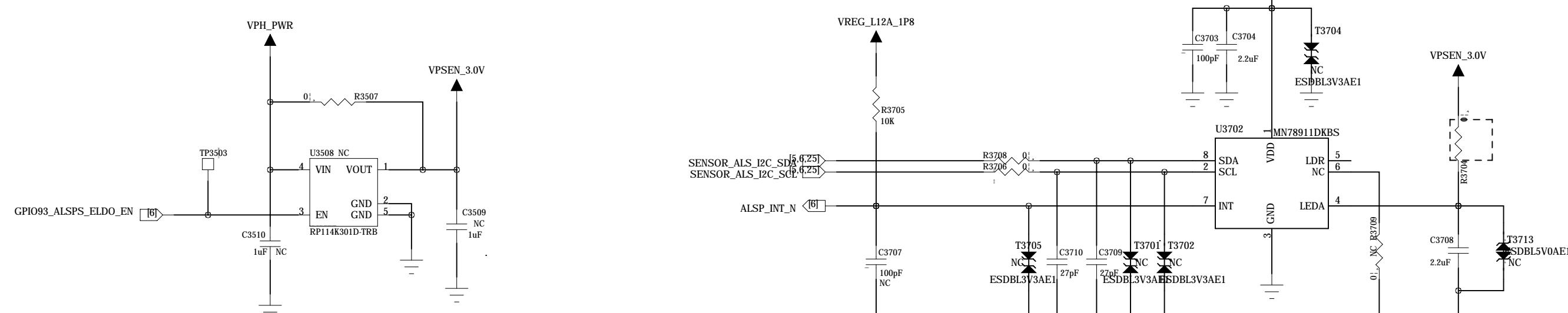
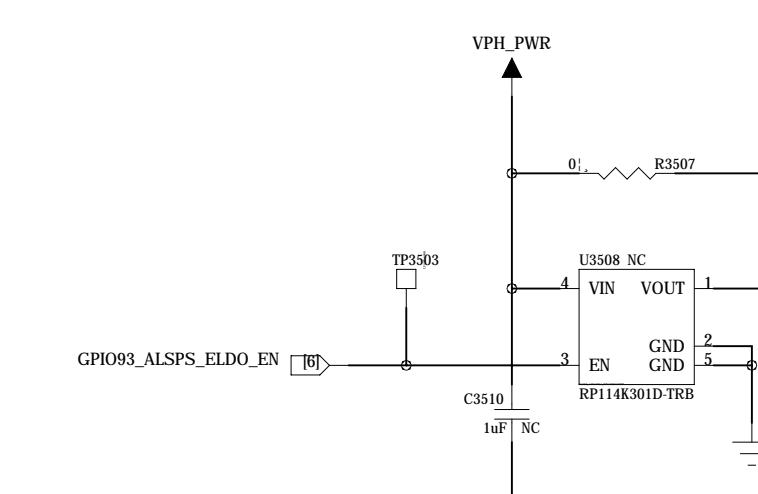
Note: 77-1

Note: 77-6



Proximity Sensor charge

	I2C addr
MN78911DKBS	0x82(w)/0x83(r)



Schematic design notice of "77_PERI_SENSORS_MEMs_ALS/PS" page.

[M sensor] Keep a minimum distance of 15mm from power ICs / PCB traces of more than 100mA / magnet component. Check HW design notice for more detail

[A+G] For optimized GPS performance, please check HW design notice for Sensor selection guide

[A+G] MUST use SPI for optimized sensor hub performance DO NOT USE I2C

A [A+G] Suggest choose sensor support FIFO watermark interrupt otherwise we cannot support Hifi-sensor, daydream VR. And Sensor-location accuracy will become worse.

[Baro] Reserve Baro sensor for LPPe feautre (Must for North America Operator / NA SKU)

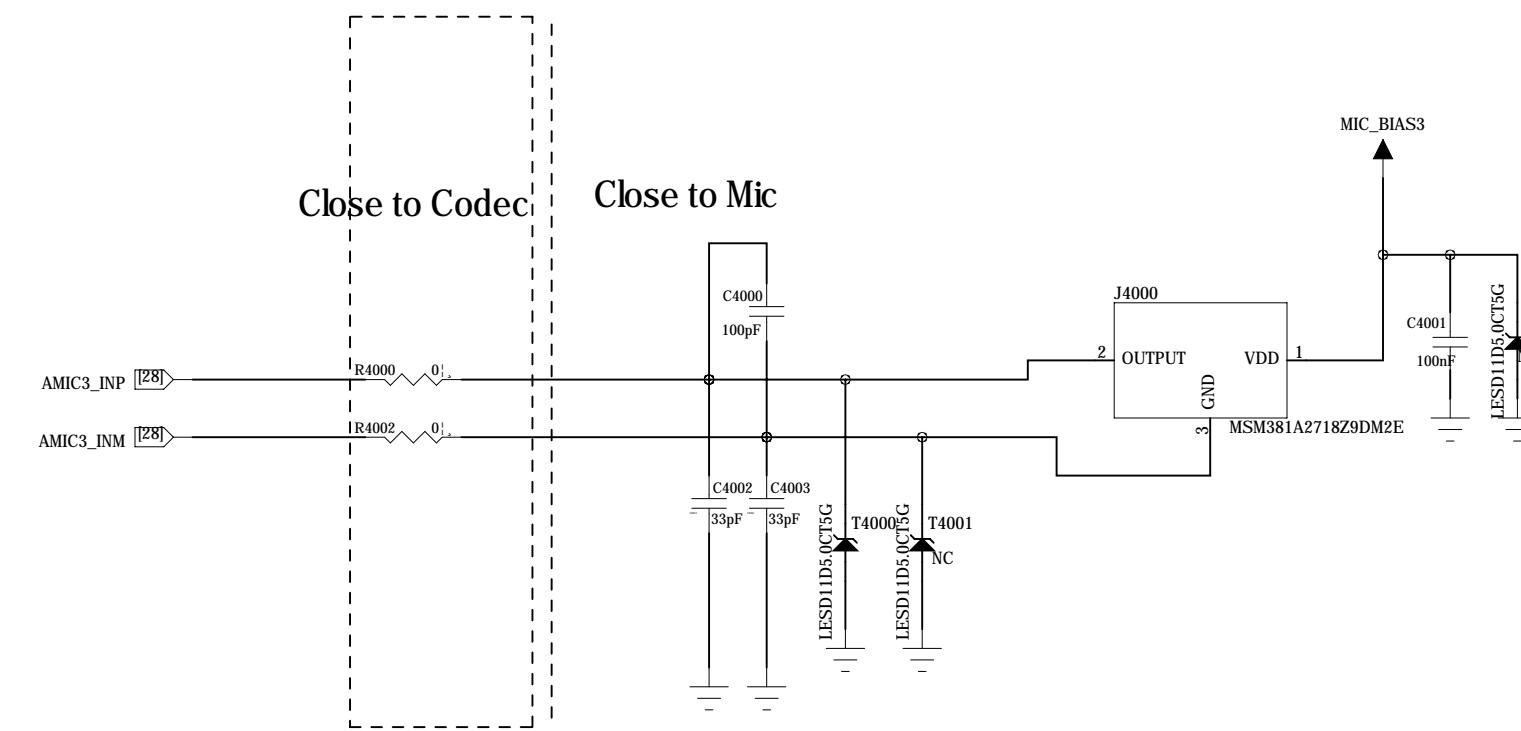
DO NOT share Sensor hub I2C to other non-SCP device

Interrupt pin of MEMS sensor must be assign to ball EINT[12:0]

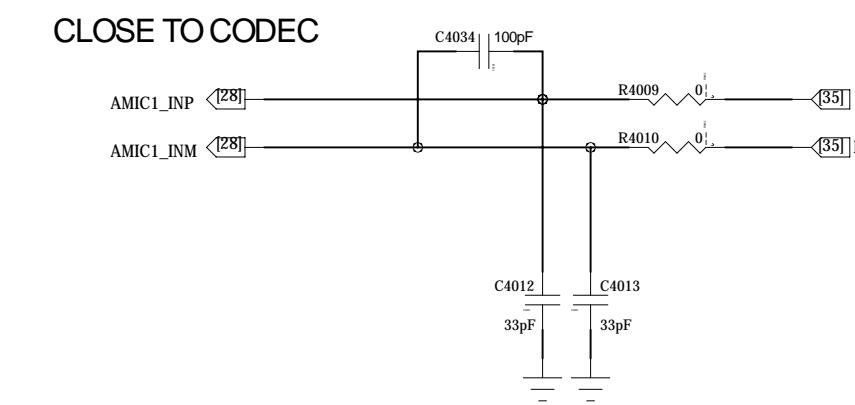
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TITLE: SM4350
DRAWN: weiye.feng DATED: 20210820
CHECKED: <Checked By> DATED: <Checked Date>
QUALITY CONTROL: <QC By> DATED: <QC Date>
RELEASED: <Released By> DATED: <Release Date>
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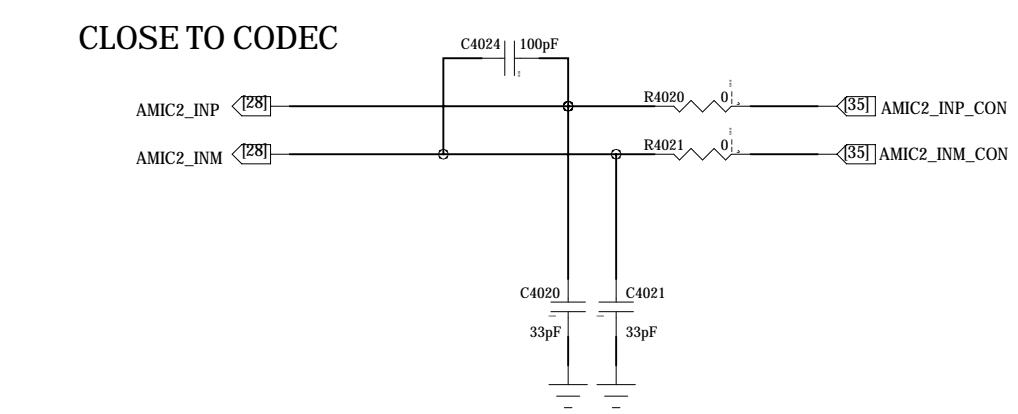
Sub Mic



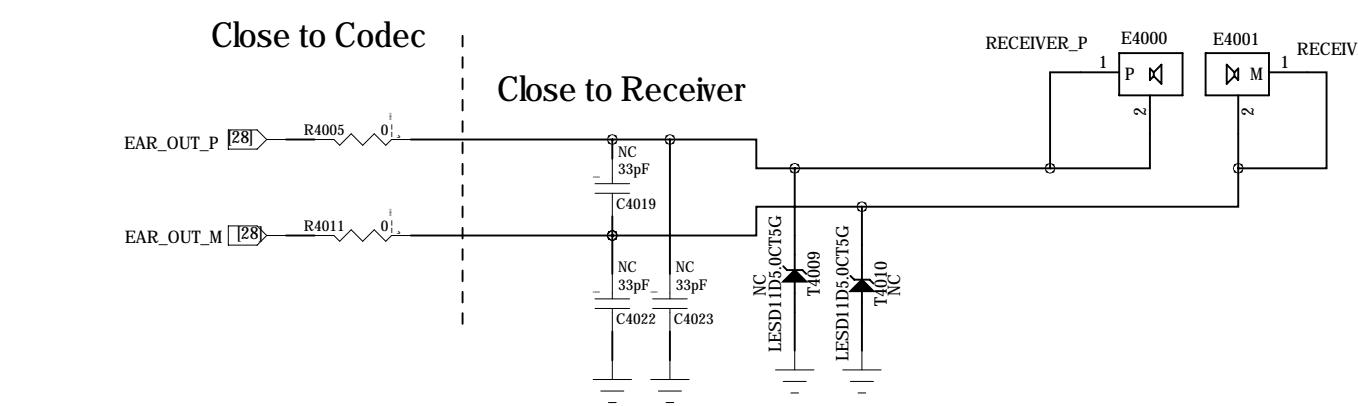
Main Mic



Headset Mic



Handset Receiver

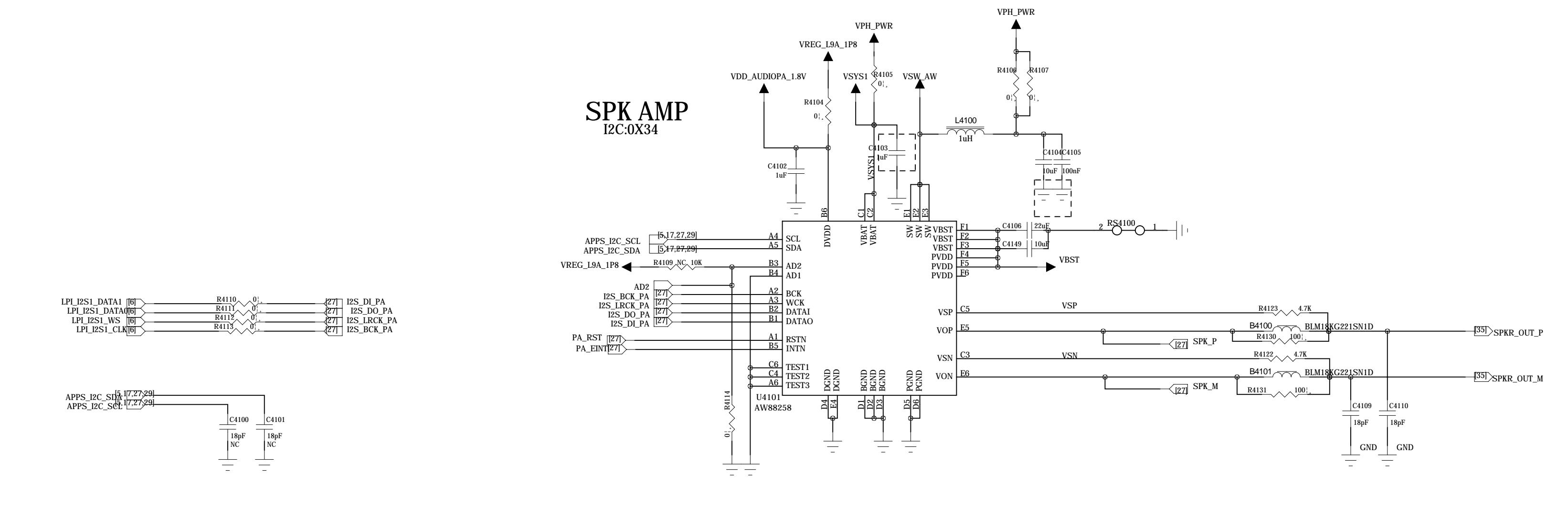


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DRAWN: weiyefeng	DATE: 20210820	CODE: P720AE	SIZE: D
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO.: P720AE	REV: V1.0
QUALITY CONTROL: <QC By>	DATED: <QC Date>		
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REVISION RECORD			
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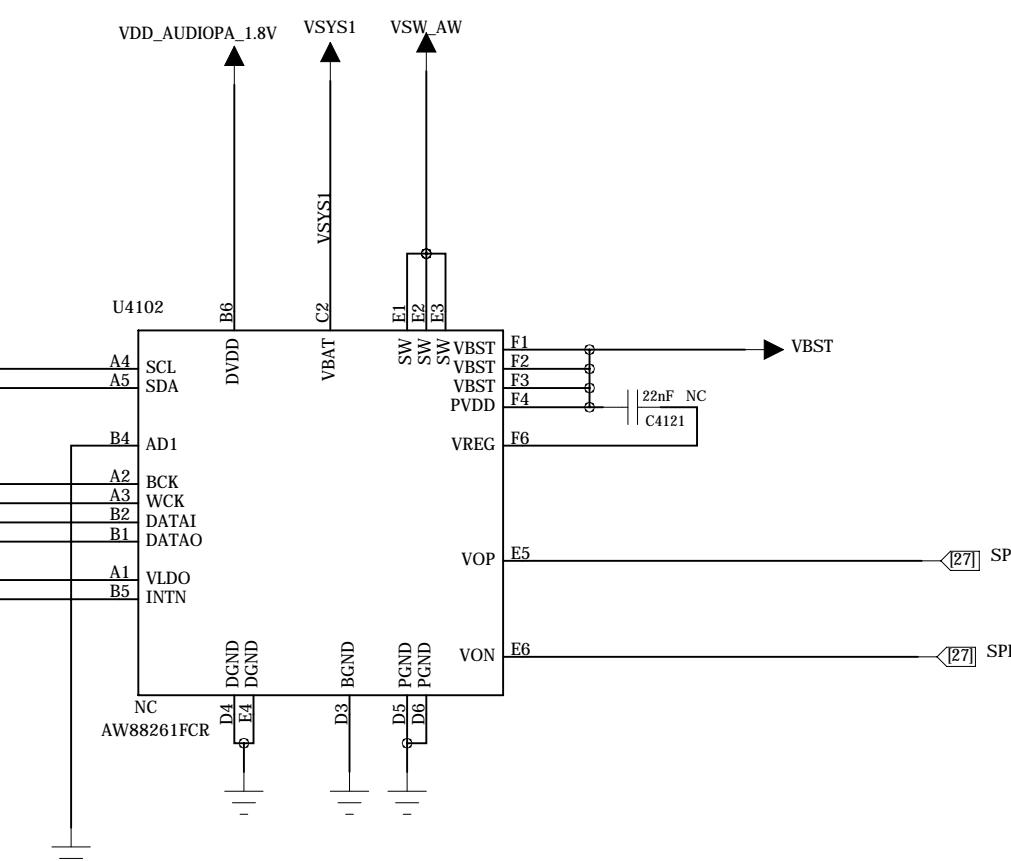
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Note:	C4121	C4120	R4123	R4122	R4124
AW88258CSR	NC	NC	mount	mount	mount
AW88261FCR	mount	mount	NC	NC	NC



E

B

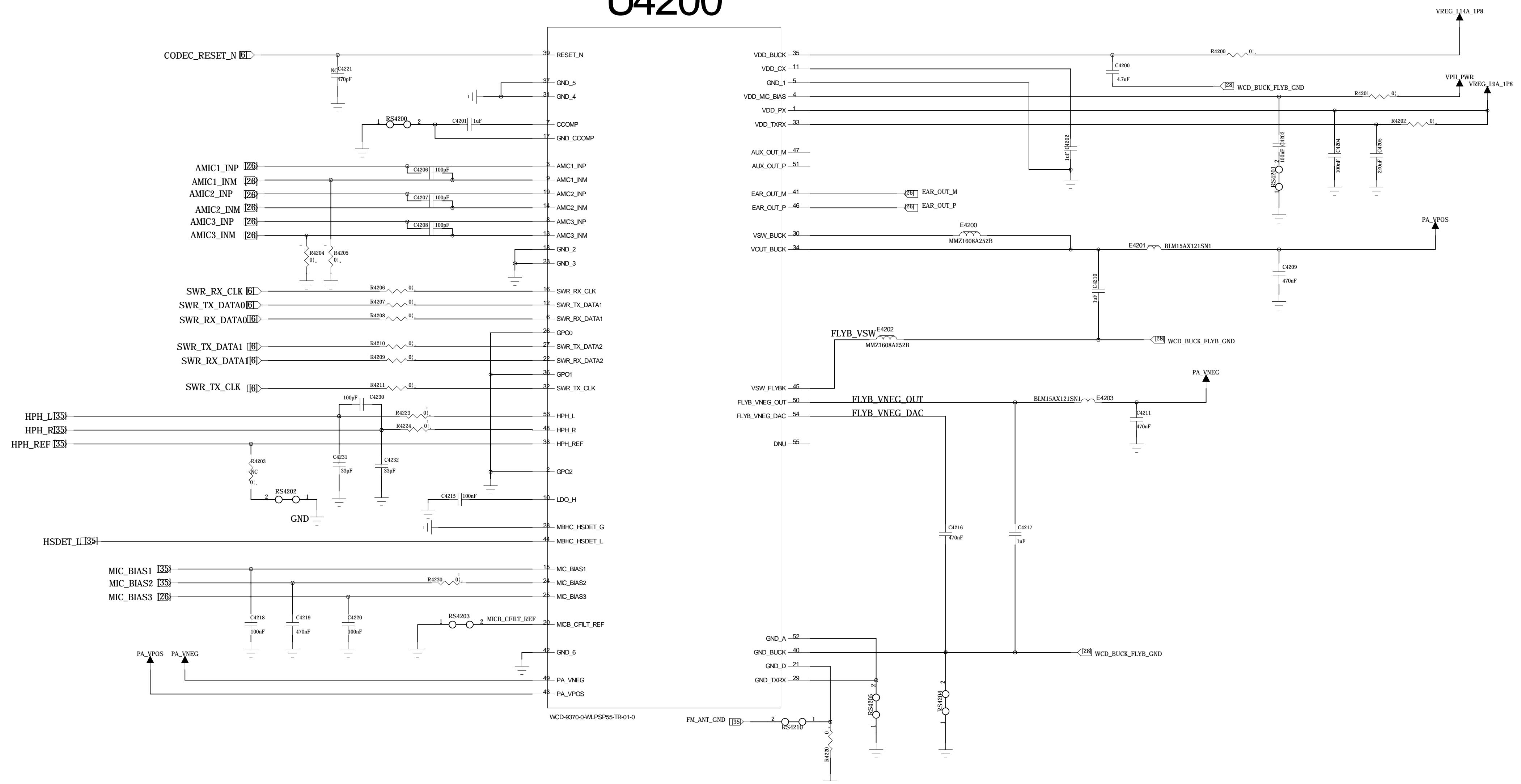
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VN: weiye.feng	DATED: 20210820	CODE: P720AE	SIZE: D	DRAWING NO: P720AE	REV: V1.0
KED: <Checked By>	DATED: <Checked Date>				
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LTR	ECO NO:	APPROVED:	DATE:

U4200



COMPANY: <Company Name>			
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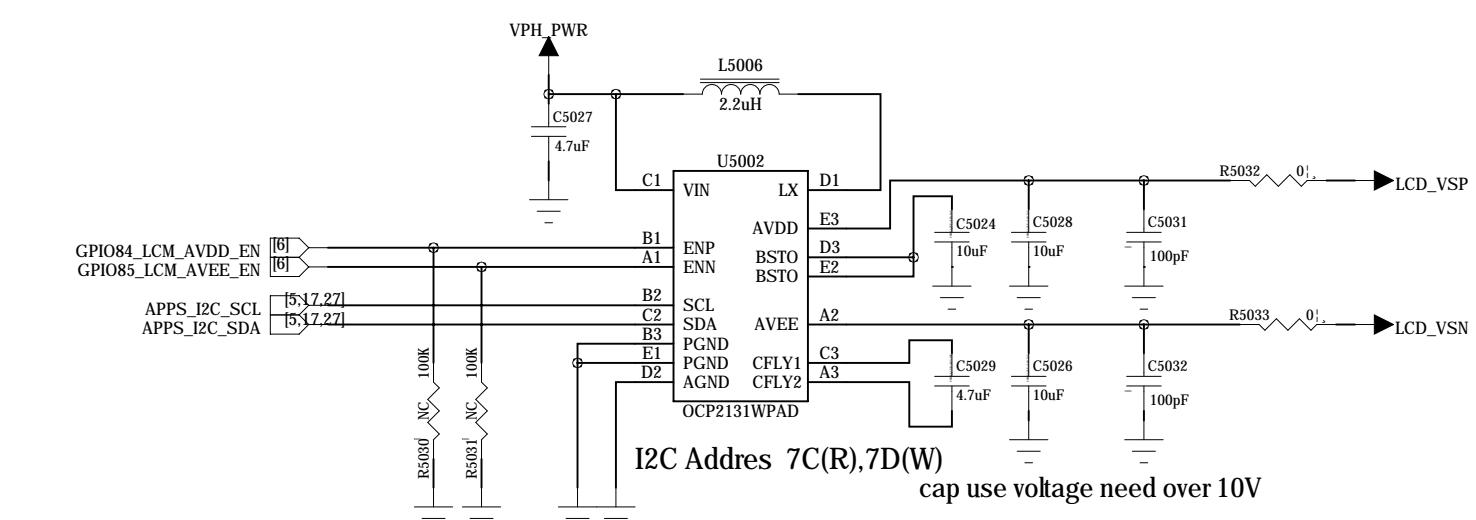
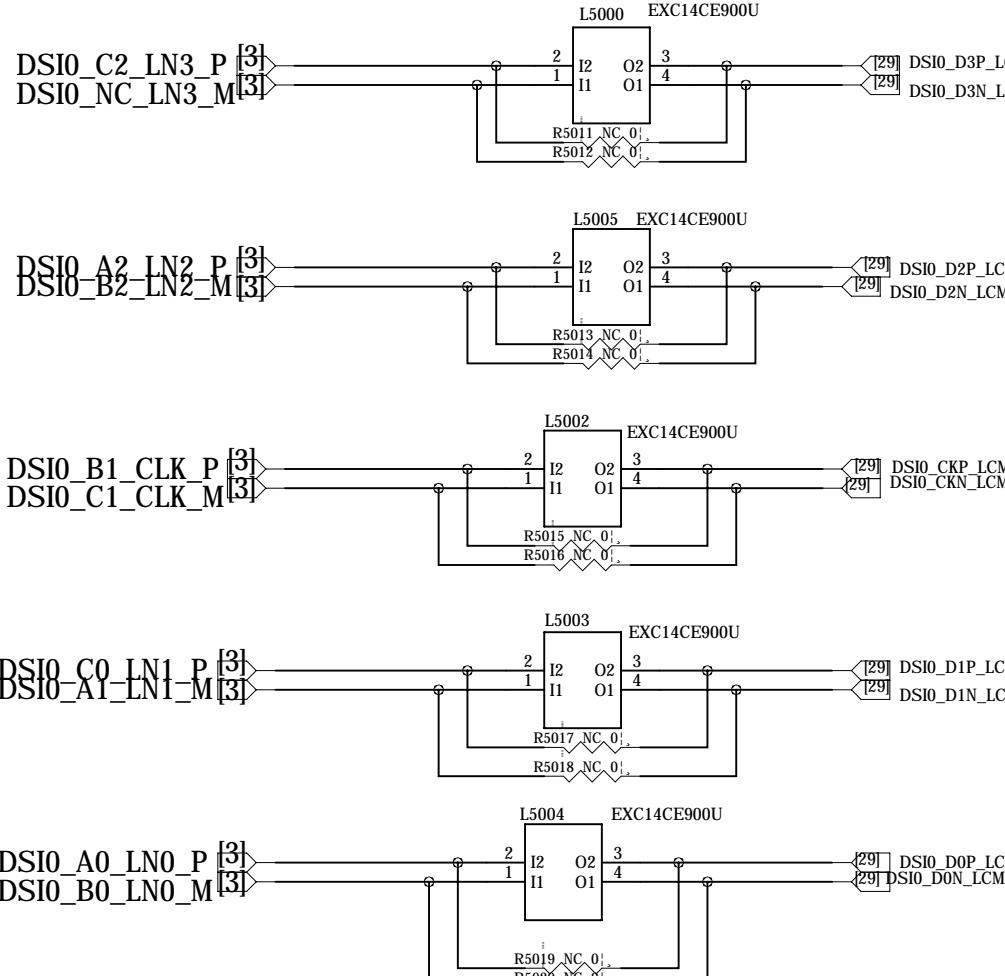
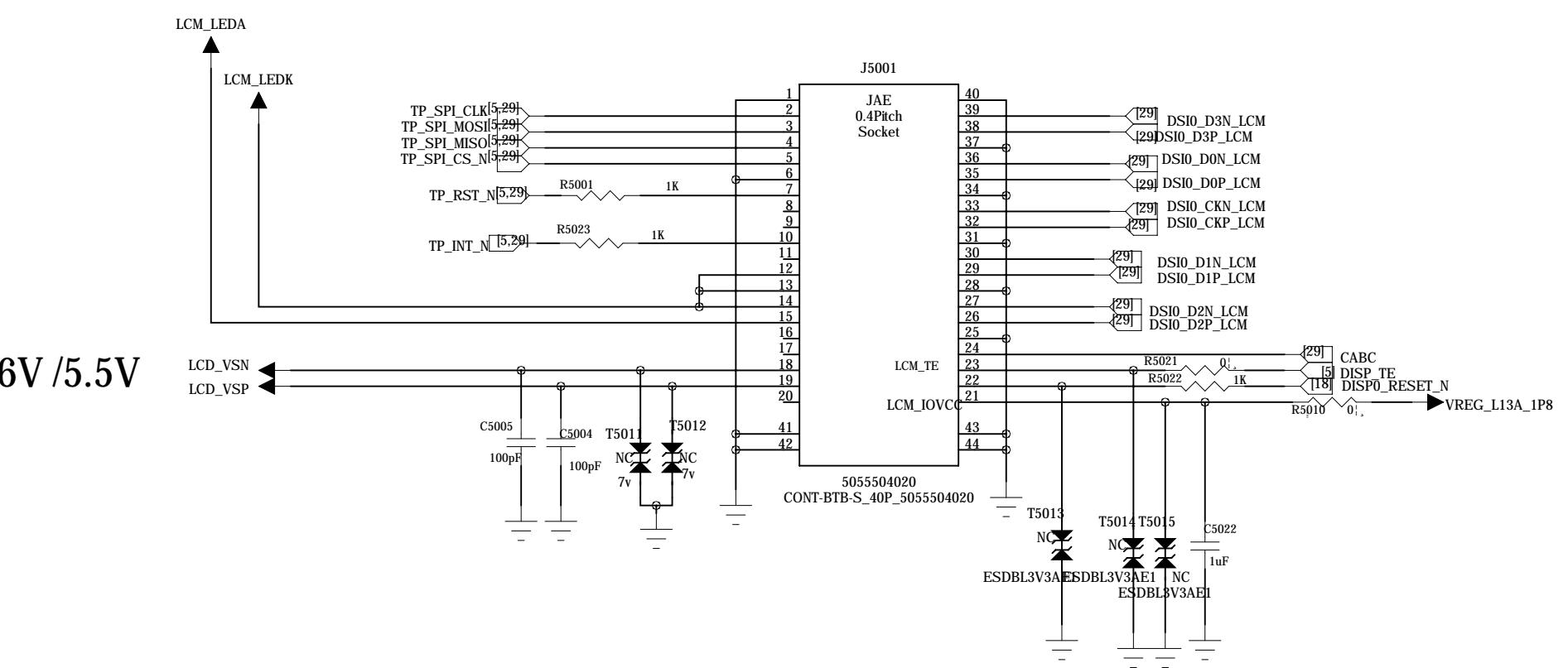
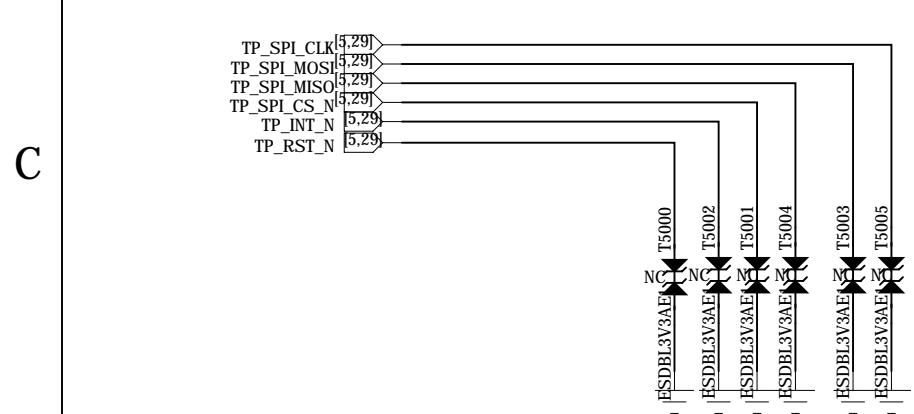
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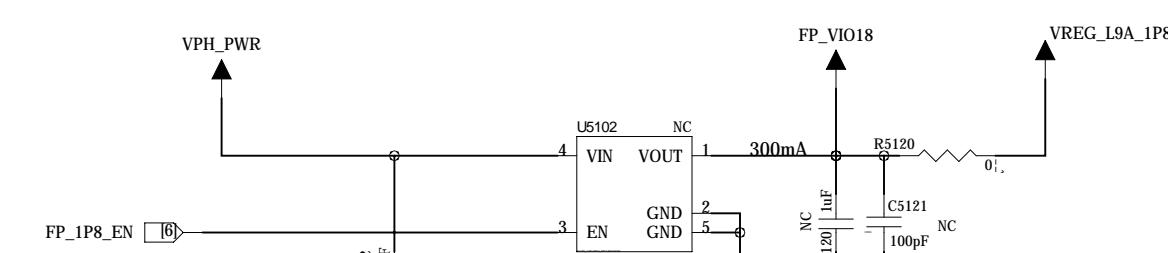
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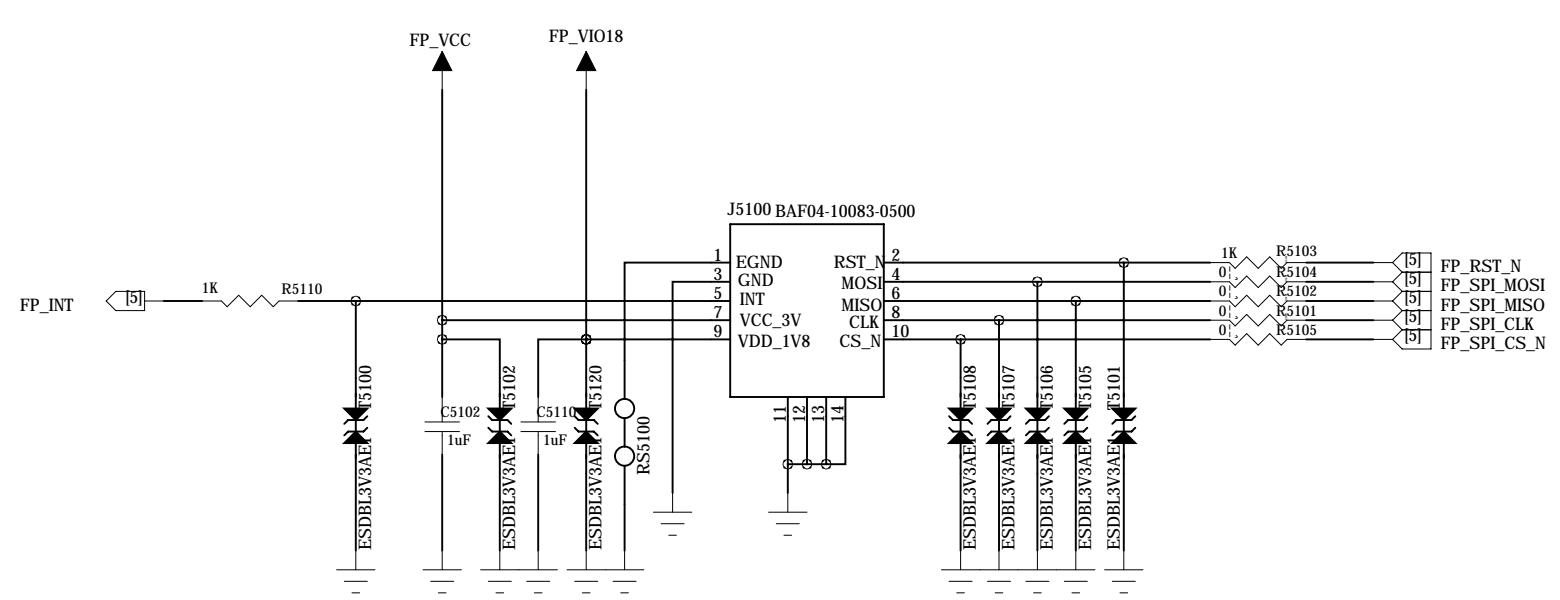
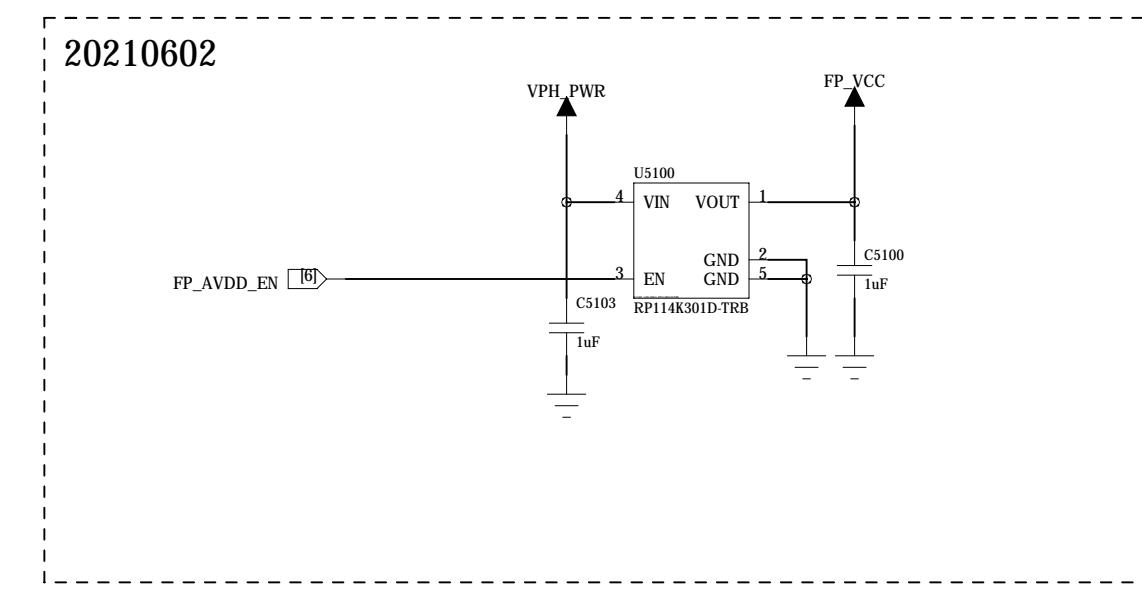
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Fingerprint Connector



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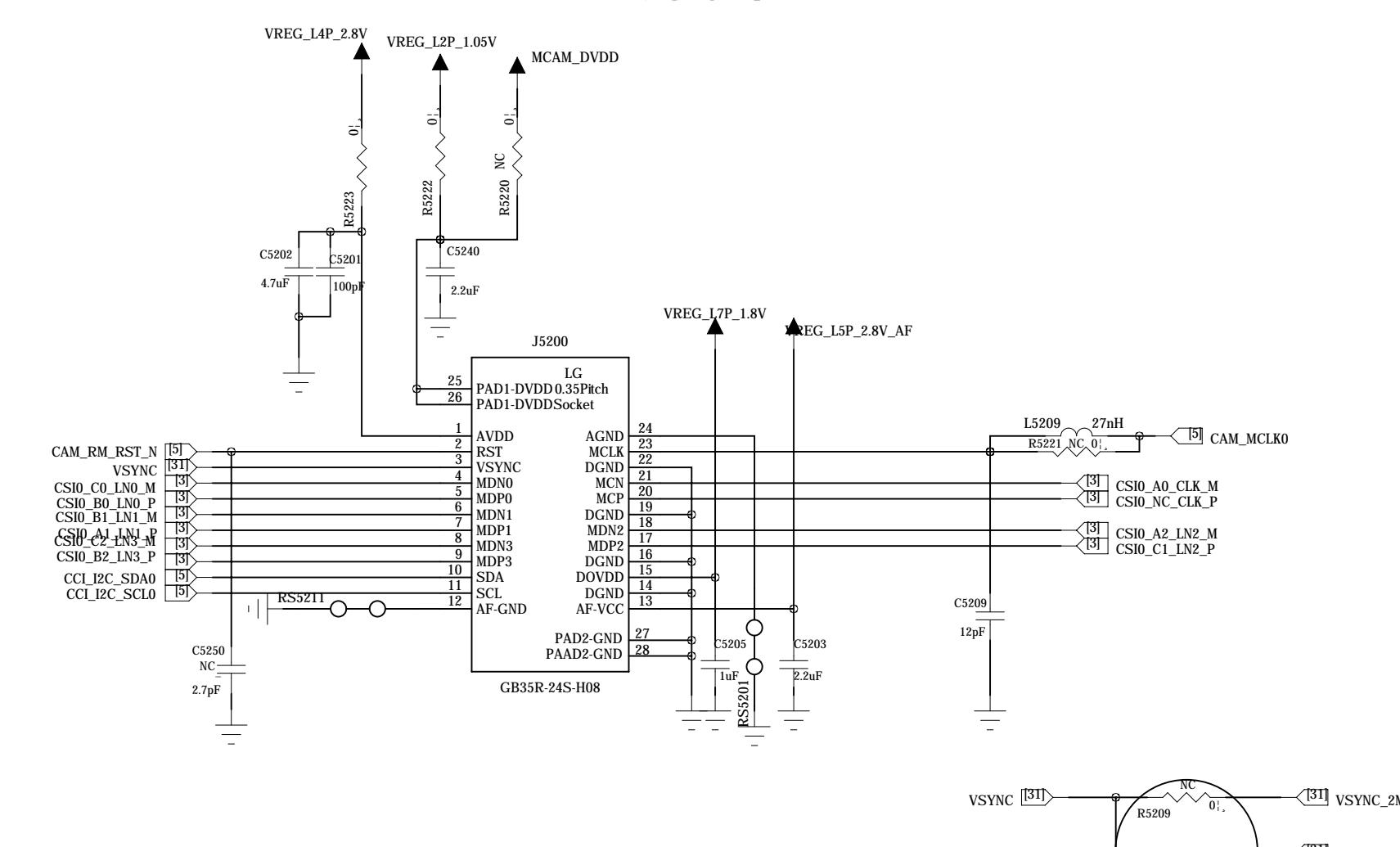
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QUALITY CONTROL: <QC By>	DATED: <QC Date>	REV: V1.0	
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 30 51

Main-CAMERA

50M	AVDD	DVDD	DOVDD	AFVDD	ADDR
S5KJN1SQ03	2.8V	1.05V	1.8V	2.8V	0xAC(W)/0xAD(R)
DRIVER					0x18W/0x19R
EEPROM					0xA0W/0xA1R
64M	AVDD	DVDD	DOVDD	AFVDD	ADDR
OV64B40-GA5A-002A	2.8V	1.1V	1.8V	2.8V	0x44(W)/0x45(R)
DRIVER					0x18W/0x19R
EEPROM					0xA0W/0xA1R

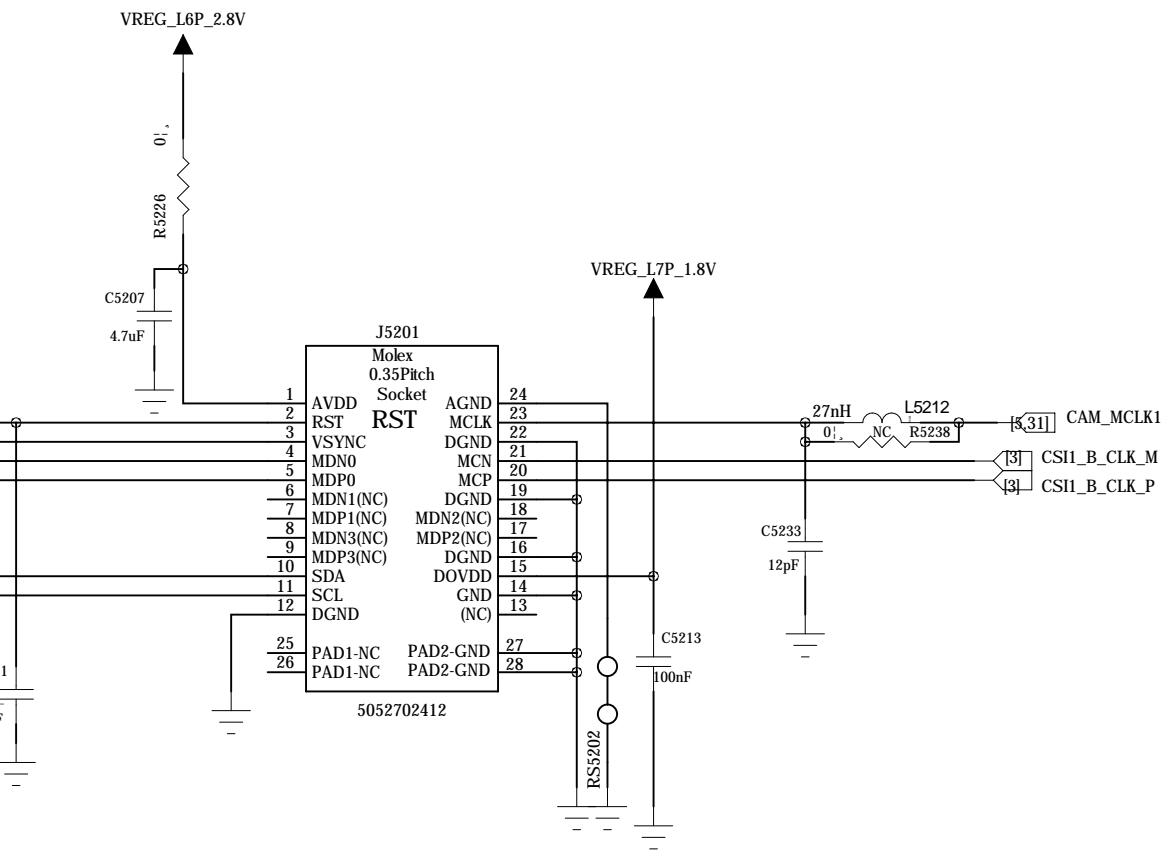
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1.05V



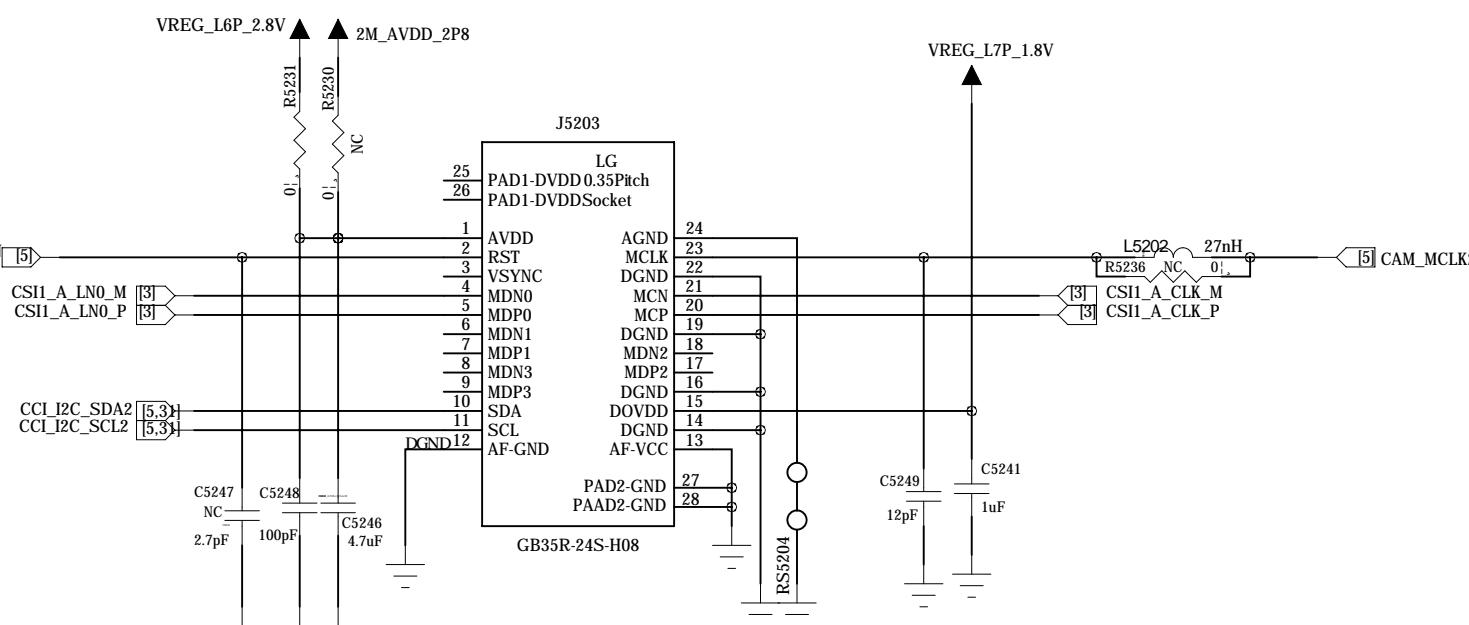
2M FF-Depth Side

	AVDD	DVDD	DOVDD		I2C
OVO2B1B-A25A-001A	2.8V	NA	1.8V		0X7A(W)/0X7B(R)



2M FF-MACRO

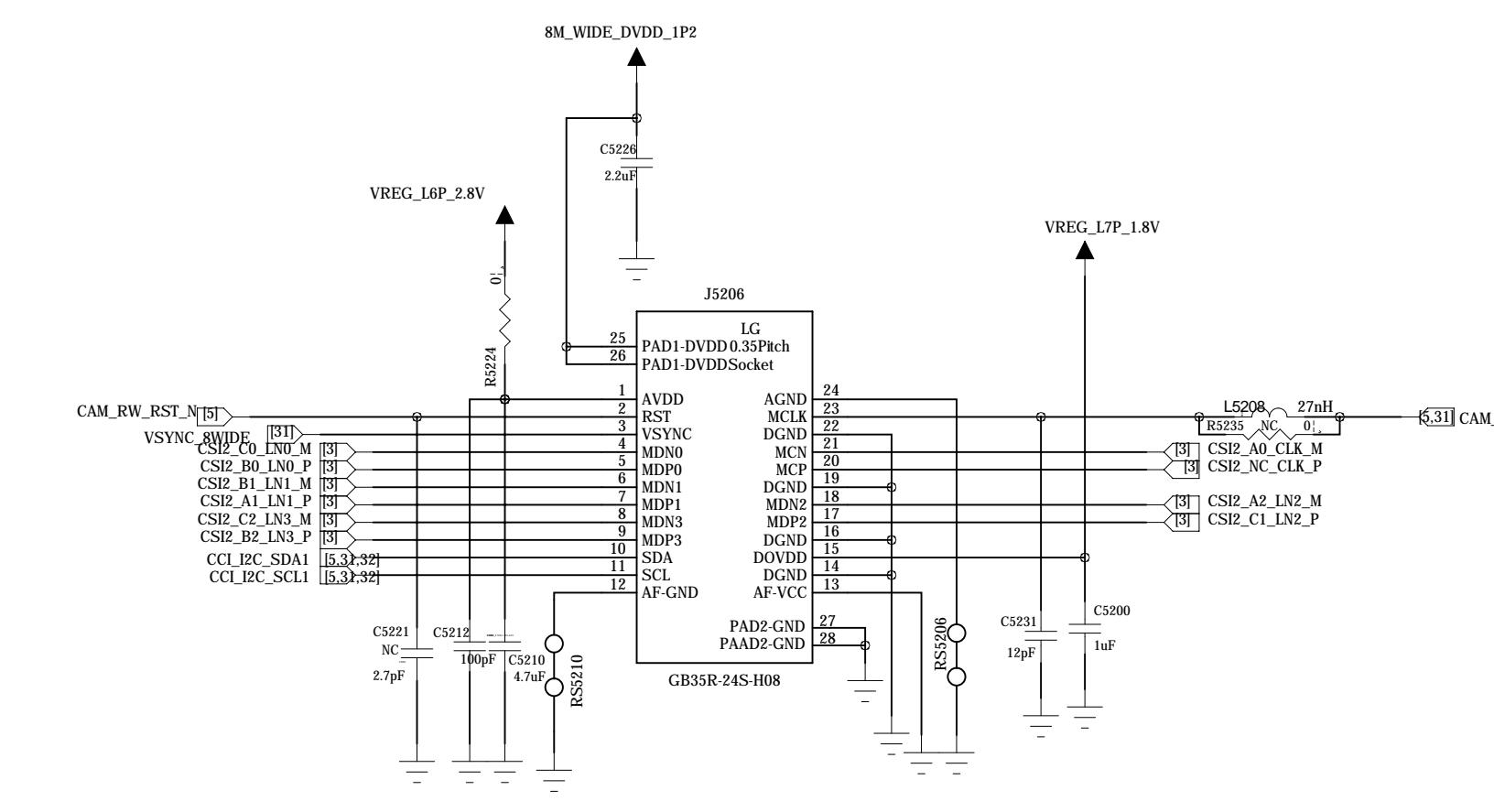
	AVDD	DVDD	DOVDD		I2C
OVO2B1B-A25A-001A	2.8V	NA	1.8V		0X7A(W)/0X7B(R)



Wide-CAMERA

8M-FF	AVDD	DVDD	DOVDD	AFVDD	ADDR
S5K4H7YX03-FGX9	2.8V	1.2V	1.8V	N/A	0X5A(W)/0X5B(R)
8M-AF	AVDD	DVDD	DOVDD	AFVDD	ADDR
S5K4H7YX03-FGX9	2.8V	1.2V	1.8V	2.8V	0X5A(W)/0X5B(R)

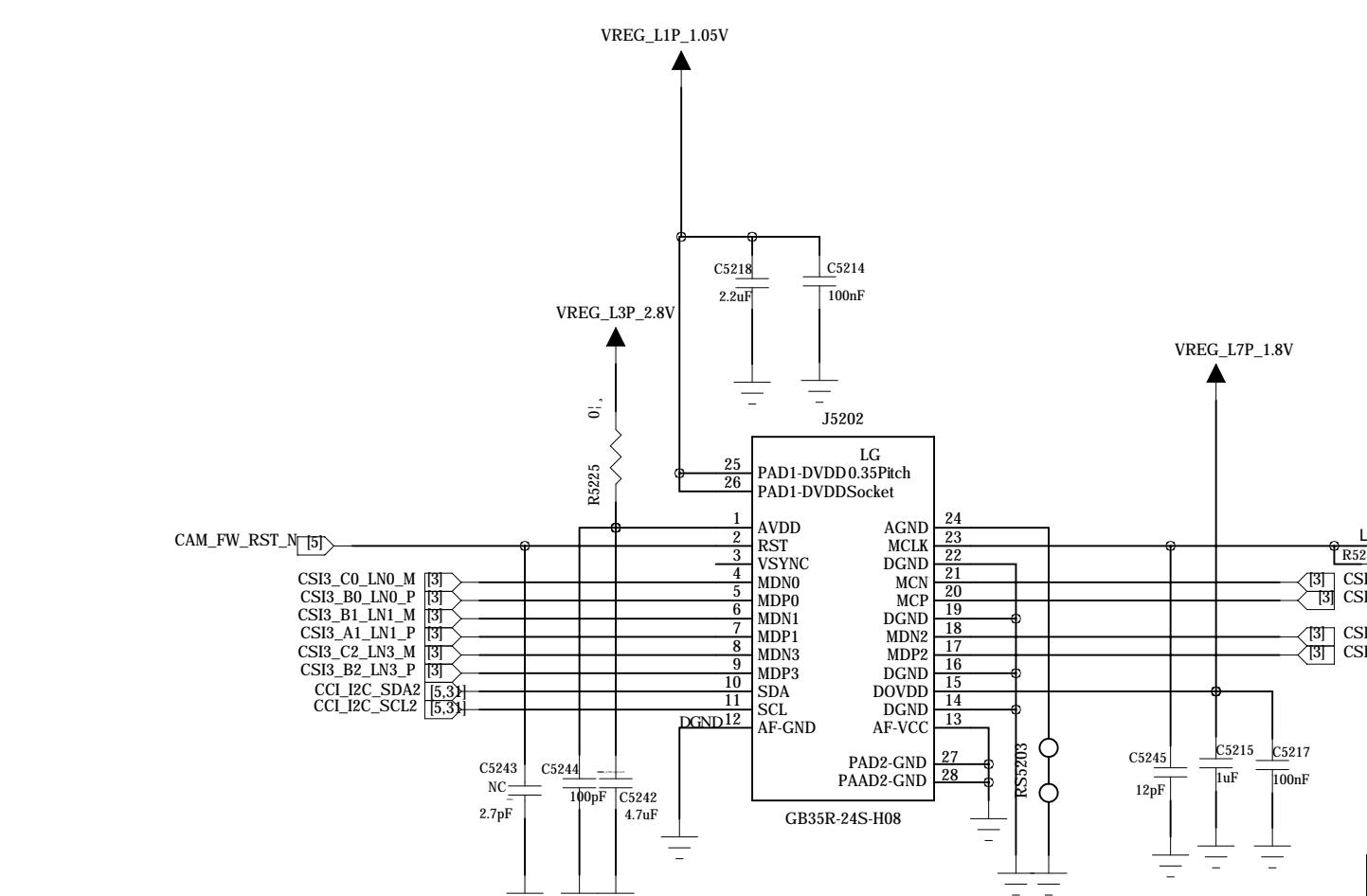
B



Front-CAMERA

	AVDD	DVDD	DOVDD	I2C	
S5K3L6XX03-FGX9	13M	2.8V	1.05V	1.8V	0X46(W)/0X47(R)
S5K4H7YX03-FGX9	8M	2.8V	1.2V	1.8V	0X5A(W)/0X5B(R)
OV16A1Q-GA5A-Z	16M	2.8V	1.2V	1.8V	0X20(W)/0X21(R)

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: <Company Name>	TITLE: SM4350	DRAWING NO: P720AE	REV: V1.0
DRAWN: weiye.feng	DATED: 20210820		
CHECKED: <Checked By>	DATED: <Checked Date>		
QUALITY CONTROL: <QC By>	DATED: <QC Date>		
RELEASED: <Released By>	DATED: <Release Date>		
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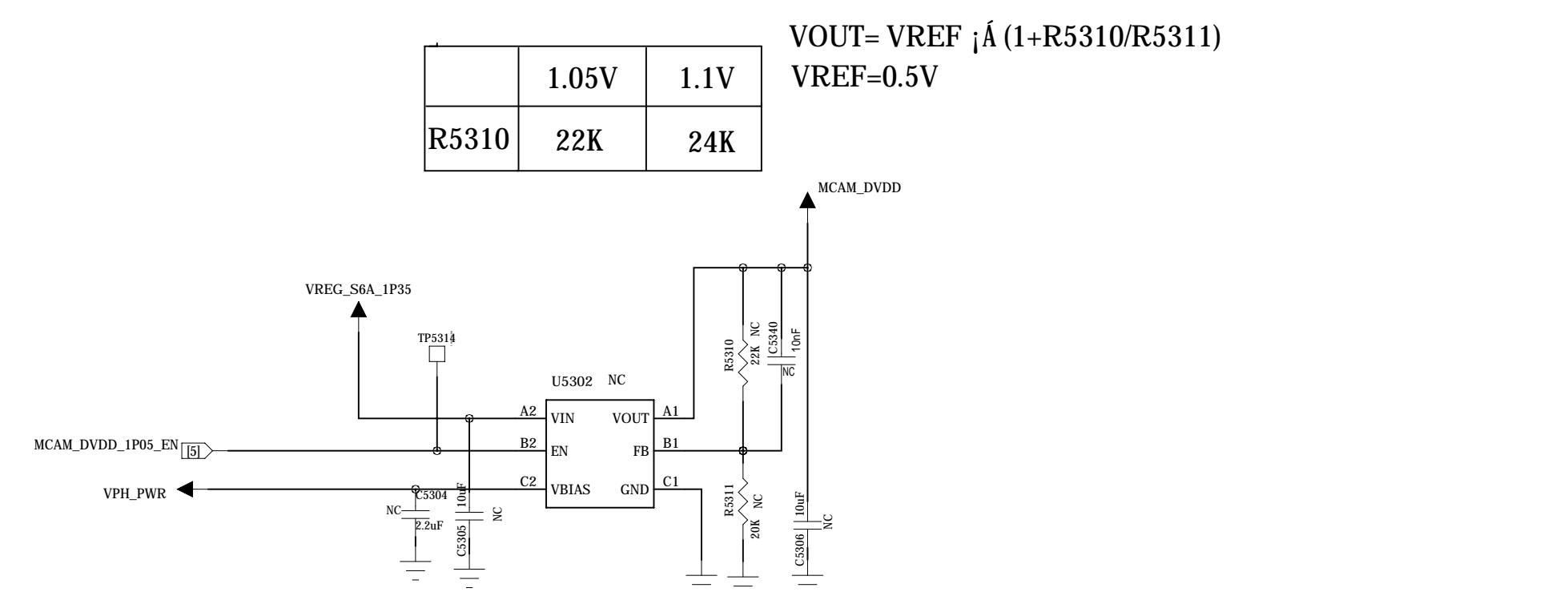
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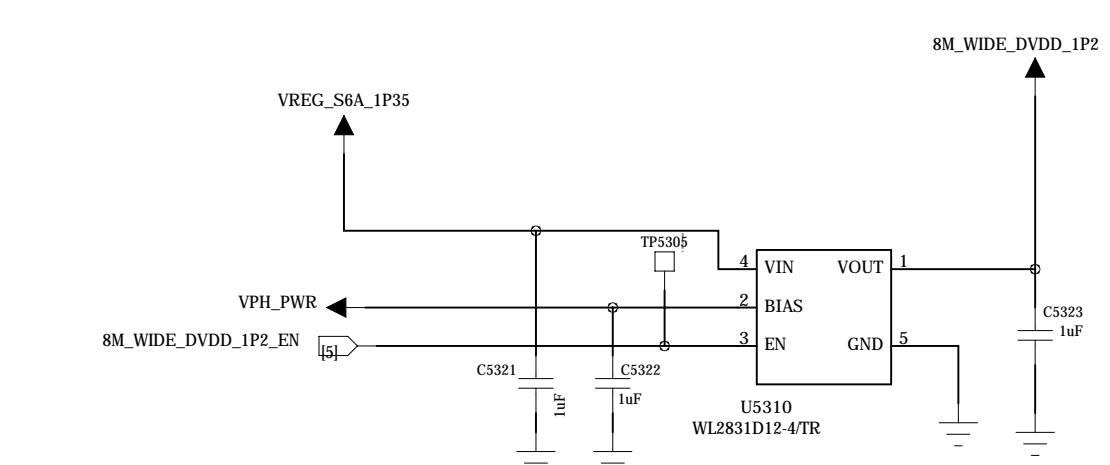
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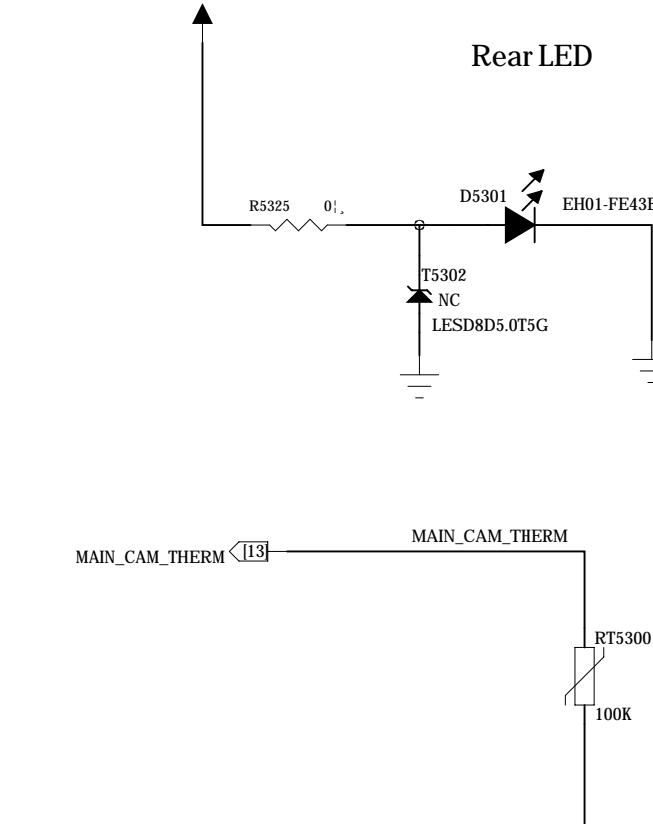
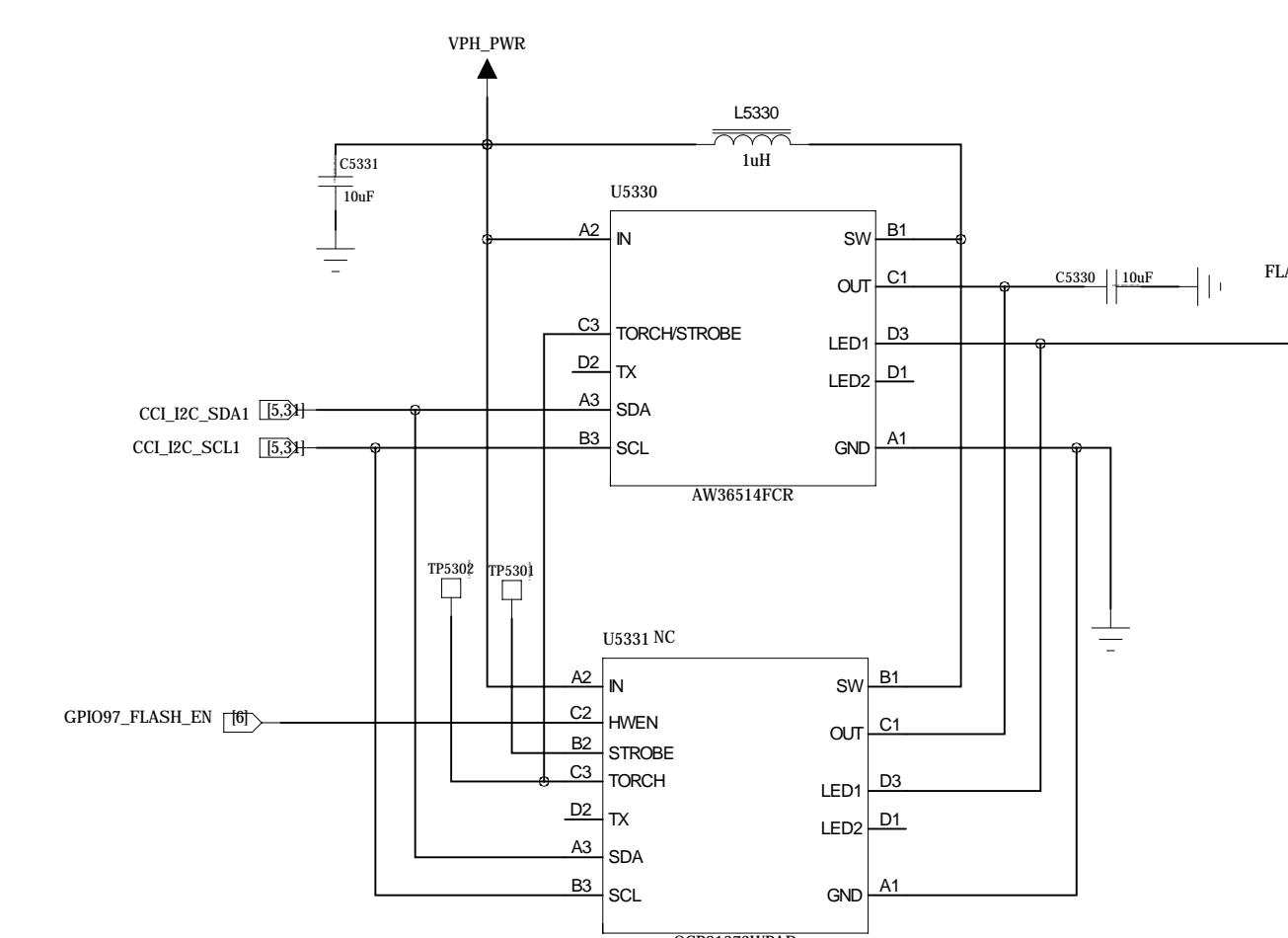


2M CAM AVDD 2.8v



8M Wide DVDD 1.2v

NOTE:
common pads

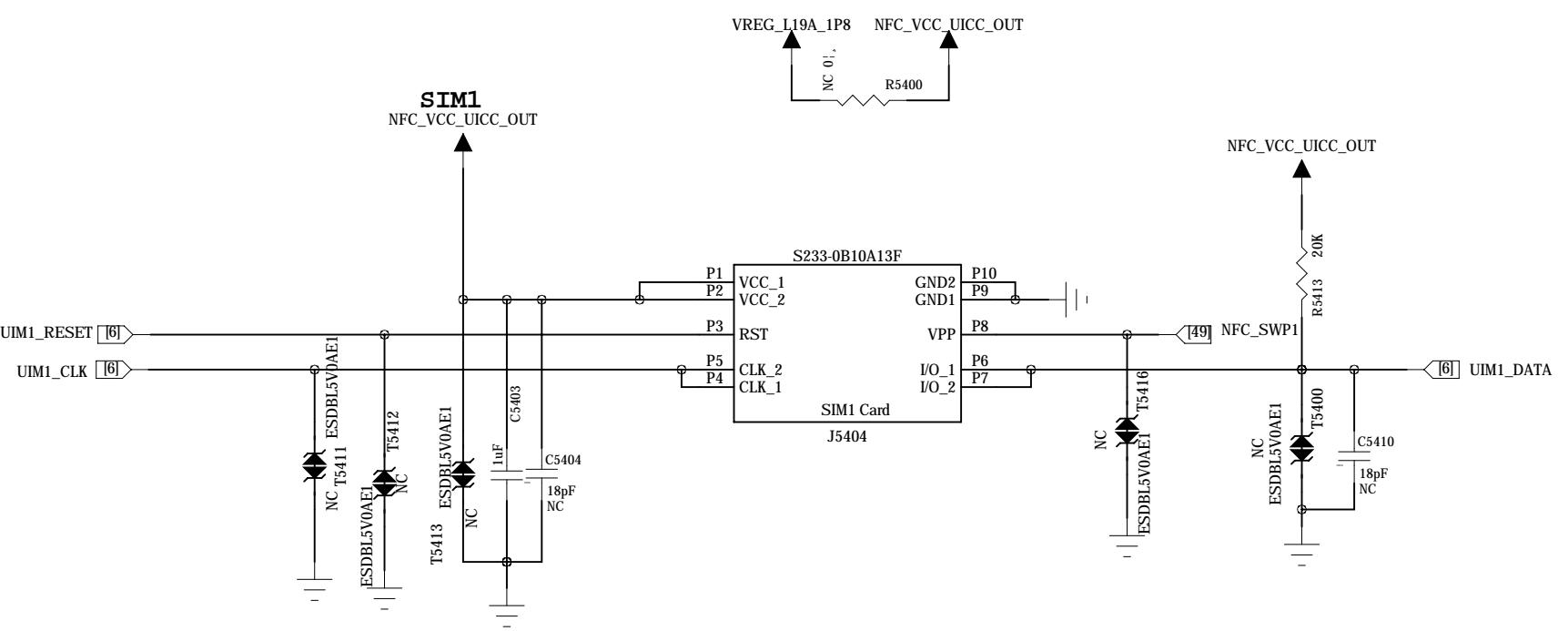


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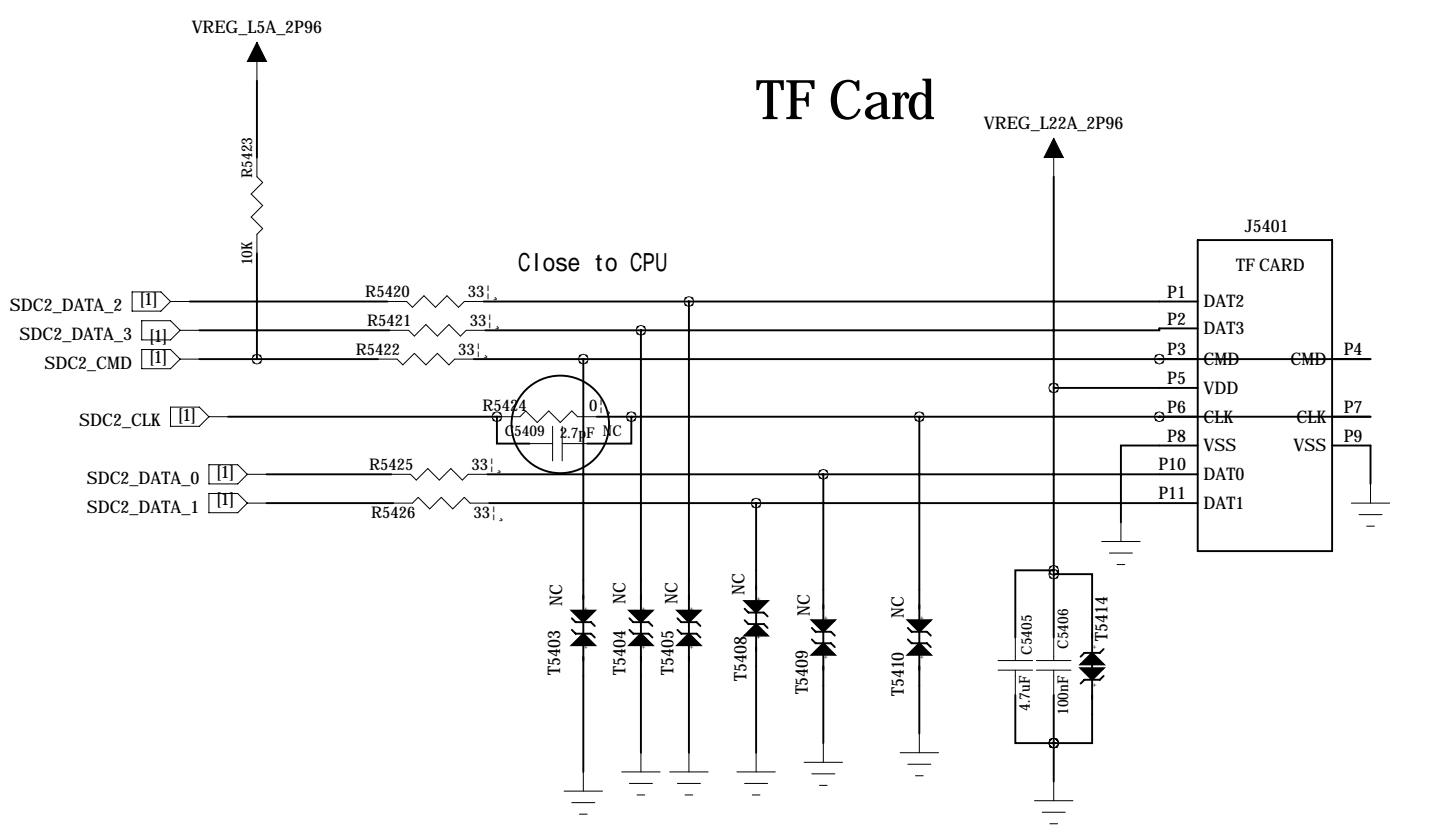
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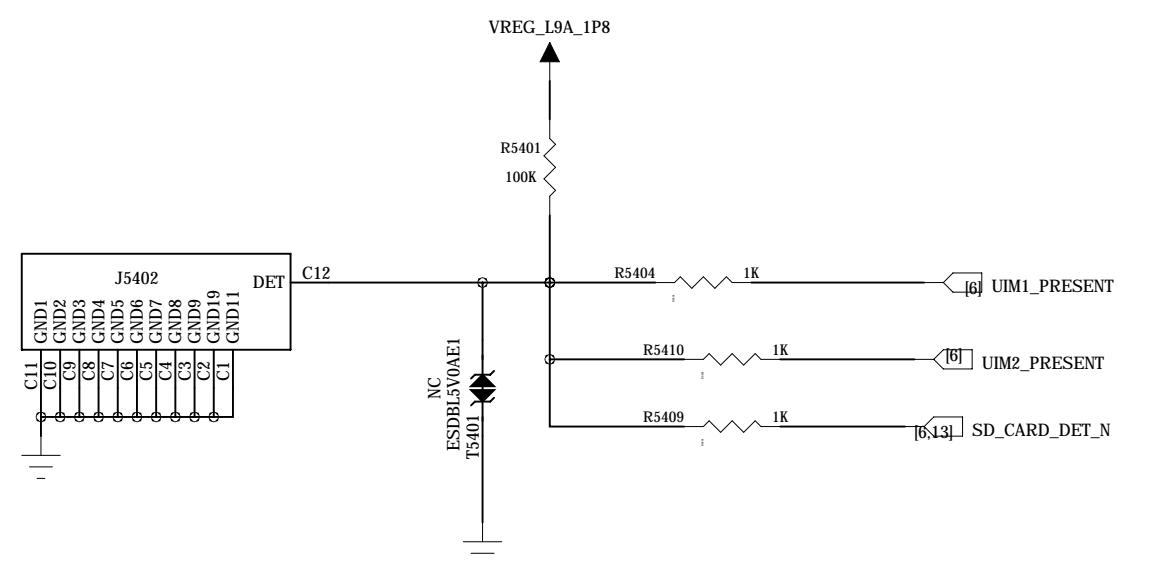


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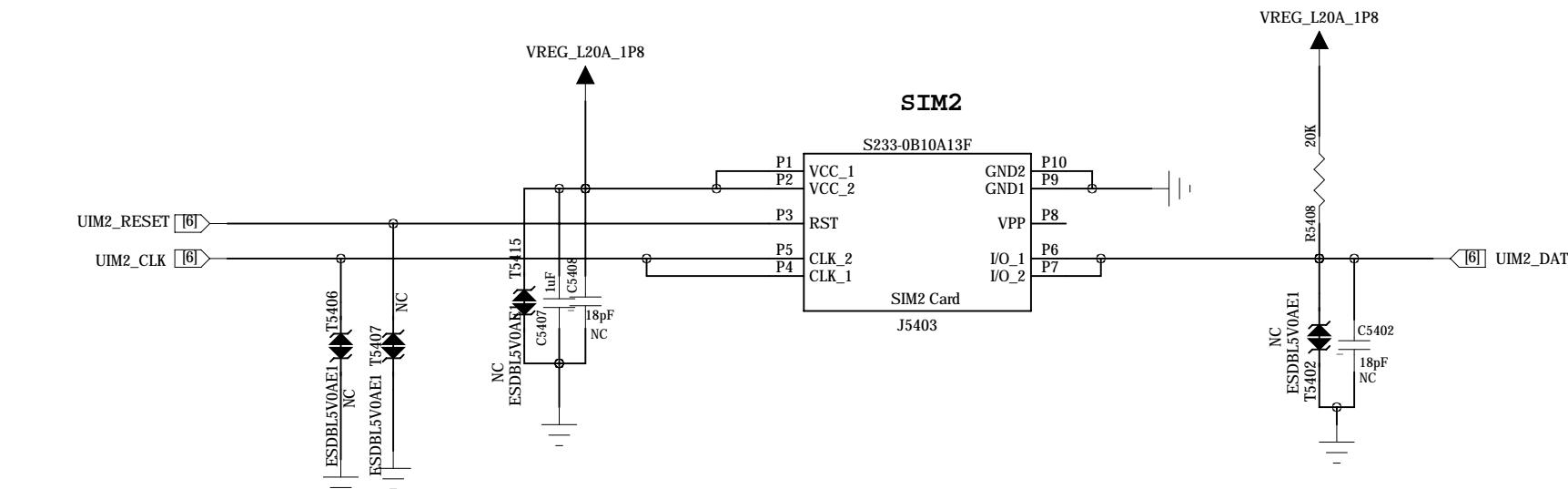


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Insert:1



B

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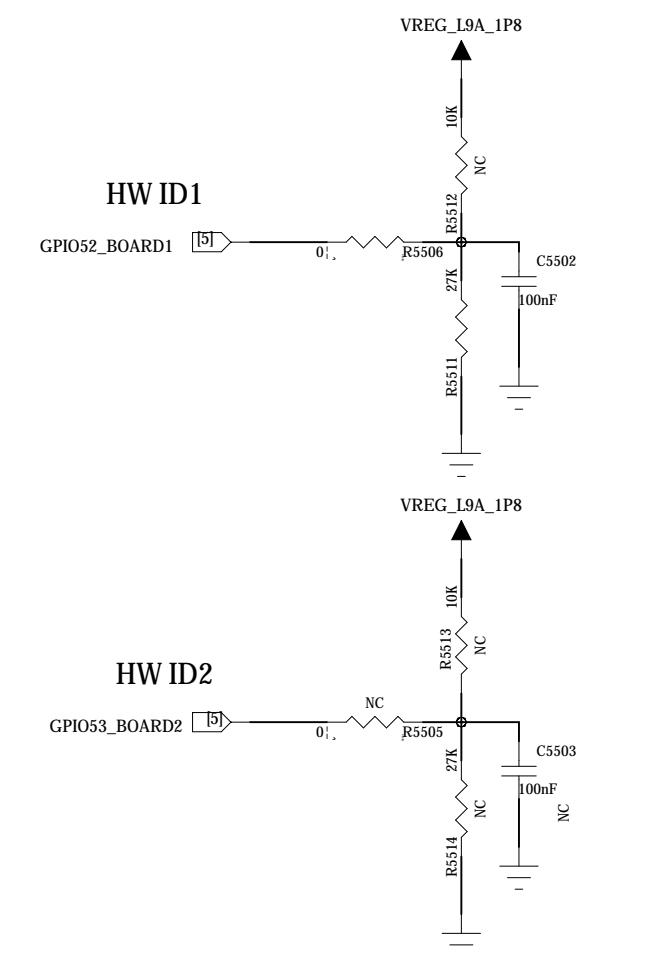
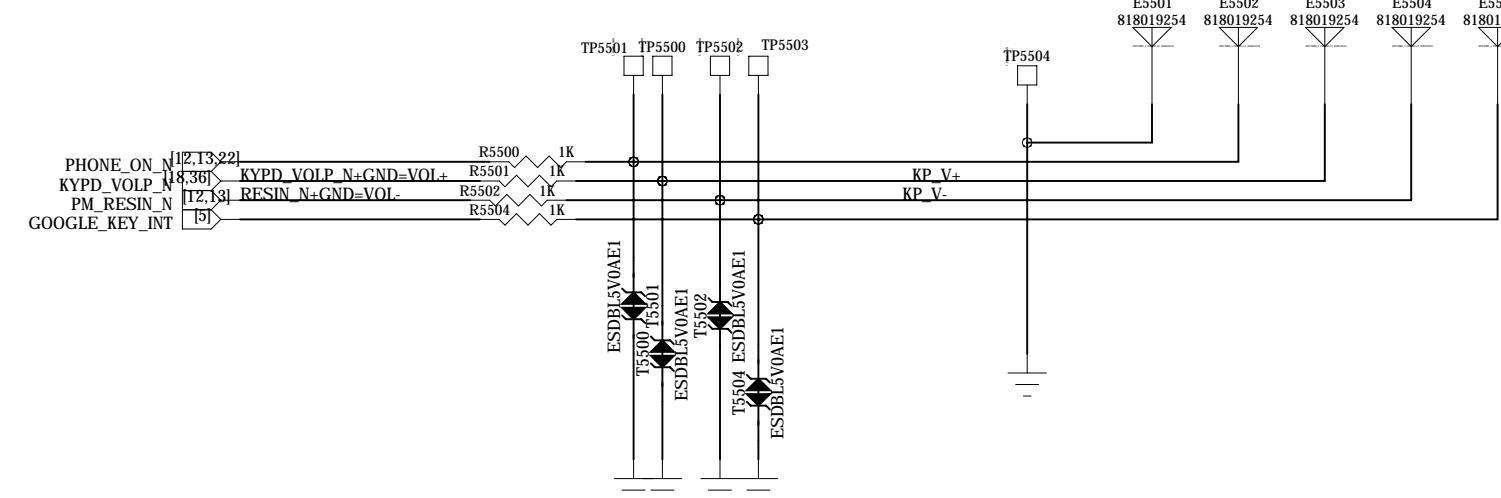
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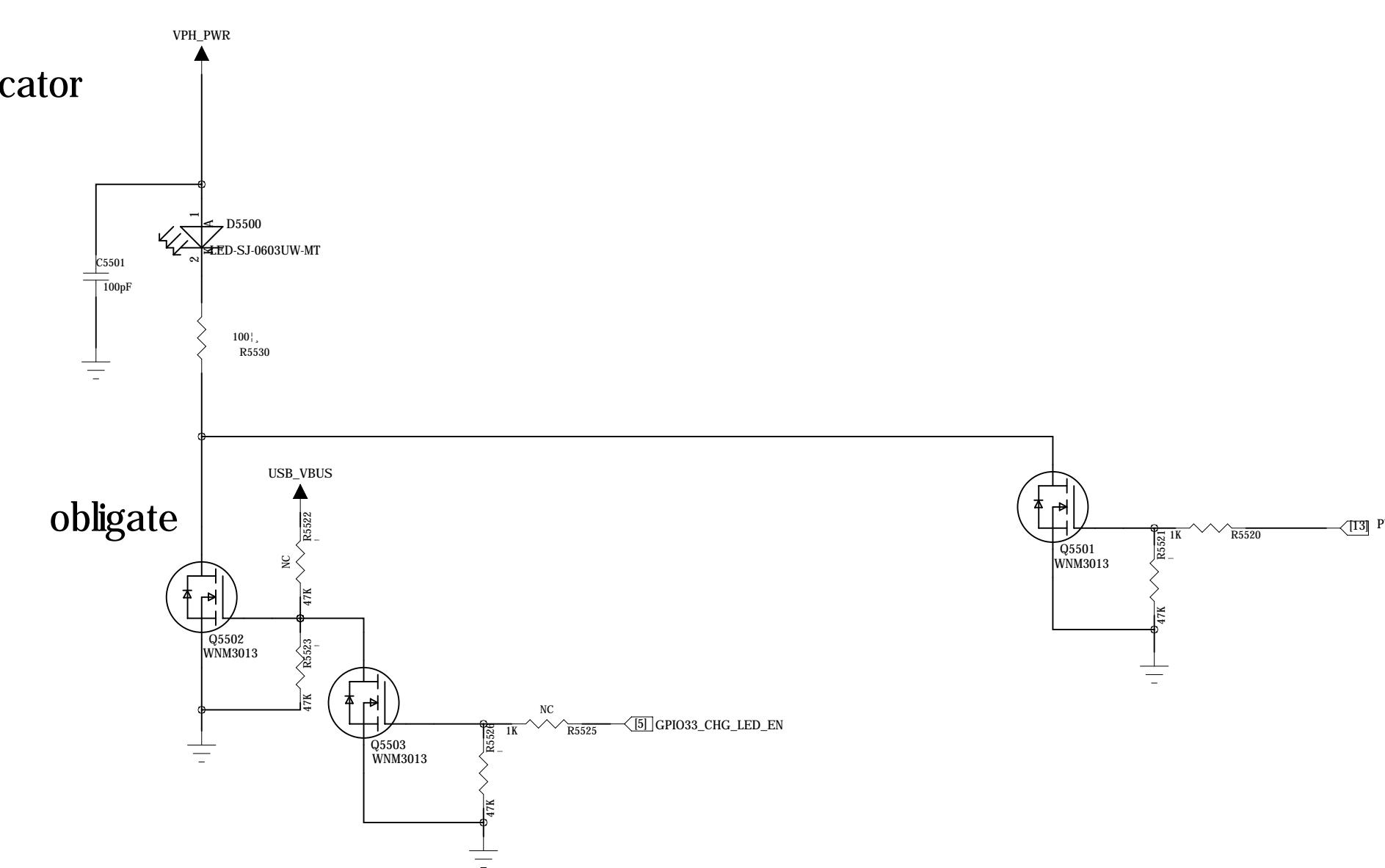
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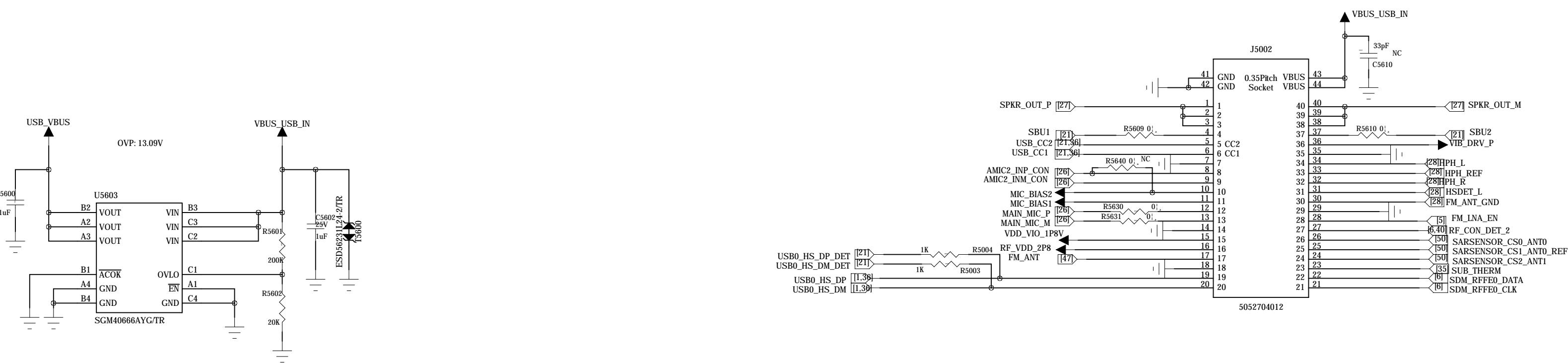
Charge Indicator



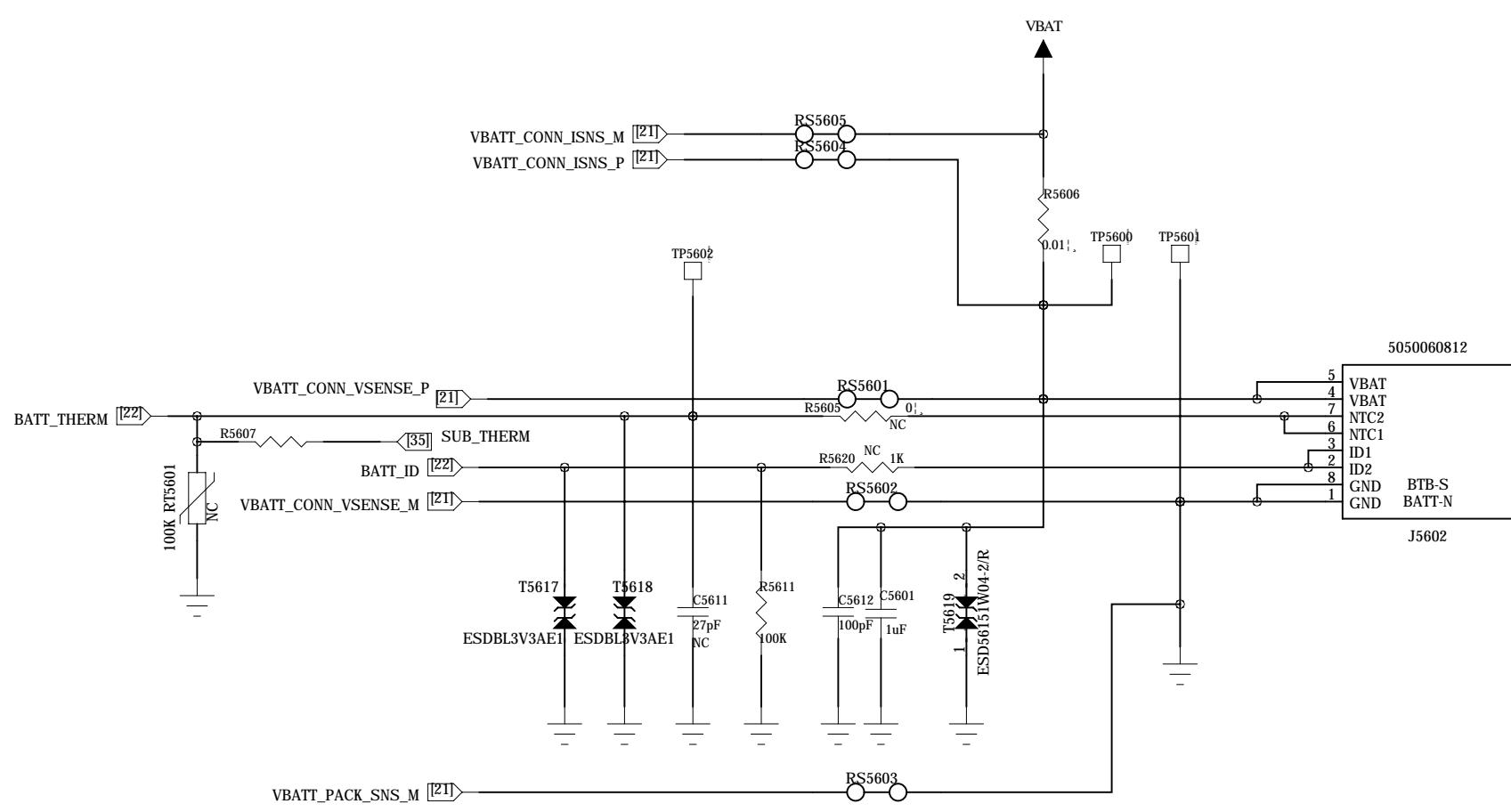
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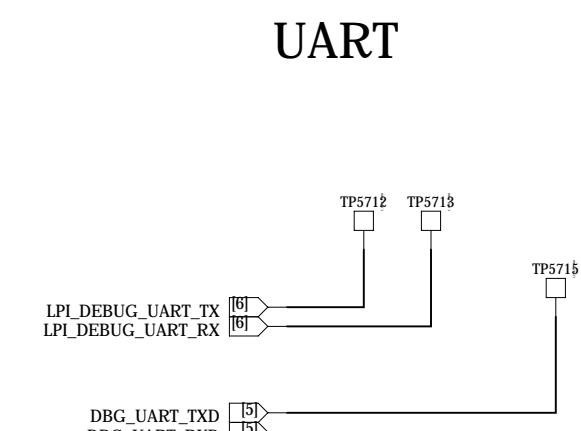
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LTR	ECO NO:	APPROVED:	DATE:

Debug Test Point

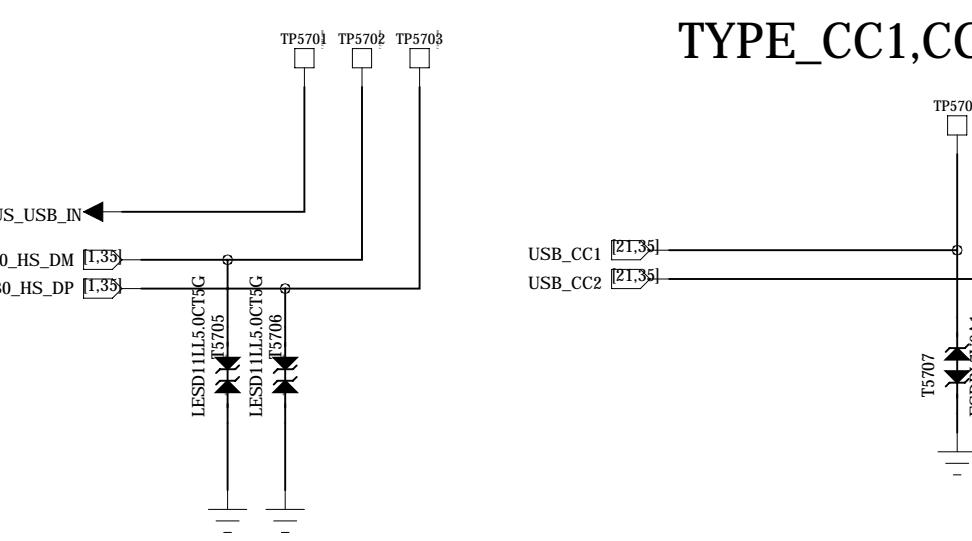
RAM dump

UART



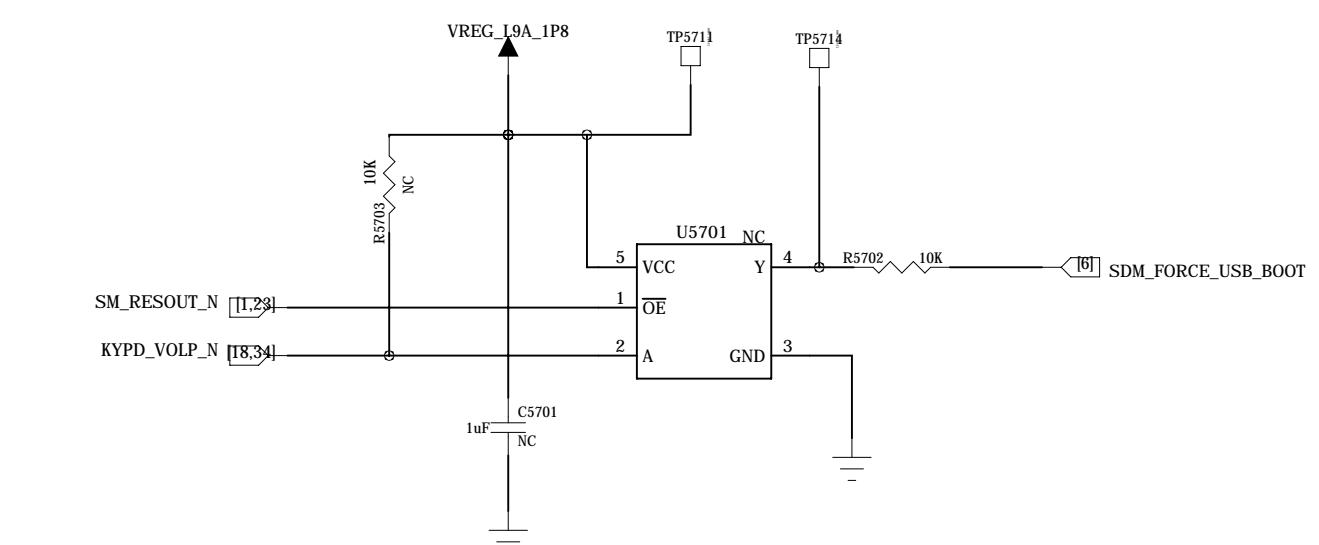
Fixture Test Point

USB



TYPE_CC1,CC2

FORCED_USB_BOOT

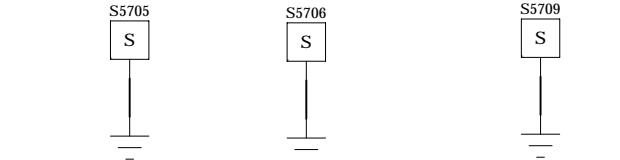


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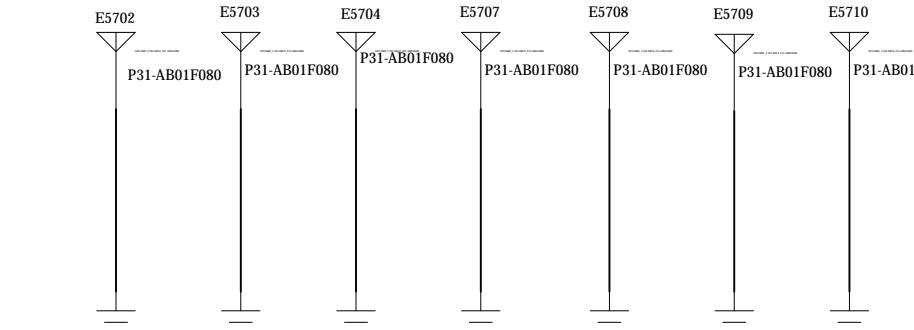
M5707
M5708
M5709PCB5701LAB5701
PCB
LidM5710
M5711
M5712

BB 4G PA 5G PA UHB PA WCN sensor

RF TXM RF DRX PDN

*shielding can*

Metal Grounding

*Bottom*

COMPANY: <Company Name>	TITLE: SM4350		
DRAWN: weiye.feng	DATED: 20210820	CODE: P720AE	SIZE: D
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: P720AE	REV: V1.0
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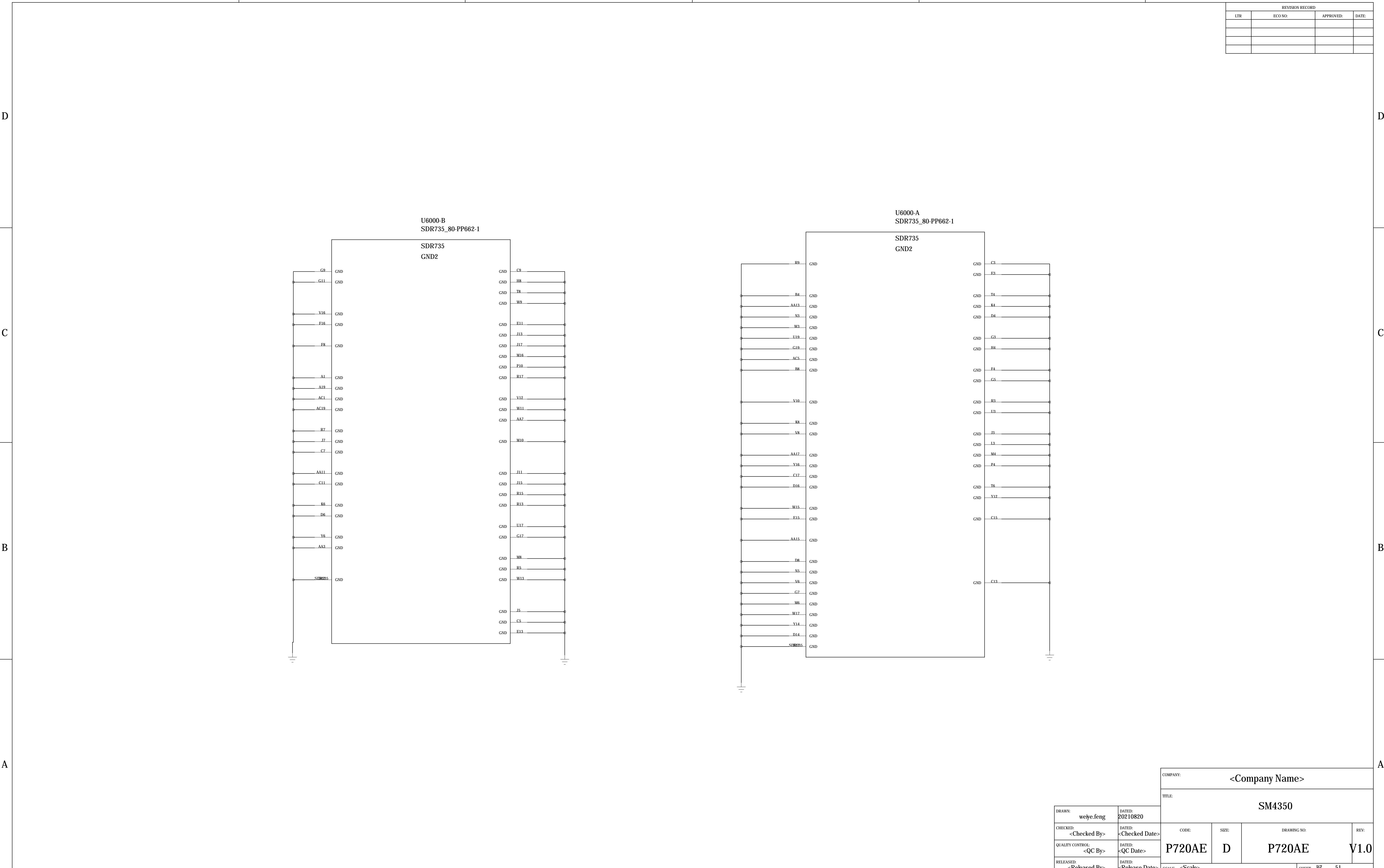
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P720AE D P720AE V1.0

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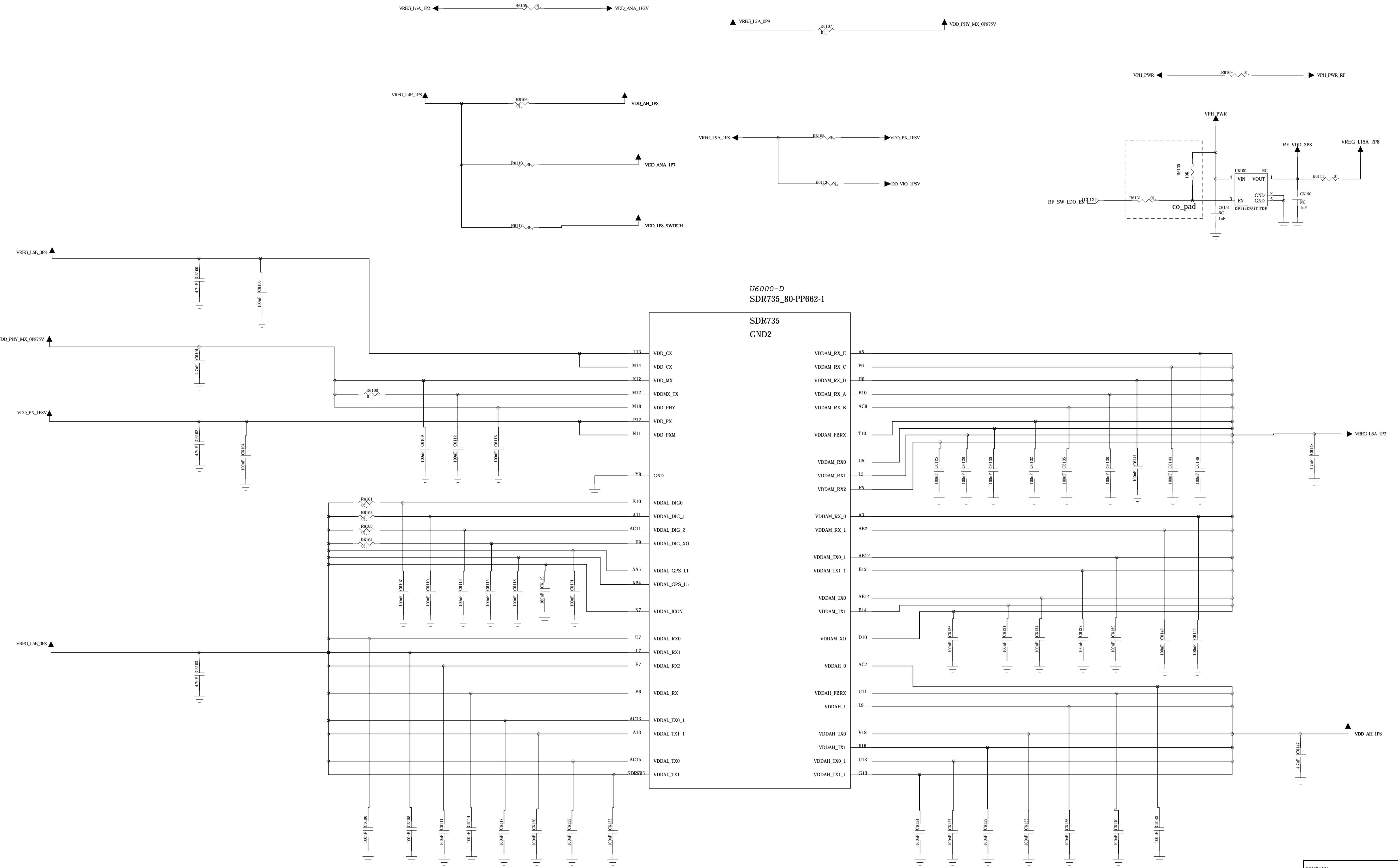
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P720AE	D	P720AE	V1.0
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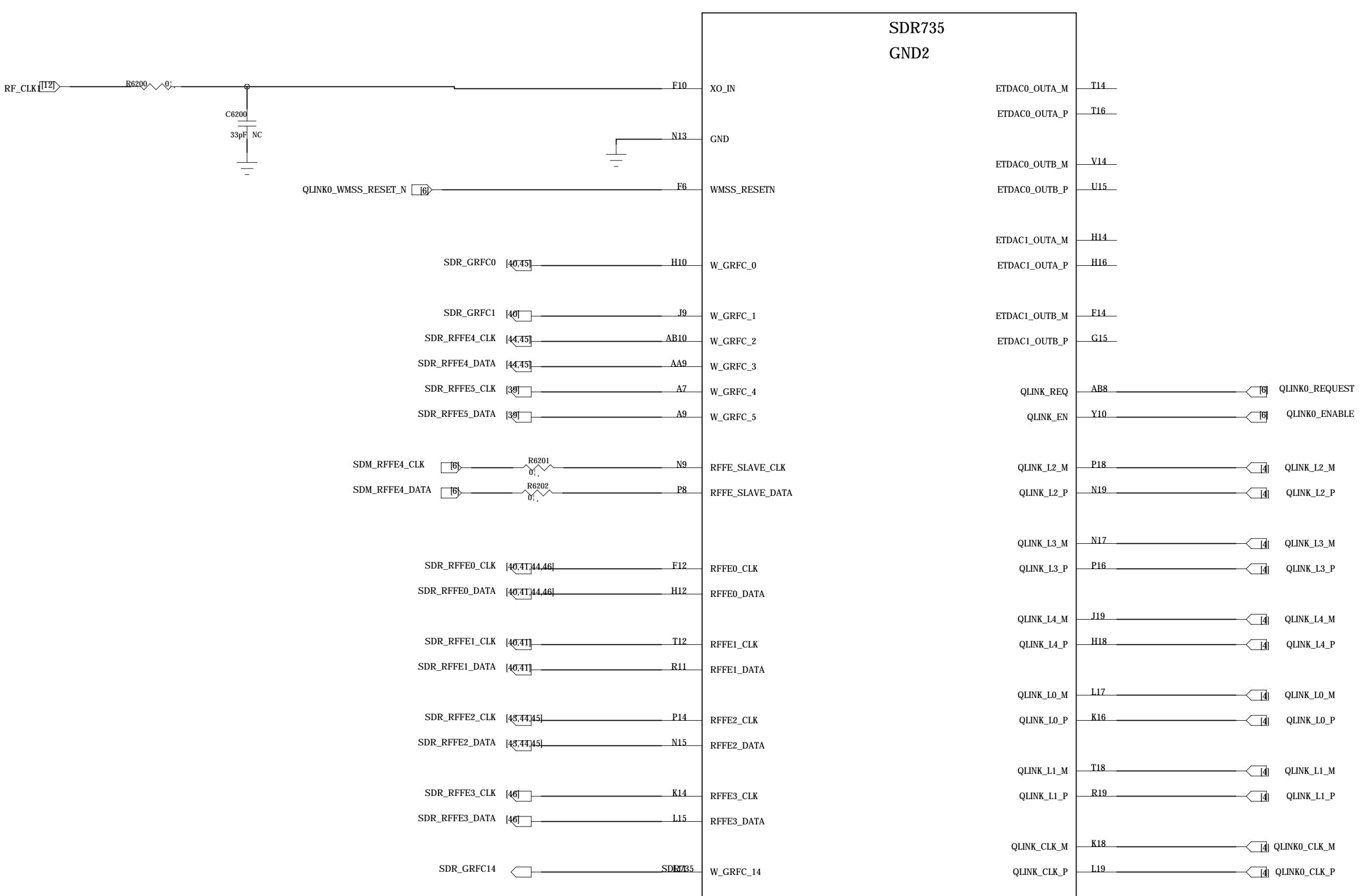
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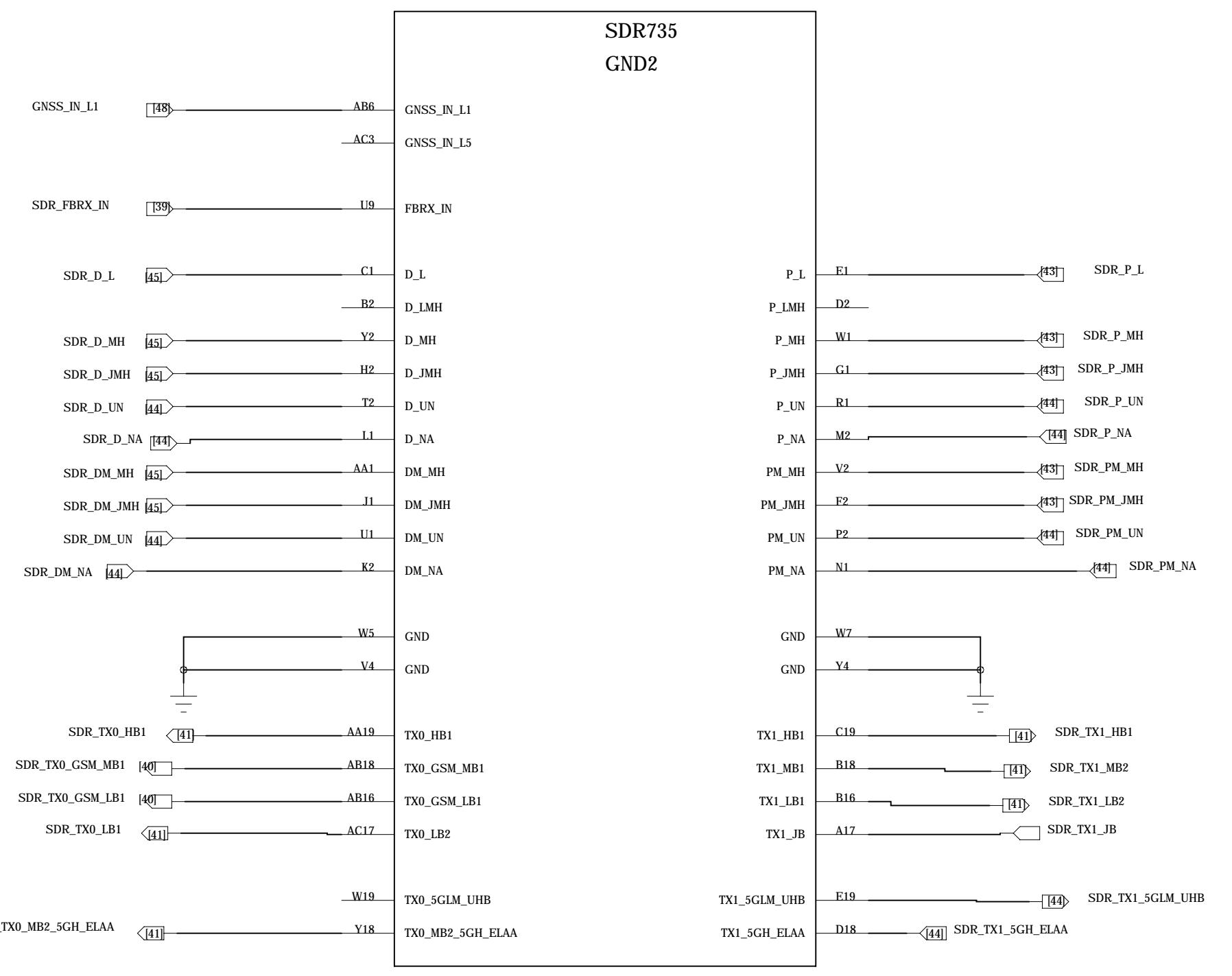
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SDR735_80-PP662-1

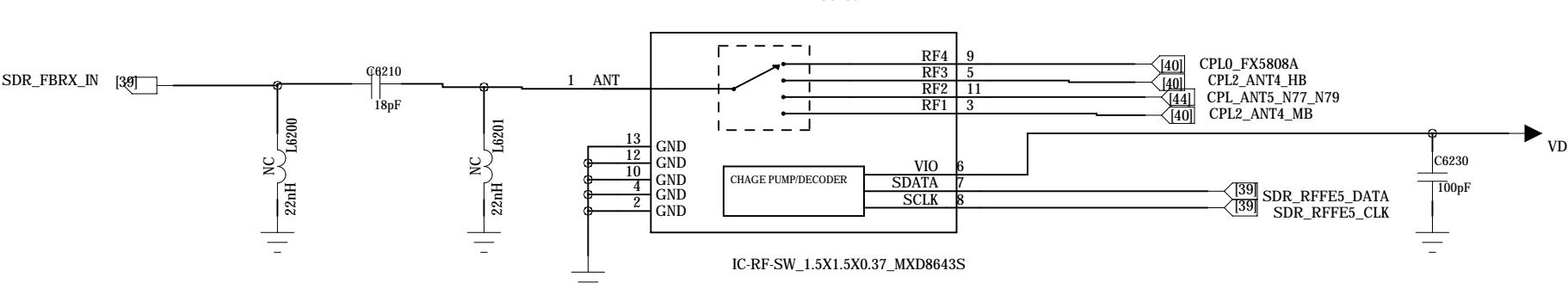


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SDR735_80-PP662-1



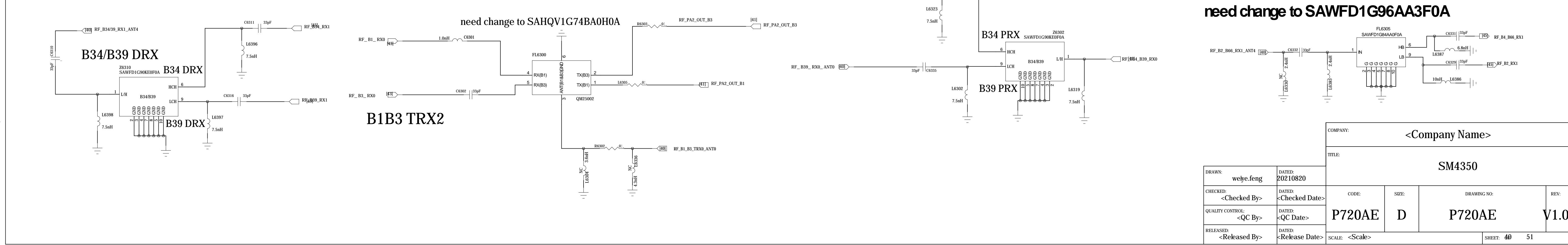
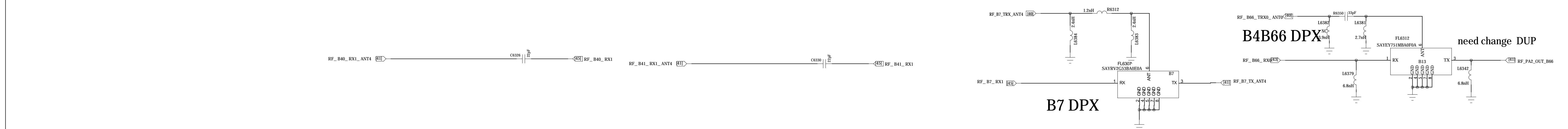
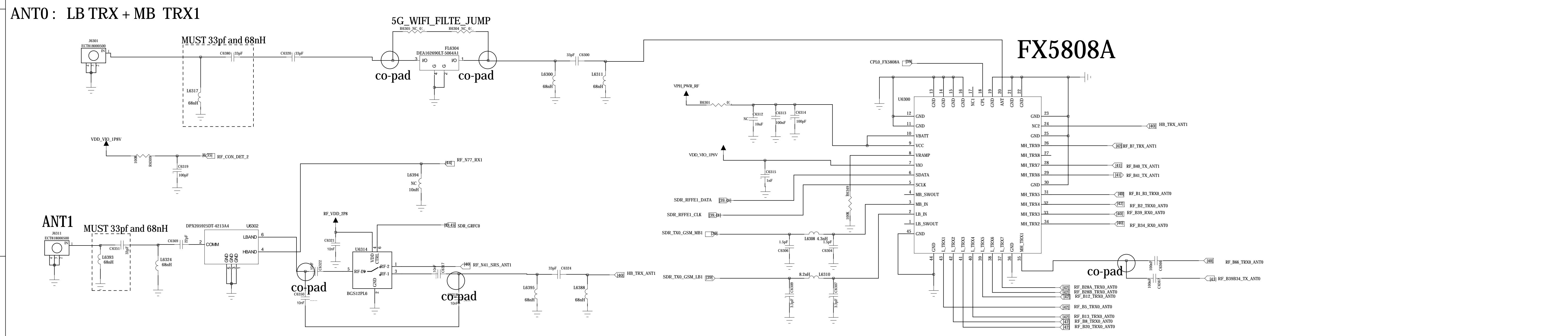
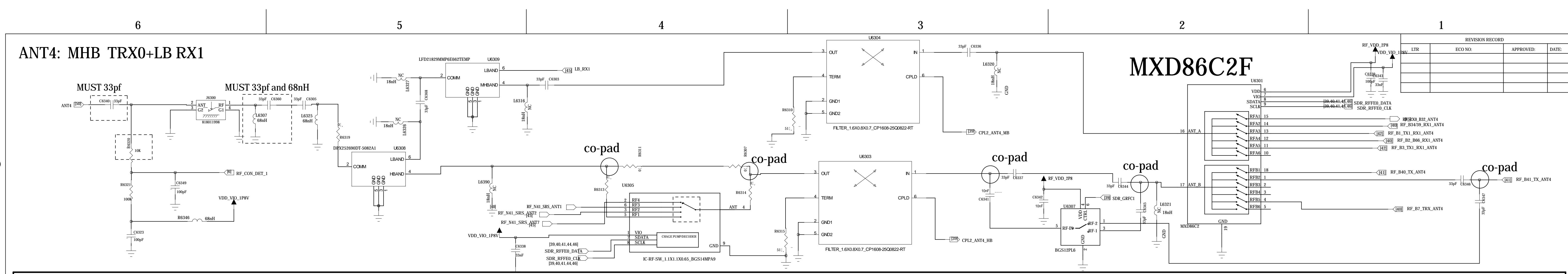
FBRX

U6203



DRAWN: wejye.feng	DATED: 20210820
CHECKED: <Checked By>	DATED: <Checked Date>
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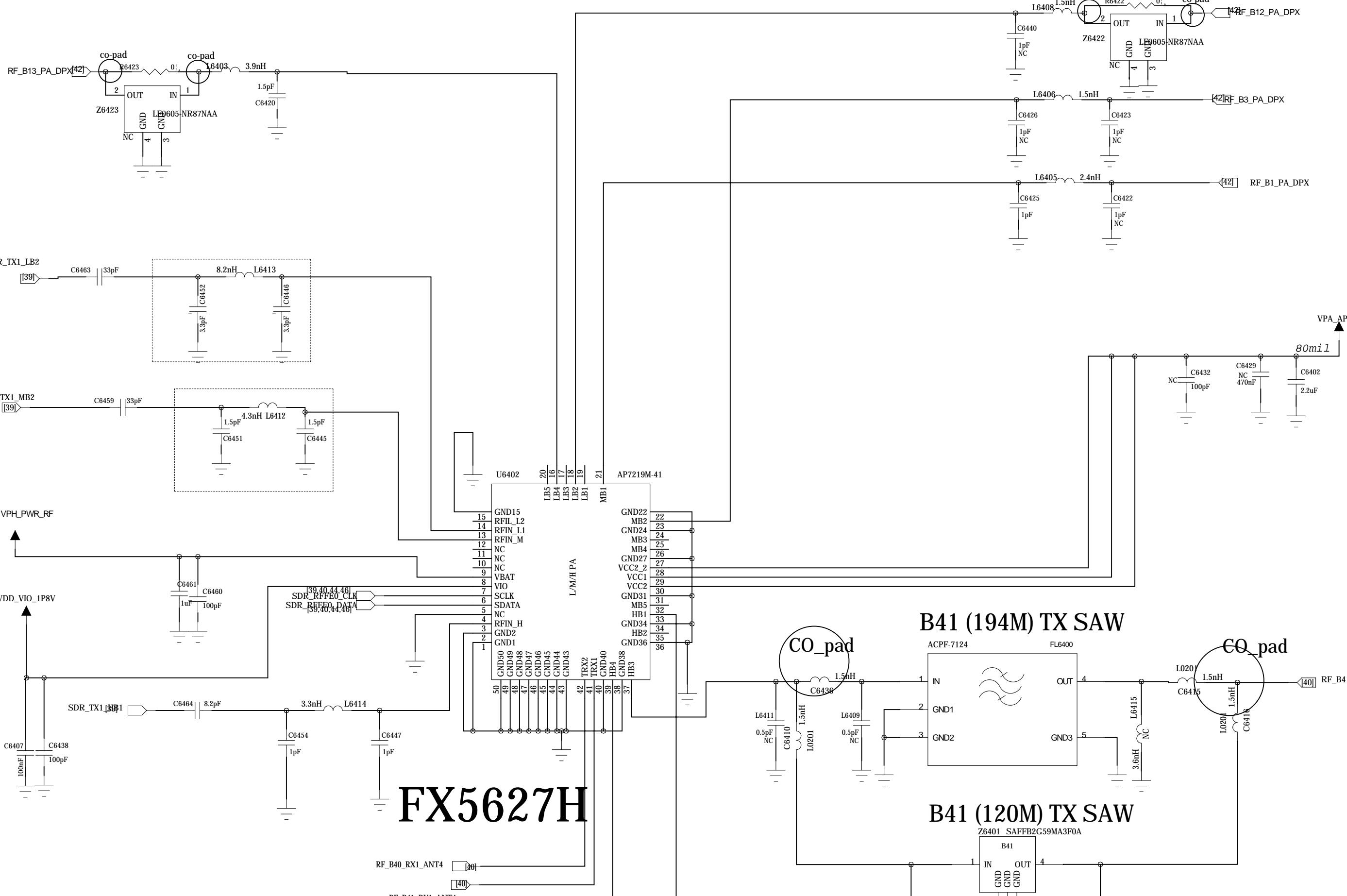
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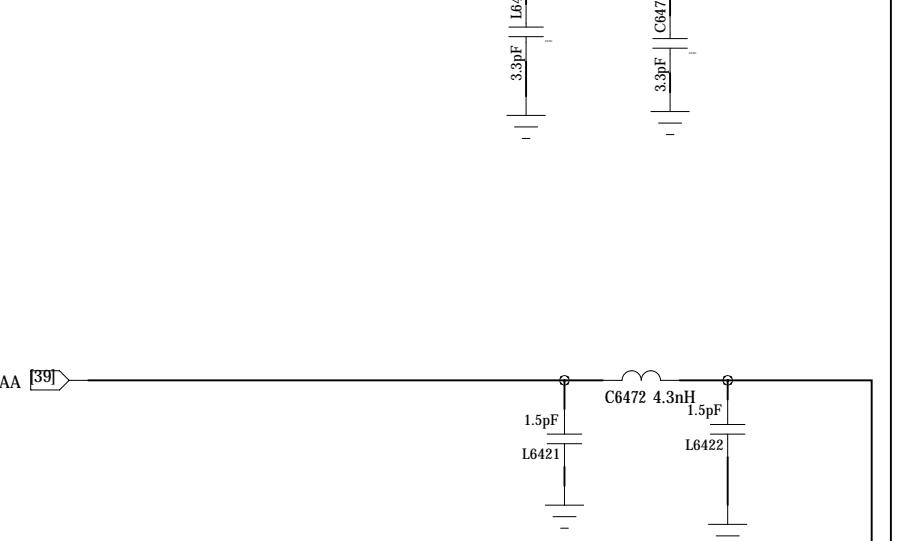


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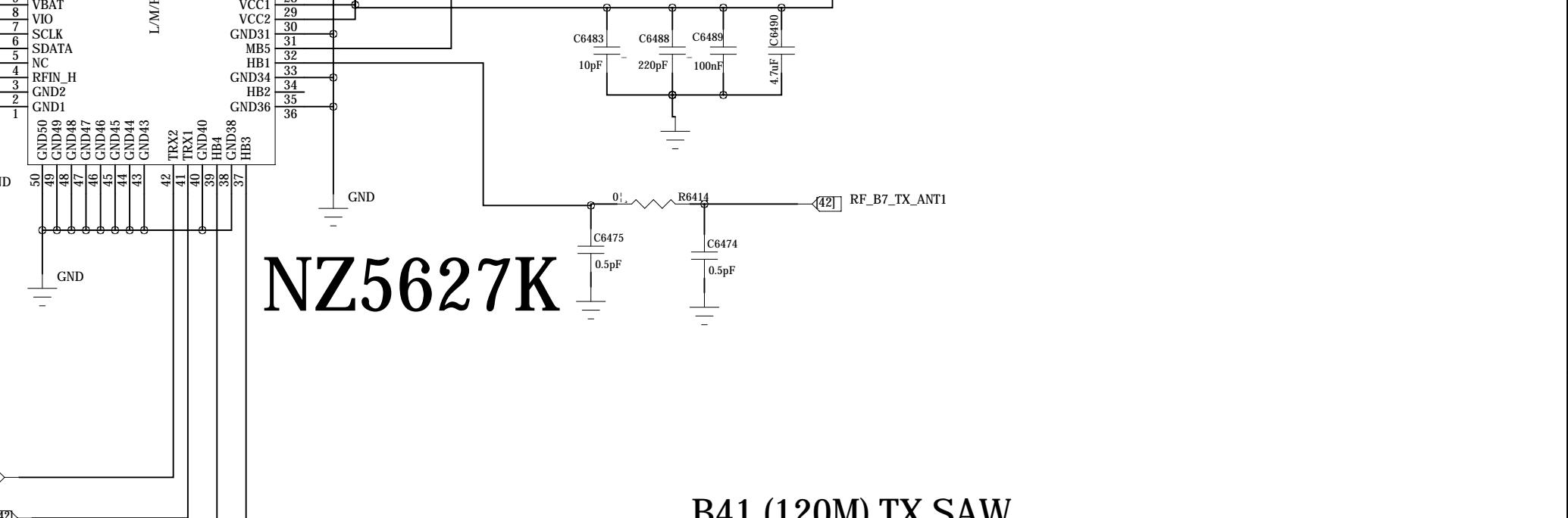
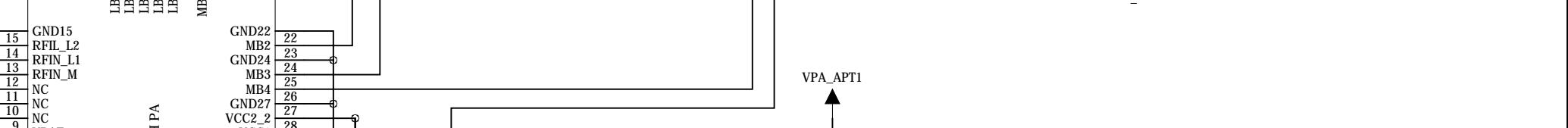
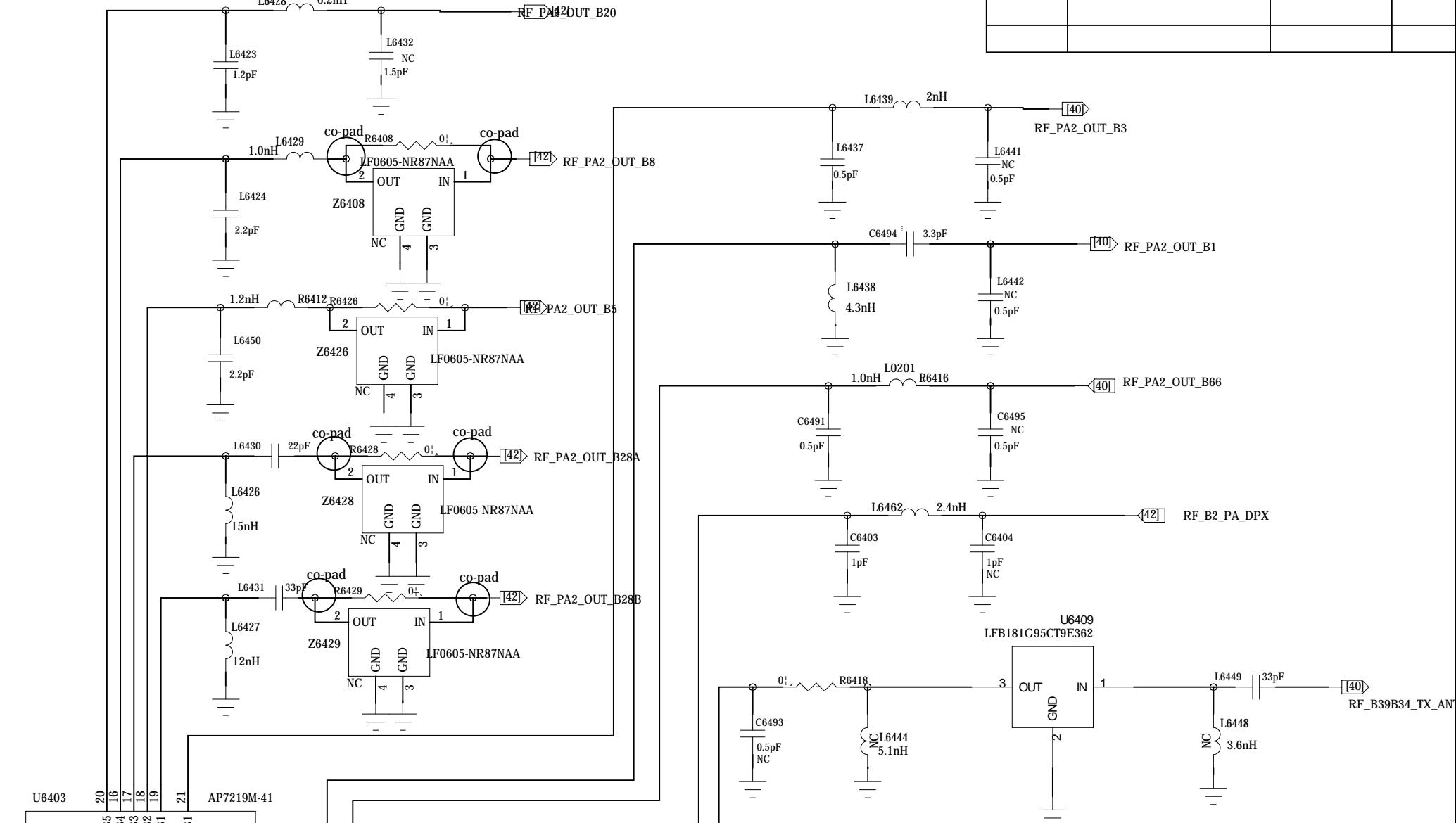
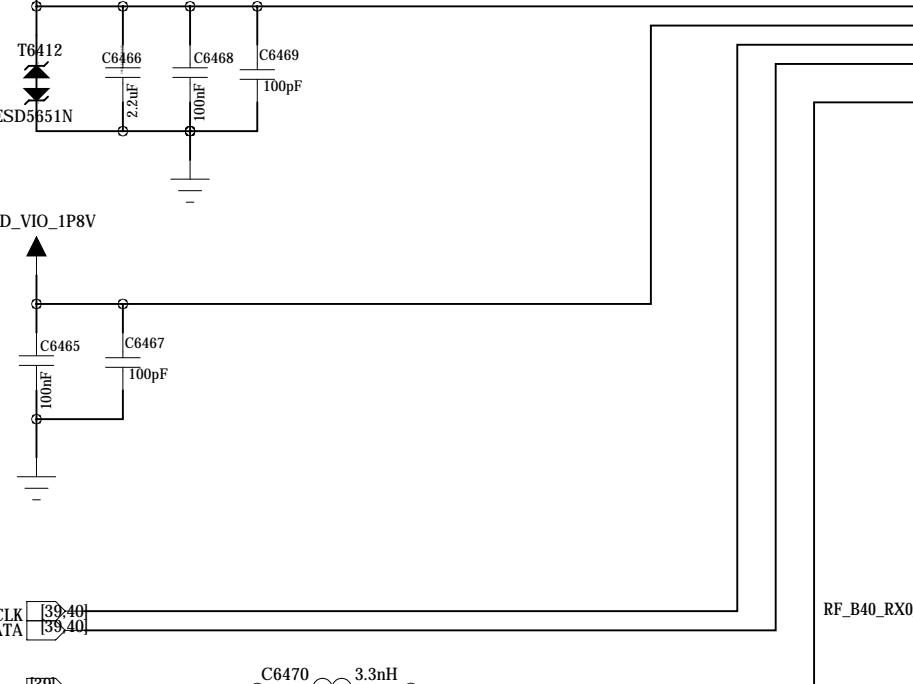
Thermistor Close to PA



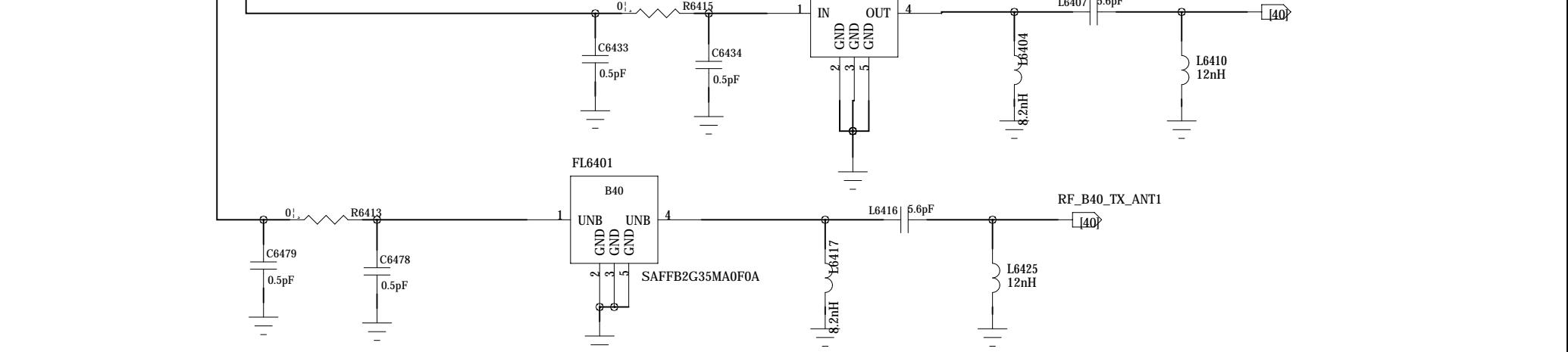
SDR_TX0_LB1 [39]



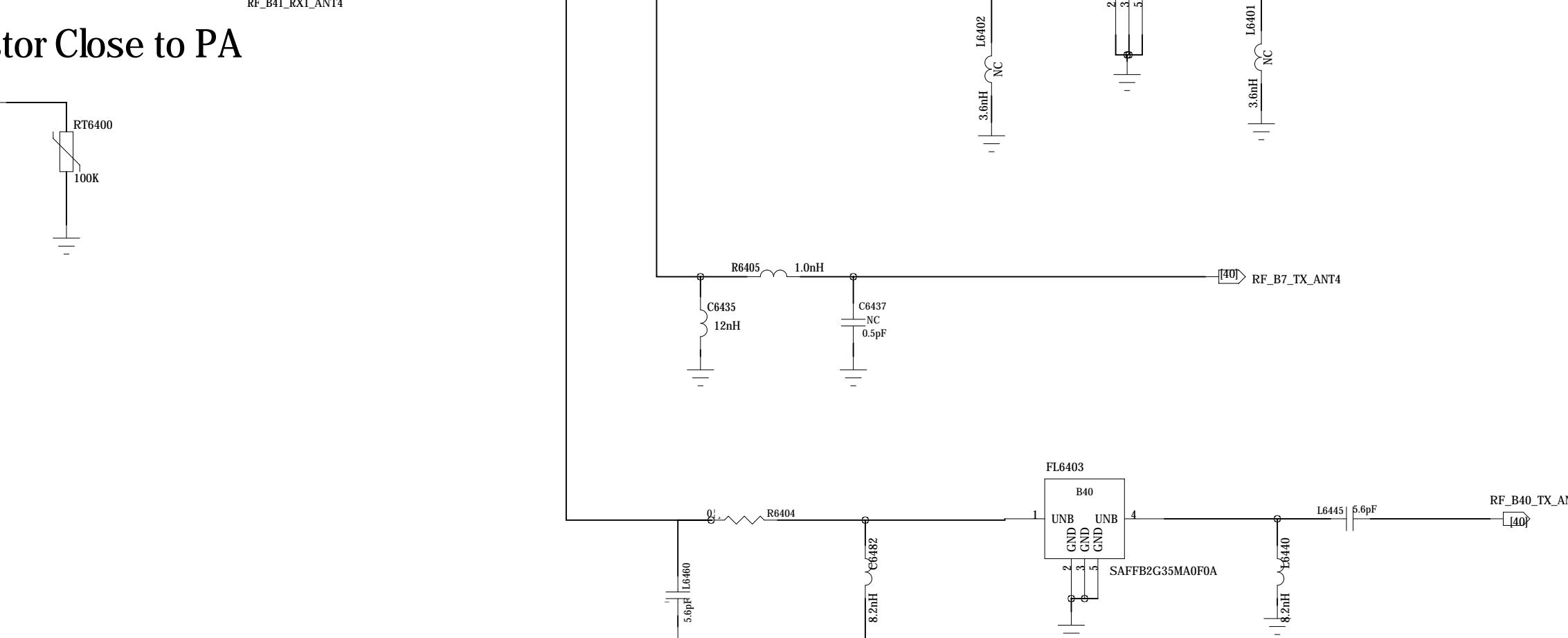
SDR_TX0_MB2_5GH_ELA [39]



NZ5627K
B41 (120M) TX SAW
£ Notef°SKU3 need change to B41 (194M) RX SAW£-AS B41/N41 DRX£©



B



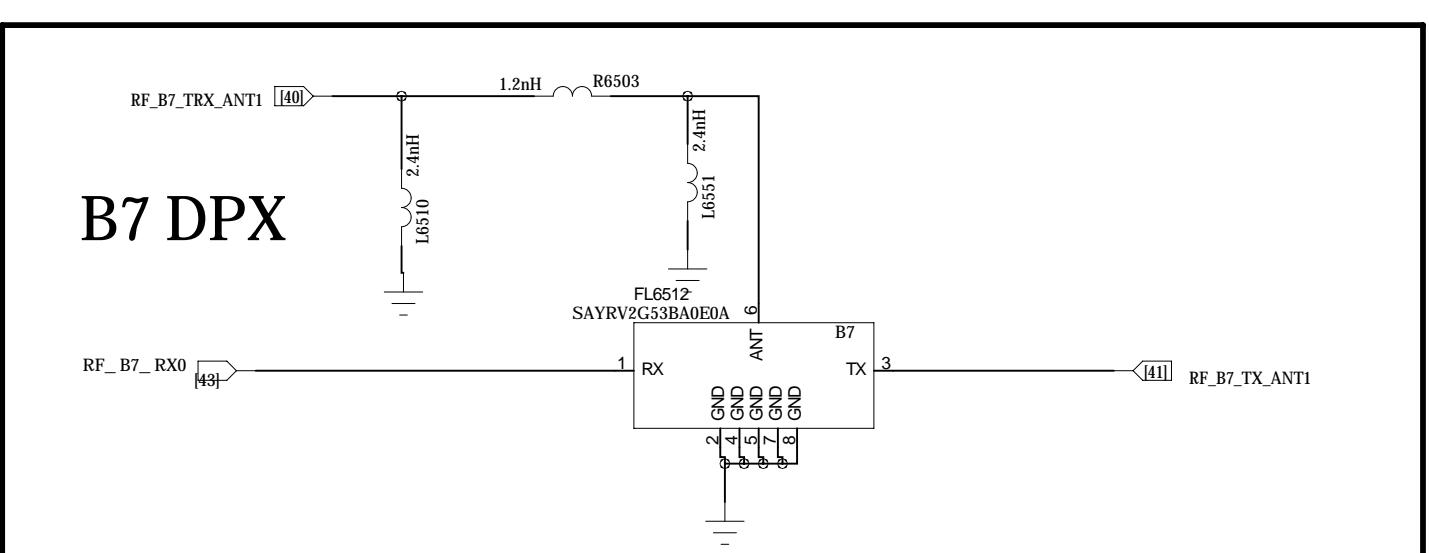
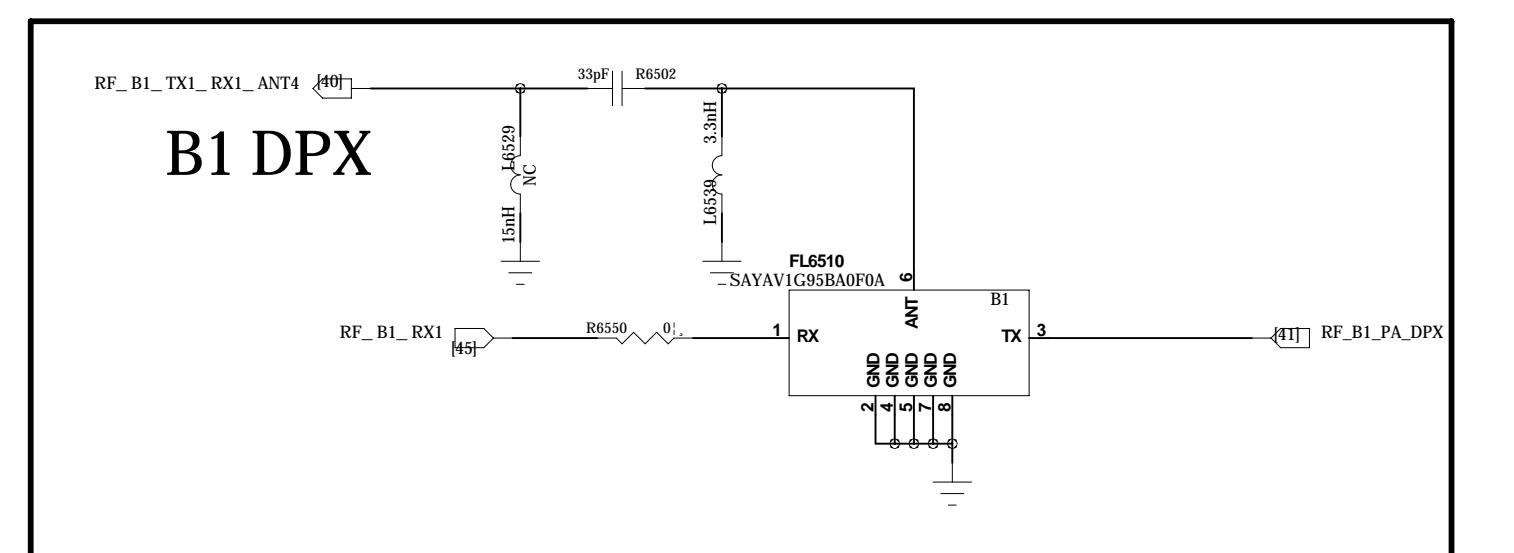
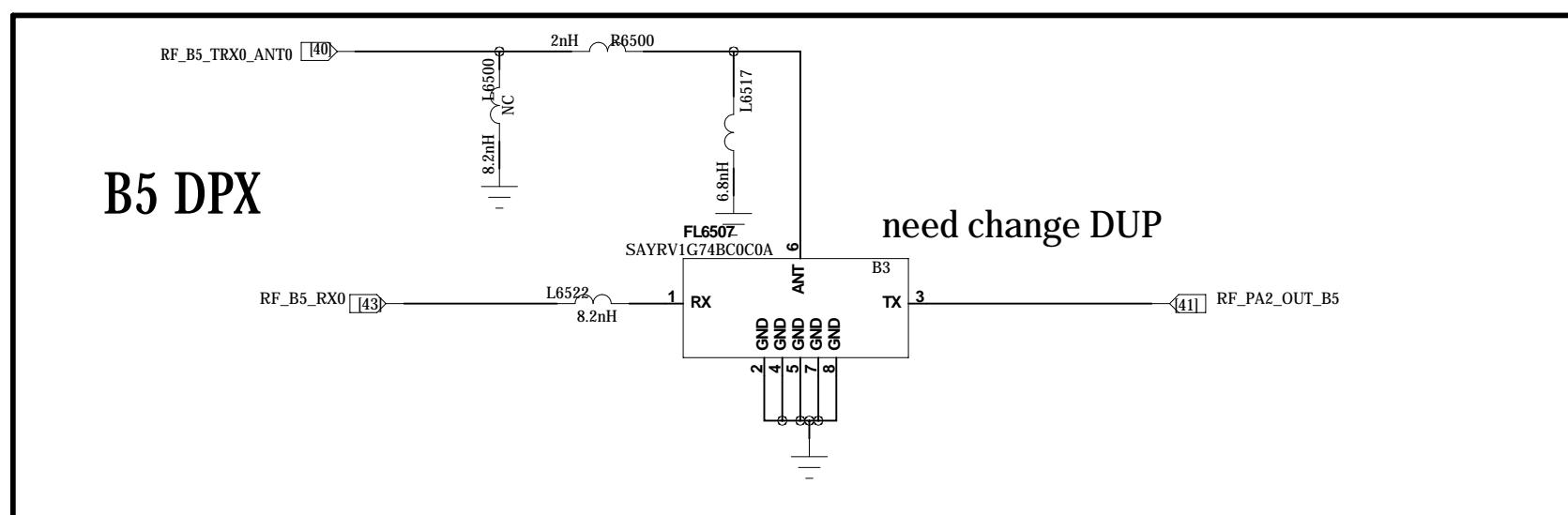
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TITLE: SM4350
DRAWN: weiyefeng DATED: 20210820
CHECKED: <Checked By> DATED: <Checked Date>
QUALITY CONTROL: <QC By> DATED: <QC Date>
RELEASED: <Released By> DATED: <Release Date>
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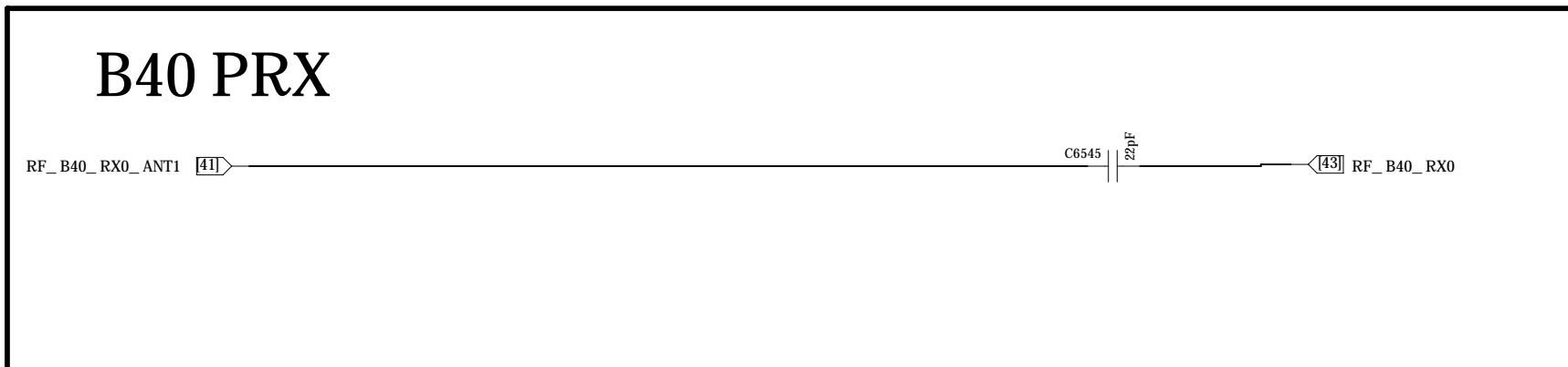
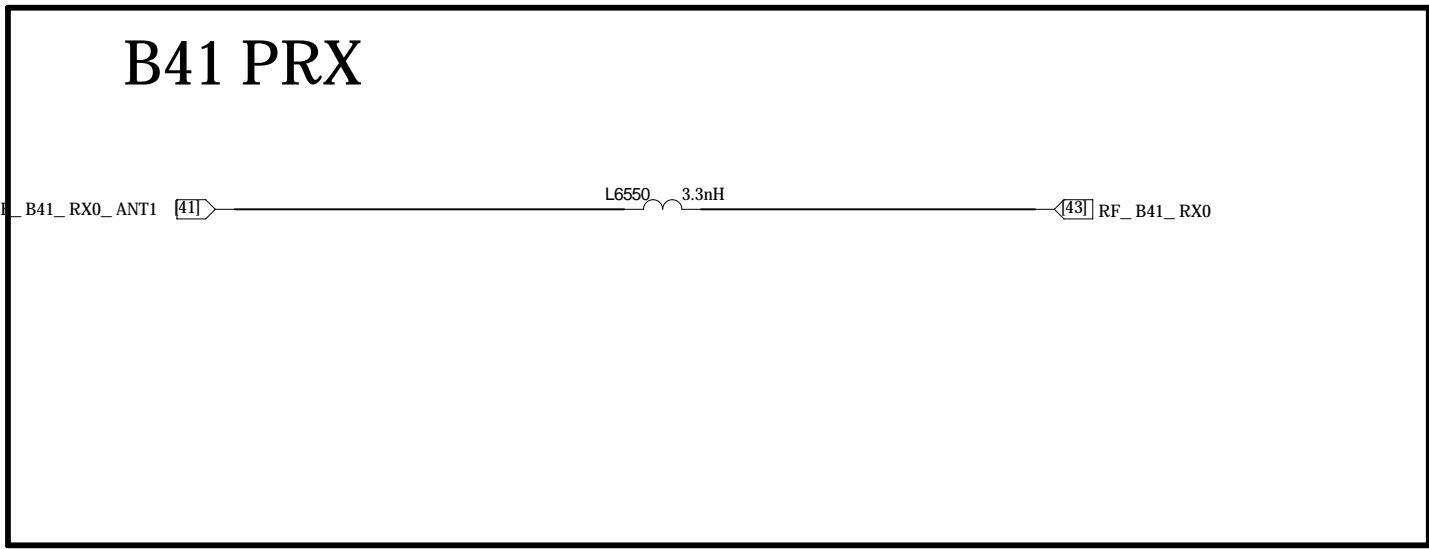
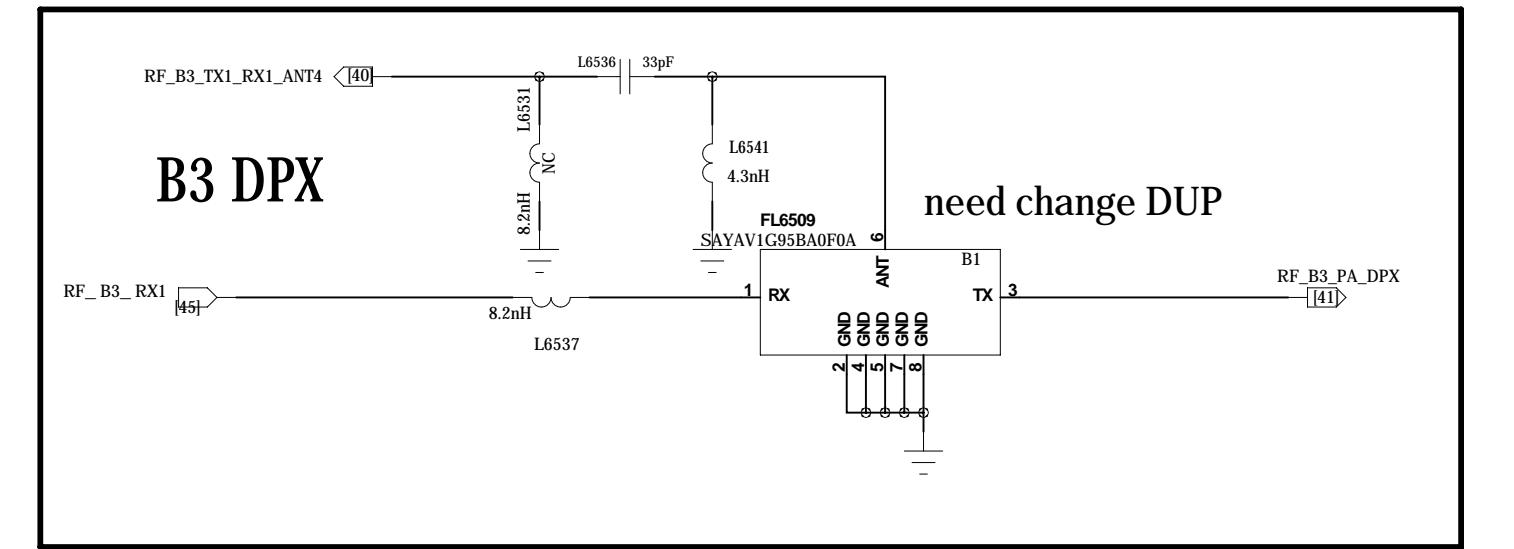
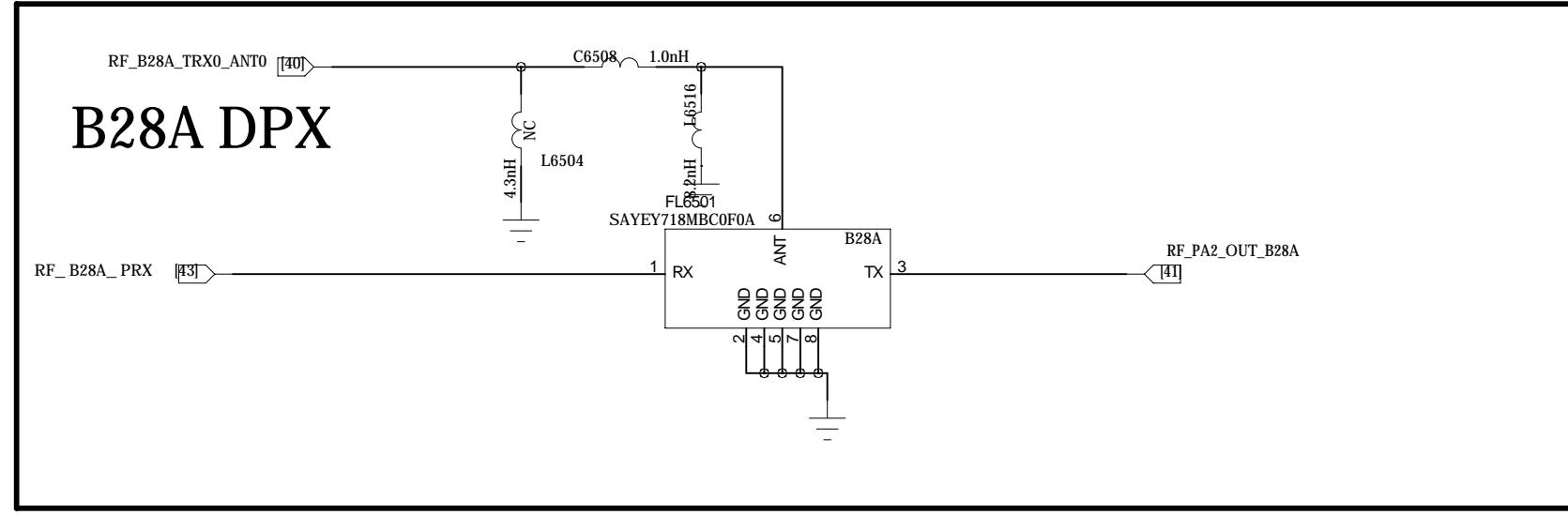
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LTR	ECO NO:	APPROVED:	DATE:

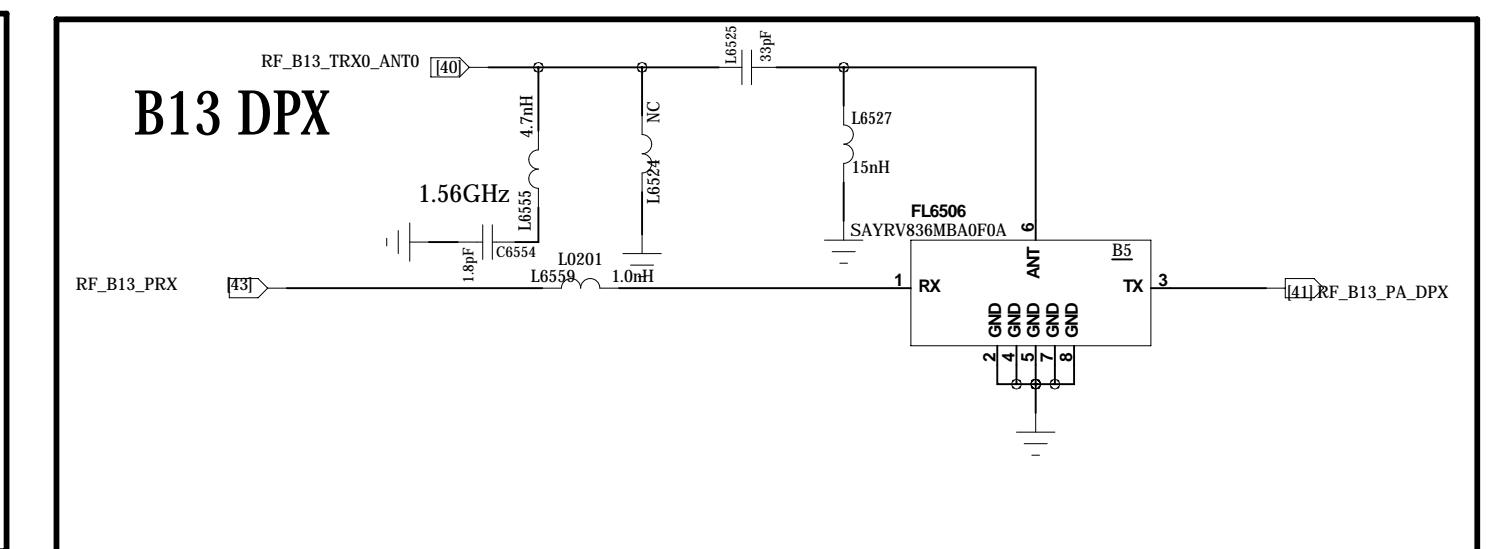
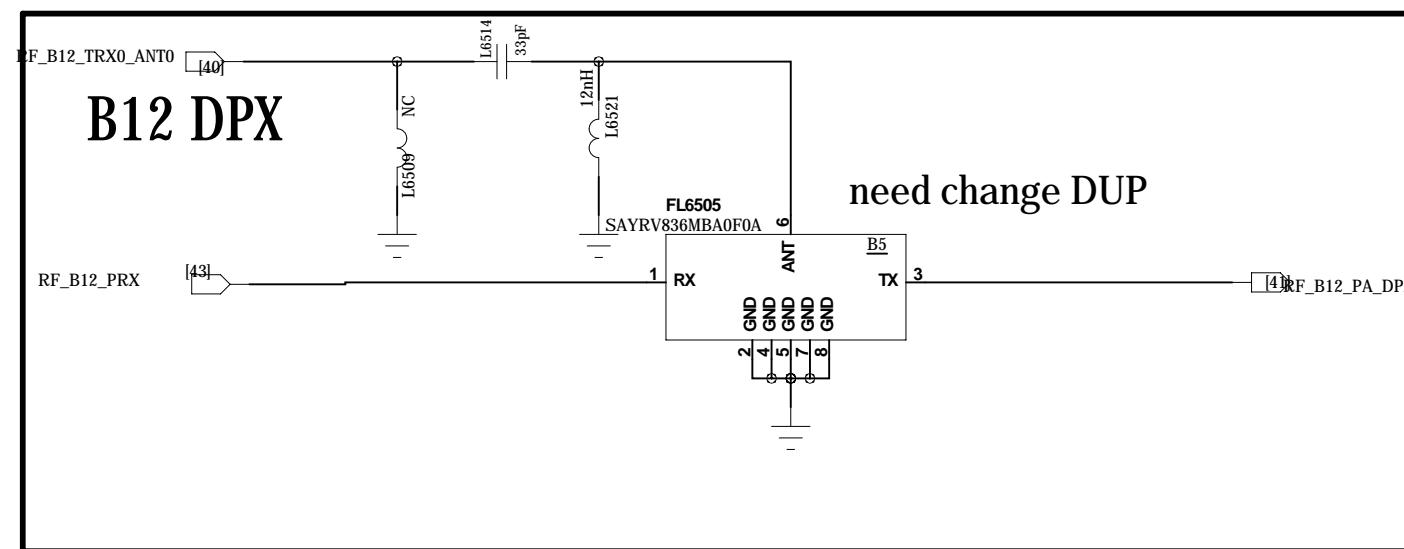
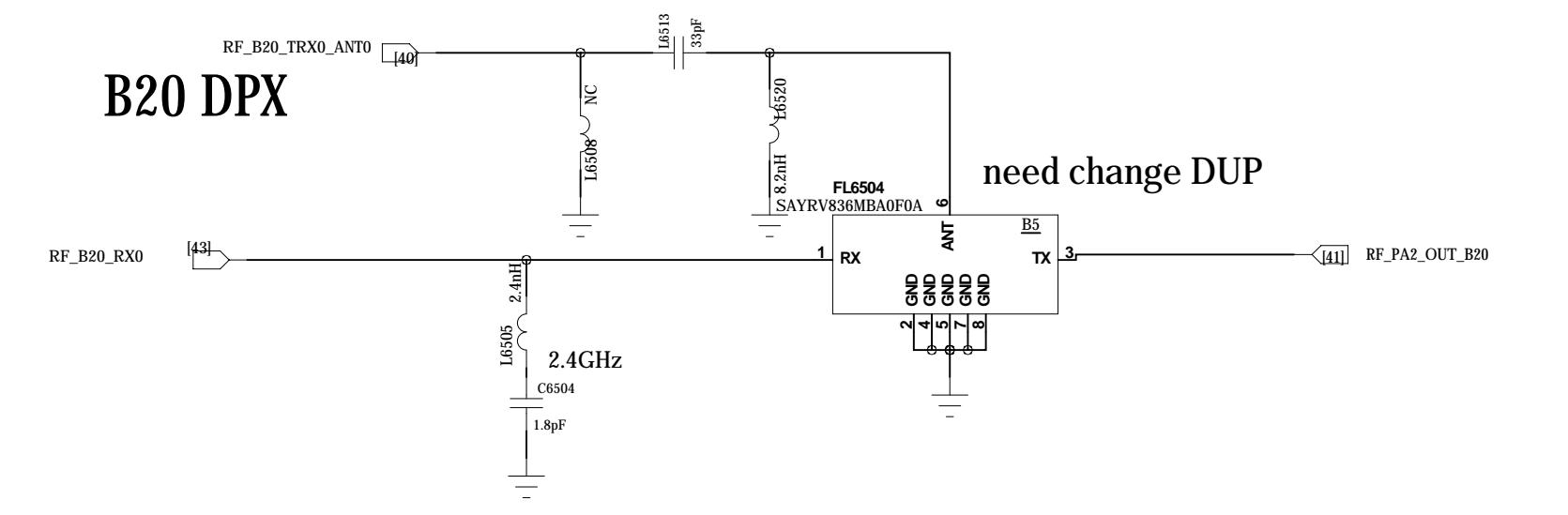
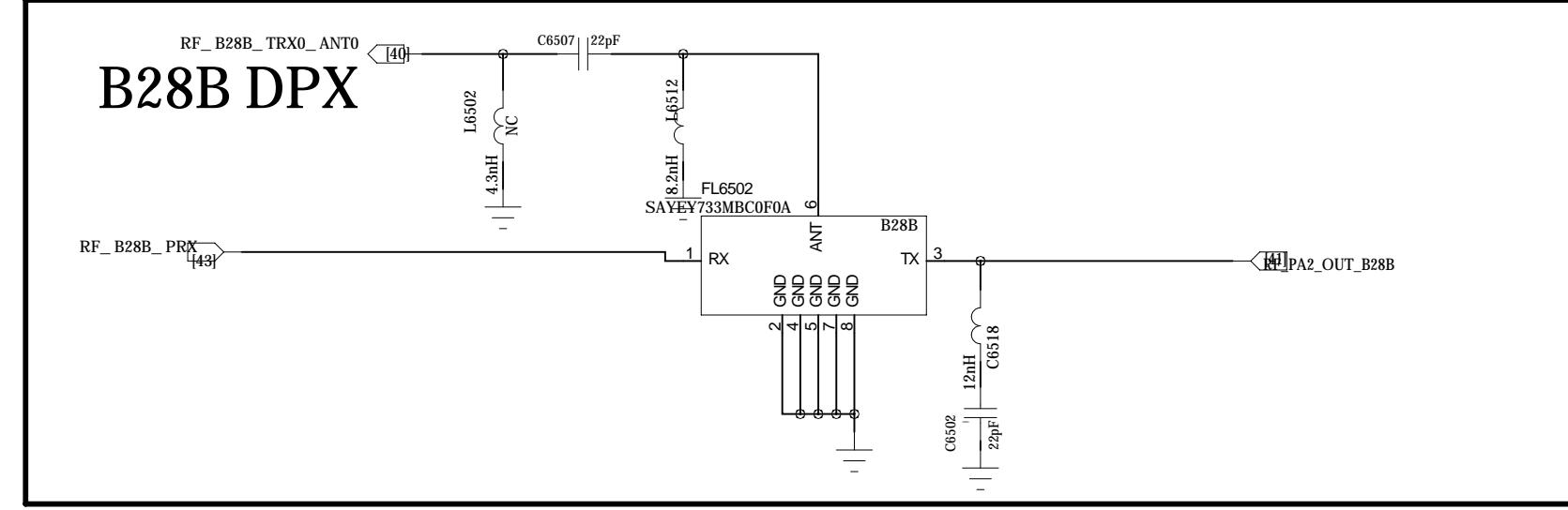
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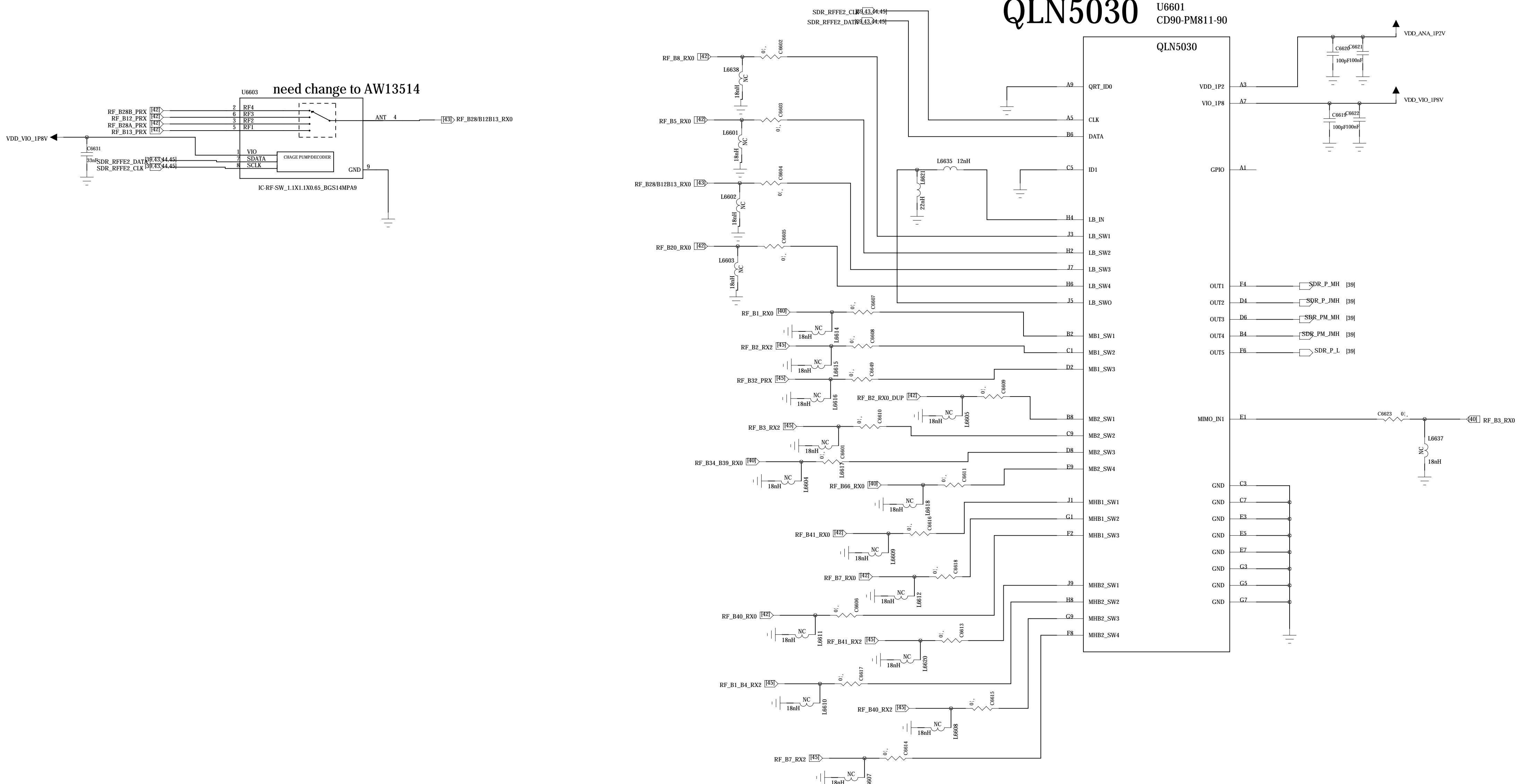
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CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
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P720AE	D	P720AE	V1.0
SCALE:	<Scale>	SCALE:	42 51

REVISION RECORD			
LTR	ECO NO:	APPROVED	DATE:

D

D

QLN5030



C

C

B

B

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A

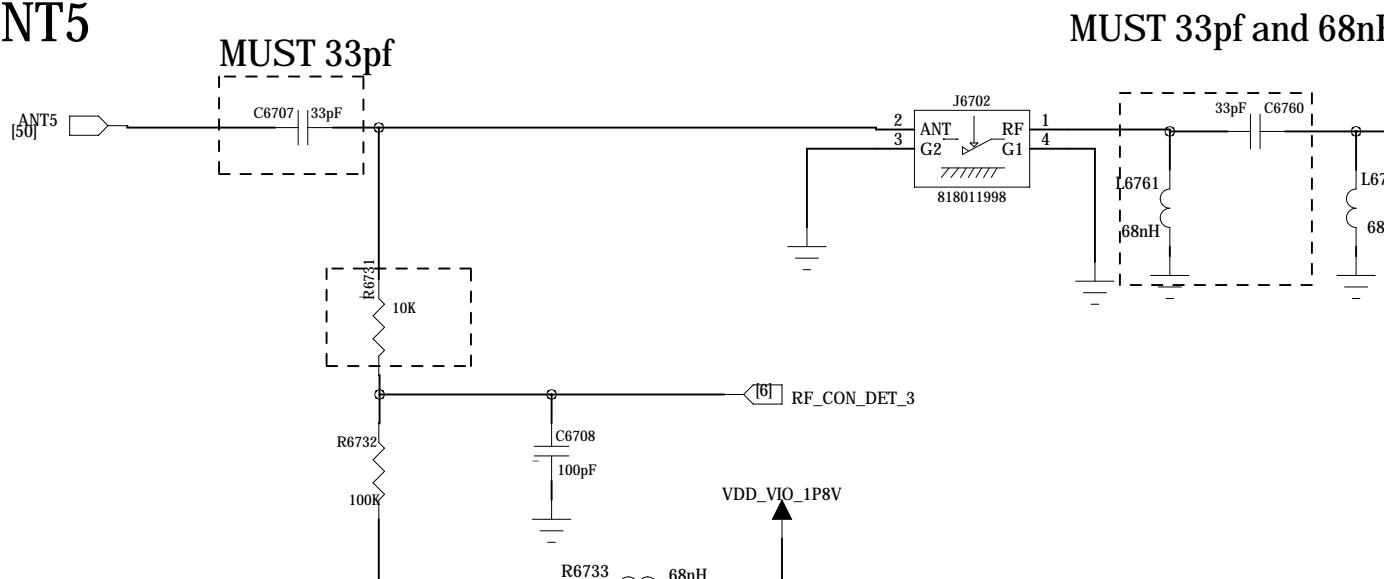
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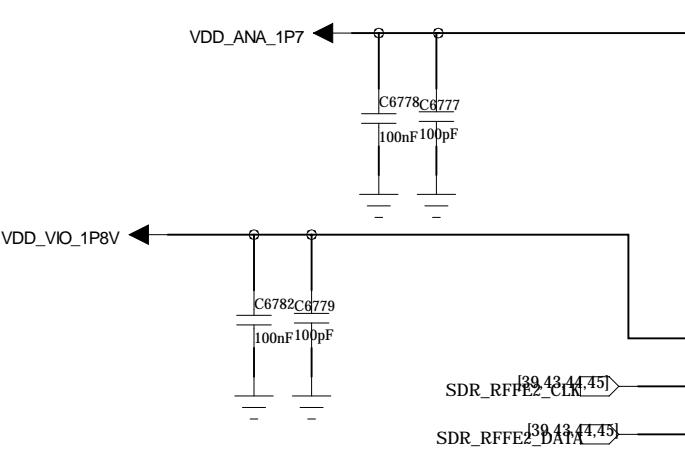
FX6877

QPM5477_PROTO
PRELIMINARY

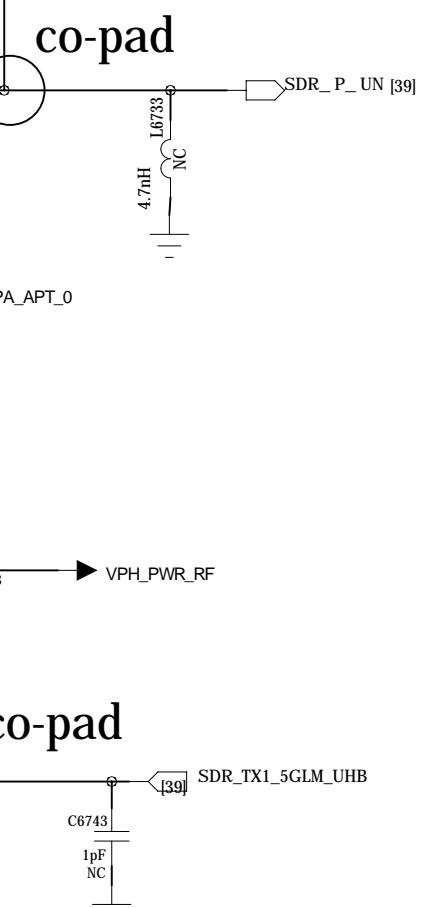
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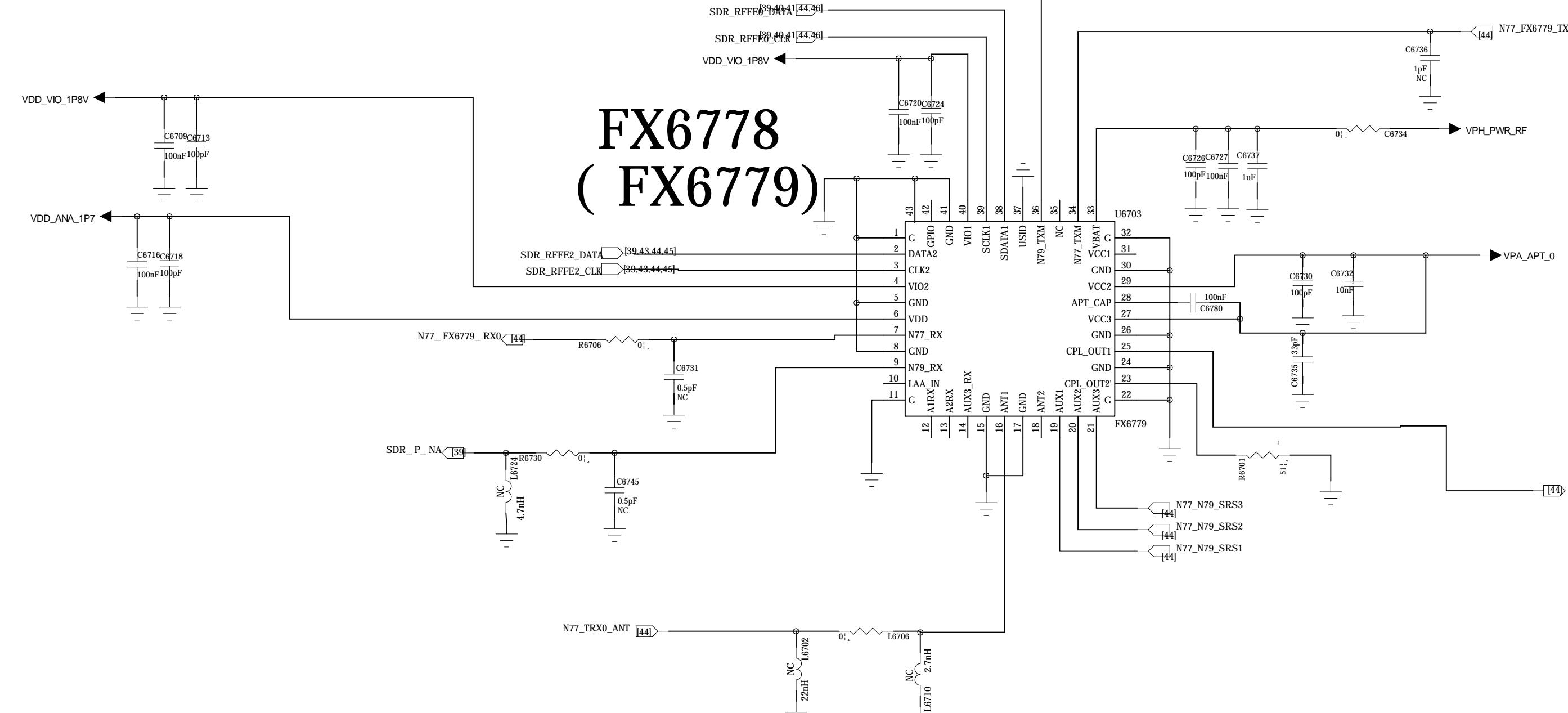
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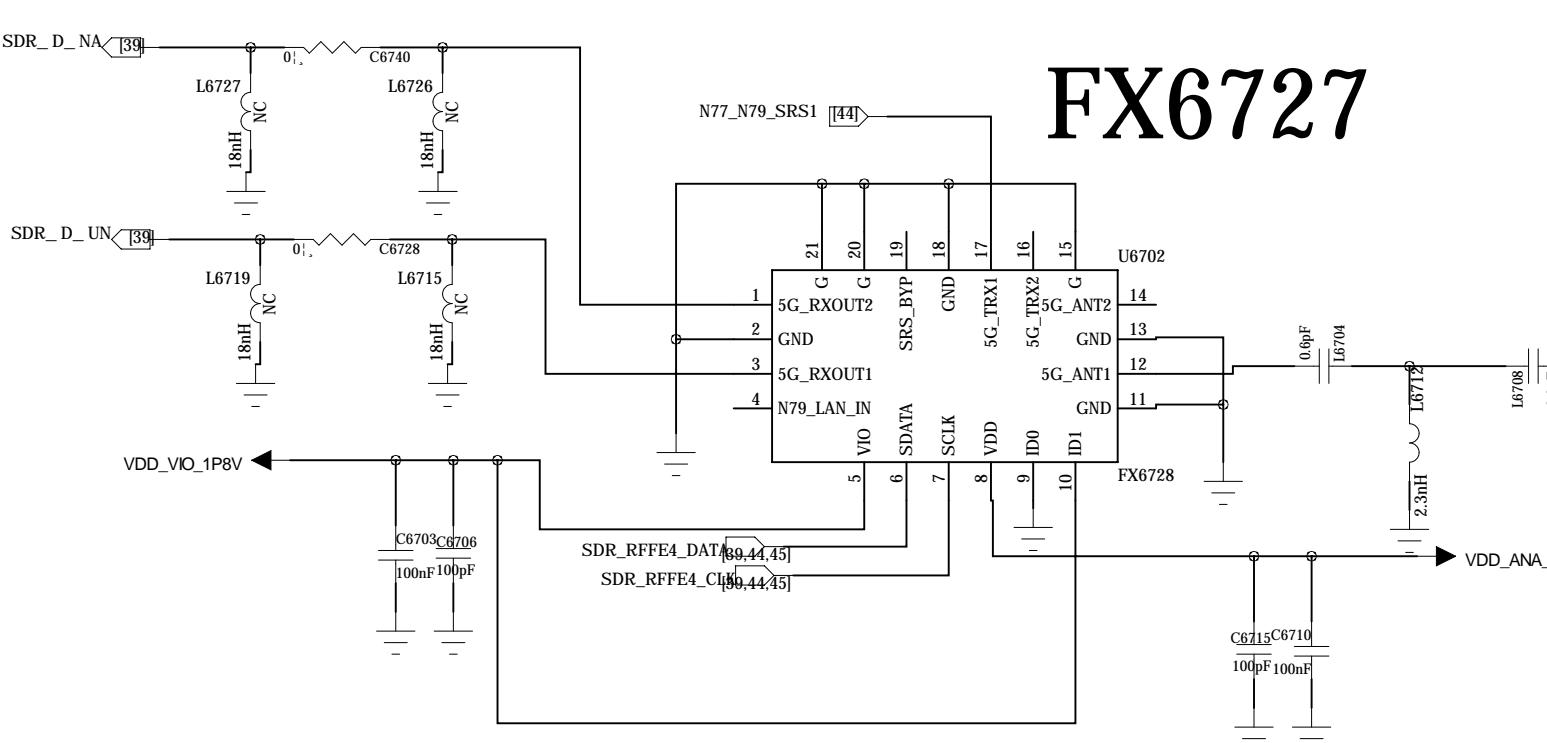
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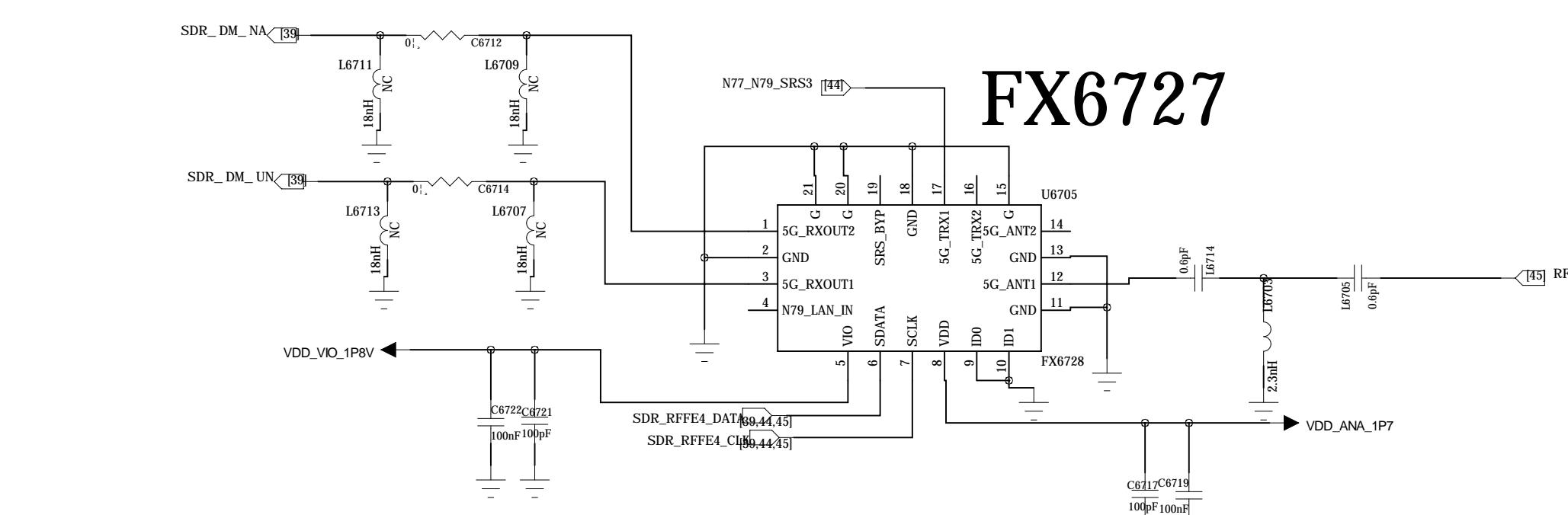
FX6778 (FX6779)



FX6727



FX6727



COMPANY: <Company Name>

TITLE: SM4350

DRAWN:	weye.feng	DATED:	20210820
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

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SCALE: <Scale> SHEET: 44 51

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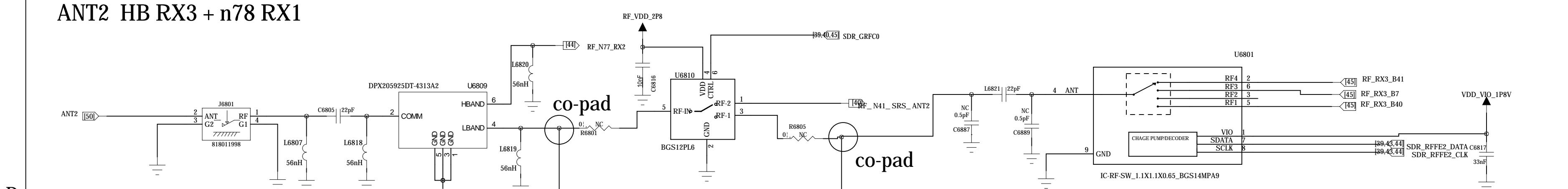
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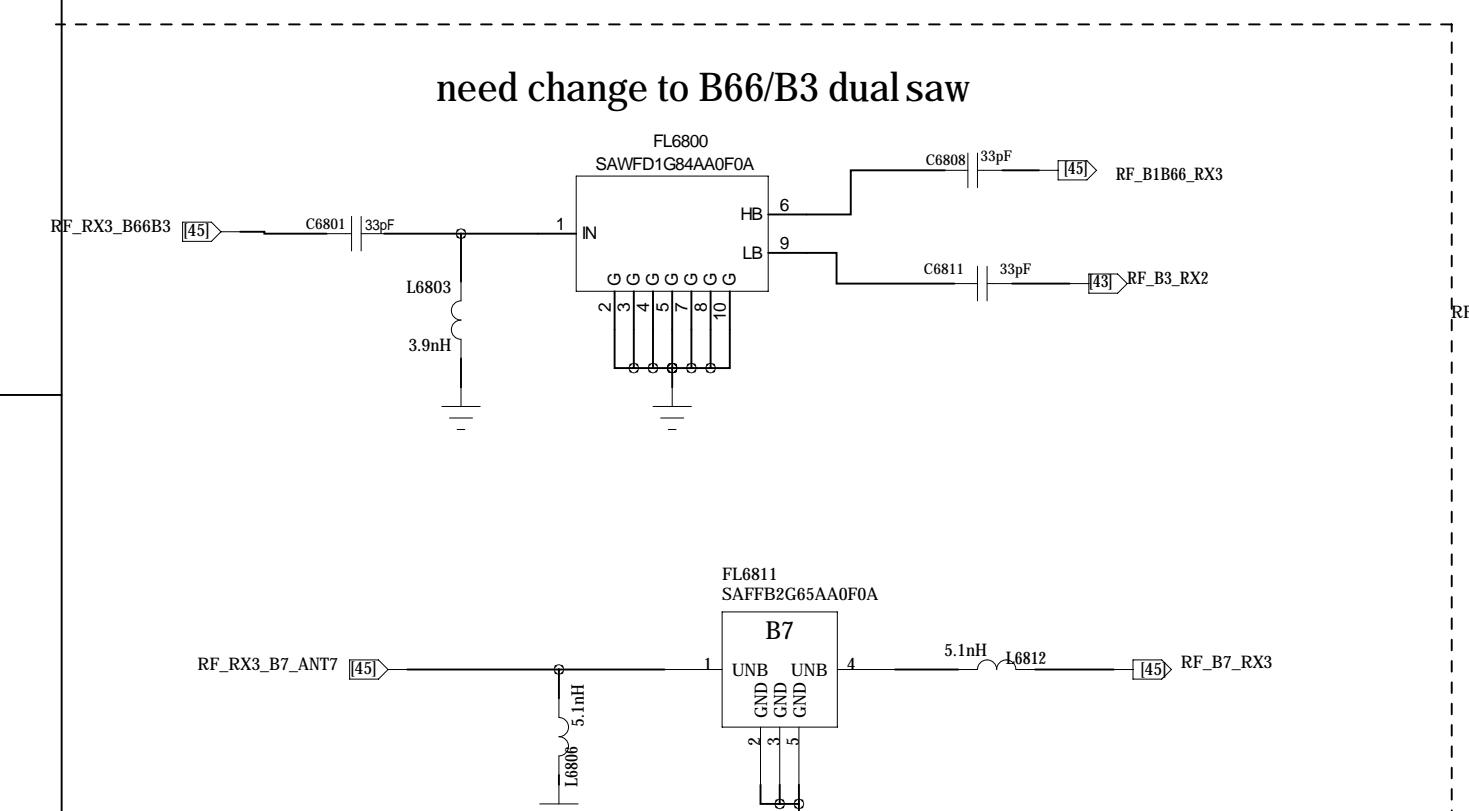
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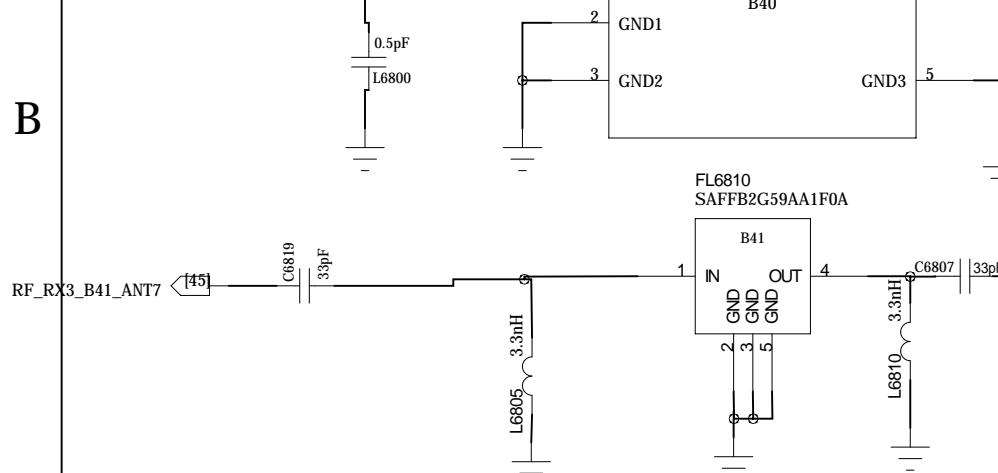
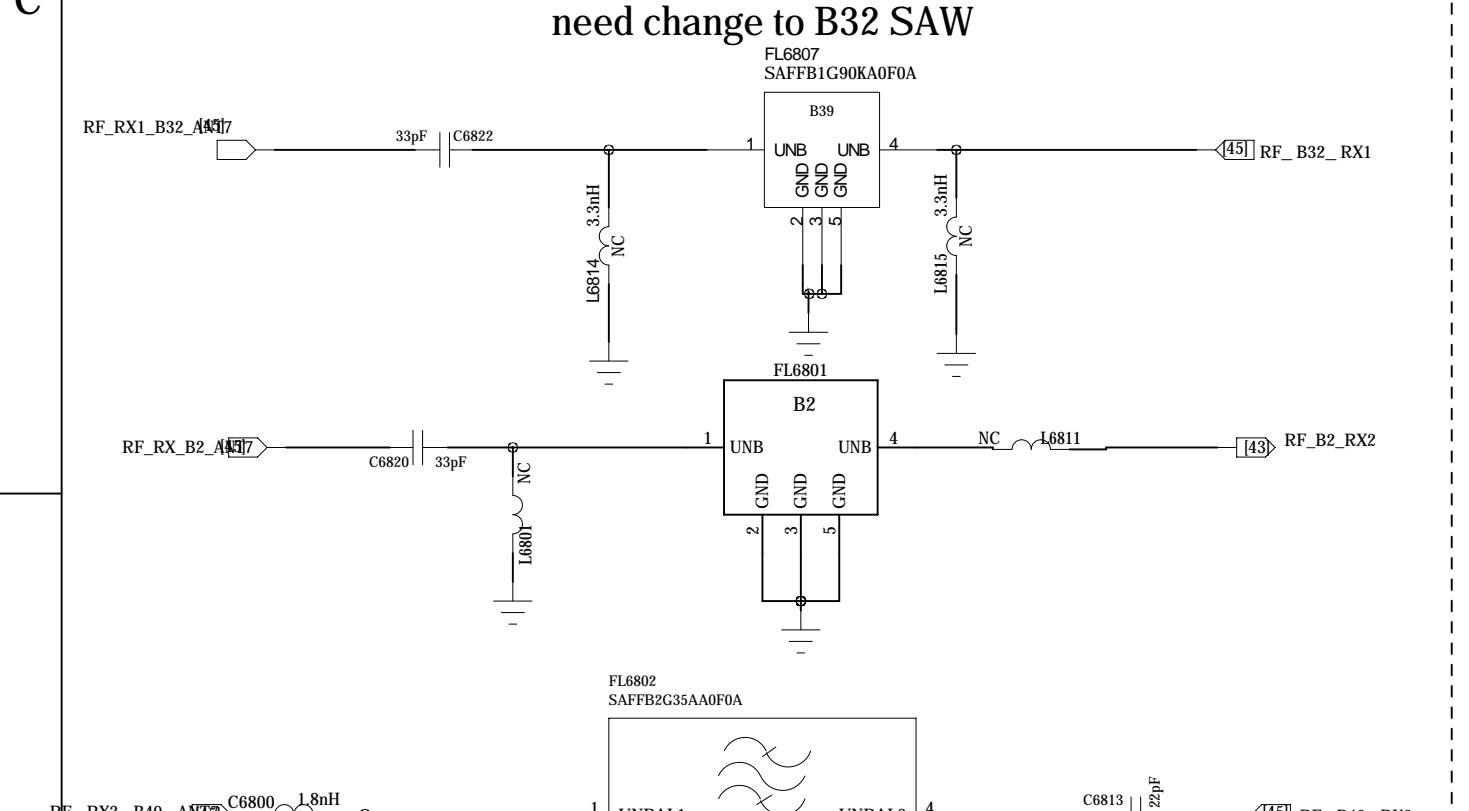
ANT2 HB RX3 + n78 RX1



need change to B66/B3 dual saw

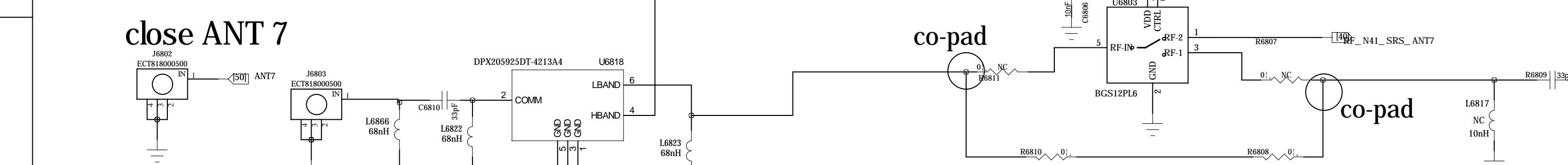


need change to B32 SAW

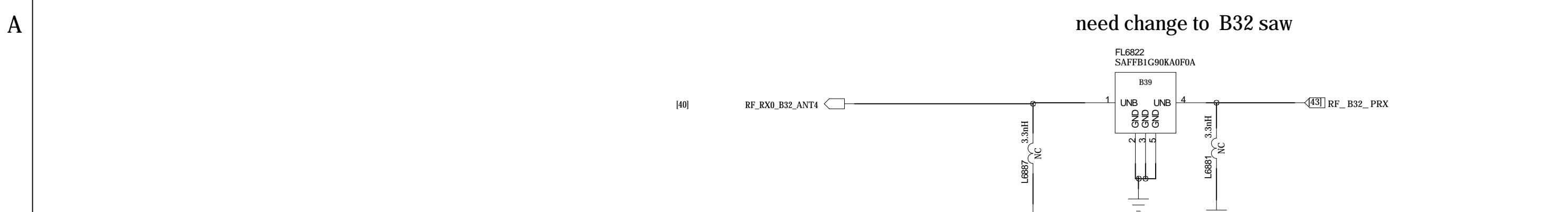


ANT 7 : MHB RX3+N77 RX3

close ANT 7

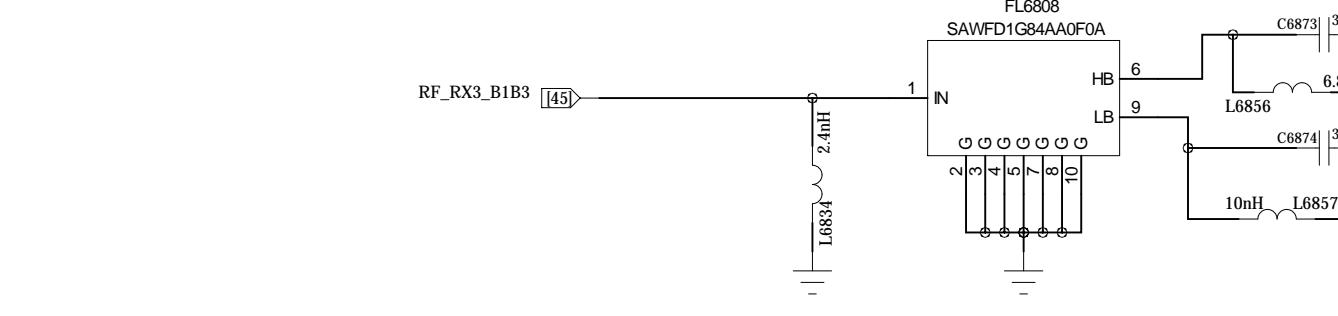


need change to B32 saw

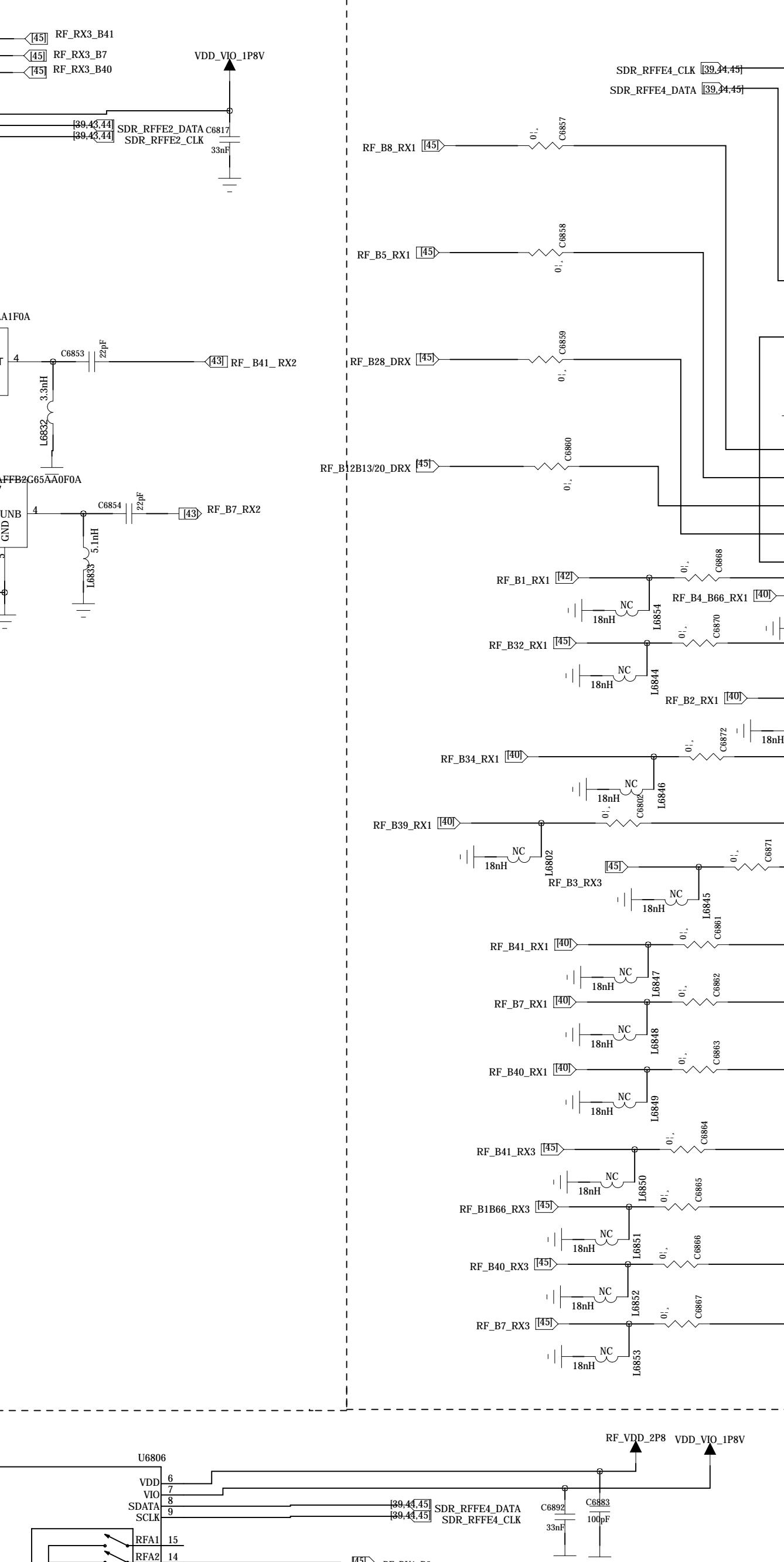


MXD86C2F

need change to B66/B3 dual saw

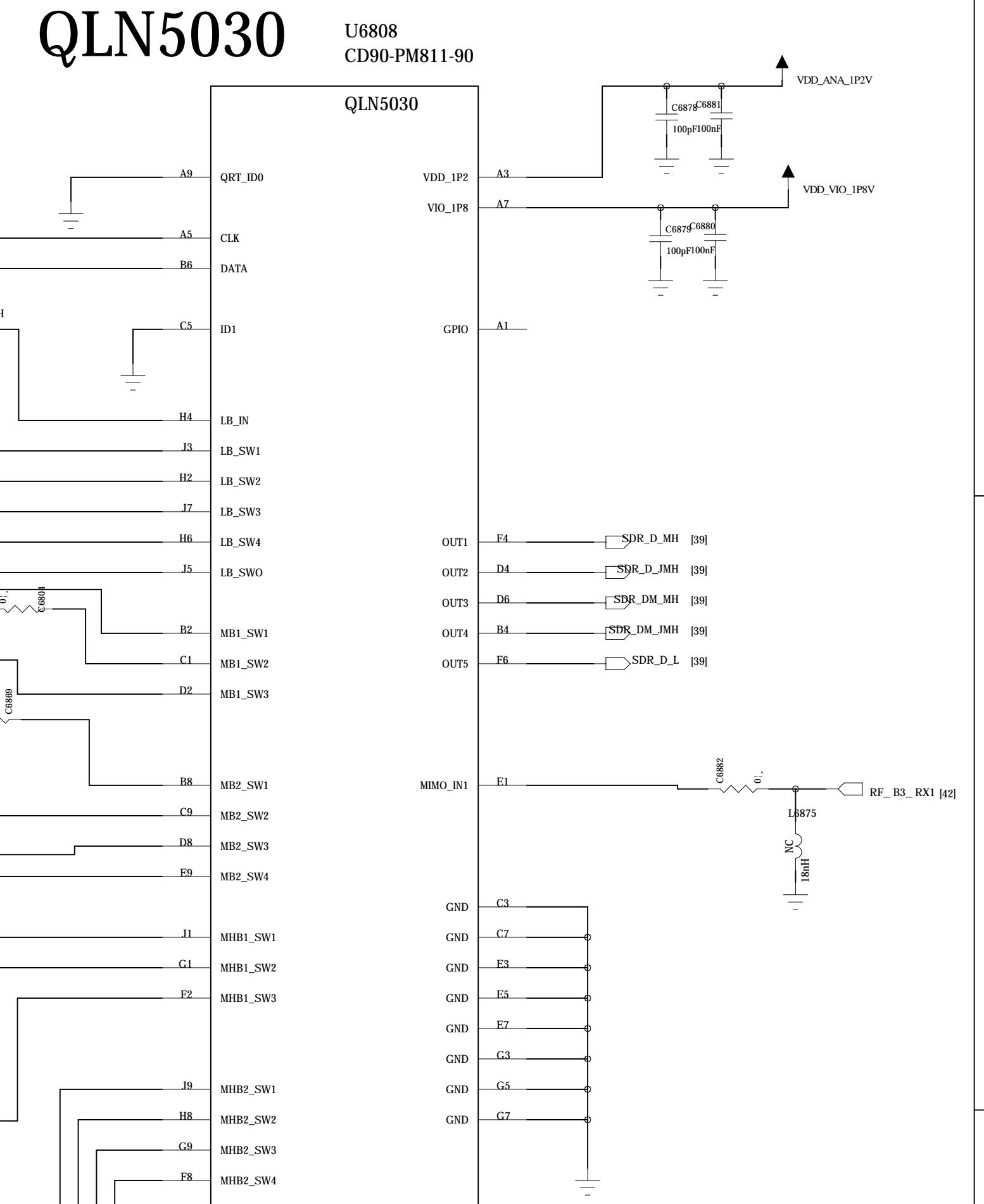


ANT 3: MB RX



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

QLN5030



<Company Name>

TITLE: SM4350

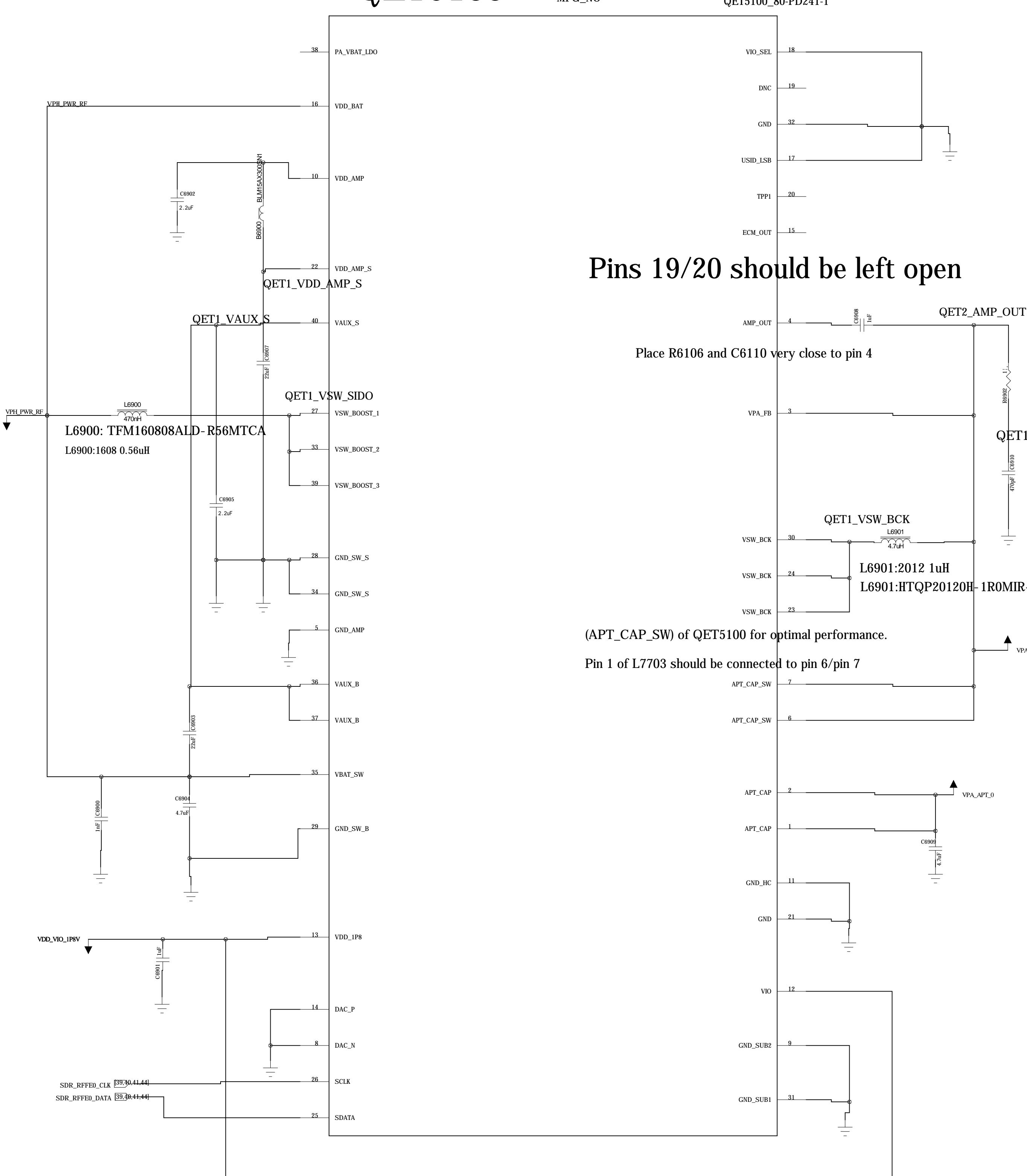
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TITLE:	SM4350
DRAWN:	weiye.feng
DATED:	20210820
CHECKED:	<Checked By>
DATED:	<Check Date>
QUALITY CONTROL:	<QC By>
DATED:	<QC Date>
RELEASED:	<Released By>
DATED:	<Release Date>
SCALE:	<Scale>
SHEET:	45 51

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

QET5100

U6900
MFG_NO

QET5100_80-PD241-1



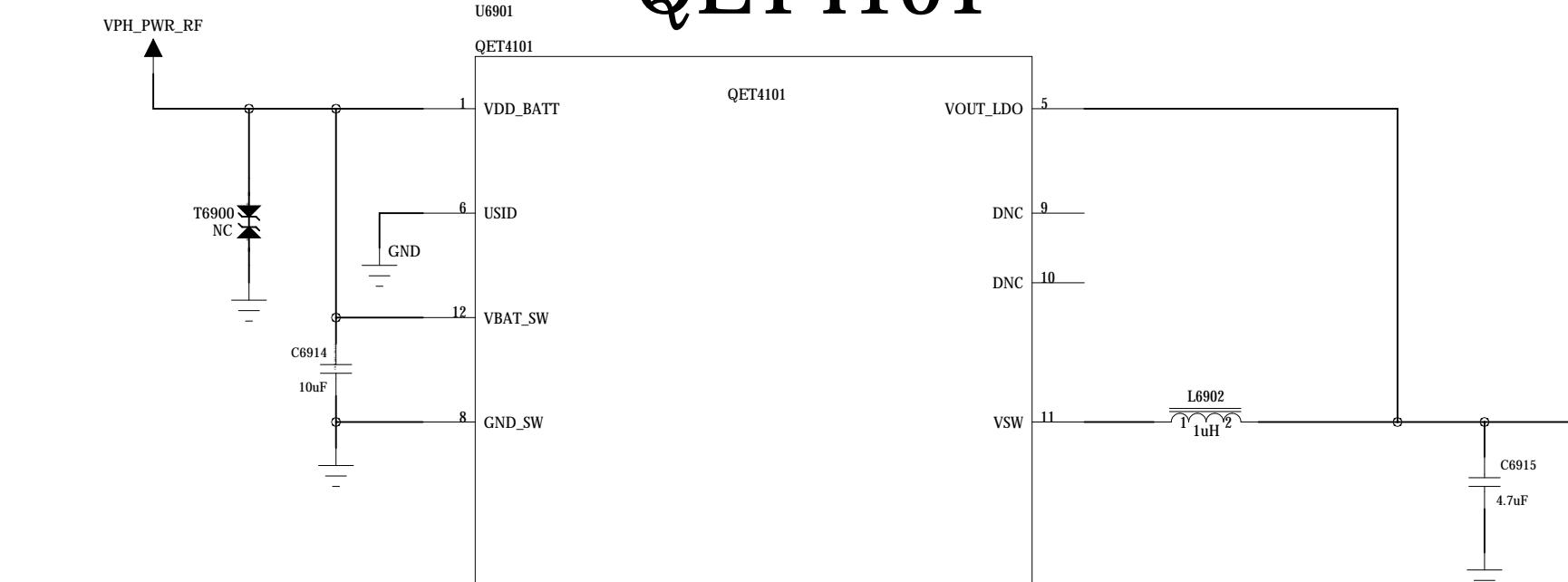
Pins 19/20 should be left open

Place R6106 and C6110 very close to pin 4

(APT_CAP_SW) of QET5100 for optimal performance.

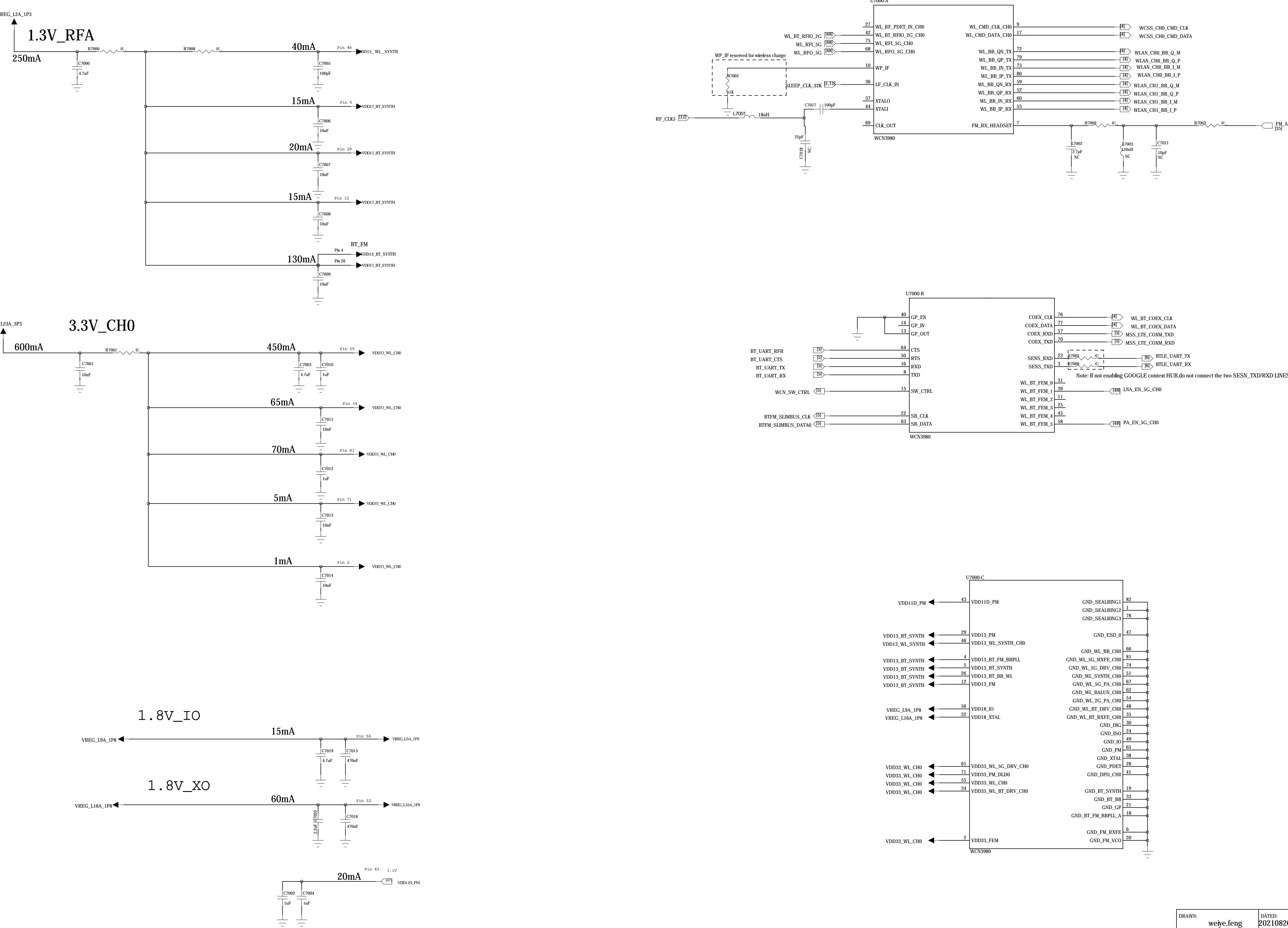
Pin 1 of L7703 should be connected to pin 6/pin 7

QET4101



COMPANY: <Company Name>	
TITLE: SM4350	
DRAWN: weiye.feng	DATED: 20210820
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>
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DRAWING NO: P720AE	REV: V1.0
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



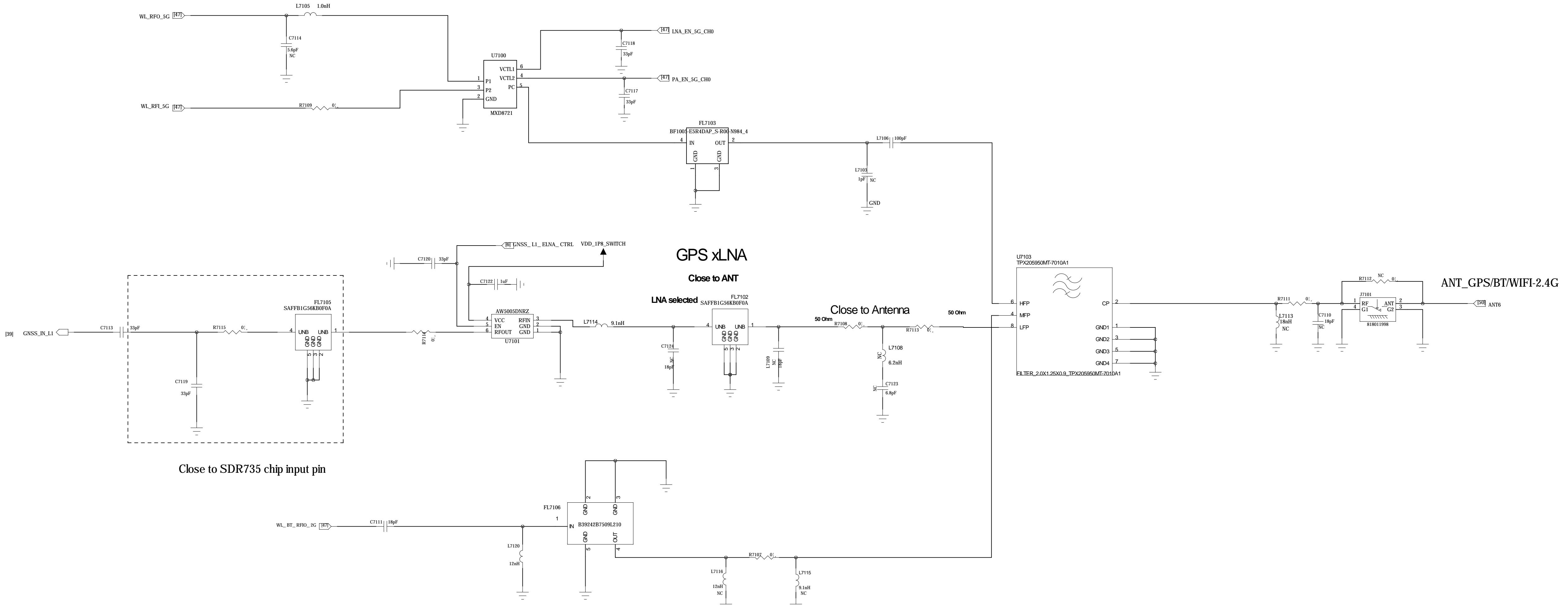
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REVISION RECORD			
ltr	ECO NO:	APPROVED:	DATE:

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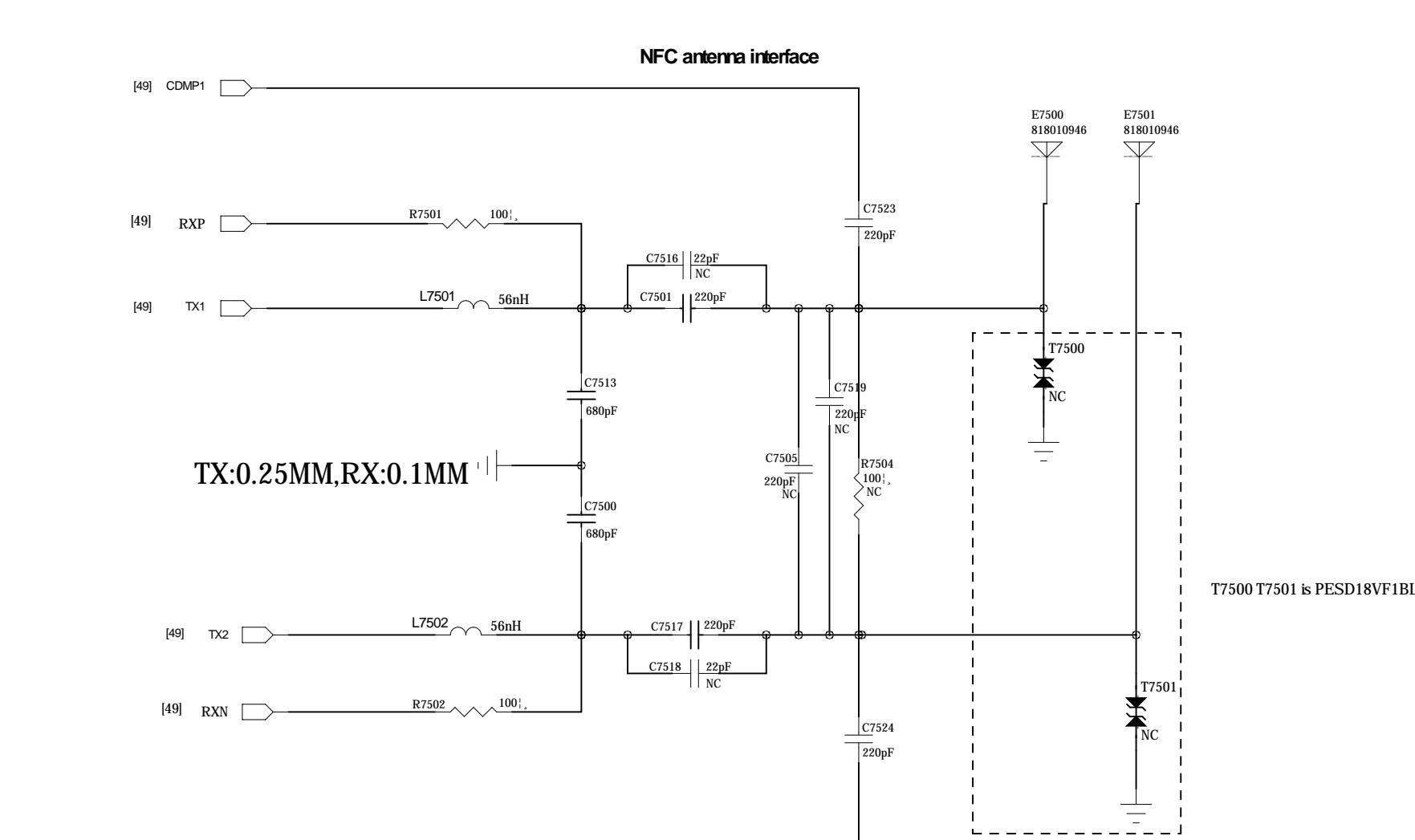
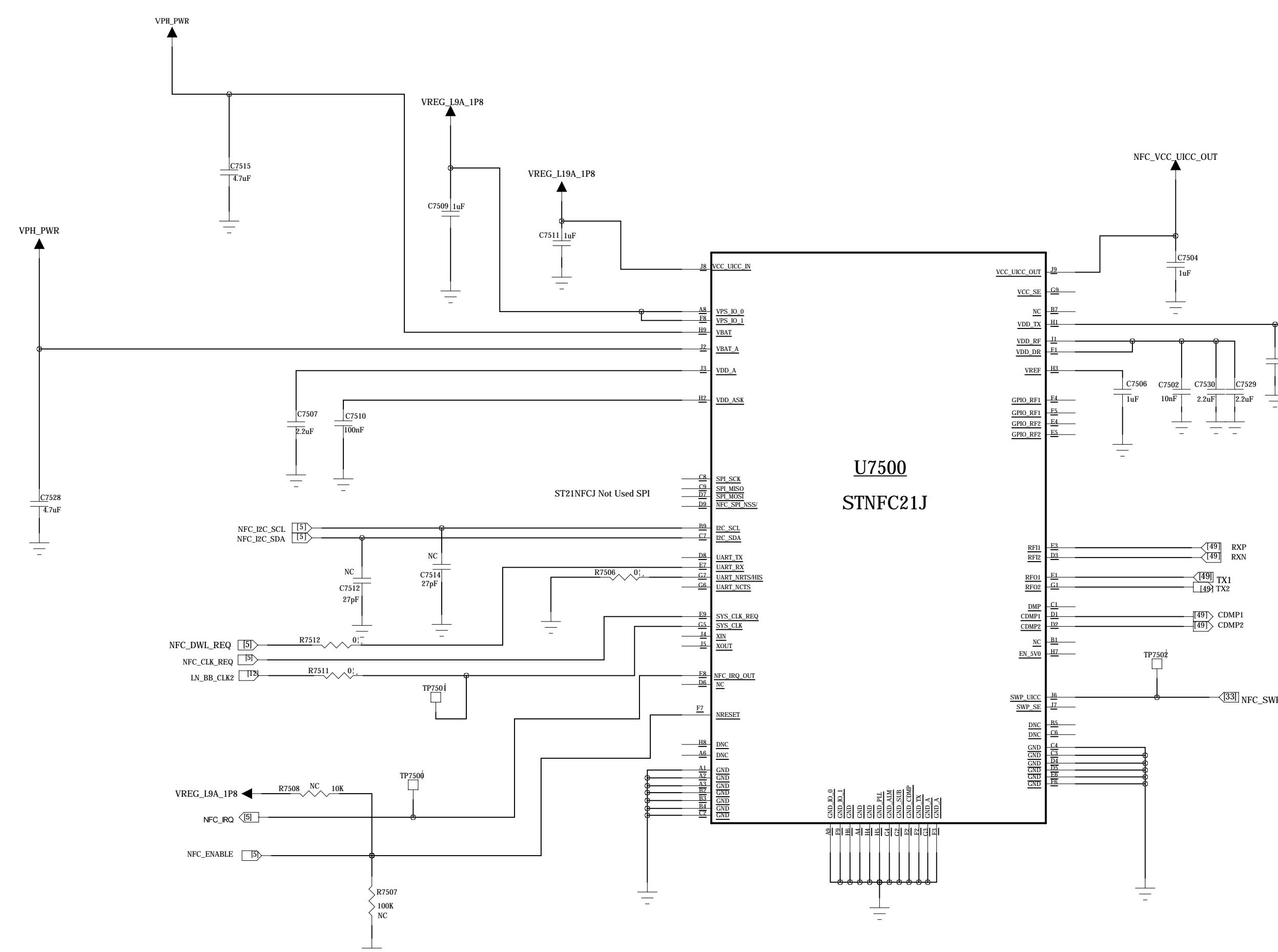
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WiFi BT GNSS L1 RF Circuit



COMPANY: <Company Name>			
TITLE: SM4350			
DRAWN: weye.feng	DATED: 20210820		
CHECKED: <Checked By>	DATED: <Checked Date>		
QUALITY CONTROL: <QC By>	DATED: <QC Date>		
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STNFC21J NFC Circuit



COMPANY:		<Company Name>		
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