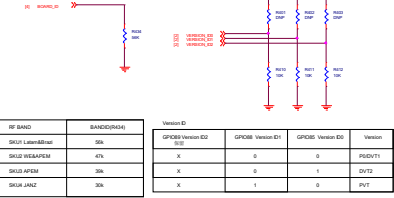


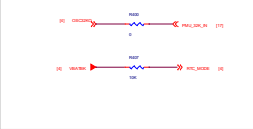
BOARD ID



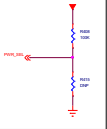
BKBT



RTC

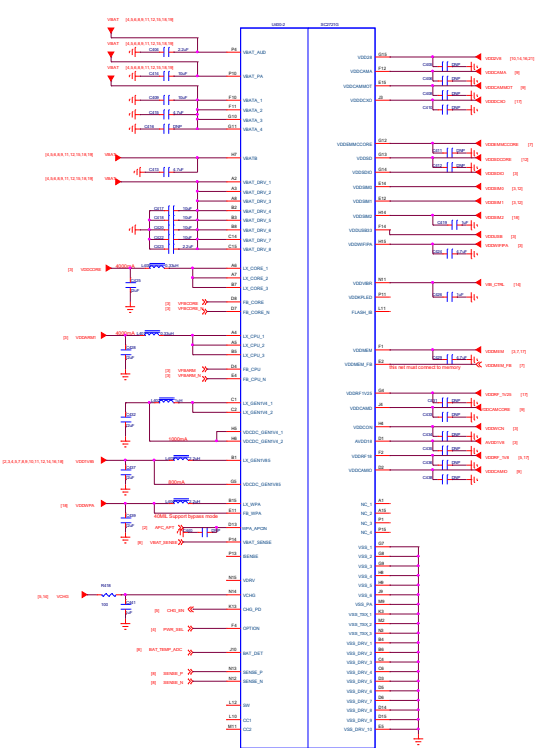
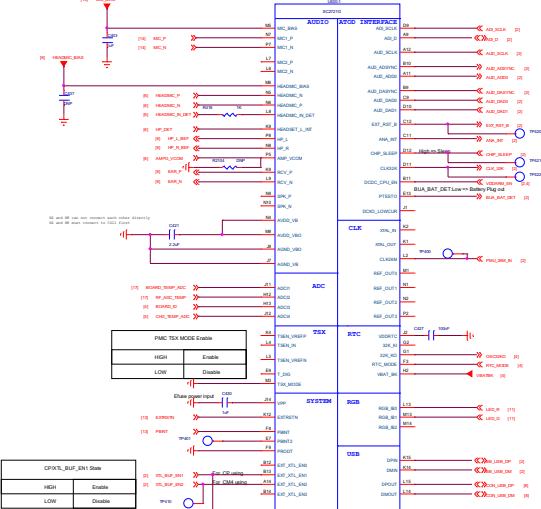


BKBT

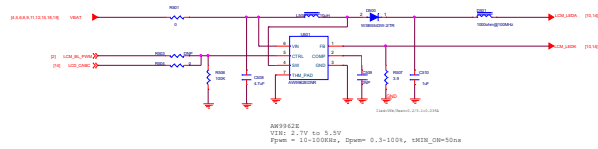


REGULATOR					
Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
VBAT	DCDCCPU	0.6-1.3	400mA	0.9V	ON
	DCDCCORE	0.6-1.3	400mA	0.9V	ON
	DCDCMEM	0.6-1.3	1000mA	1.2V	ON
	DCDCSEN	0.6-1.3	200mA	1.85V	ON
	DCDCWPA	0.6-1.3	1000mA		OFF

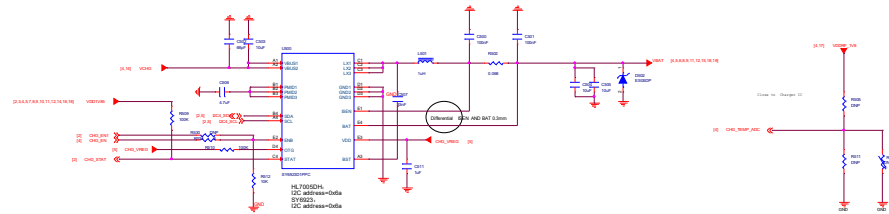
REGULATOR					
Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
LDO FOR VBAT	VDDCAM	1.8-3.3	100mA	2.0V	OFF
	VDDCAMDET	1.8-3.3	100mA	2.0V	OFF
	VDDSM0	1.8-3.3	100mA	1.8V	OFF
	VDDSM1	1.8-3.3	100mA	1.8V	OFF
	VDDSM2	1.8-3.3	100mA	1.8V	OFF
	VDDI2S	1.8-3.3	100mA	2.0V	ON
	VDDMEMCORE	1.8-3.3	400mA	3.0V	ON
	VDDSD	1.8-3.3	400mA	3.0V	ON
	VDDSD0	1.8-3.3	100mA	3.0V	ON
	VDDI2S	1.8-3.3	100mA	2.0V	ON
	VDDI2S	2.0-3.3	200mA	3.0V	ON
	VDDI2S	1.8-3.3	100mA	2.0V	OFF
	VDDI2S	1.8-3.3	100mA	2.0V	OFF
	VDDI2S	1.8-3.3	100mA	2.0V	OFF
	VDDI2S	1.8-3.3	100mA	2.0V	OFF
	VDDI2S	1.8-3.3	100mA	2.0V	OFF
DCDC CPU	VDDCPU	1.1-1.4	200mA	0.9V	OFF
	VDDMEM	1.1-1.4	400mA	1.2V	OFF
	VDDCAM	1.1-1.4	200mA	0.9V	OFF
	VDDI2S	1.1-1.4	200mA	0.9V	OFF
DCDC SEN	VDDSEN	1.1-1.4	200mA	1.8V	ON
	VDDSEN	1.1-1.4	200mA	1.8V	ON
	VDDSEN	1.1-1.4	200mA	1.8V	ON
	VDDSEN	1.1-1.4	200mA	1.8V	ON



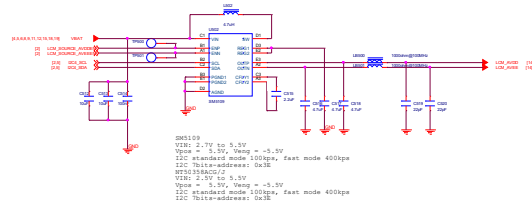
## LCM Backlight LED driver



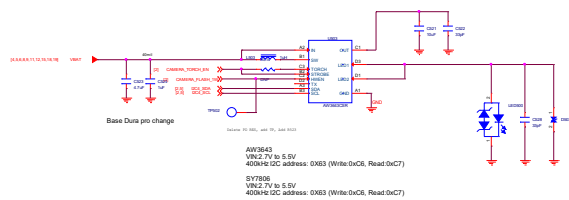
## Charger



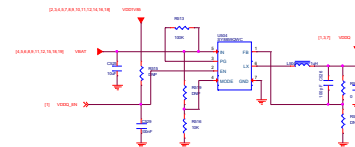
## LCD Bias



## Flash LED 5V Boost



## Buck default for VDDQ of LPDDR4X



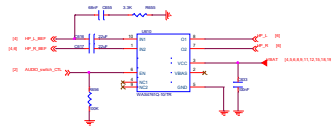
Schematic design notice of "27\_POWER\_SubPMIC-HV powers" page:

Note 27-1: To minimize RF de-sense, it is recommended to reserve 0-ohm and 0402 cap for BOM fine tuning.

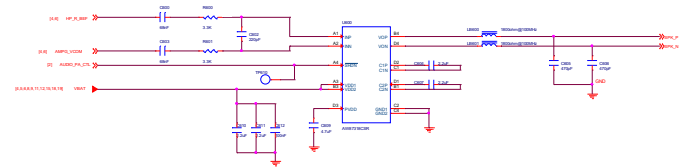
Note 27-2: To minimize RF de-sense, it is recommended to reserve 0-ohm and 0201 cap for BOM fine tuning.

Note 27-3: C2705 could be replaced with C / 1 / uF / 50V + C / 1 / uF / 50V

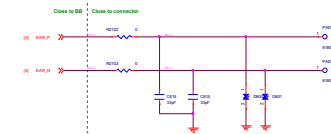
## Handset 2nd Microphone



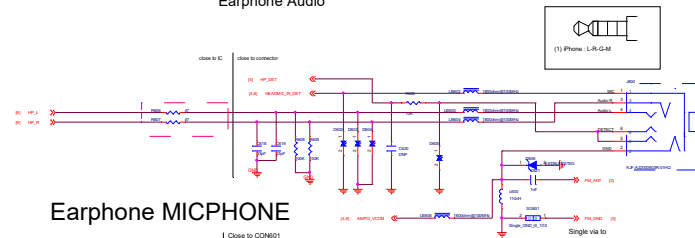
## SPEAKER



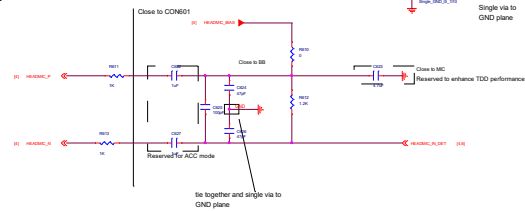
## EAR PIECE

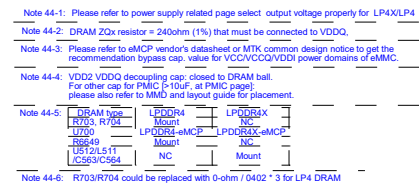


## Earphone Audio

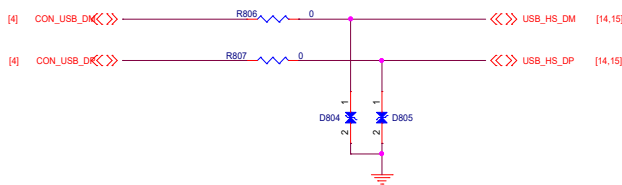
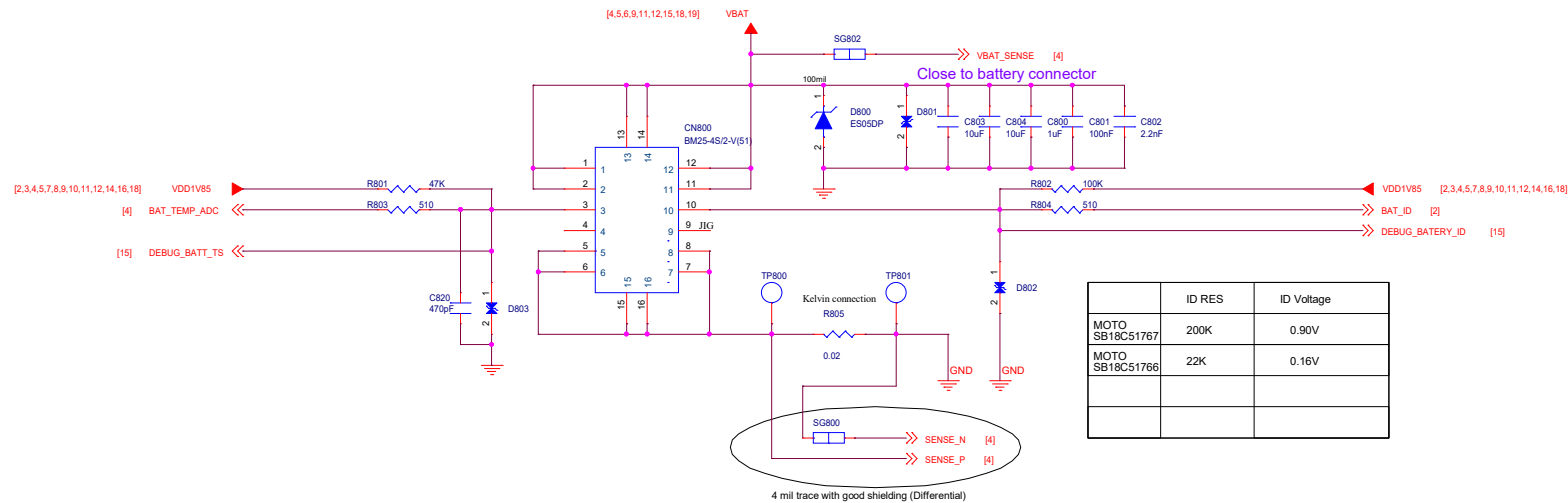


## Earphone MICPHONE



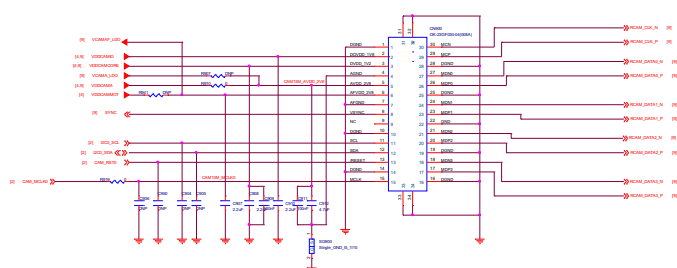


# BATTERY CONNECTOR

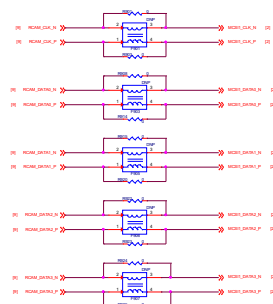




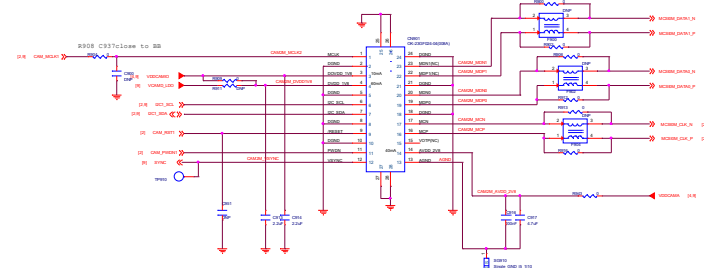
## Rear-Main Camera(AF-13M)



## Real Camera - 13M

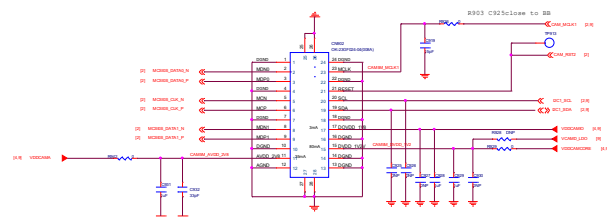


## Rear-Slave Camera(FF-2M)

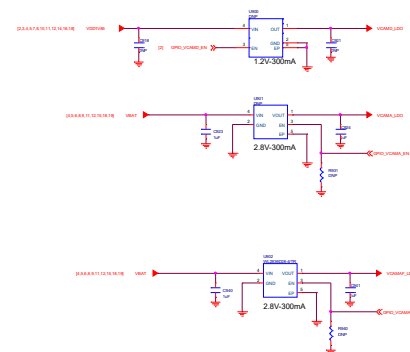


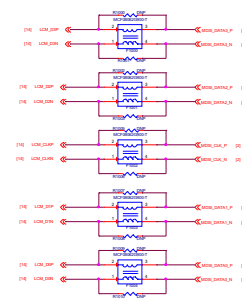
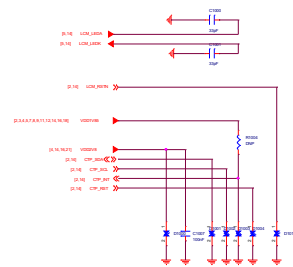
## Rear Slave Camera - 2M

## Front-FF-5M Camera Connector

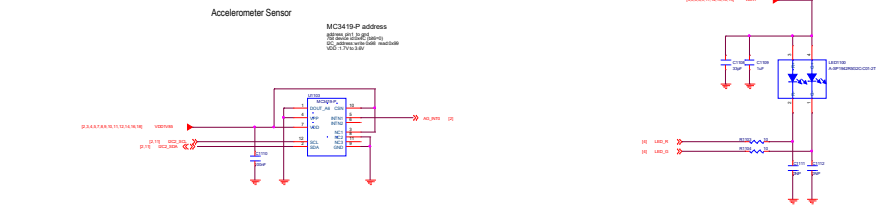


## Front Camera - 5M

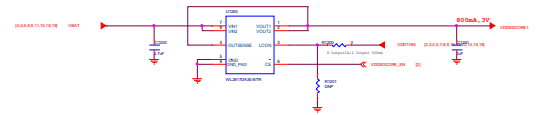




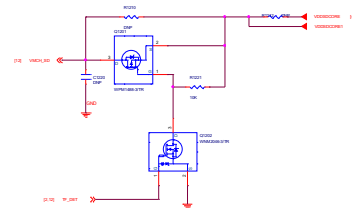
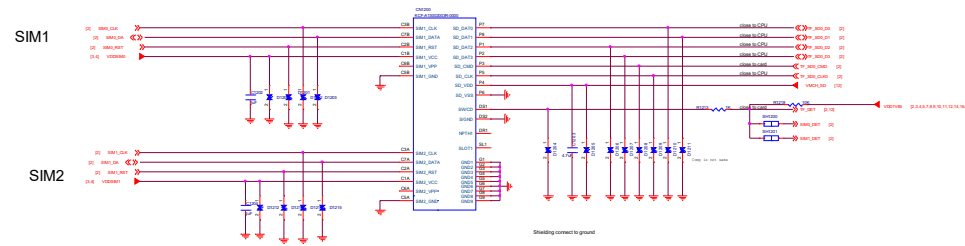
GT1151 I2C address: 0x5D (Write:0xBA, Read:0xBB)  
or 0x14 (Write:0x28, Read:0x29)



## SD3.0

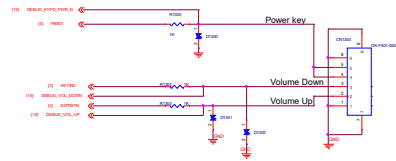


## TF-CARD

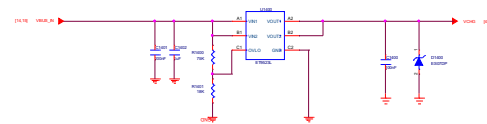


## KEY

Volume Up : HOME Key / GND  
Volume Down : KPCOL1/GND  
DO NOT put pull-up resistor on PWRKEY



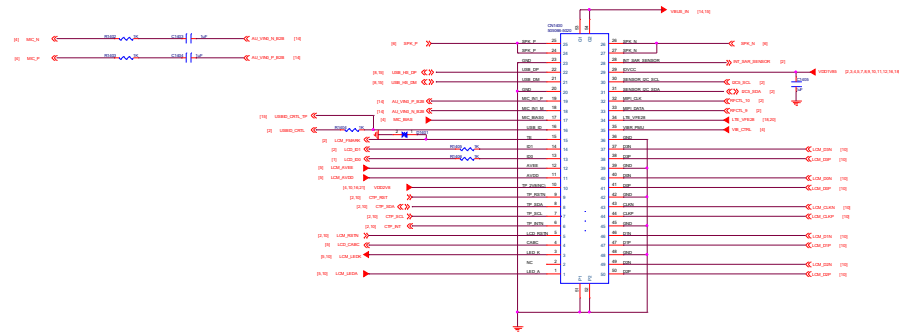
OVP



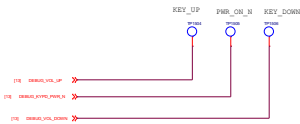
$$VIN\_OVLO = VOVLO\_TH \times (1 + R1/R2) = 1.2 \times (1 + 75/18) = 6.2$$

## Sub Connector

0xXX (Write:0xXX, Read:0xXX)



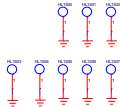
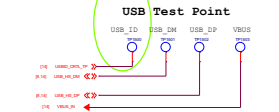
Download mode TP



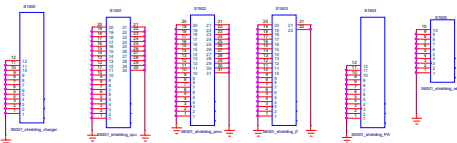
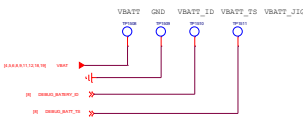
KEY Test point

USB Test point

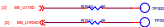
Now TP size is 1.2, need?

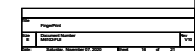


BATTERY Test point

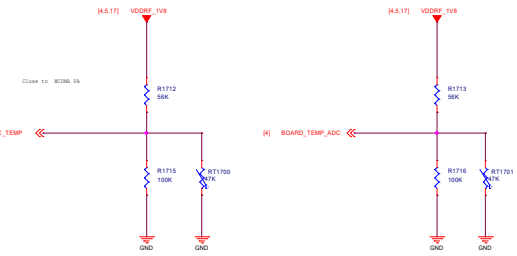
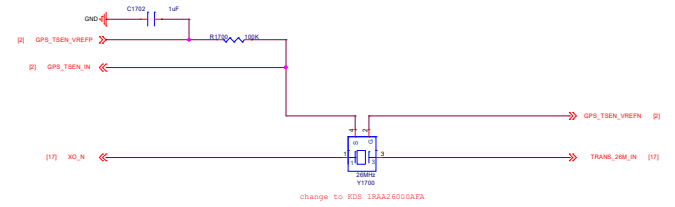
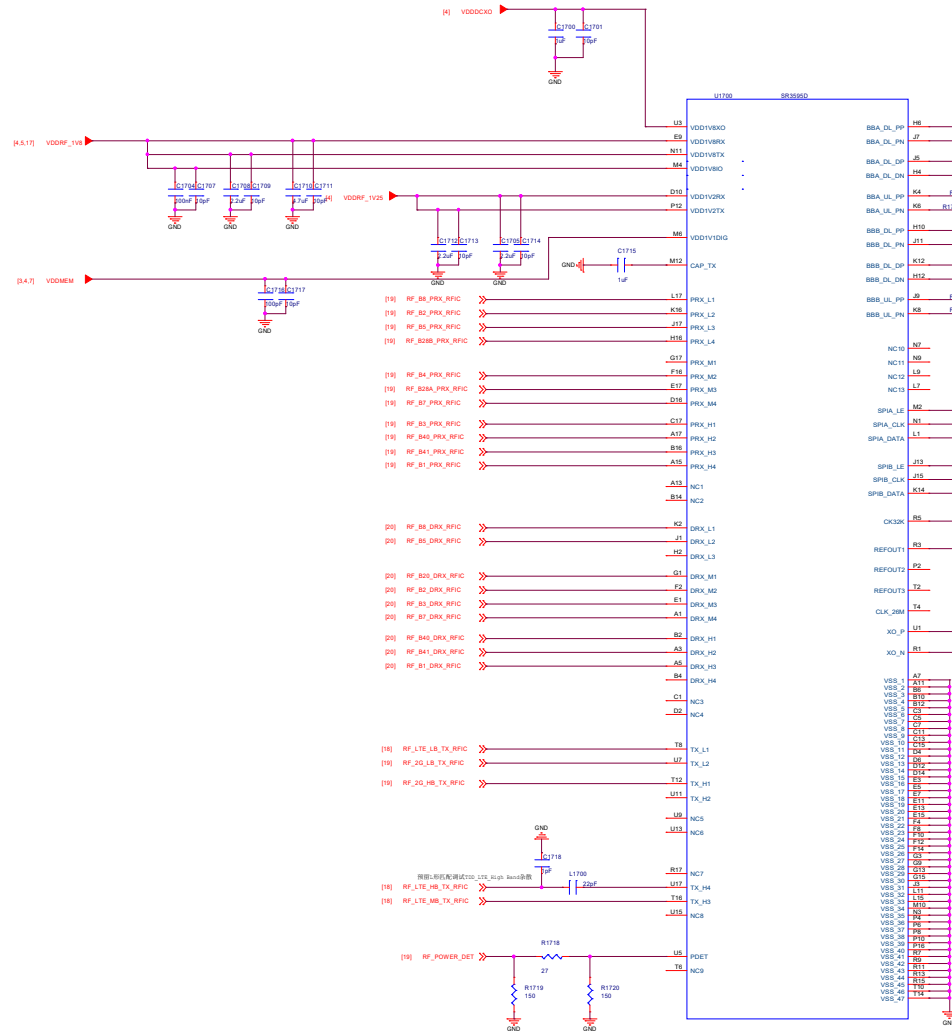


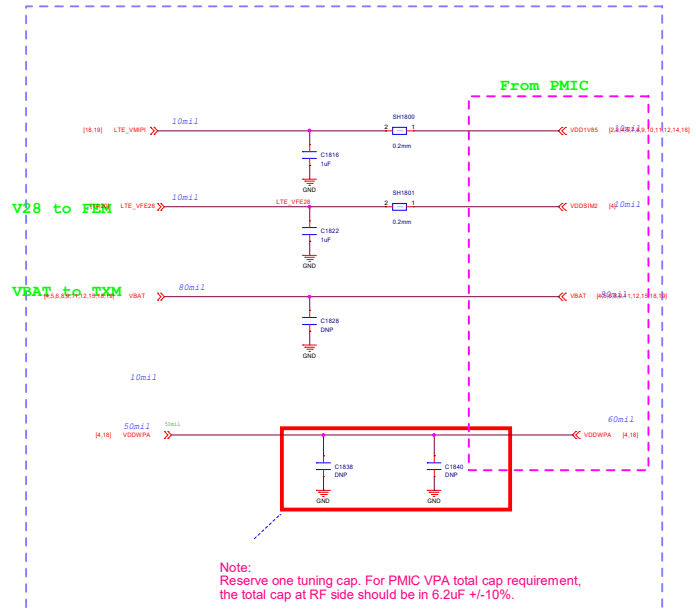
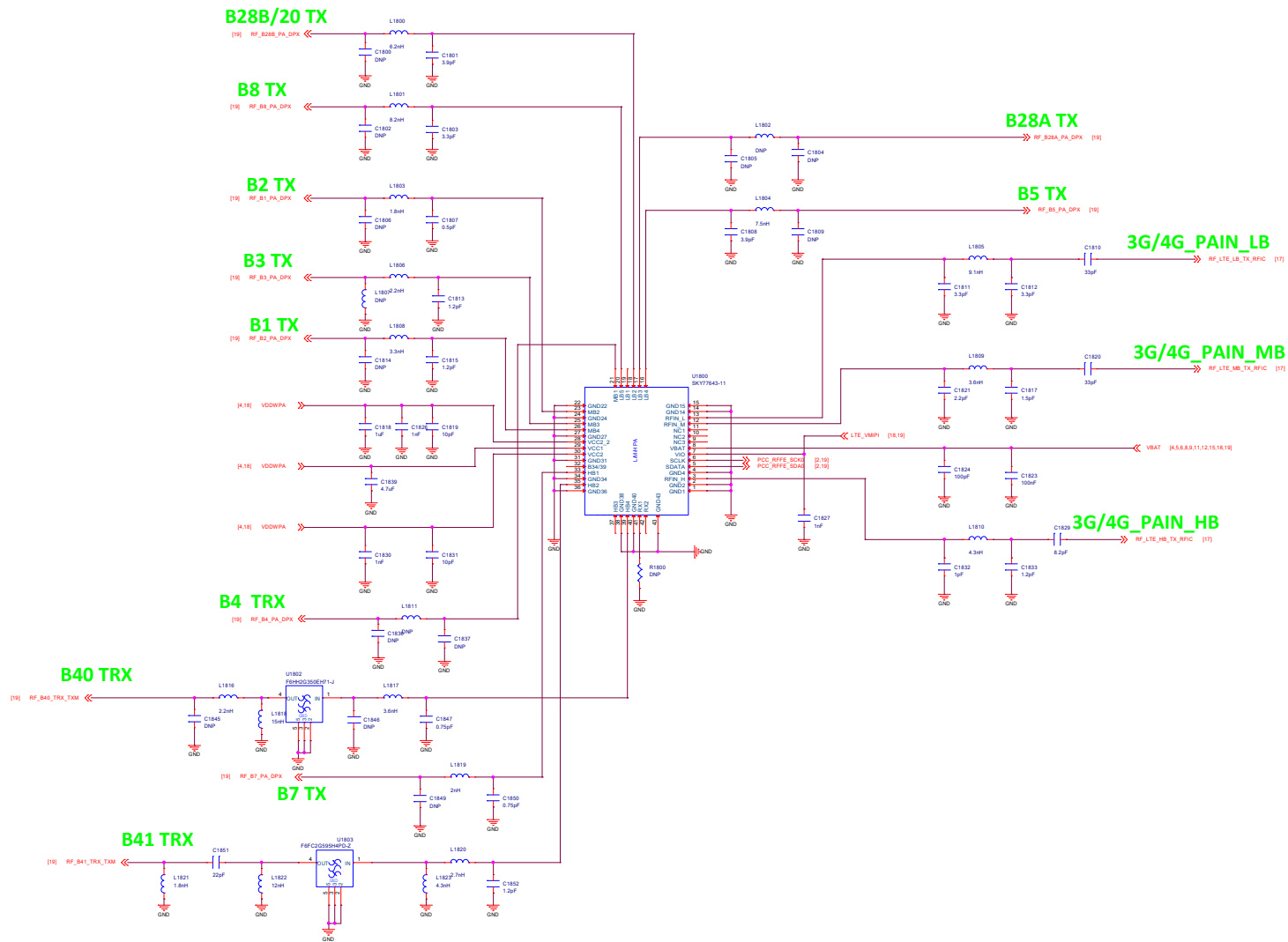
UART



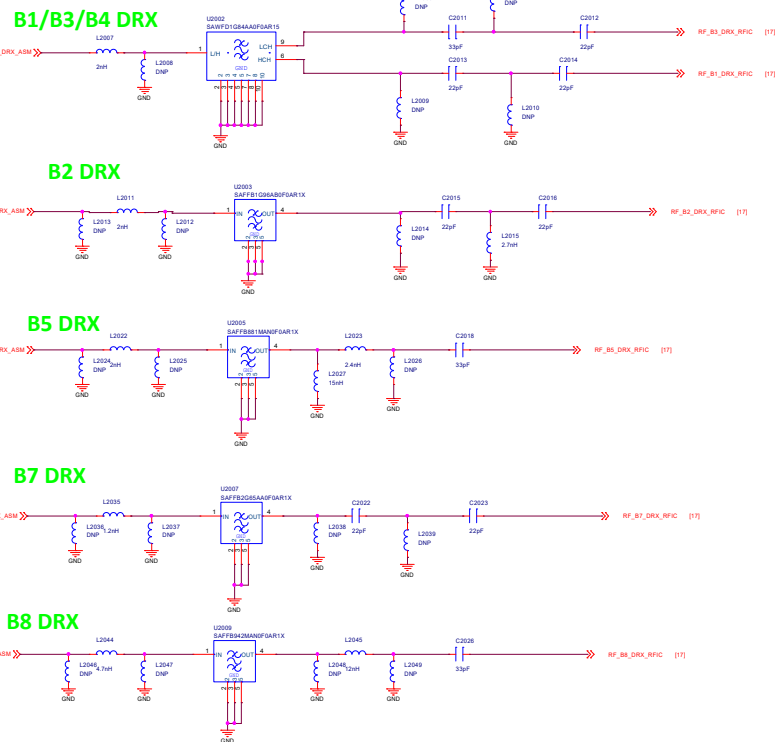




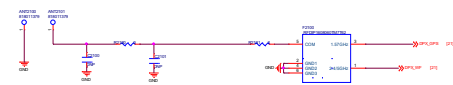




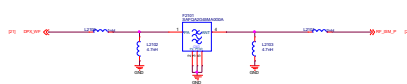




### WIFI&BT&GPS ANT



### WIFI/BT SAW



### GPS SAW LNA

