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Size A4	Number		Revision
Date:	2024/8/16		Sheet of
File:	E:\CYX\原理图\项目5 15\0.SchDoc		Drawn By:

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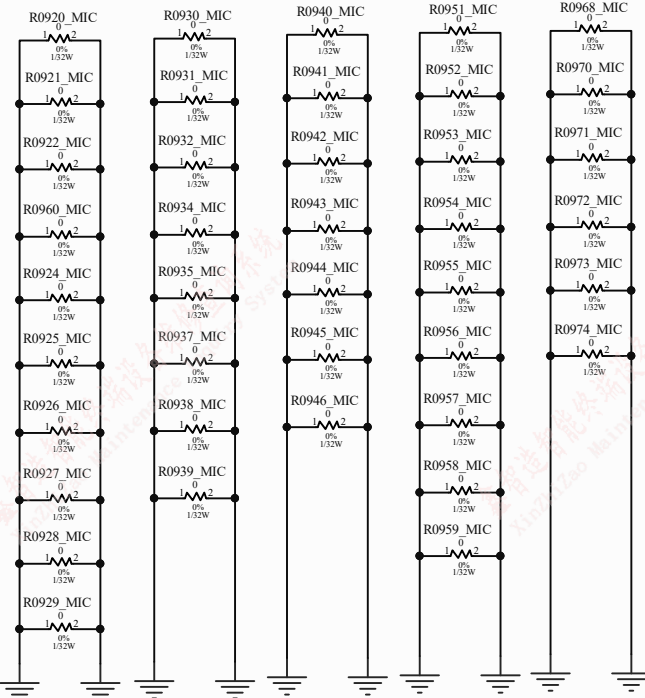
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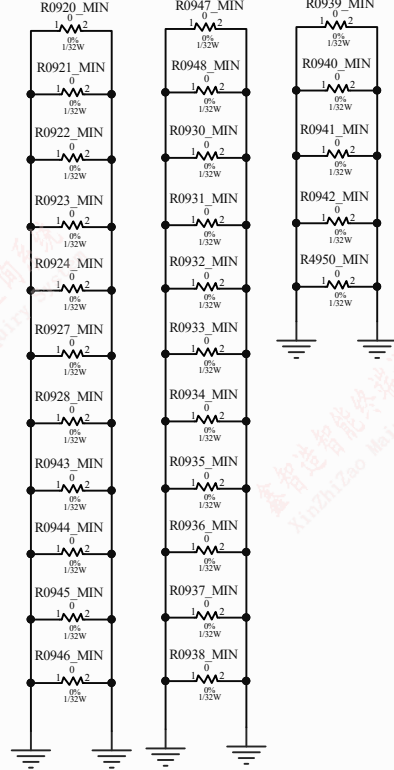
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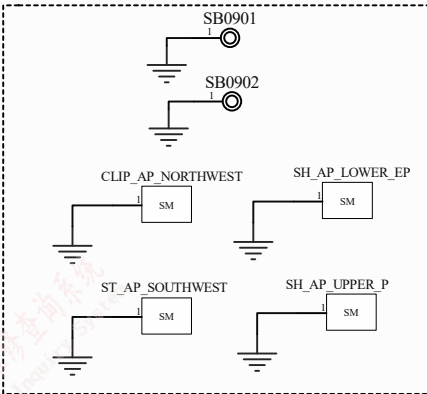
NADD MICRO UF



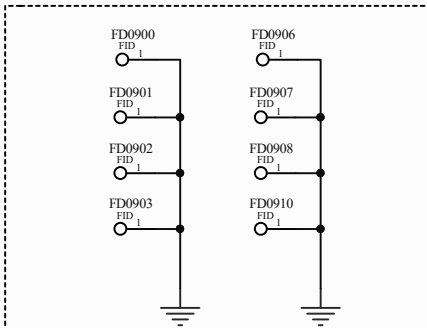
NADD MINI UF



STANDOFFS



FIDUCIALS



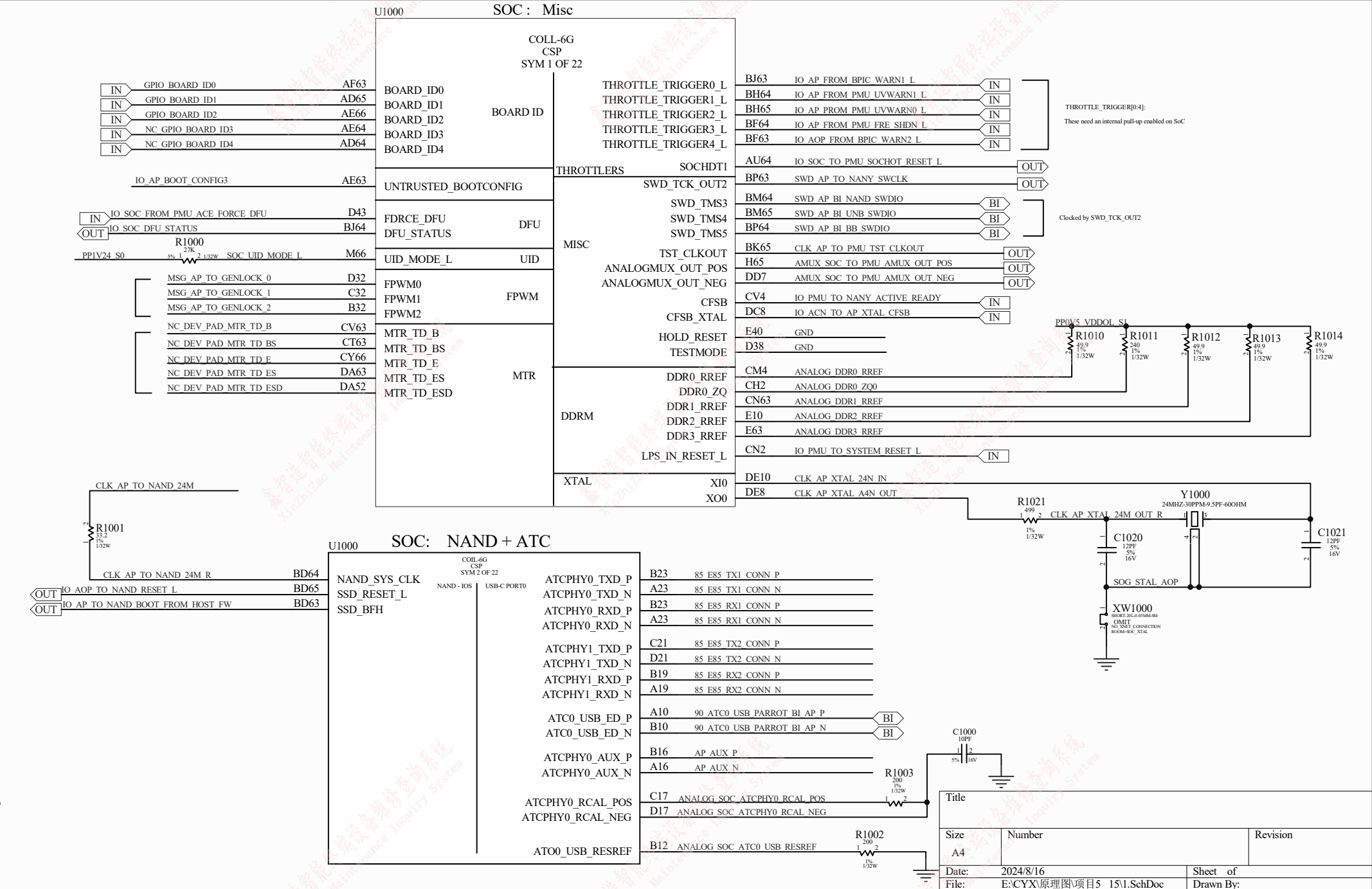
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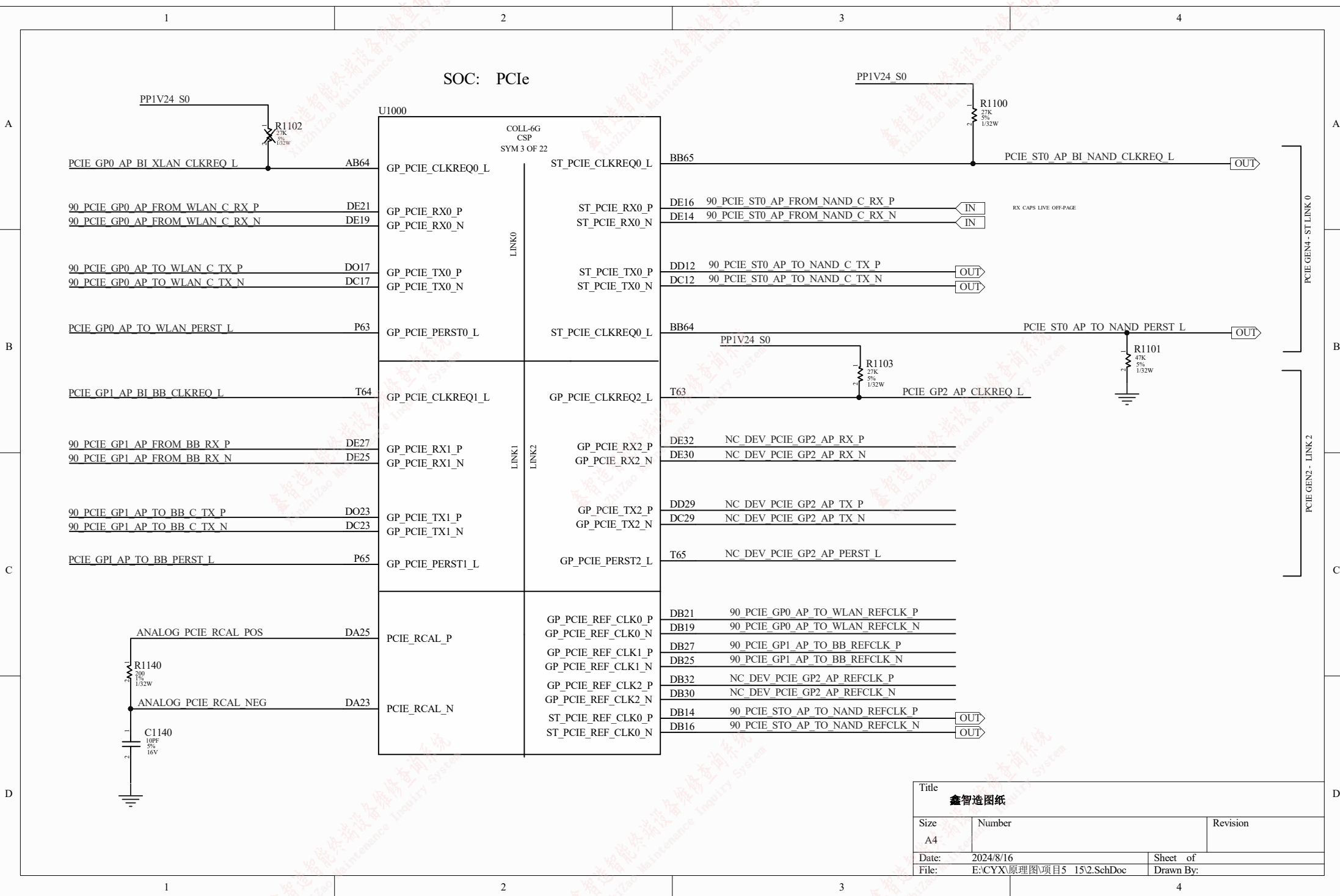
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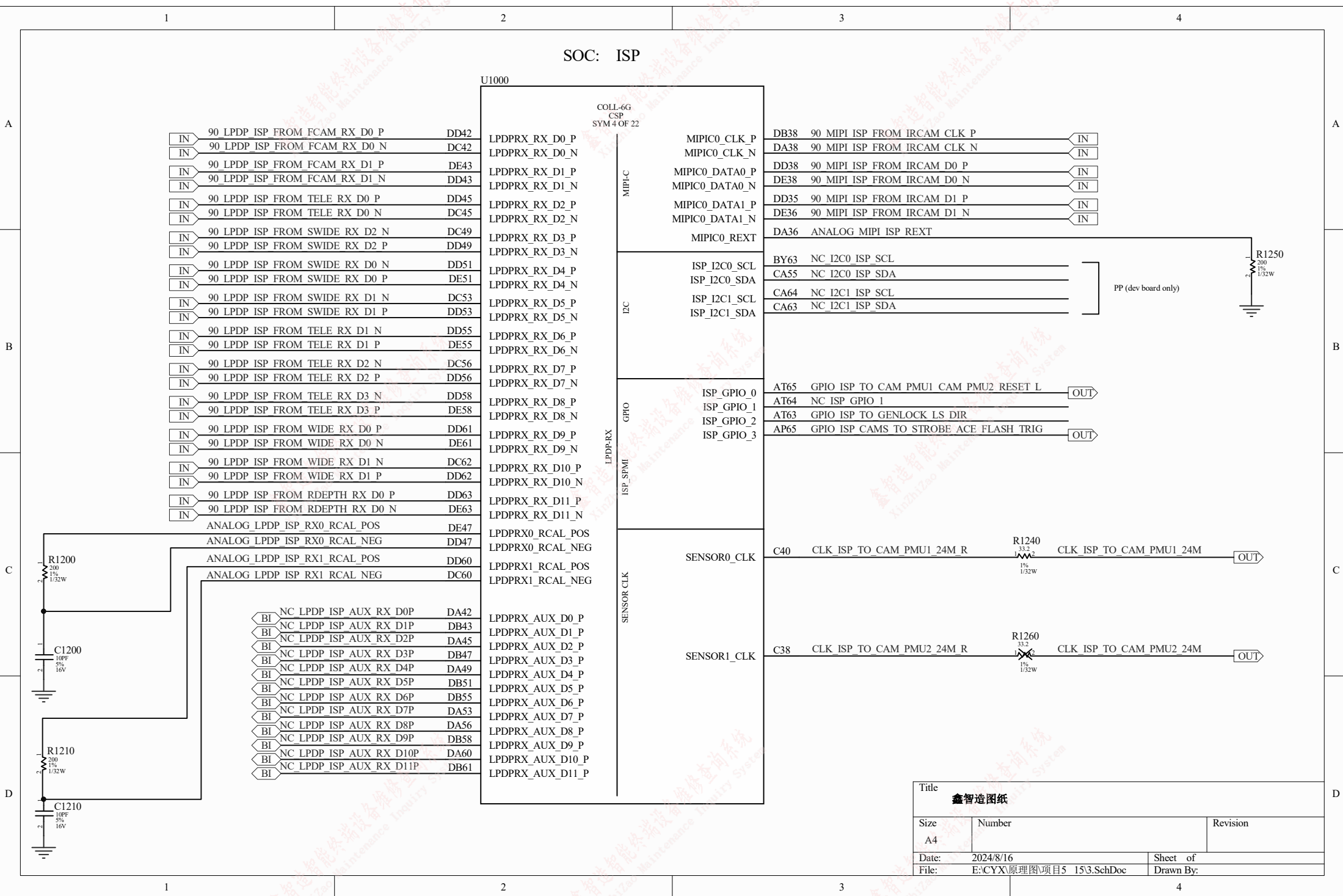
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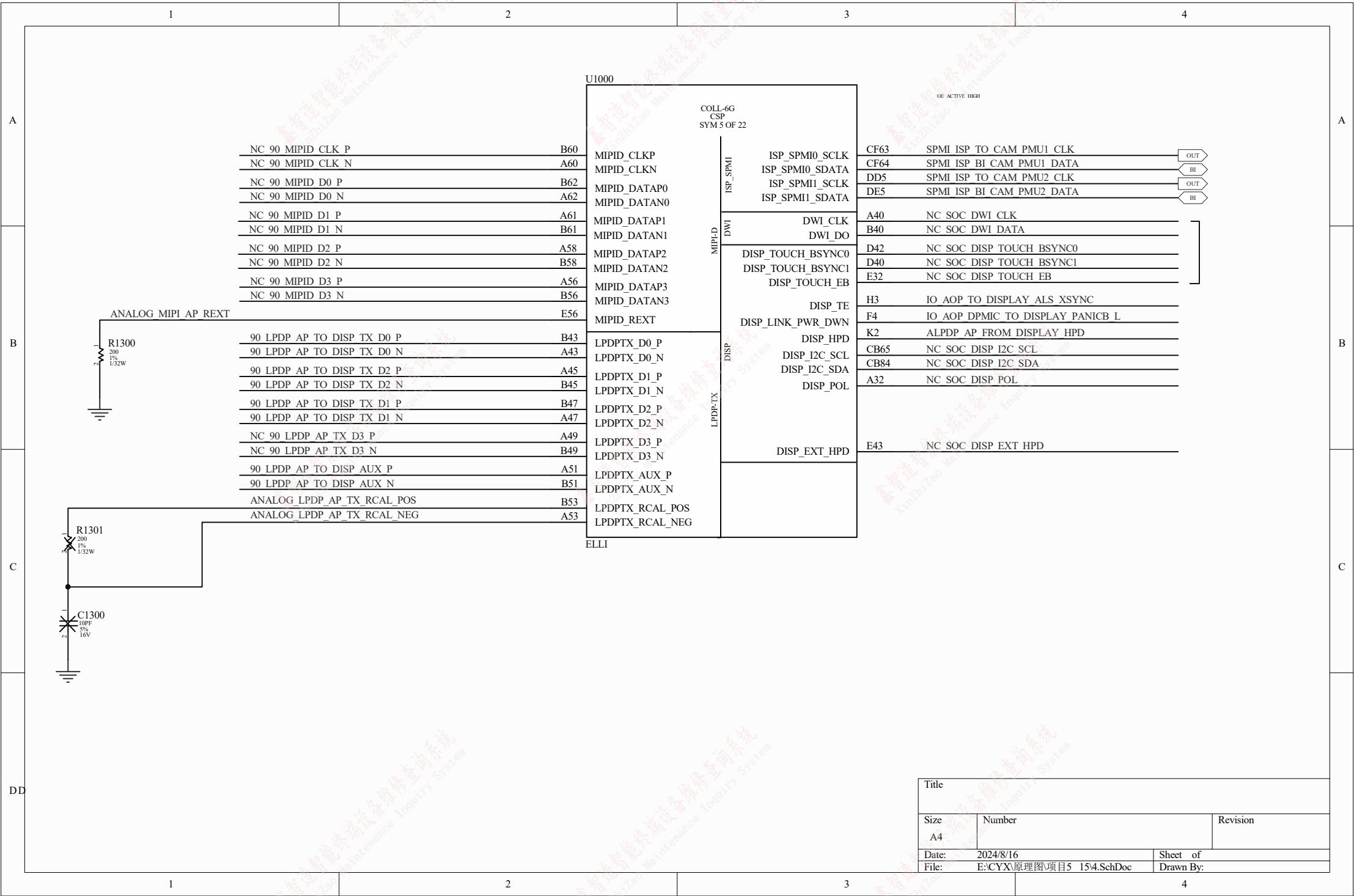
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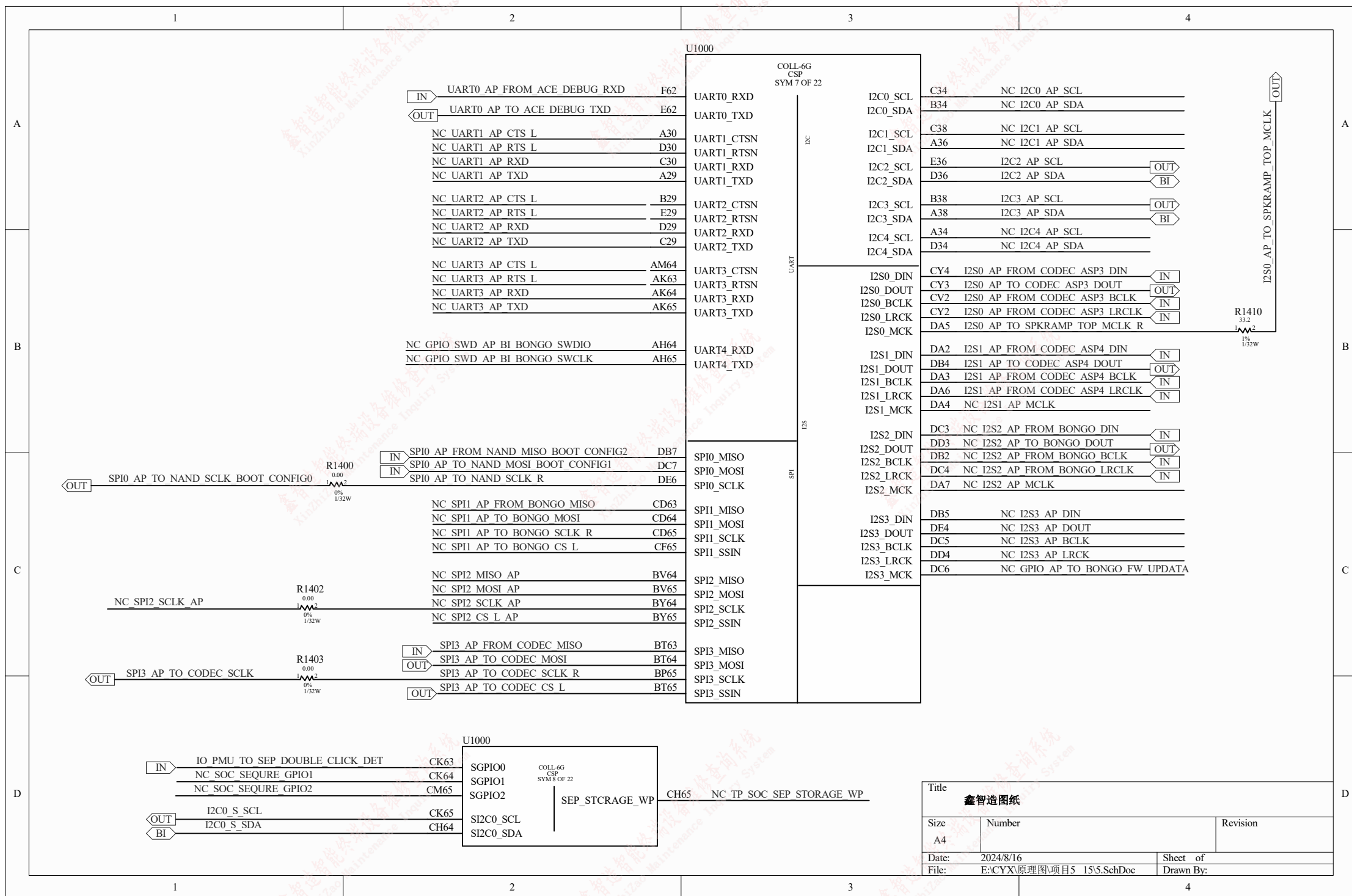
4











SOC: AP GPIO

U1000

SICILY-6G-I2NM-M
CSP
SYM 7 OF 23
TMMU72B0-CA
GPIO

GPIO BOARD REV3	AF64
GPIO BOARD REV2	AF65
GPIO BOARD REV1	AY63
GPIO BOARD REV0	AY64
GPIO PSIN FSIN L	AV64
NC SPARE AP GPIO5	AV63
GPIO AP BI ACE SWDIO	BK63
GPIO AP TO ACE SWCLK	BK64
GPIO AP FROM DISPLAY PANEL ID	AB65
GPIO AP FROM BT AUDIO SYNC	Y64
GPIO AP FROM ACE DFU BOOTED L	Y65
GPIO AP TO BB COREDUMP	V64
GPIO AP SOC DC CANARY2	V66
GPIO AP INTERPOSER CANARY6	AB63
GPIO AP INTERPOSER CANARY3	Y63
GPIO AP INTERPOSER CANARY4	P64

GPIO[0]	GPIO[16]
GPIO[1]	GPIO[17]
GPIO[2]	GPIO[18]
GPIO[3]	GPIO[19]
GPIO[4]	GPIO[20]
GPIO[5]	GPIO[21]
GPIO[6]	GPIO[22]
GPIO[7]	GPIO[23]
GPIO[9]	GPIO[8]
GPIO[10]	GPIO[25]
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GPIO[13]	
GPIO[14]	
GPIO[15]	

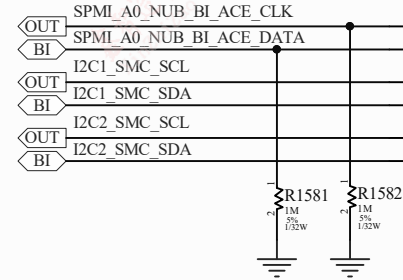
M64	GPIO AP FROM CODEC INT L
K65	GPIO AP TO AMUX PMU SYNC
K63	NC SPARE AP GPIO18
M63	NC SPARE AP GPIO19
K64	GPIO AP SOC DC CANARY4
D47	GPIO WLAN TO AP TIME SYNC 1V2
E47	NC SPARE AP GPIO22
D45	GPIO AP SOC DC CANARY3
N65	GPIO AP TO SPKRAMP TOP RESET L
N64	NC SPARE AP GPIO25

SOC: SMC

U1000

COLL-6G
CSP
SYM 11 OF 22
I2C

SMC_I2CM0_SCL	
SMC_I2CM0_SDA	
SMC_I2CM1_SCL	SUPPCRIB UART ALT FUNC1
SMC_I2CM1_SDA	
SMC_I2CM2_SCL	SUPPCRIB UART ALT FUNC1
SMC_I2CM2_SDA	



U1000

COLL-6G
CSP
SYM 11 OF 22

SPMI

SWD

DOCK

SCM0

JTAG

DEBUG_USB

MISO

CLOCK

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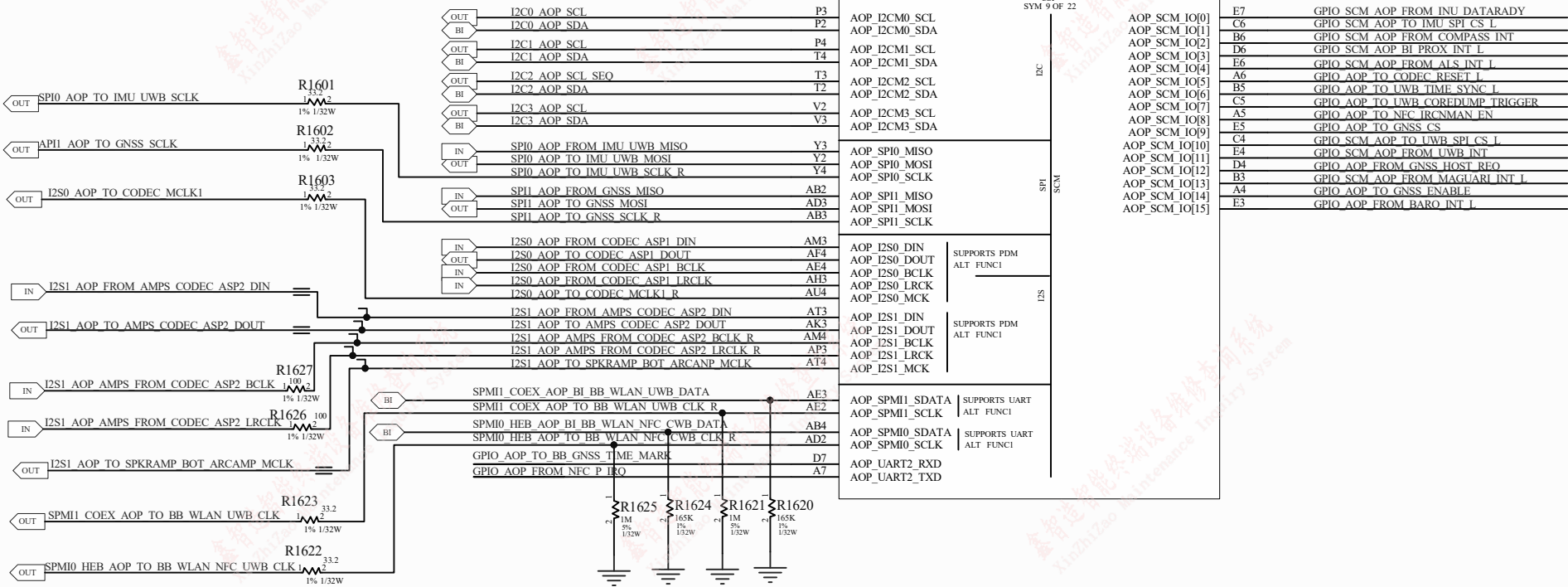
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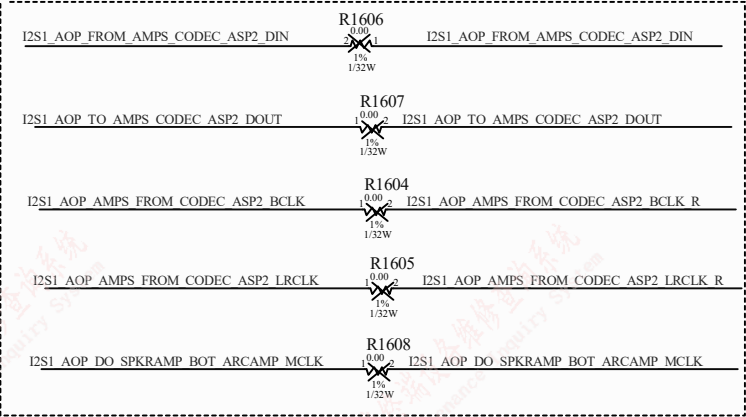
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SOC : AOP

U1000



See Redar radar://problem/68309718



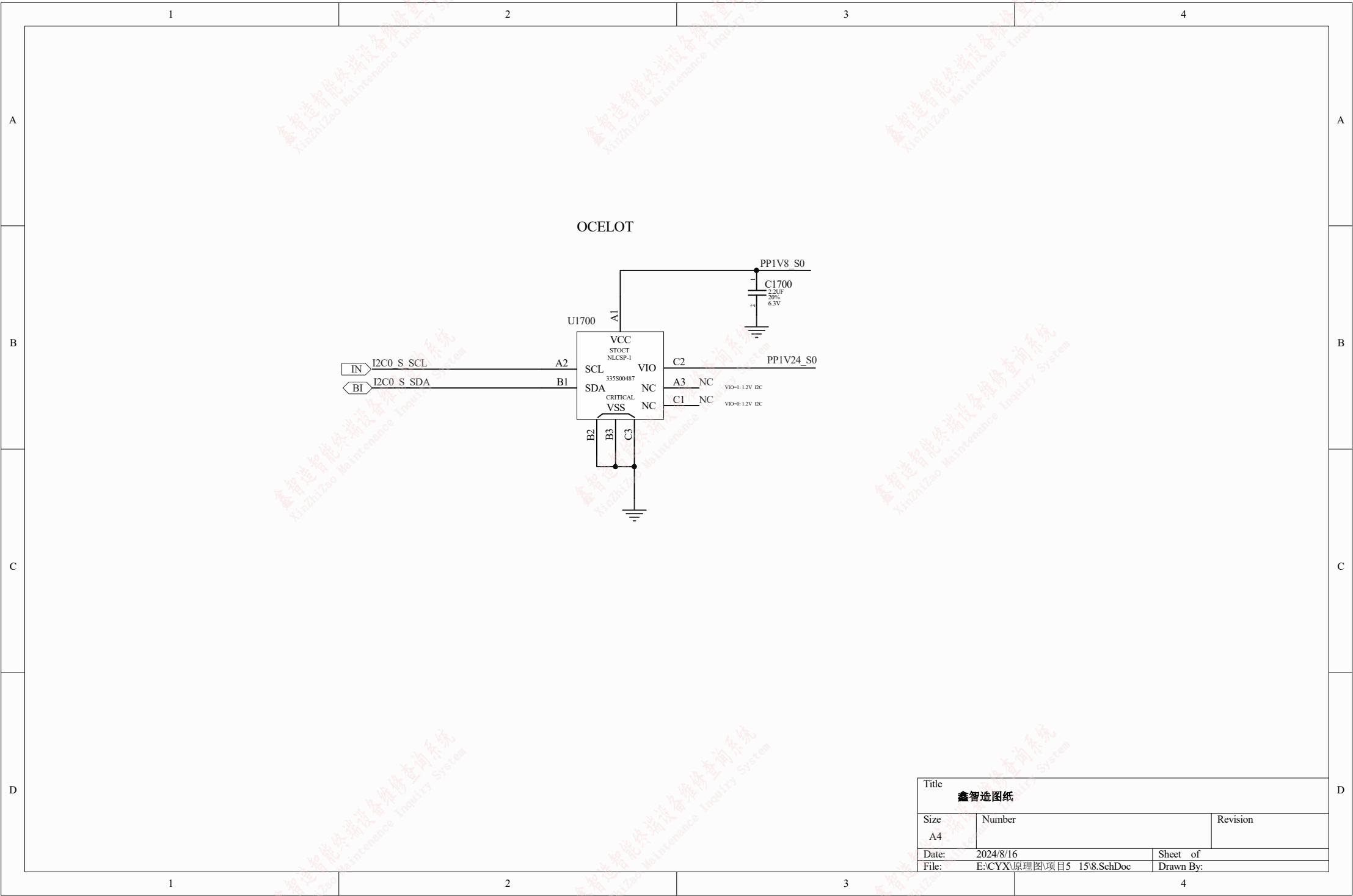
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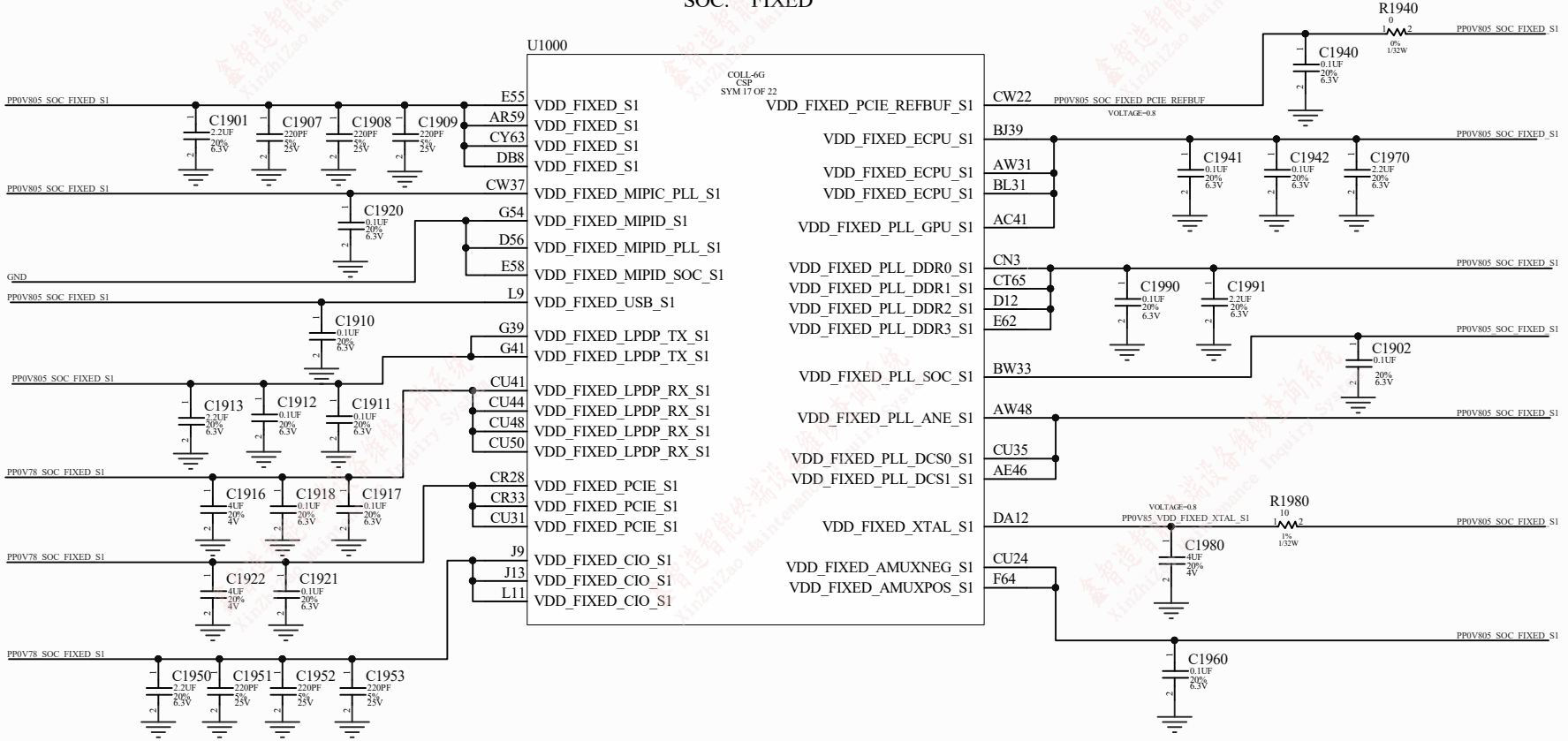
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SOC: FIXED



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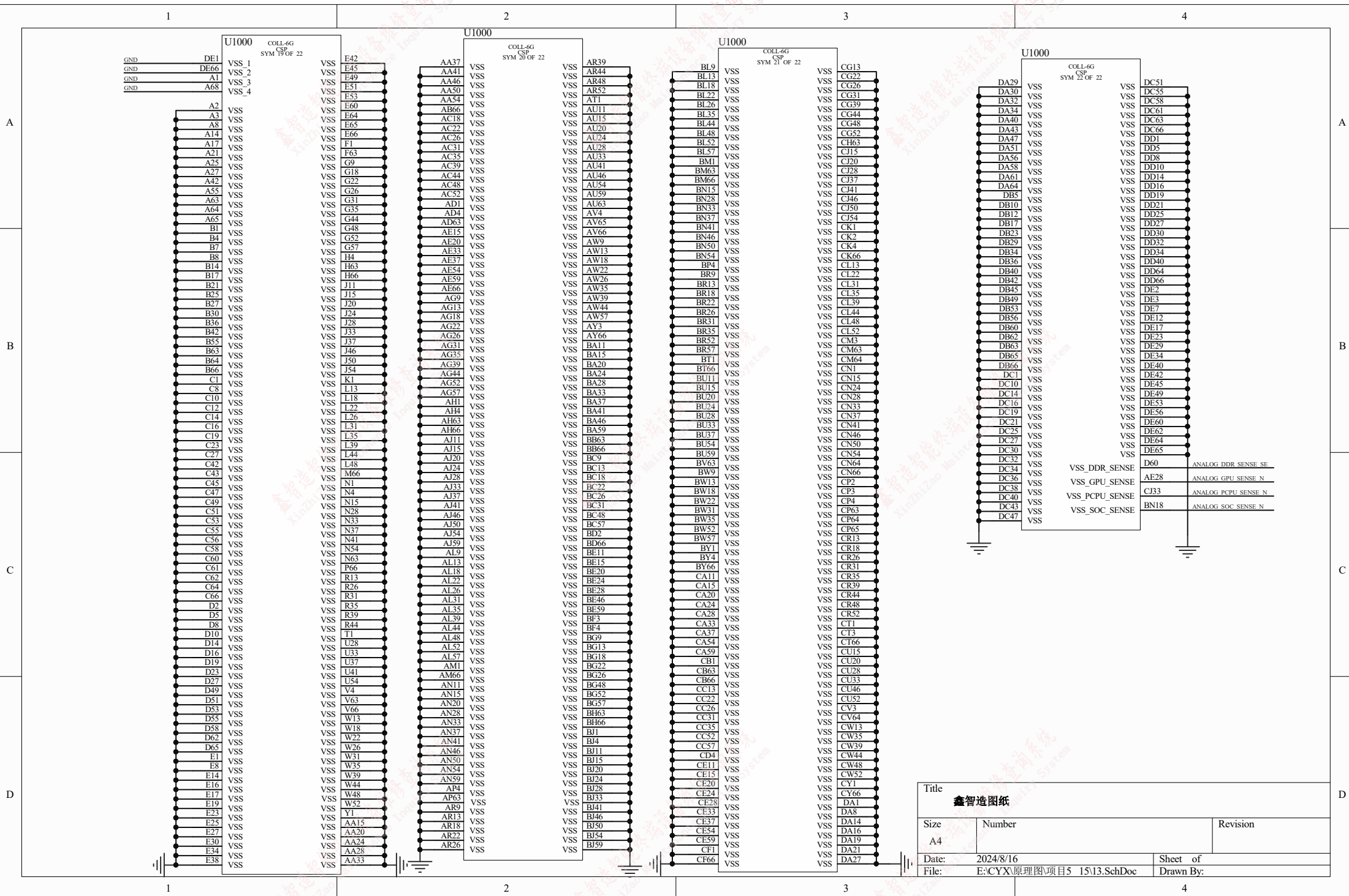
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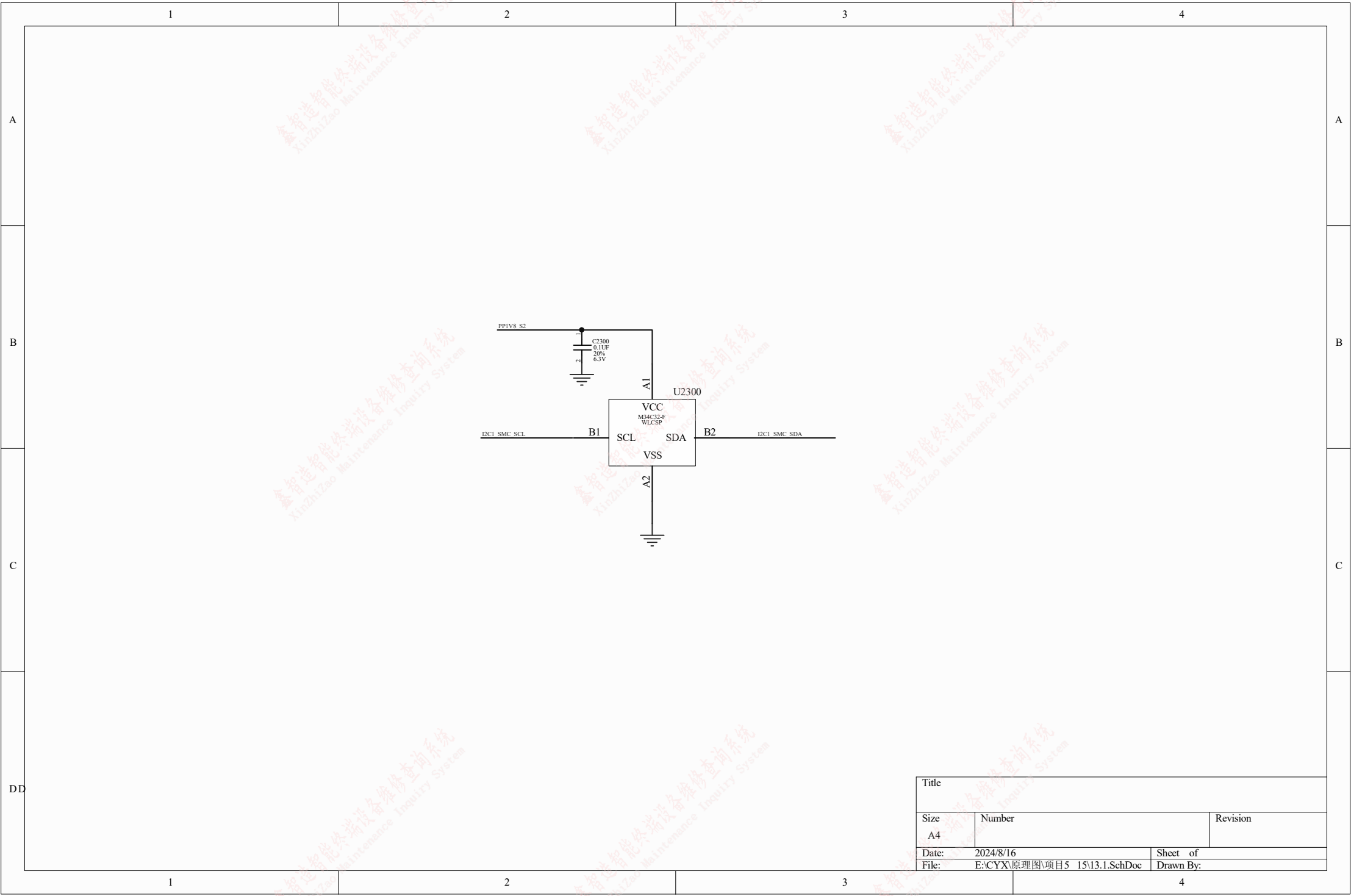
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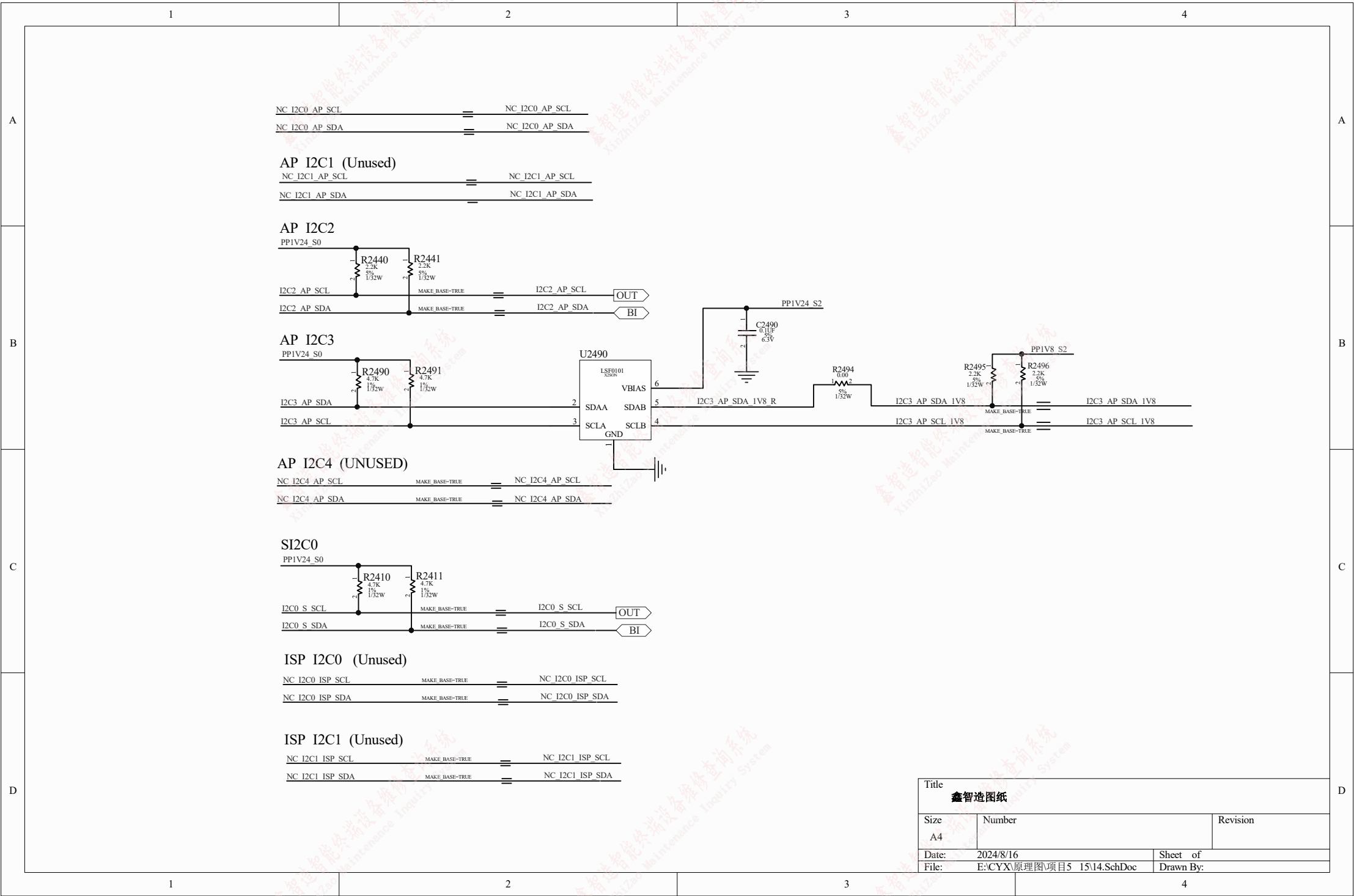
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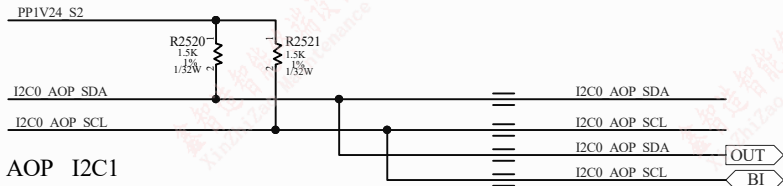
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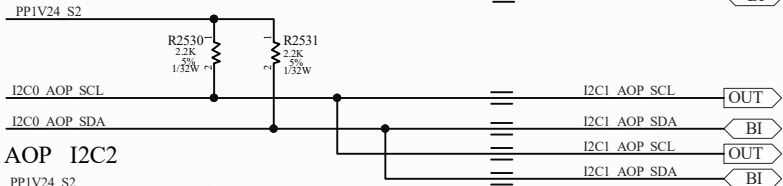
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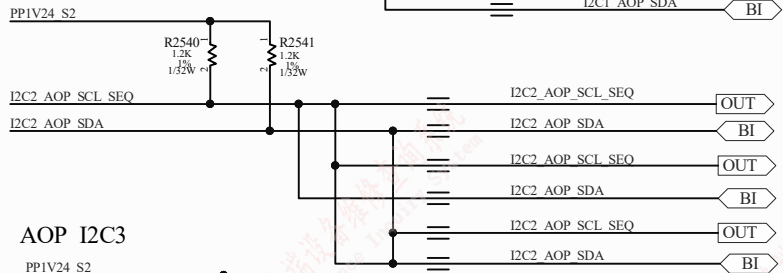
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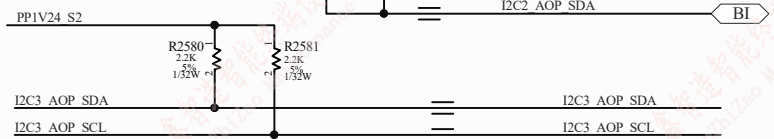
AOP I2C1



AOP I2C2



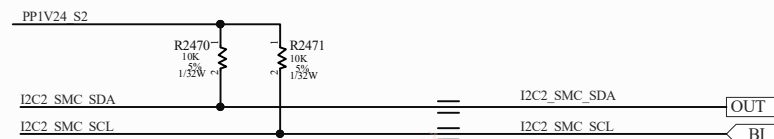
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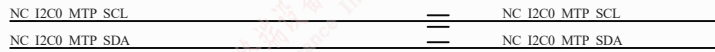
SMC I2C1



SMC I2C2



MTP I2C0 (Unused)



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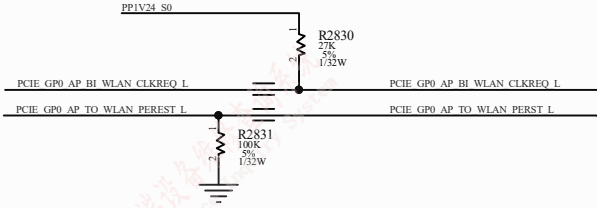
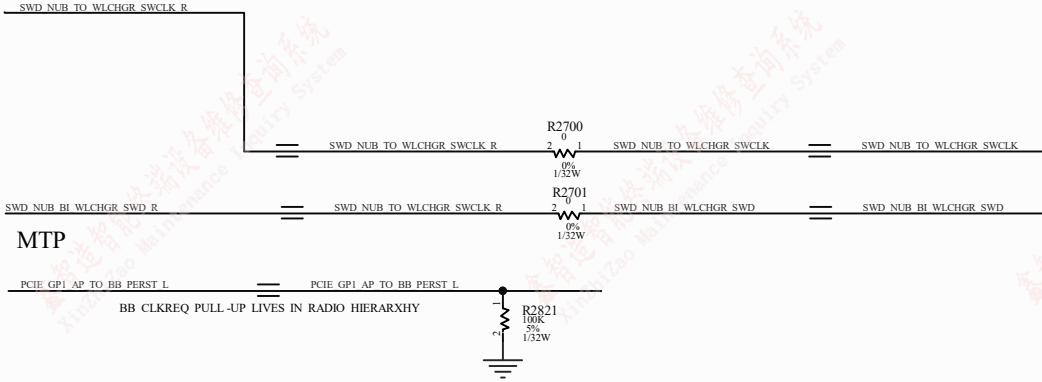
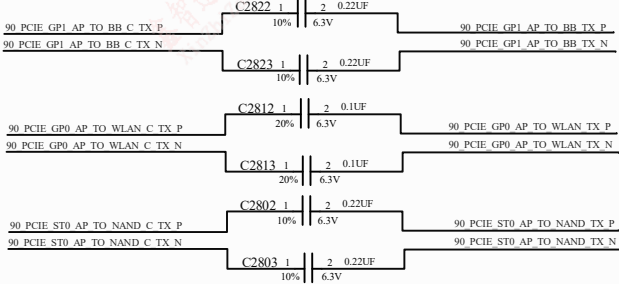
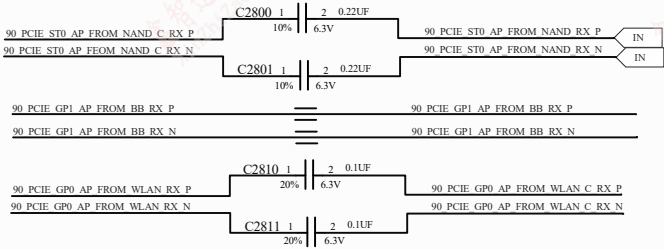
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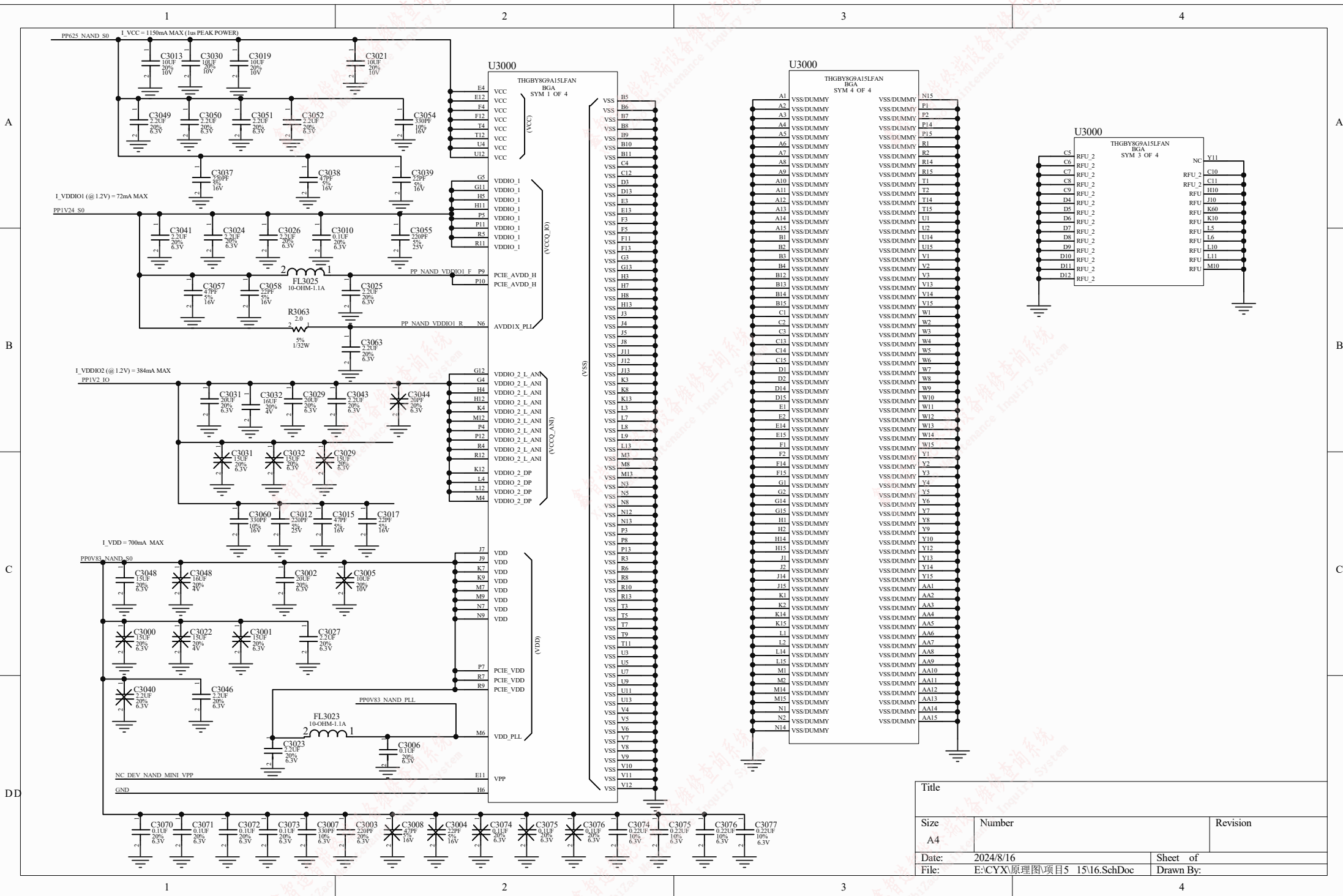
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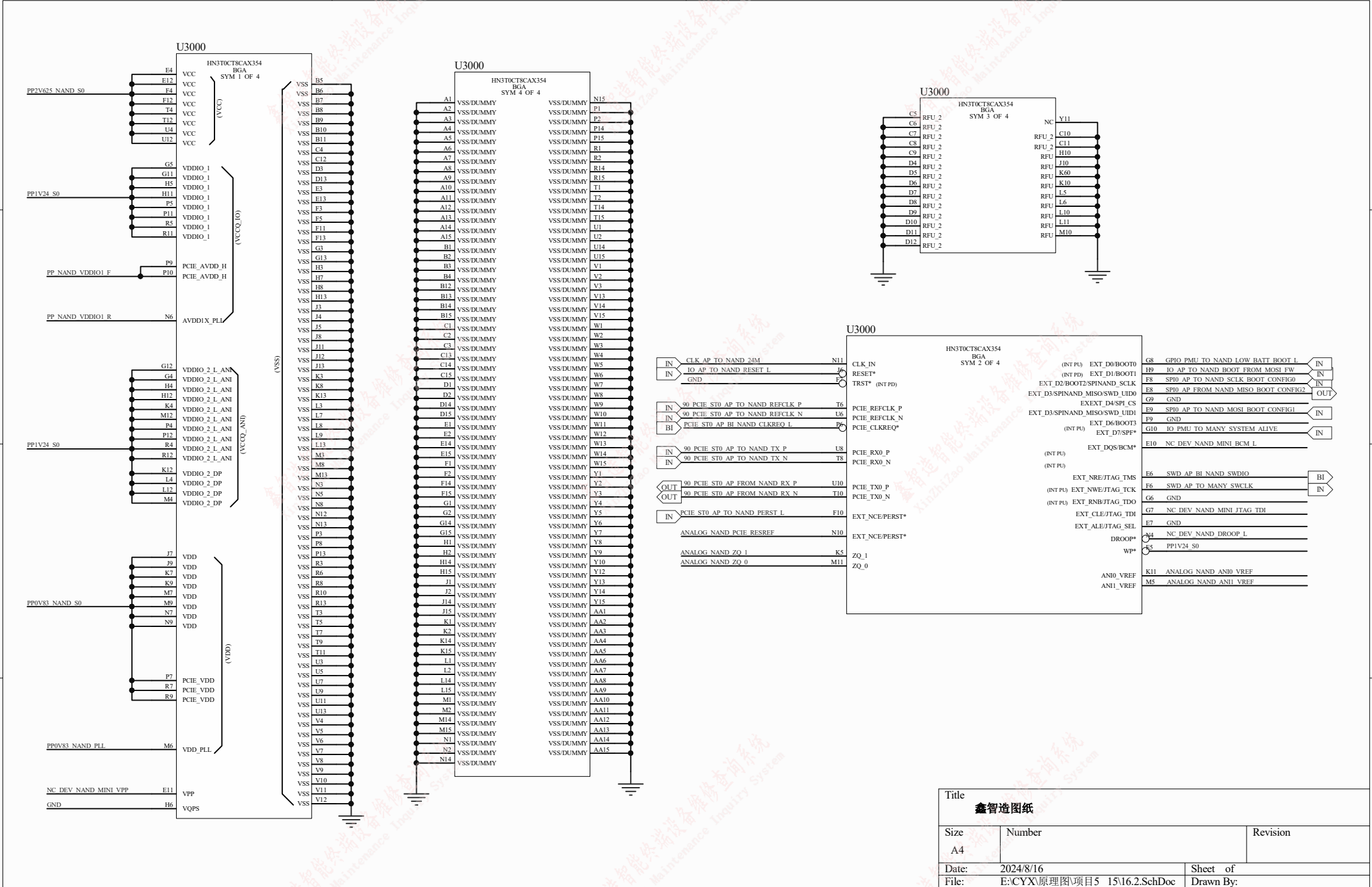
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Title

鑫智造图纸

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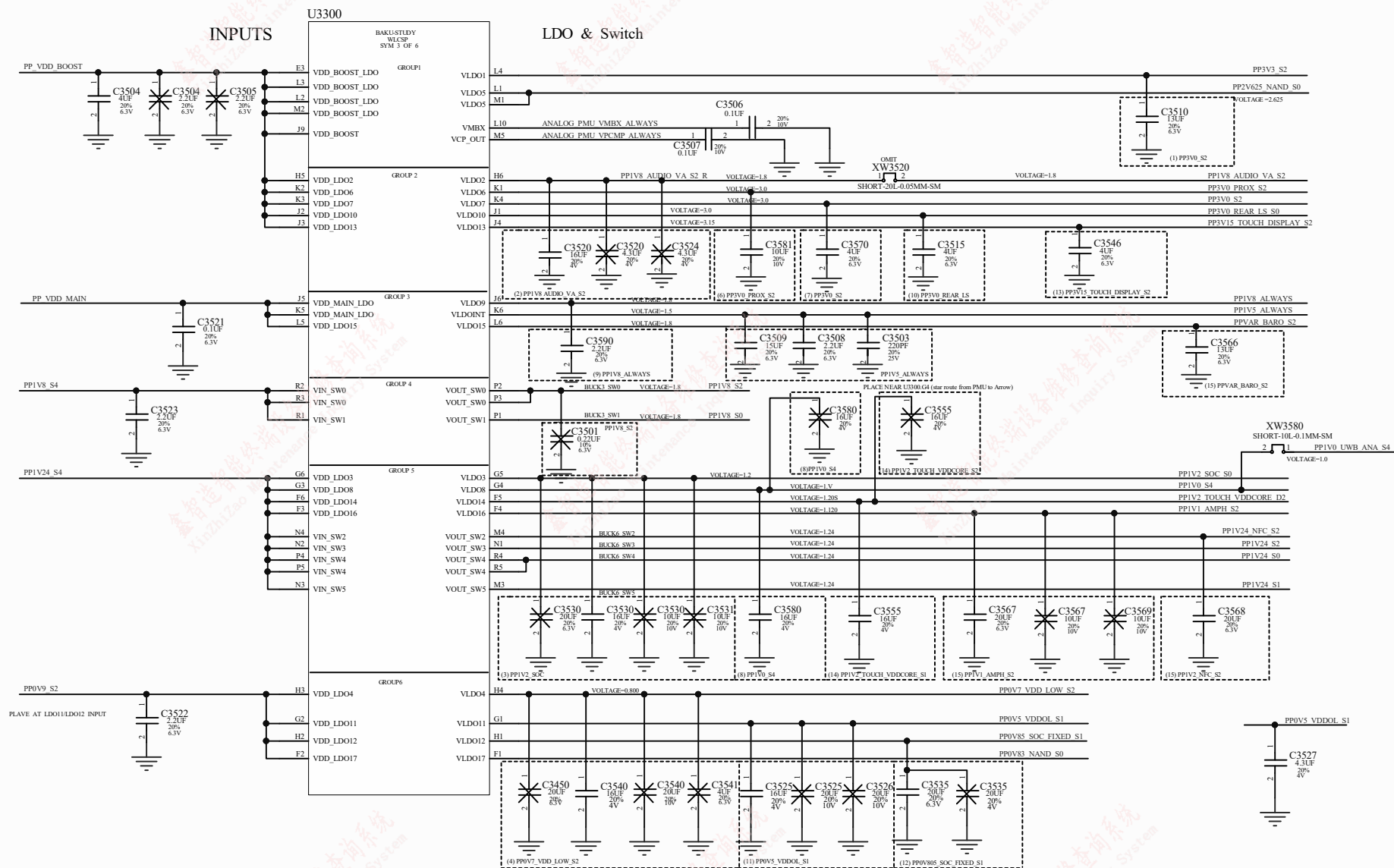
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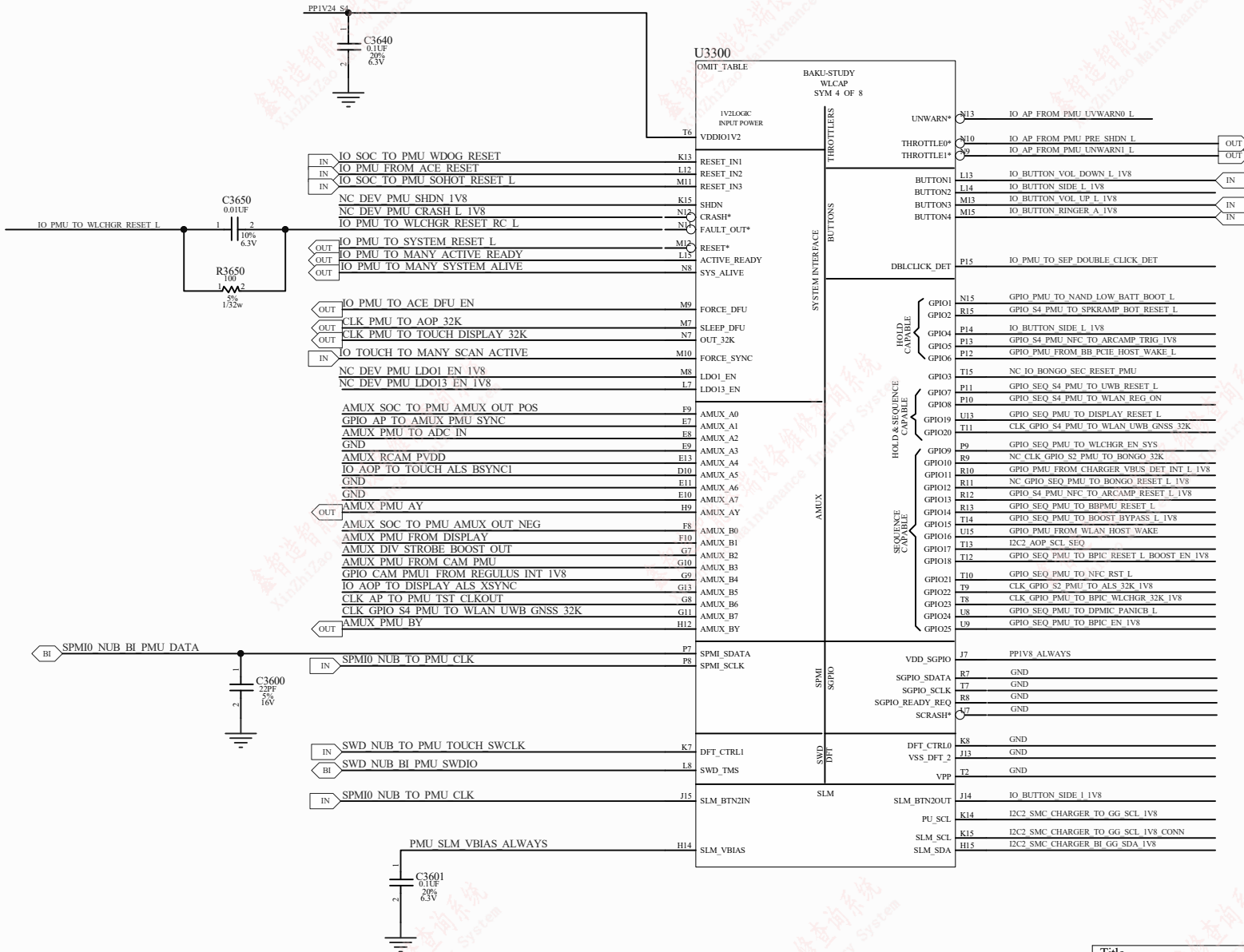
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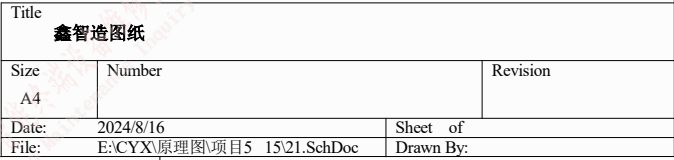
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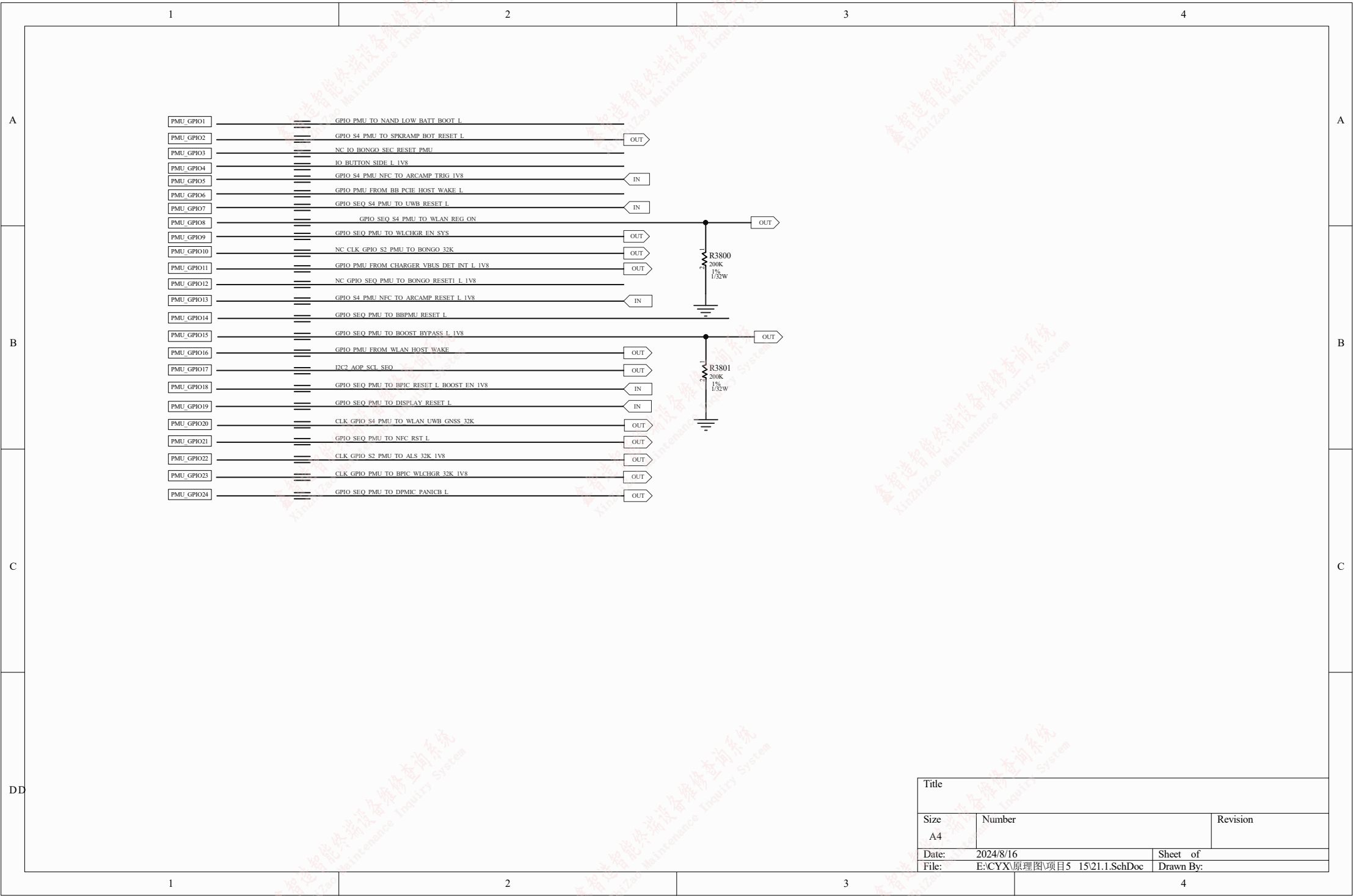
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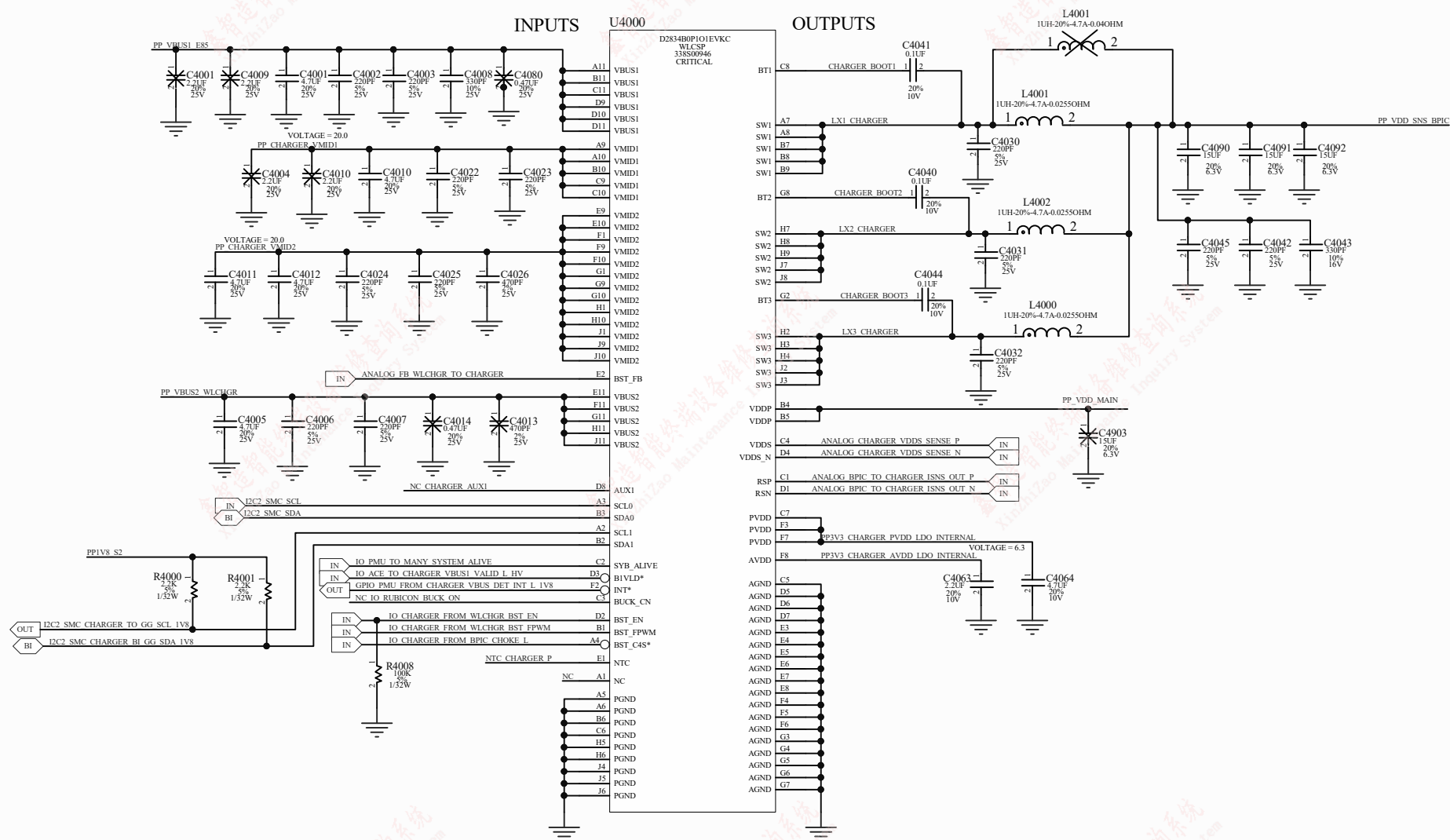


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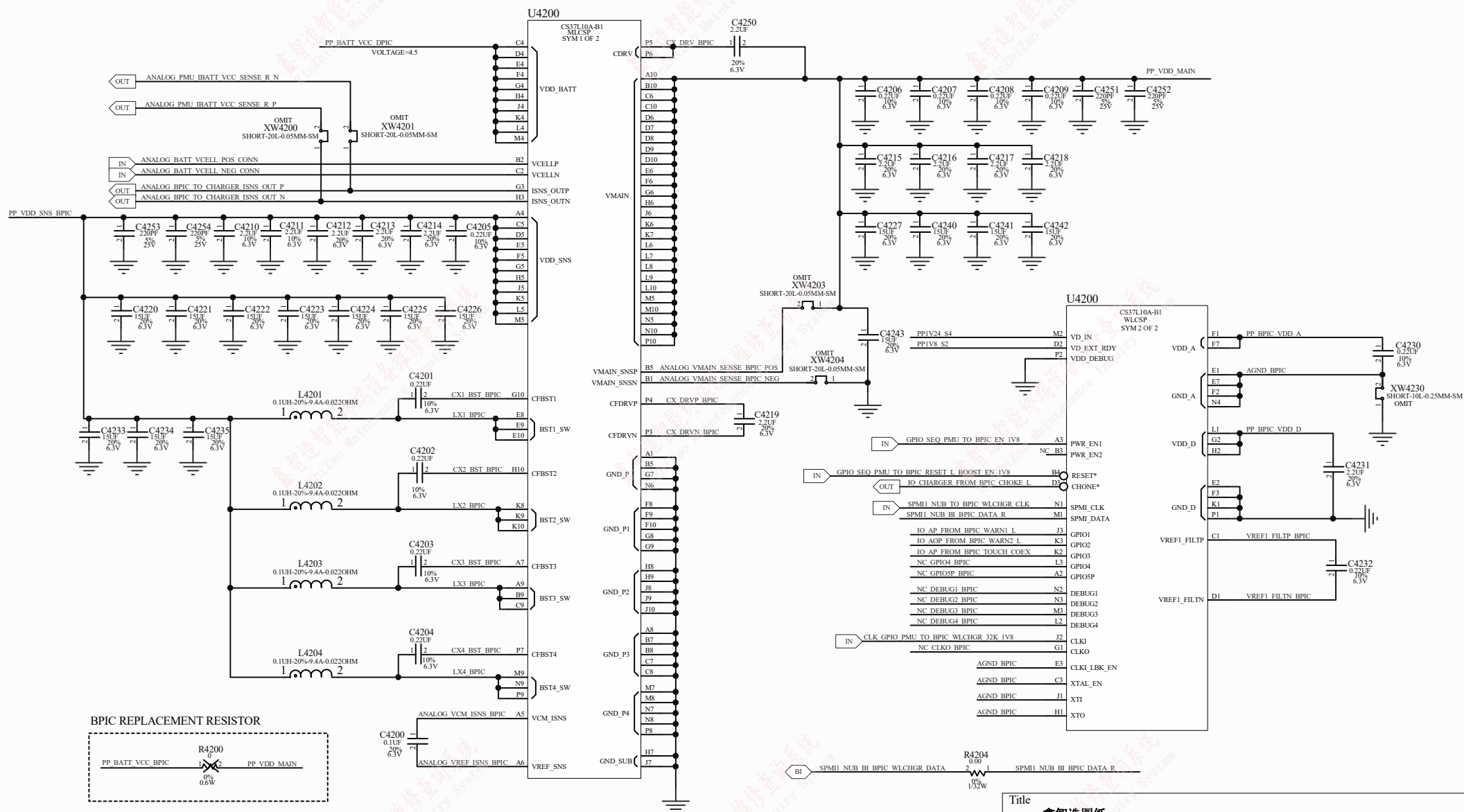
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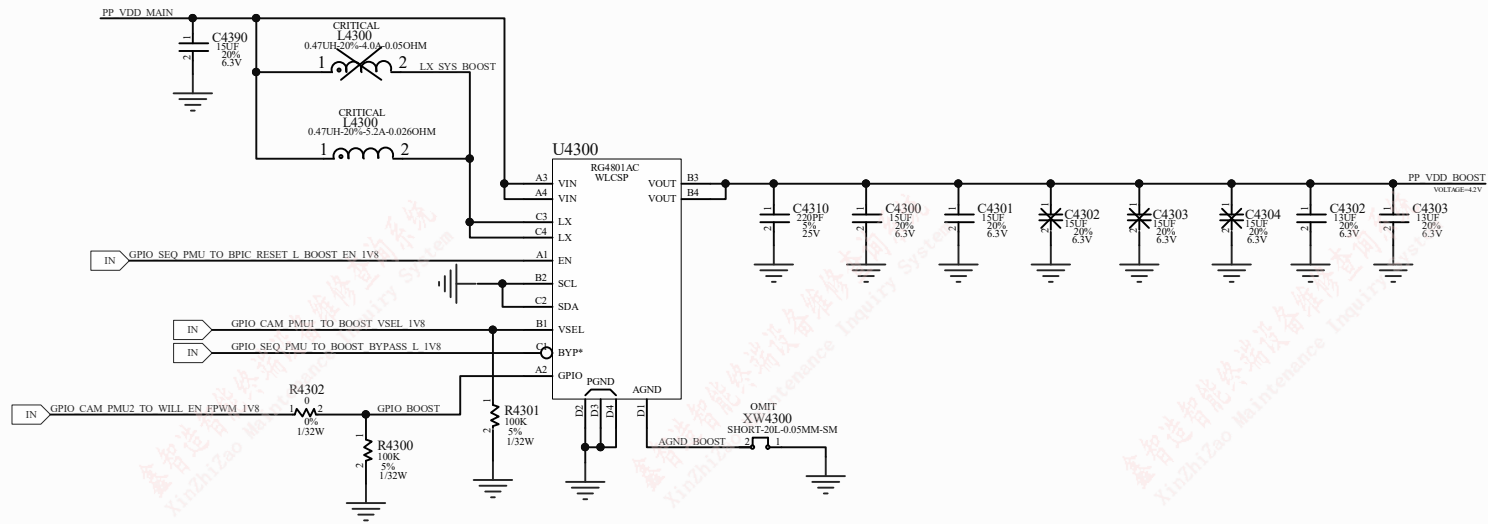
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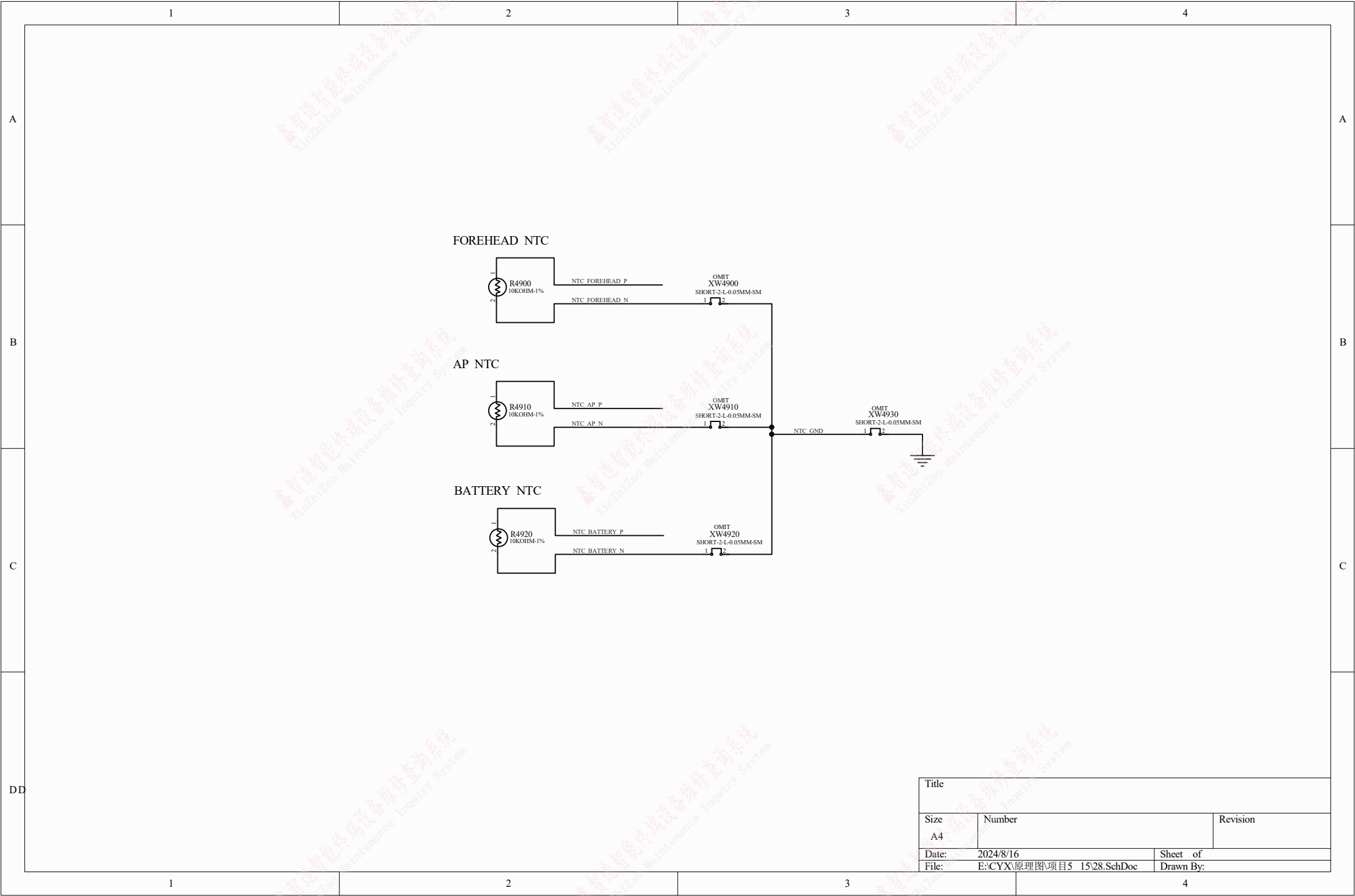
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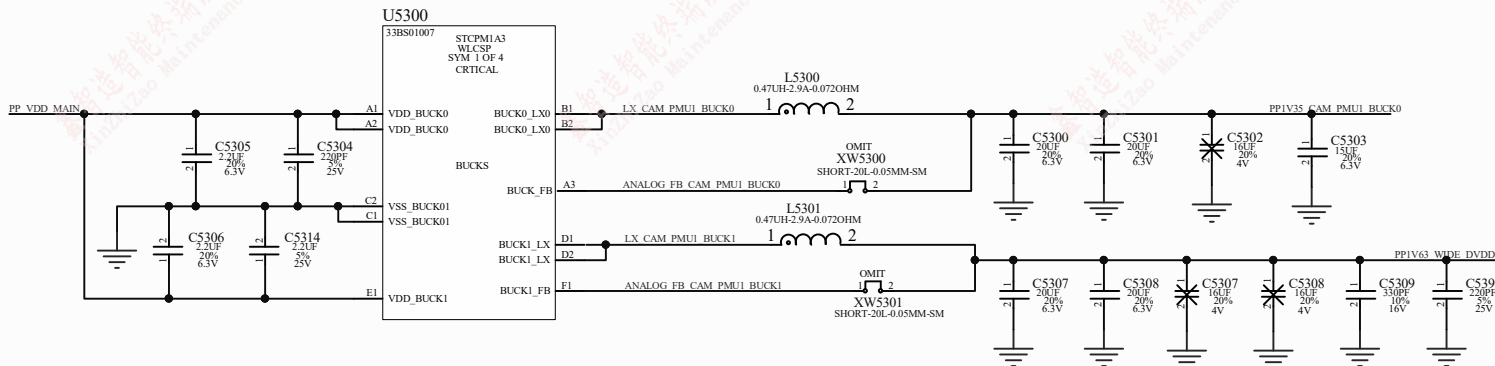
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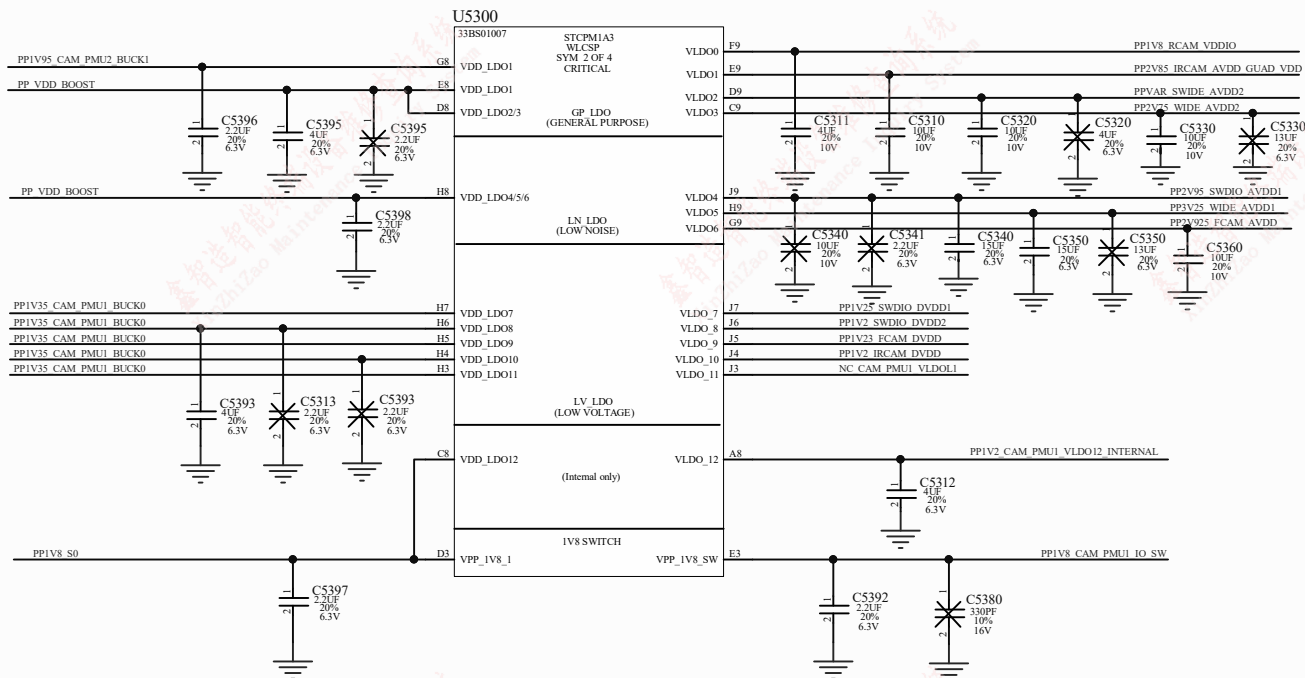
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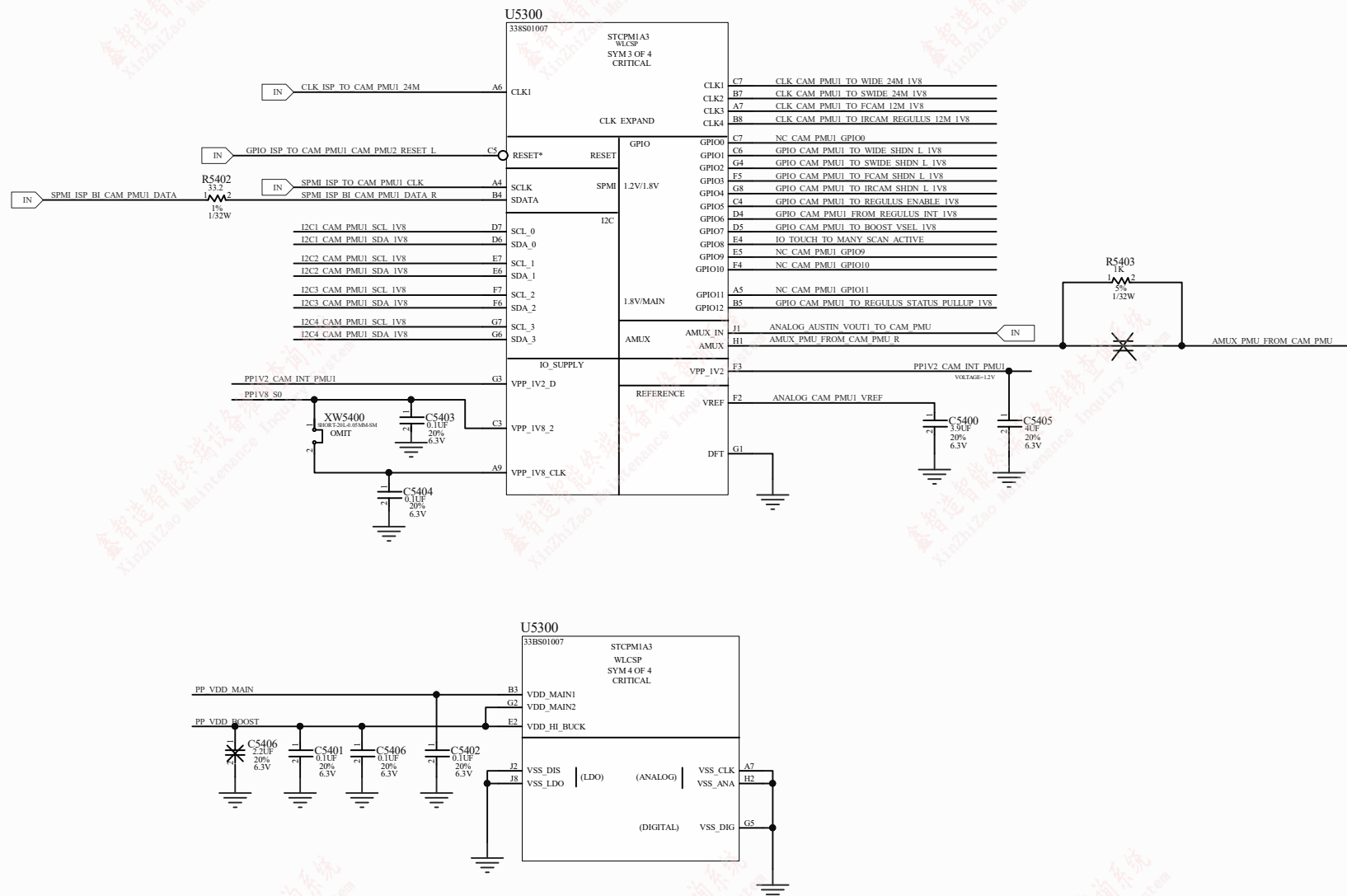
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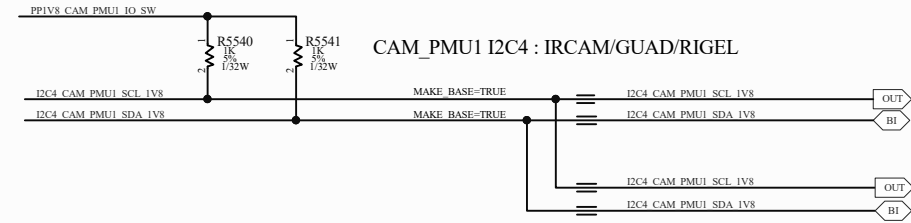
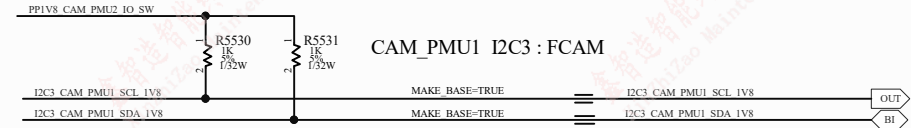
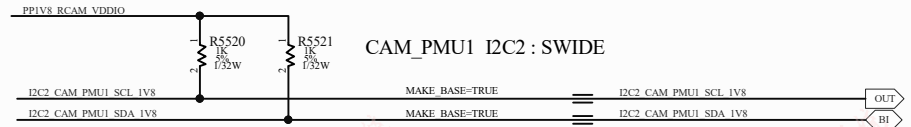
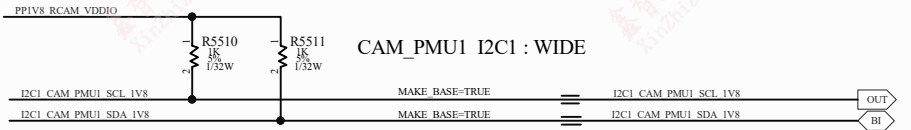
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Title		
Size	Number	Revision
A4		
Date:	2024/8/16	Sheet of
File:	E:\CYX\原理图\项目5 15\31.SchDoc	Drawn By:

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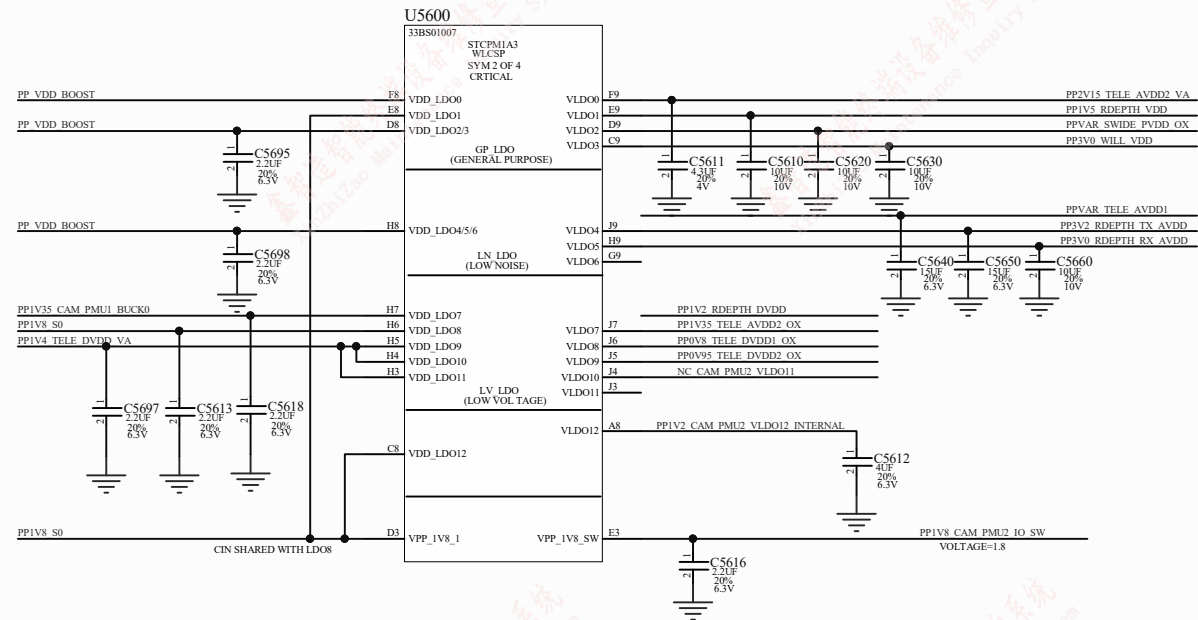
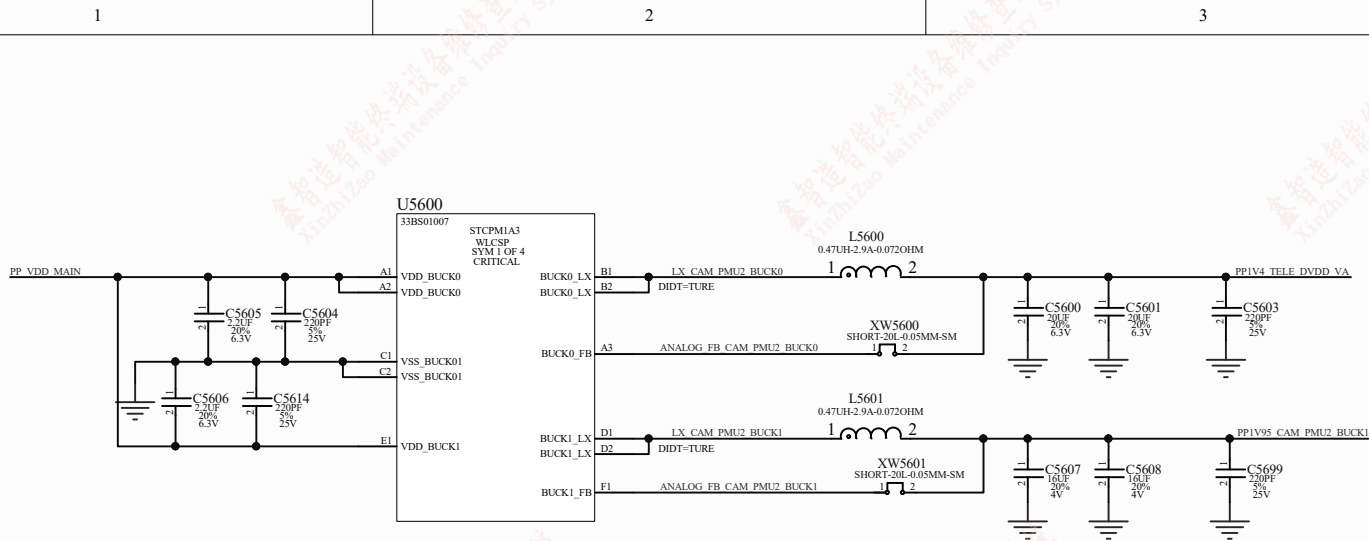
D

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Title 鑫智造图纸			
Size A4	Number		Revision
Date:	2024/8/16		Sheet of
File:	E:\CYX\原理图\项目5 15\31.1.SchDoc		Drawn By:

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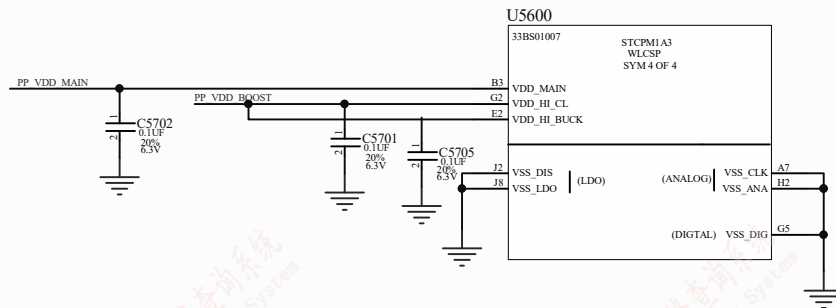
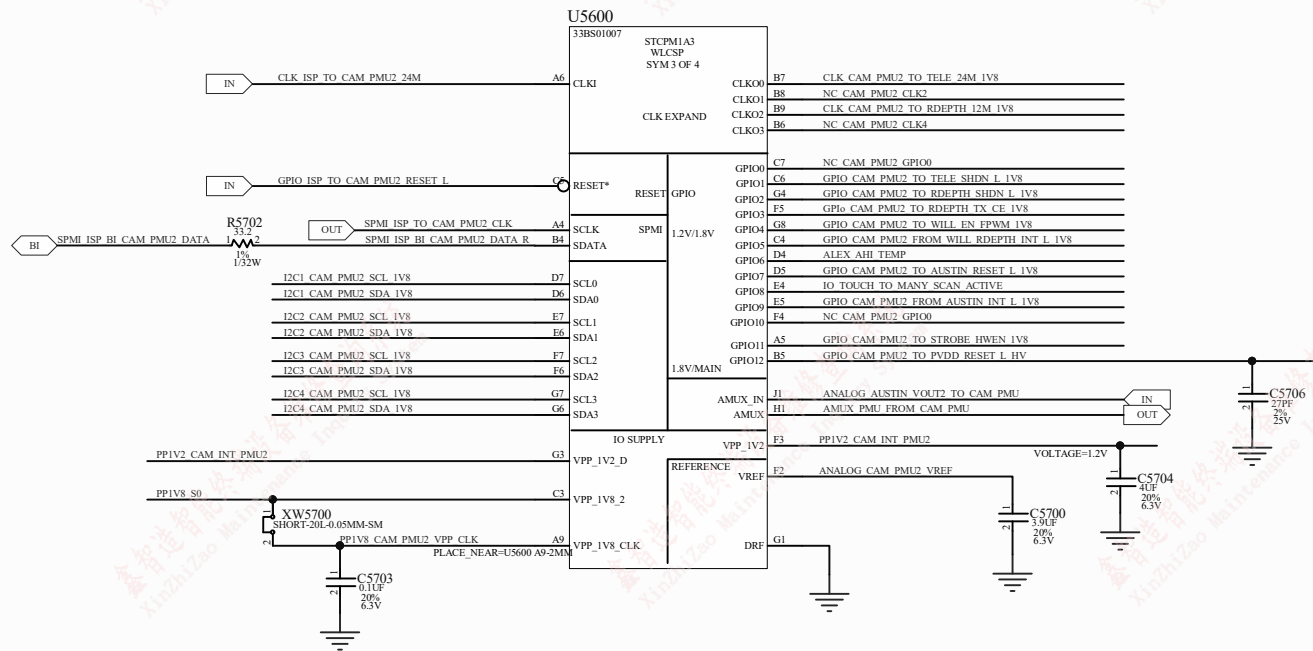
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Title 鑫智造图纸			
Size A4	Number		Revision
Date:	2024/8/16		Sheet of
File:	E:\CYX\原理图\项目5_15\31.2.SchDoc		Drawn By:

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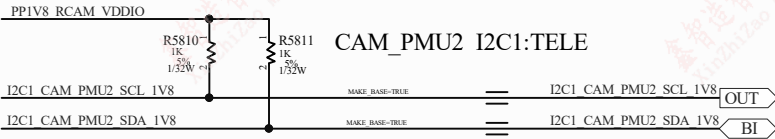
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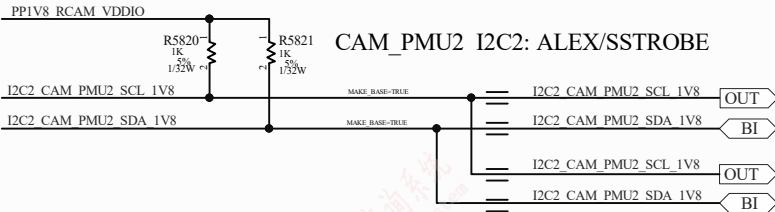
3

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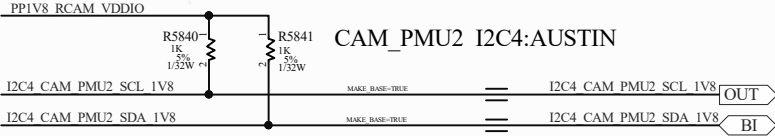
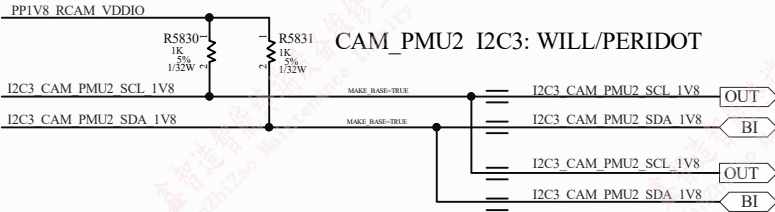
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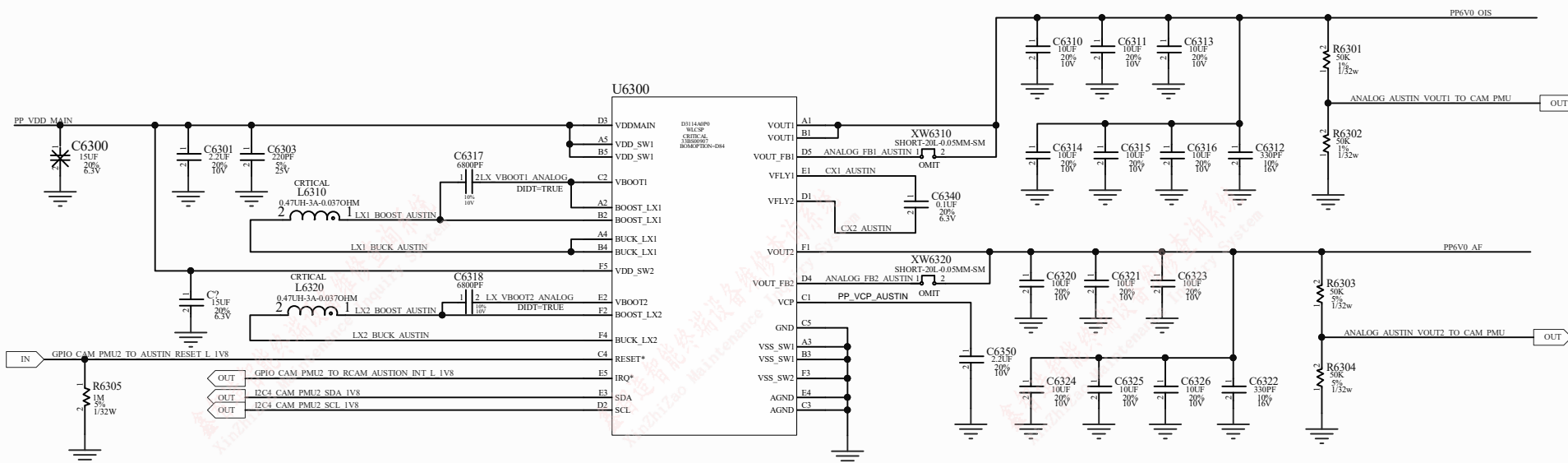
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A4		
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1

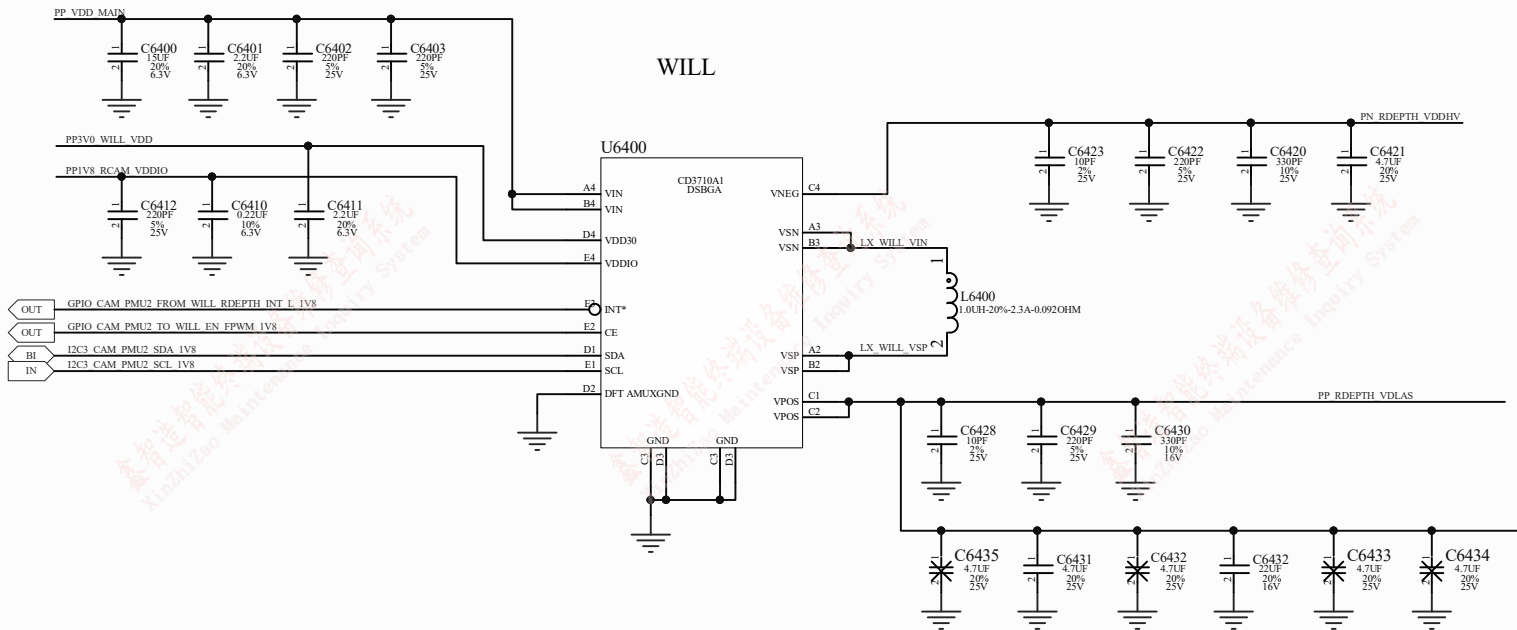
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Title 鑫智造图纸		
Size A4	Number	Revision
Date: 2024/8/16	Sheet of	
File: E:\CYX\原理图\项目5_15\33.1.SchDoc	Drawn By:	



Title		
Size	Number	Revision
A4		
Date:	2024/8/16	Sheet of
File:	E:\CYX\原理图\项目5 15\33.2.SchDoc	Drawn By:

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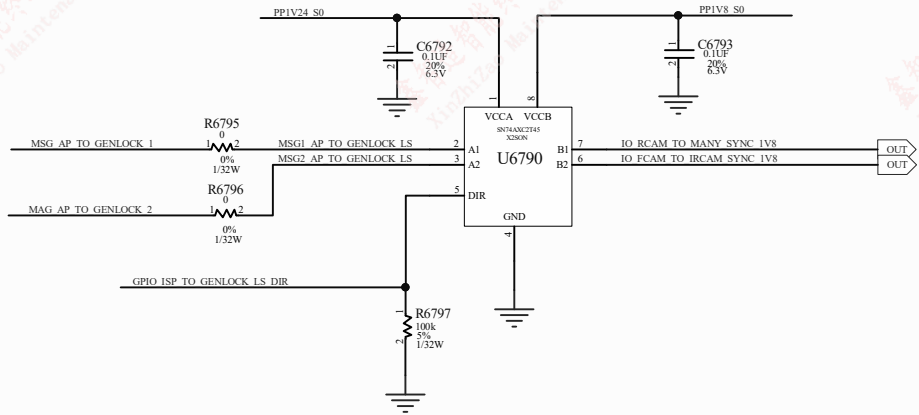
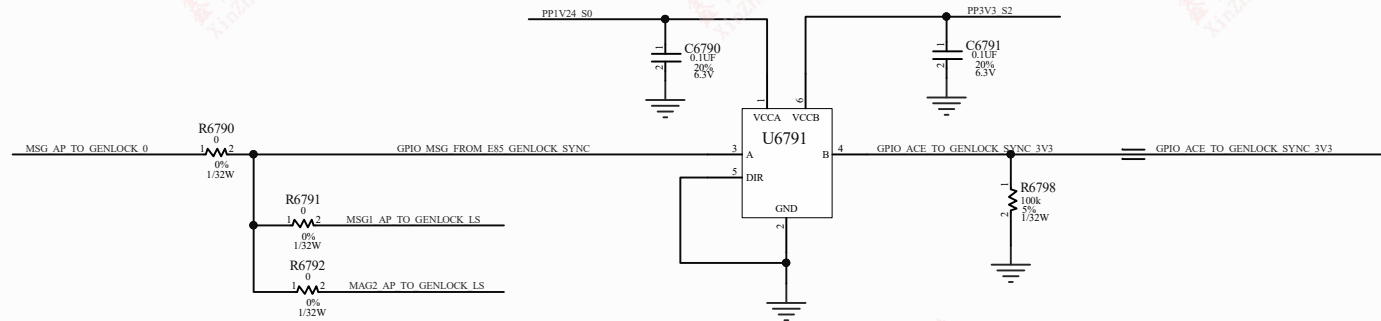
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1

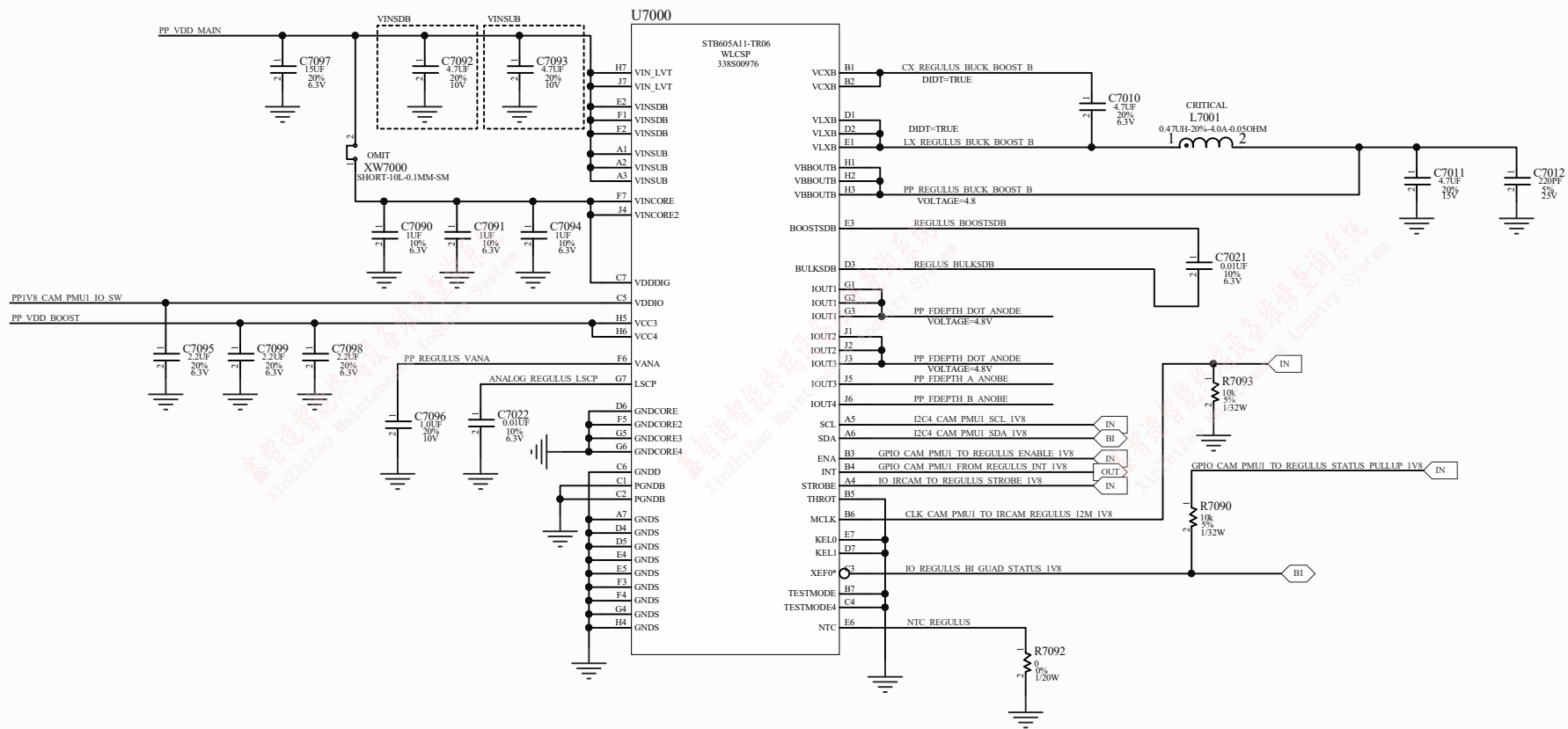
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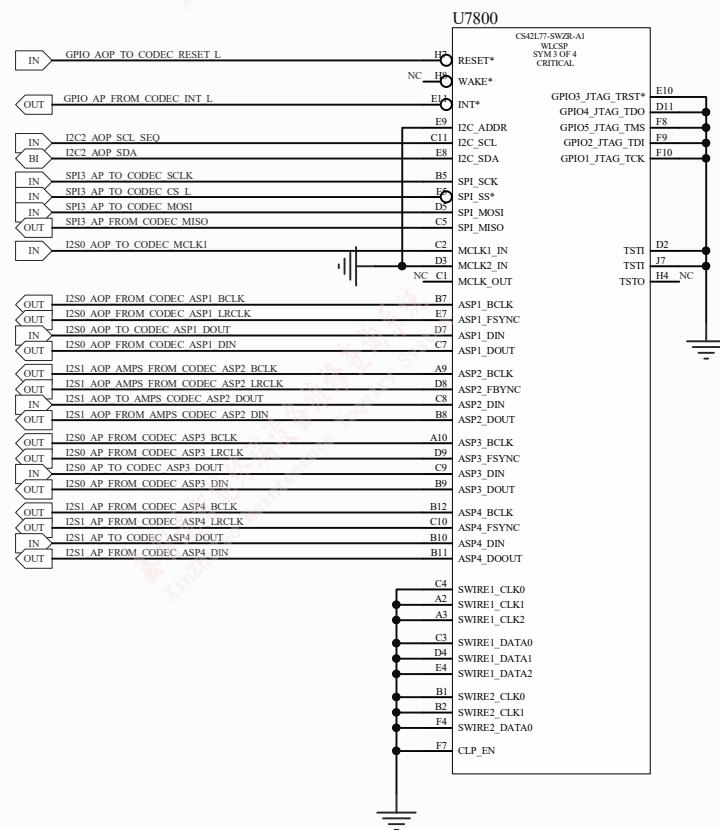
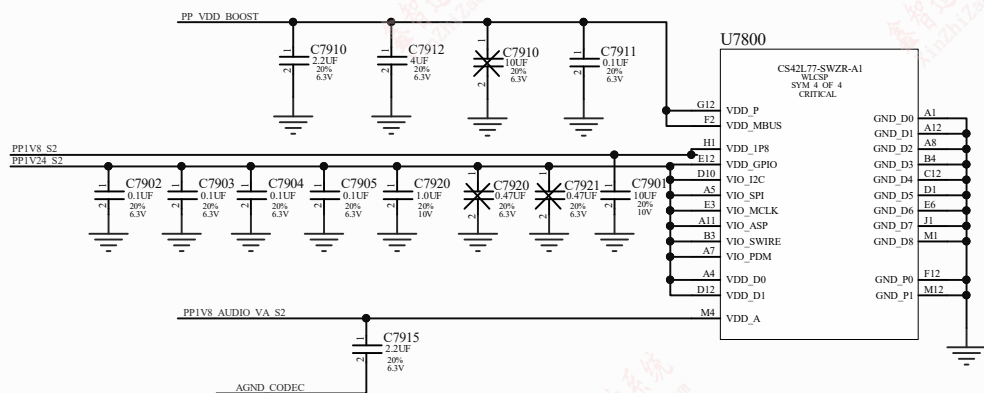
4



Title		
Size	Number	Revision
A4		
Date:	2024/8/16	Sheet of
File:	E:\CYX\原理图\项目5 15\33.3.SchDoc	Drawn By:



Title 鑫智造图纸			
Size A4	Number		Revision
Date:	2024/8/16		Sheet of
File:	E:\CYX\原理图\项目5 1534.SchDoc		Drawn By:



Title		
Size A4	Number	Revision
Date: File:	2024/8/16 E:\CYX\原理图\项目5 1536.SchDoc	Sheet of Drawn By:

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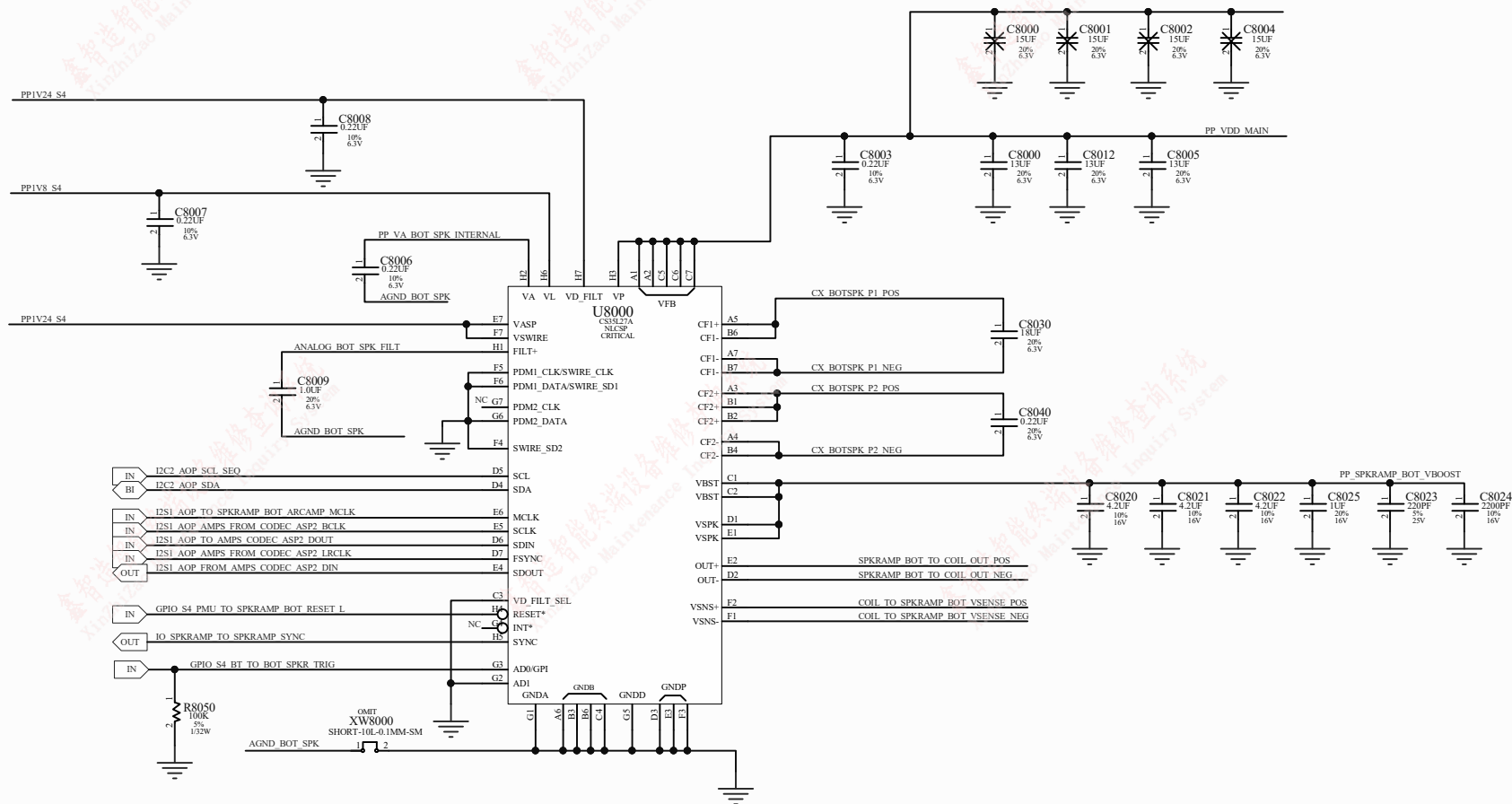
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Title 鑫智造图纸		
Size A4	Number	Revision
Date: 2024/8/16	Sheet of	
File: E:\CYX\原理图\项目5 15\37.SchDoc	Drawn By:	

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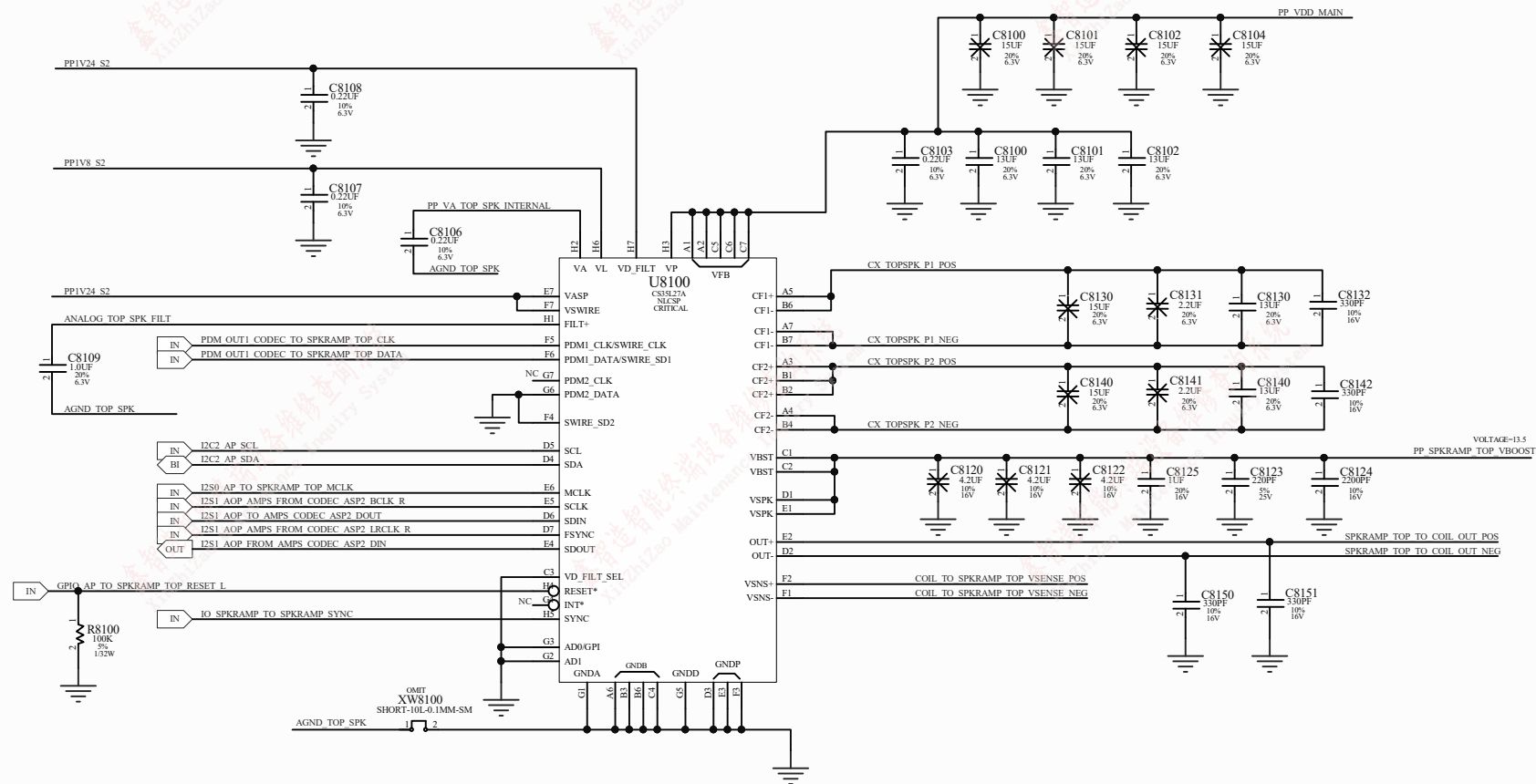
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Title 鑫智造图纸		
Size A4	Number	Revision
Date: 2024/8/16	Sheet of	
File: E:\CYX\原理图\项目5_15\38.SchDoc	Drawn By:	

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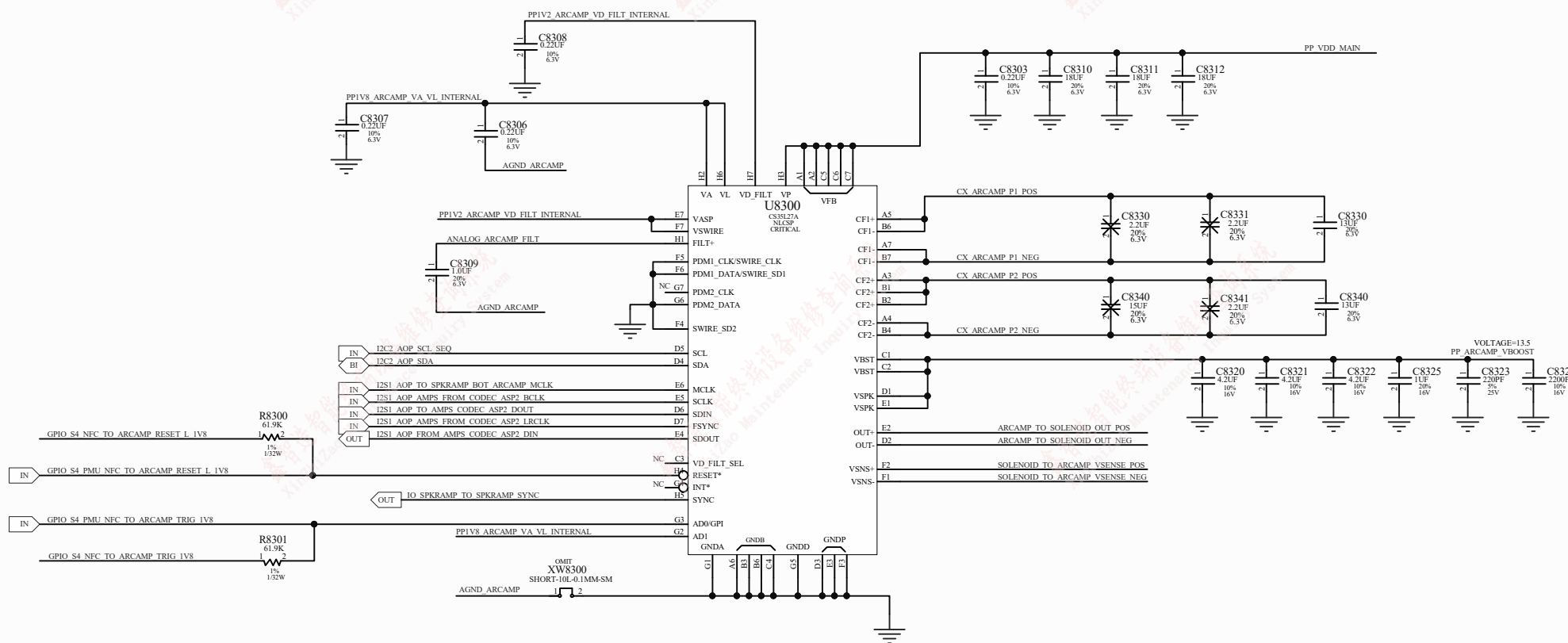
4

A

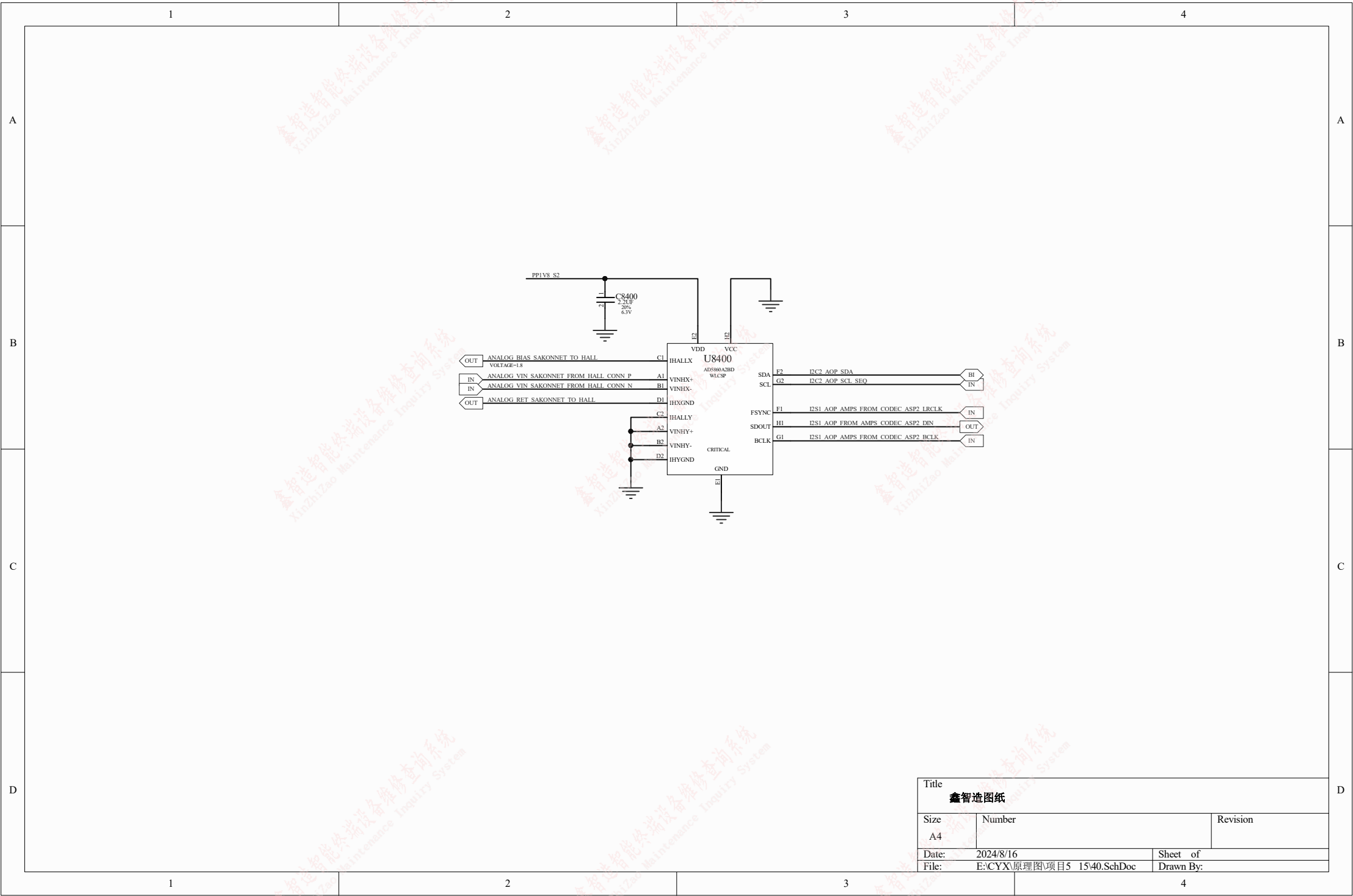
B

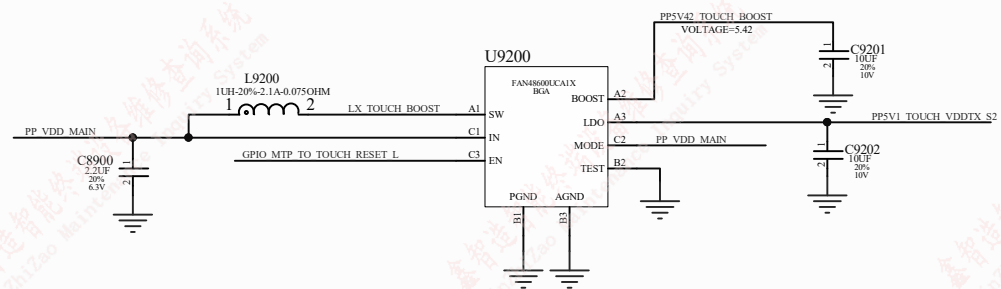
C

D

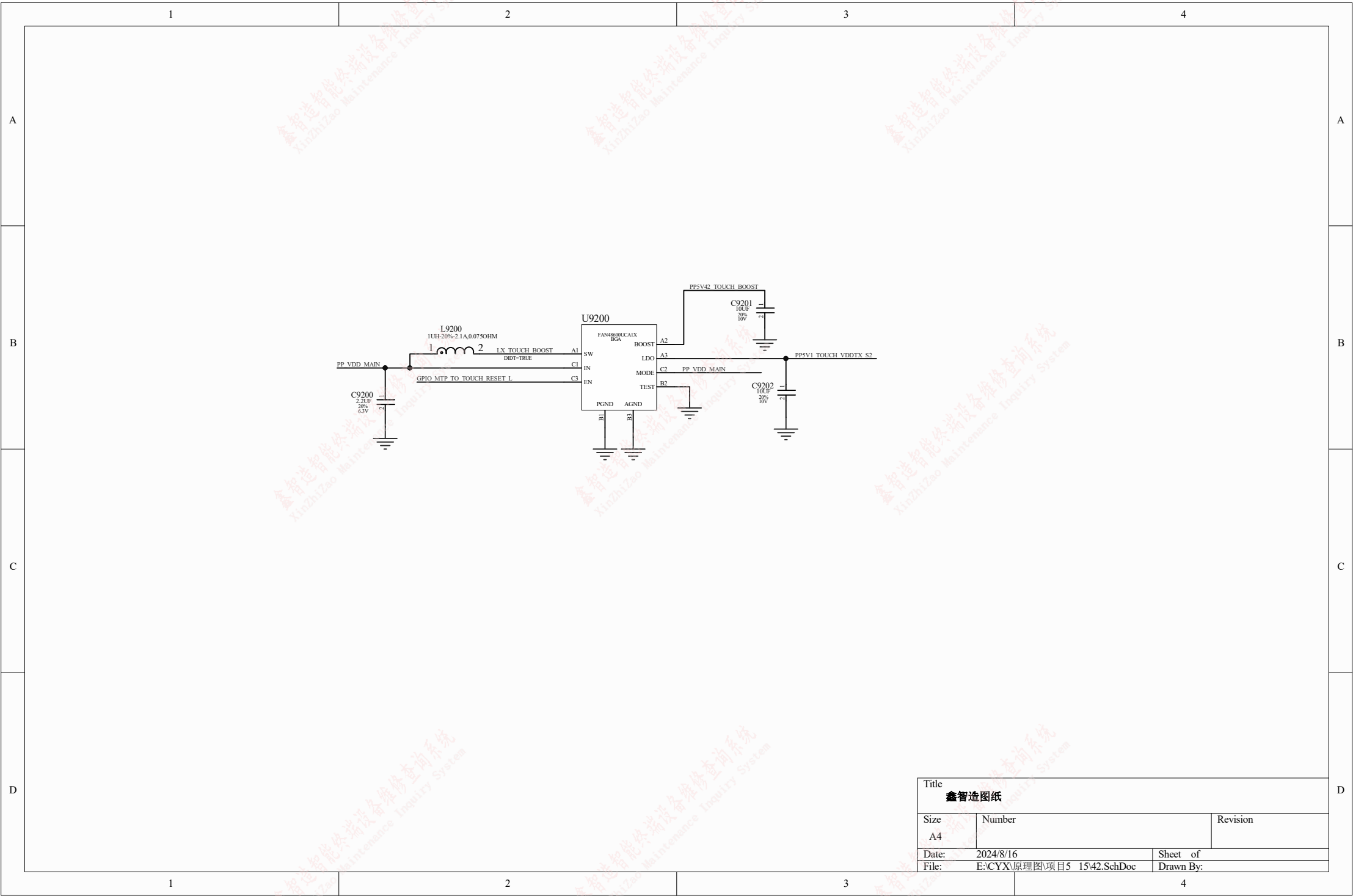


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Size A4	Number		Revision
Date:	2024/8/16		Sheet of
File:	E:\CYX\原理图\项目5 15\39.SchDoc		Drawn By:





Title		
Size A4	Number	Revision
Date: 2024/8/16	File: E:\CYX\原理图\项目5_15\41.SchDoc	Sheet of Drawn By:

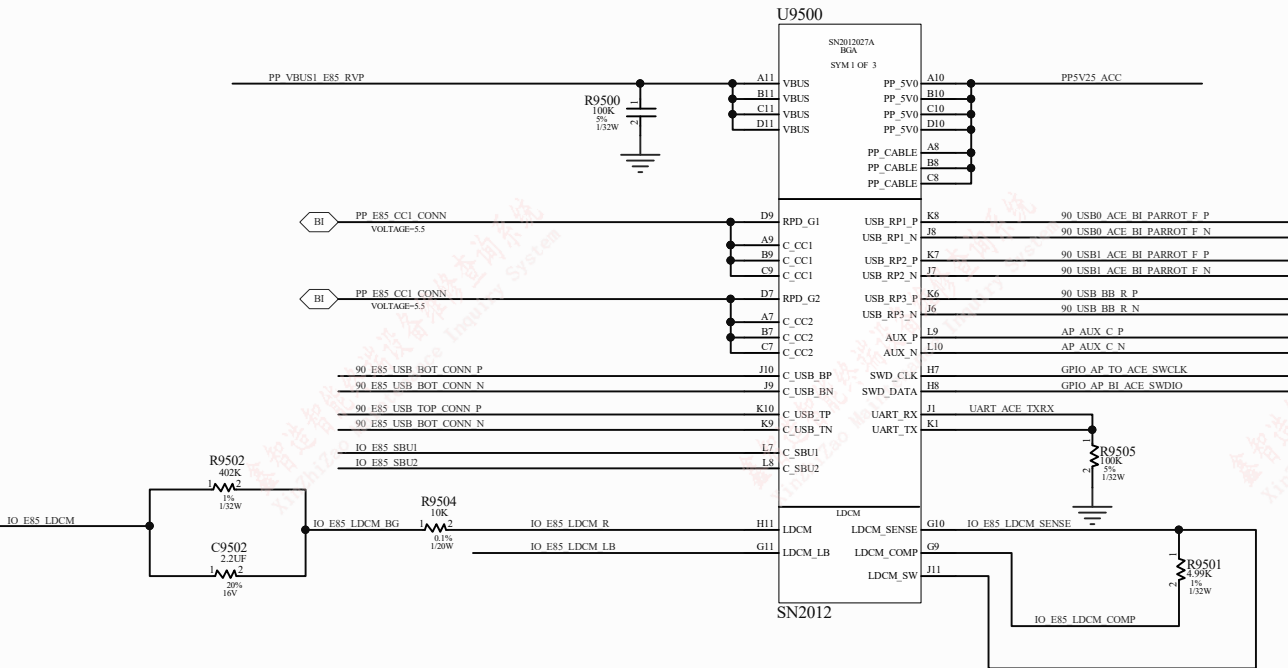


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Title		
Size	Number	Revision
A4		
Date:	2024/8/16	Sheet of
File:	E:\CYX\原理图\项目5_15\44.SchDoc	Drawn By:

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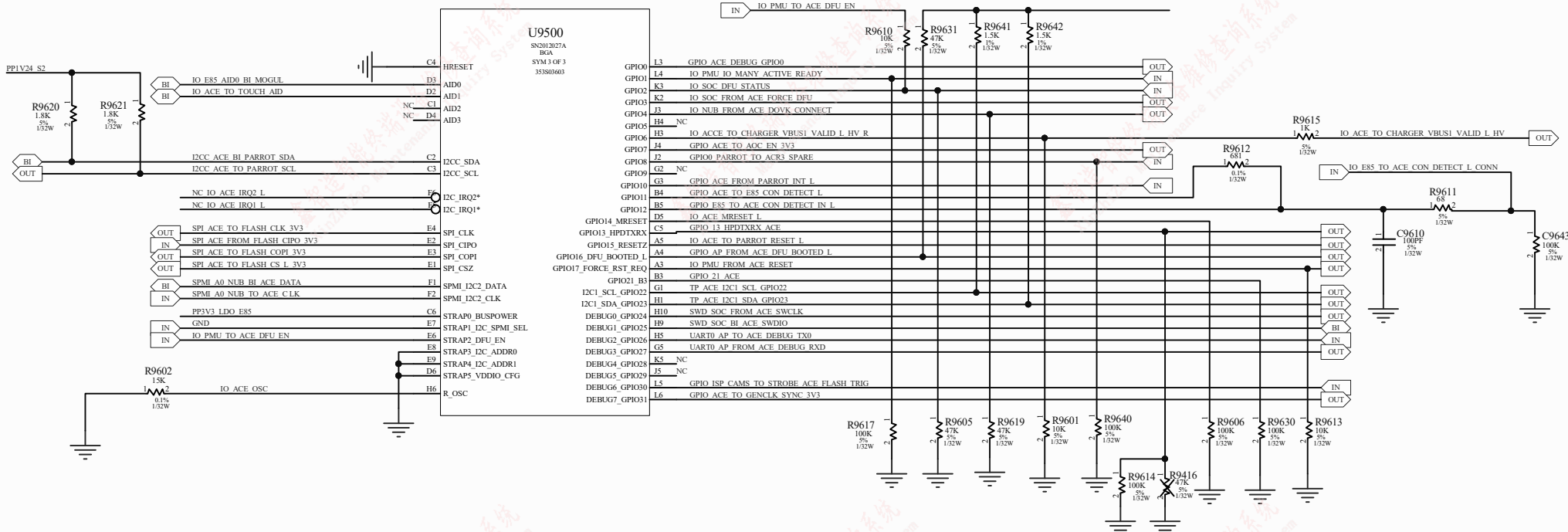
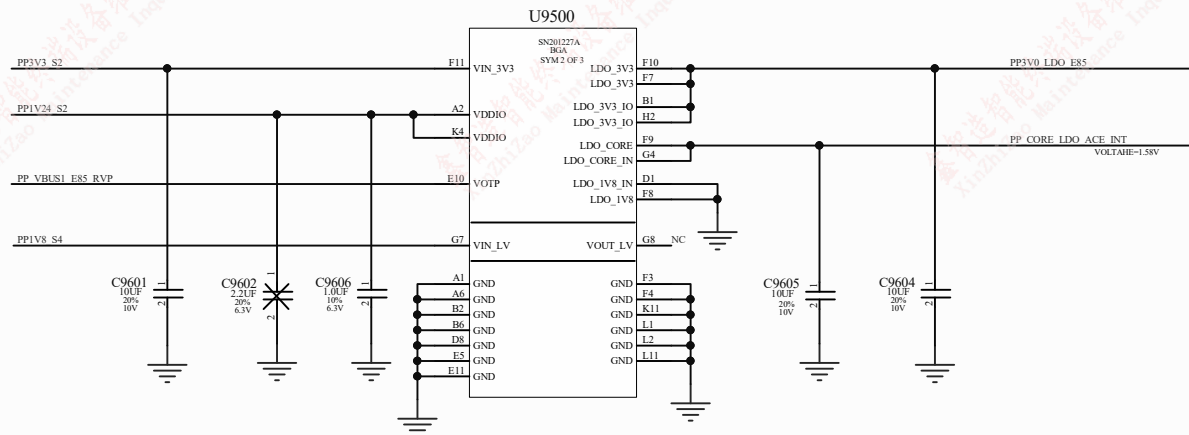
B

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Title		
鑫智造图纸		
Size	Number	Revision
A4		
Date:	2024/8/16	Sheet of
File:	E:\CYX\原理图\项目5_15\45.SchDoc	Drawn By:

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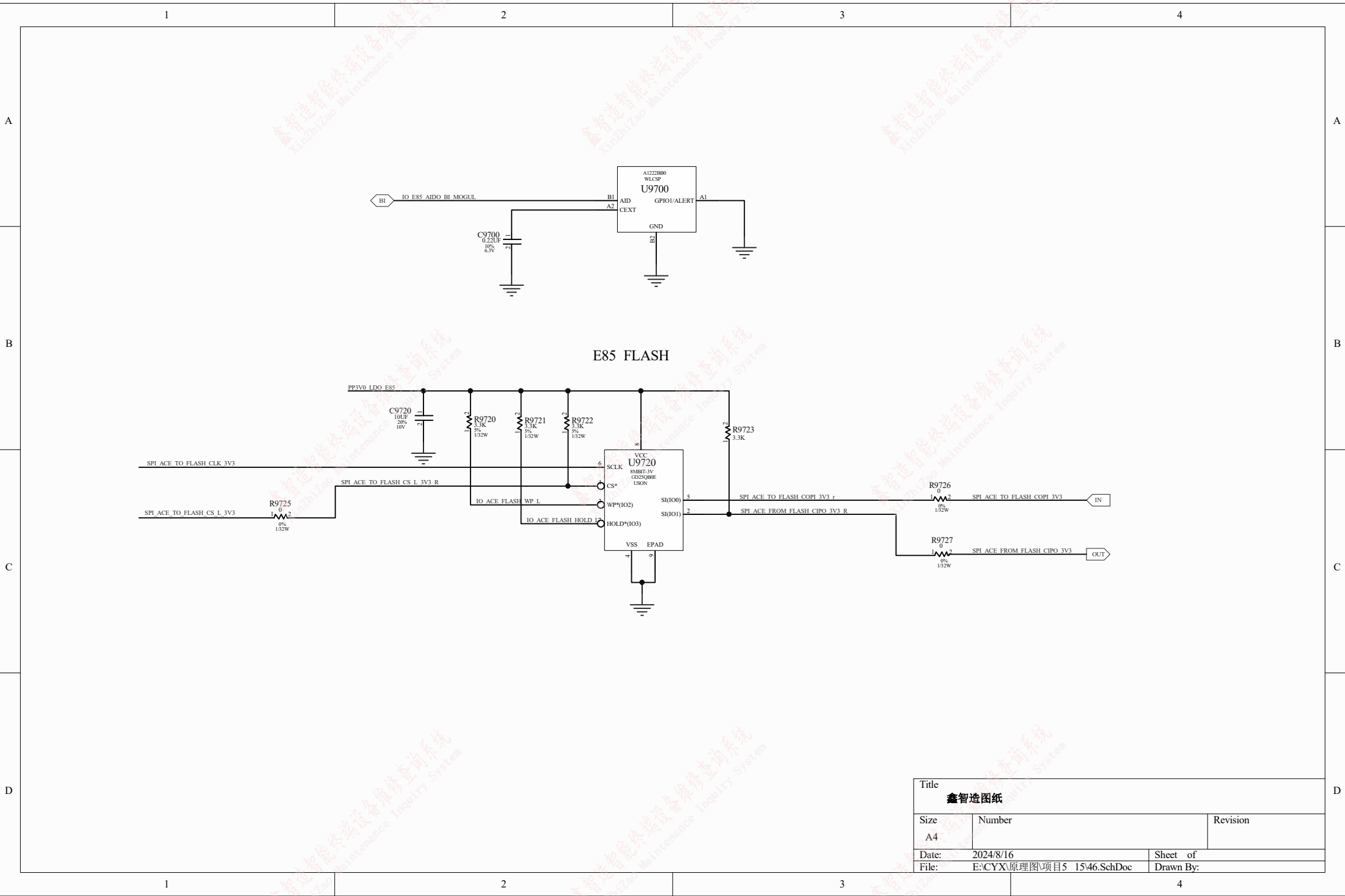
D

A

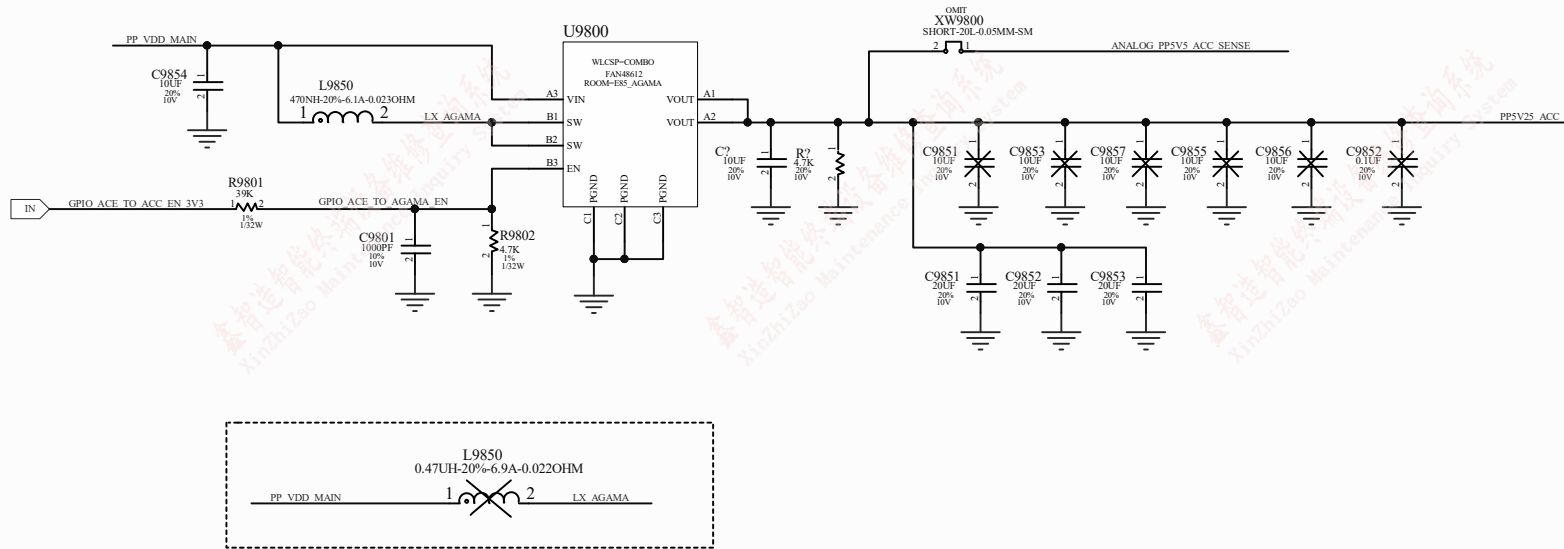
B

C

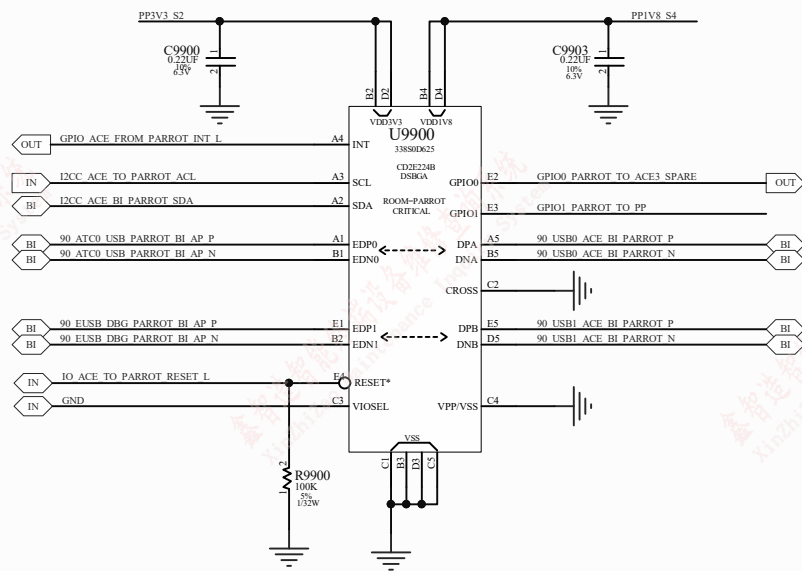
D



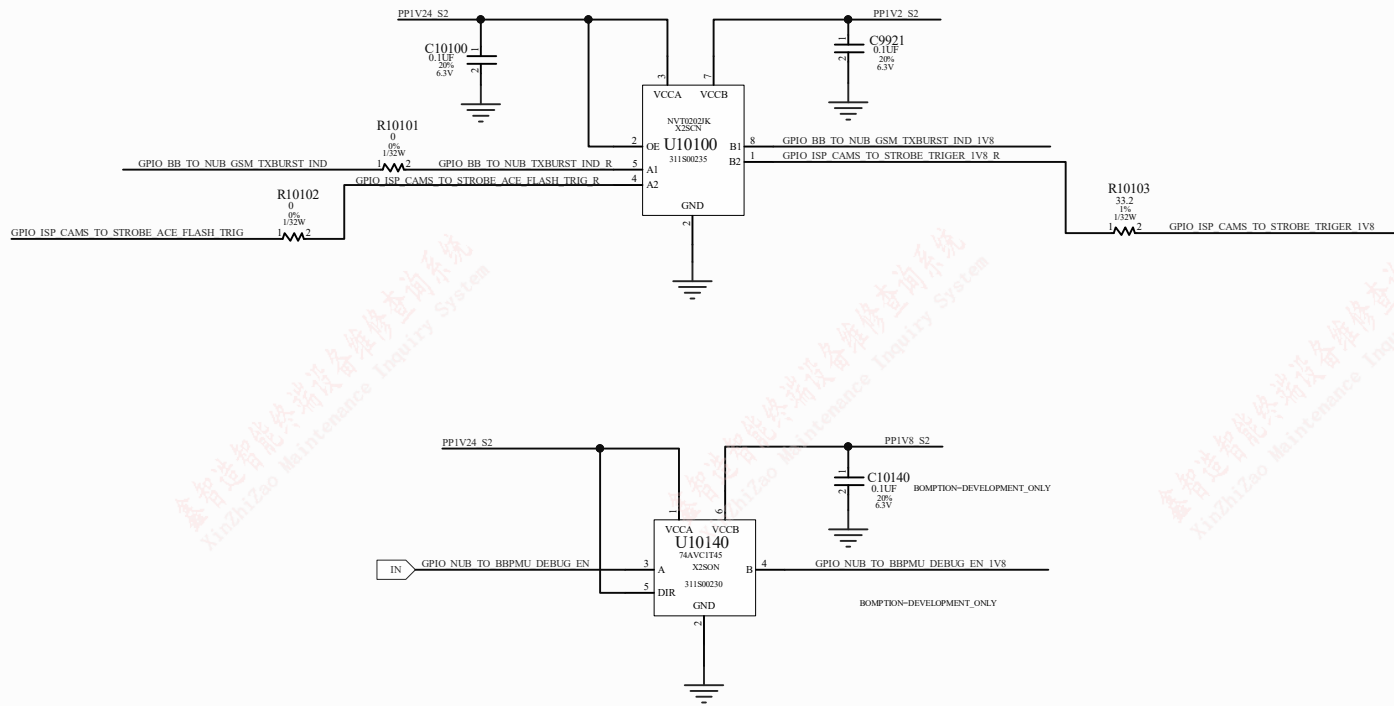
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Size A4	Number	Revision
Date:	2024/8/16	Sheet of
File:	E:\CYX\原理图\项目5 15\46.SchDoc	Drawn By:



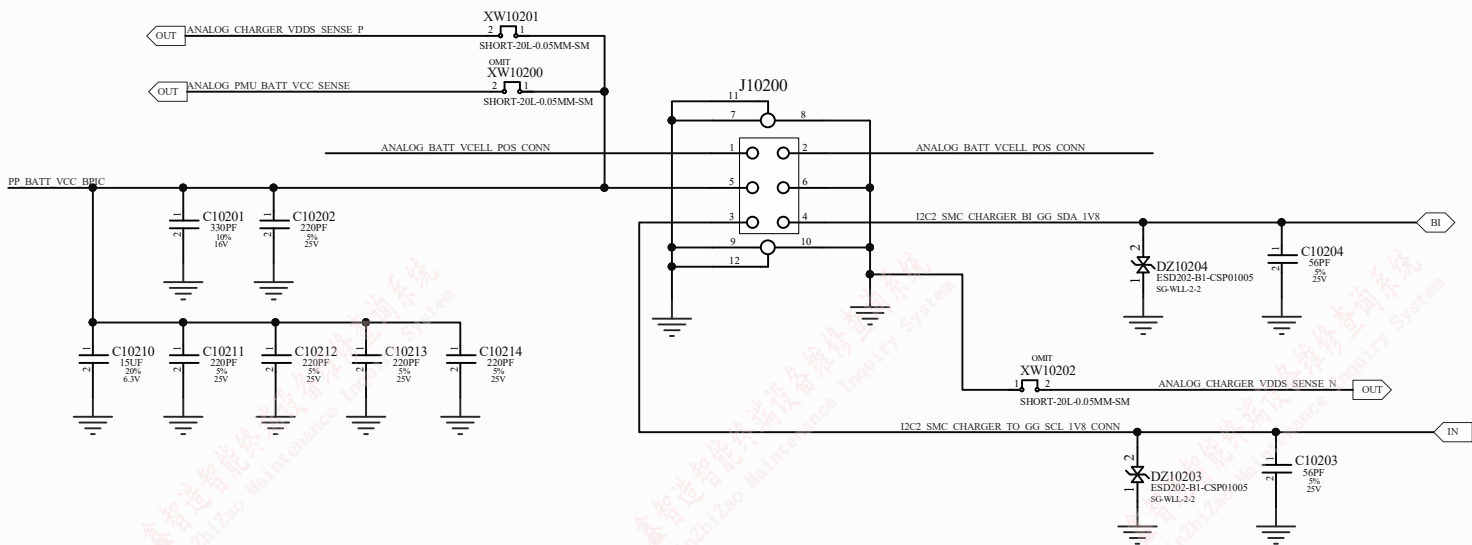
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Size A4	Number	Revision
Date:	2024/8/16	Sheet of
File:	E:\CYX\原理图\项目5_15\47.SchDoc	Drawn By:



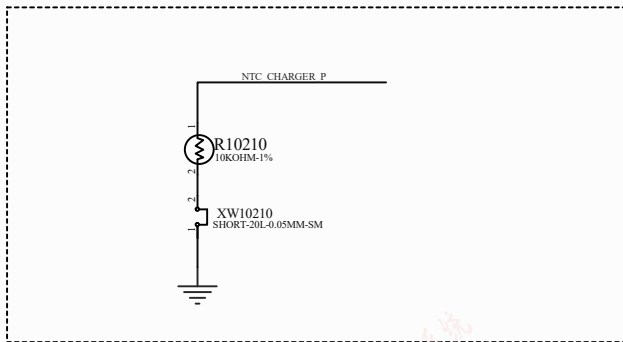
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Size A4	Number		Revision
Date:	2024/8/16		Sheet of
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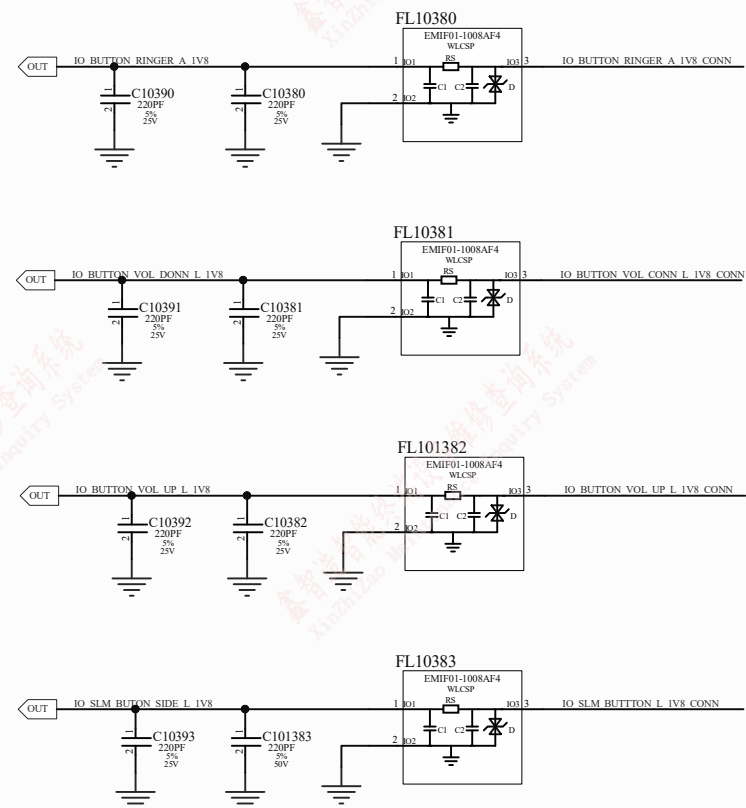
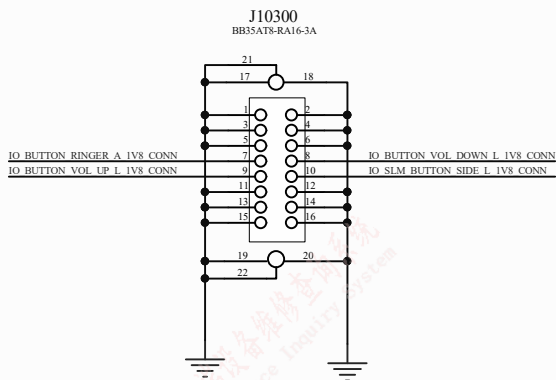
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Size A4	Number		Revision
Date:	2024/8/16		Sheet of
File:	E:\CYX\原理图\项目5 15\49.SchDoc		Drawn By:



Rubicon NTC

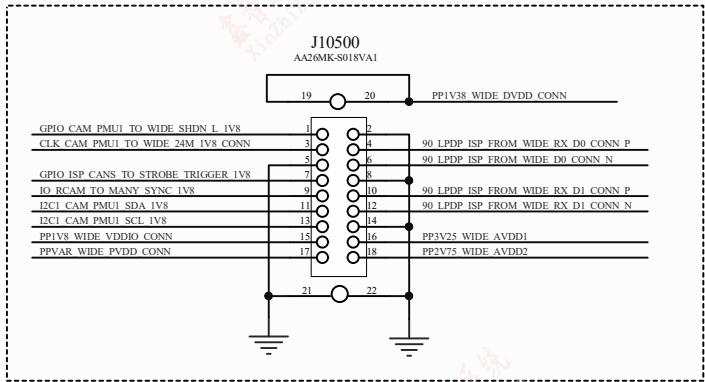


Title 鑫智造图纸			
Size A4	Number		Revision
Date:	2024/8/16		Sheet of
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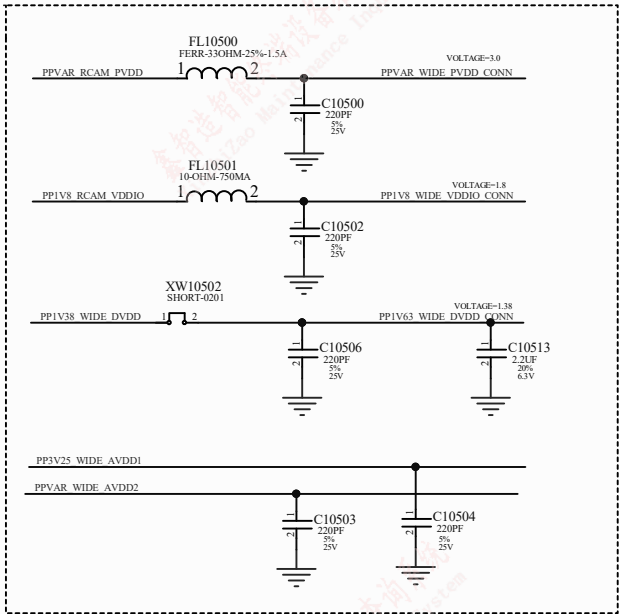


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Size A4	Number		Revision
Date:	2024/8/16		Sheet of
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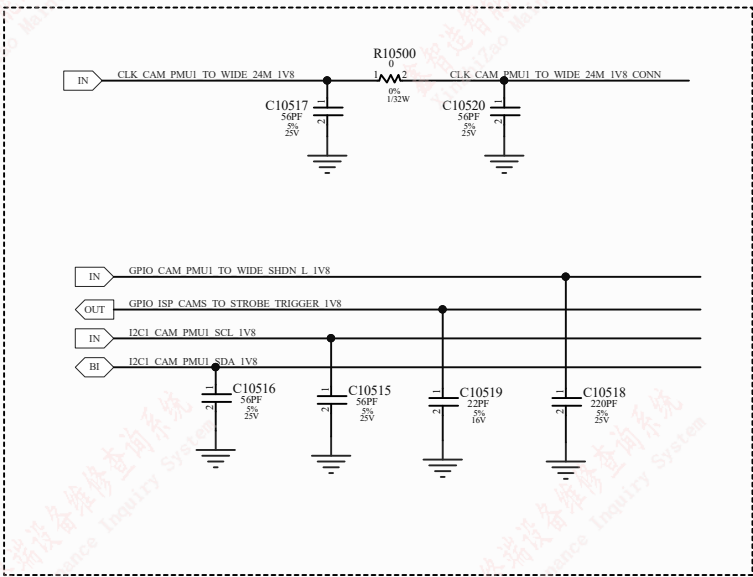
CONNECTOR



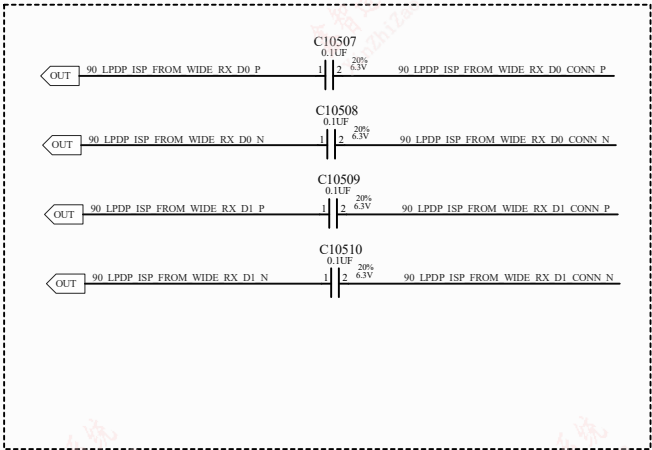
POWER FILTERS



IO FILTERS

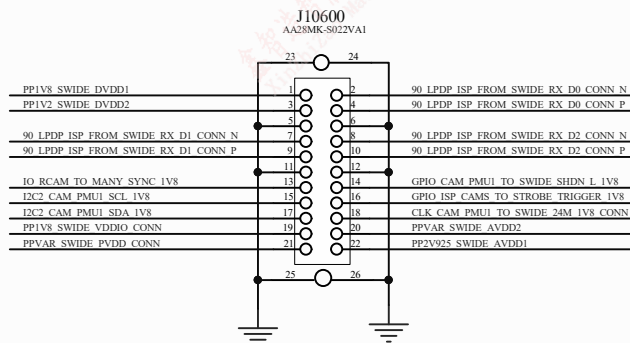


LPDP FILTERS

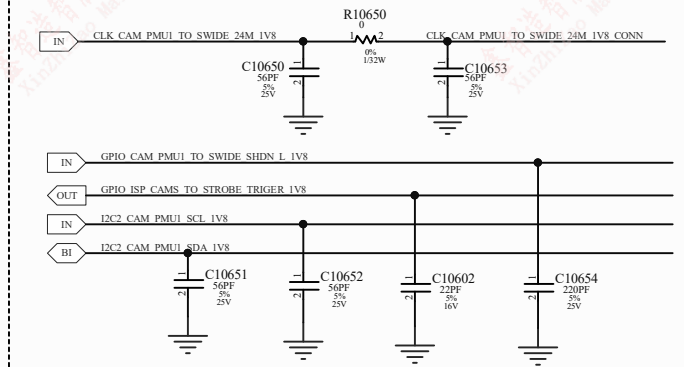


Title		
Size	Number	Revision
A4		
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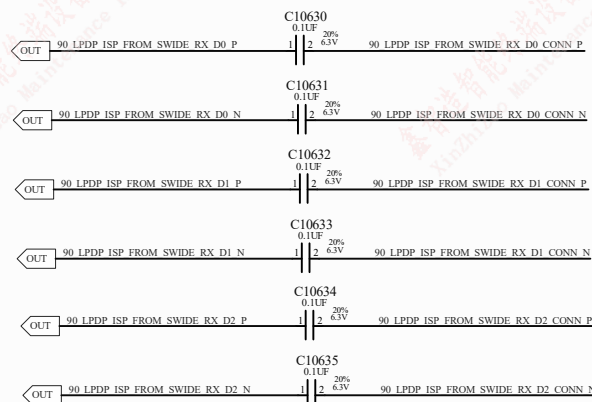
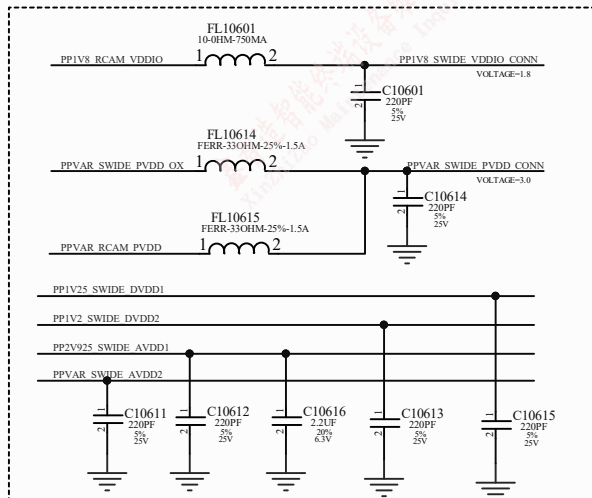
CONNECTOR



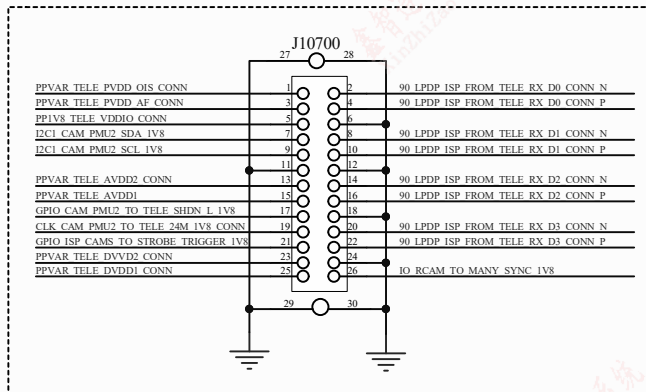
IO FILTERS



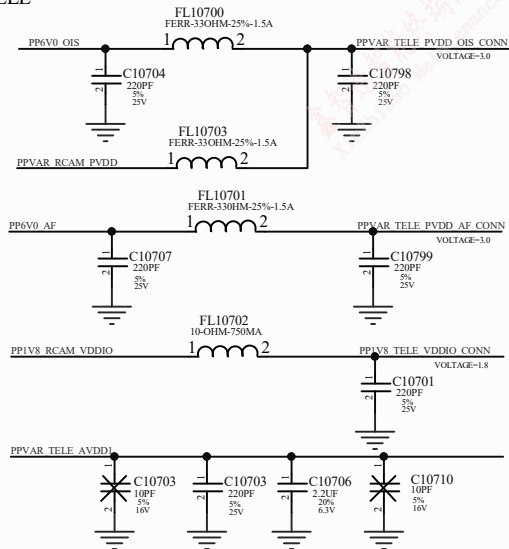
POWER FILTERS



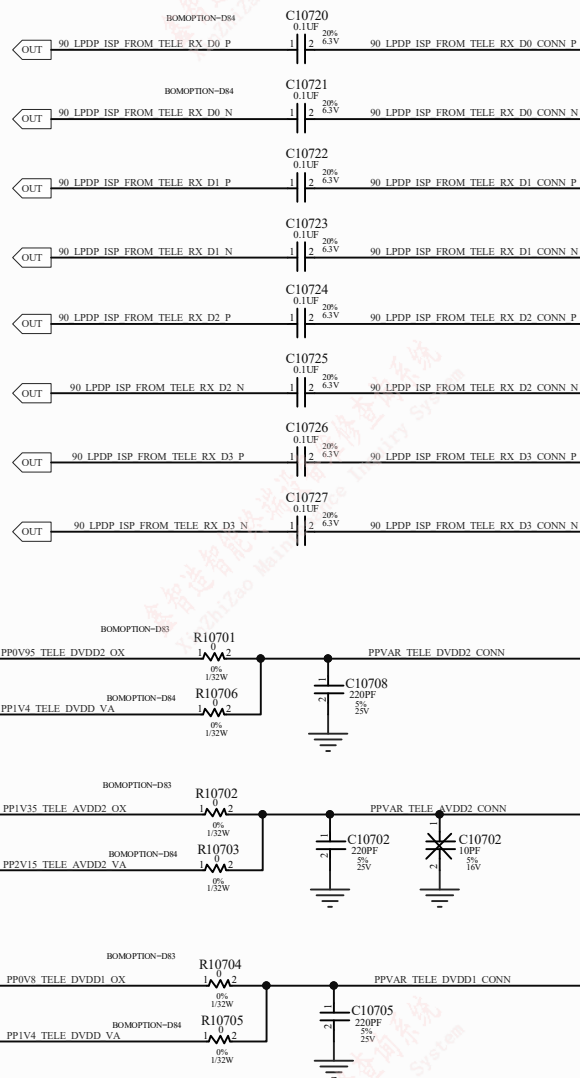
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Size A4	Number	Revision
Date: 2024/8/16	Sheet of	
File: E:\CYX\原理图\项目5 15\53.SchDoc	Drawn By:	



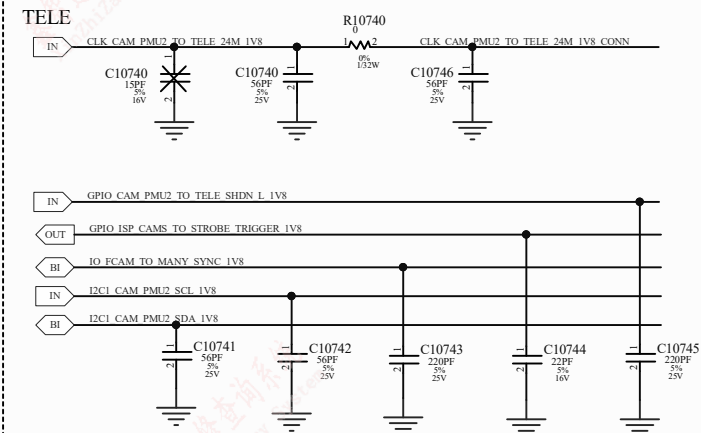
POWER FILTERS TELE



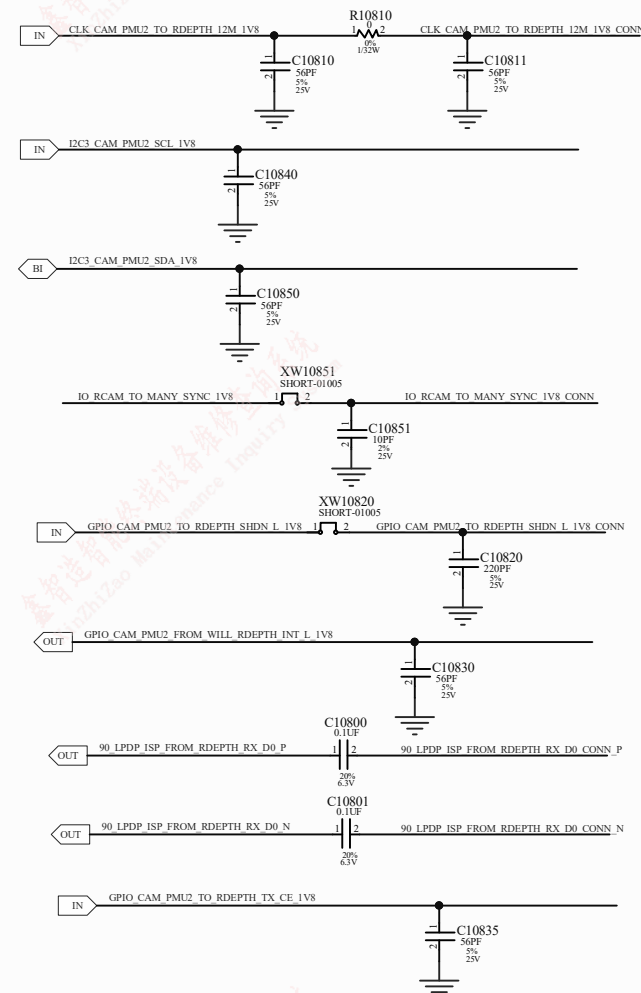
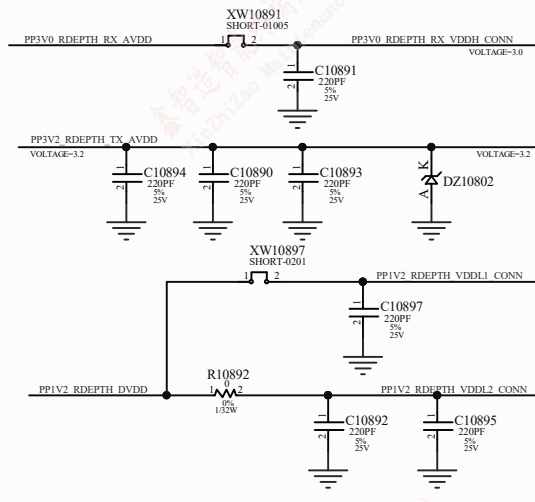
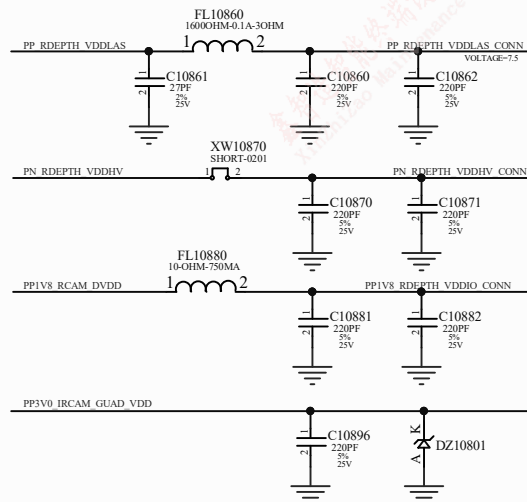
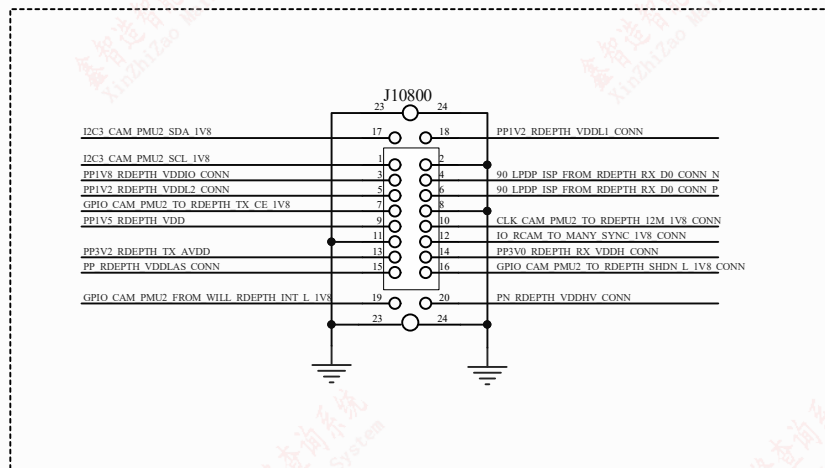
TELE



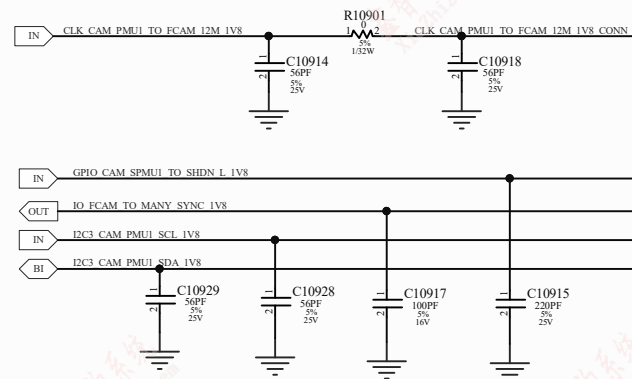
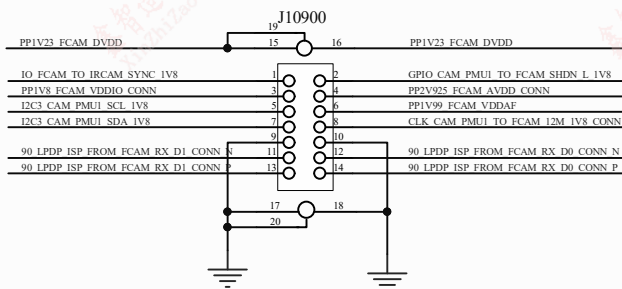
IO FILTERS



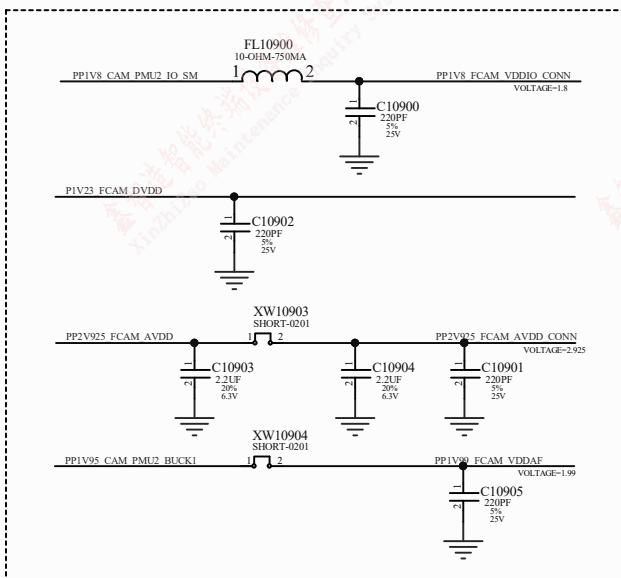
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File:	E:\CYX\原理图\项目5_15\54.SchDoc	Drawn By:



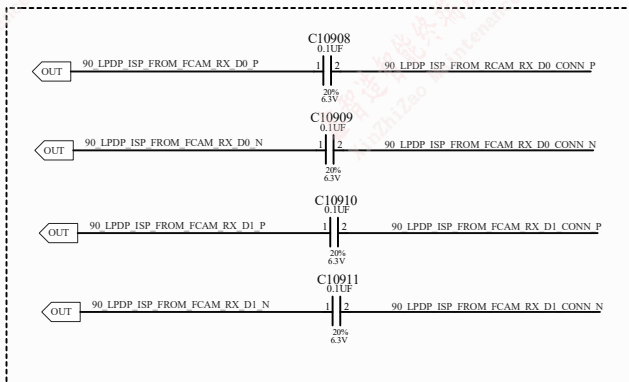
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A4		
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File:	E:\CYX\原理图\项目5 15\55.SchDoc	Drawn By:



POWER FILTERS



LPDP FILTERS



Title 鑫智造图纸			
Size A4	Number		Revision
Date:	2024/8/16		Sheet of
File:	E:\CYX\原理图\项目5_15\27.SchDoc		Drawn By:

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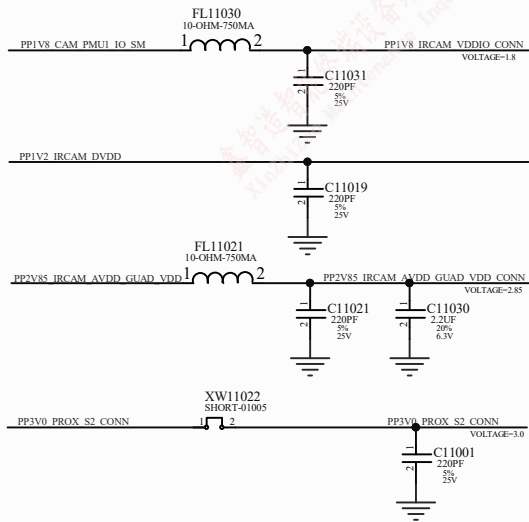
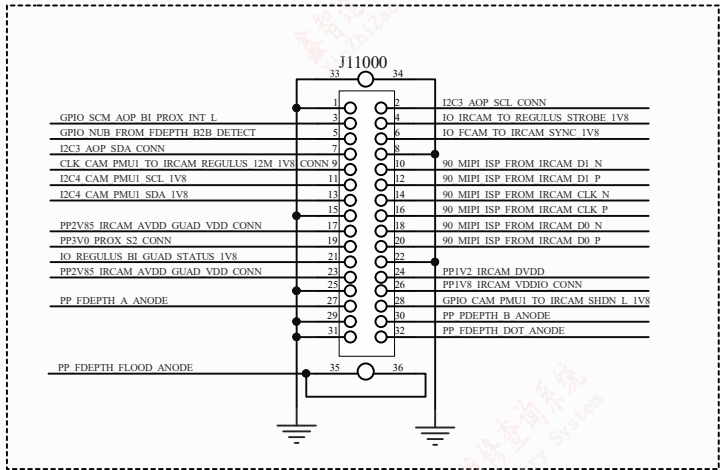
C

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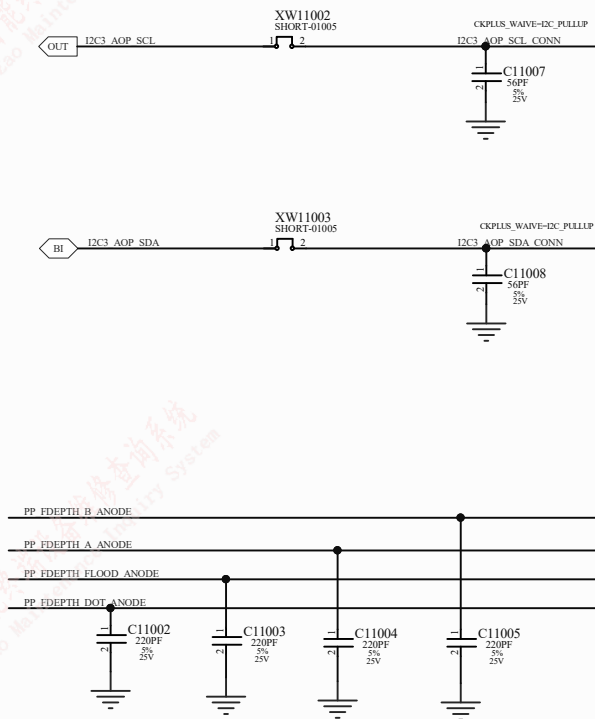
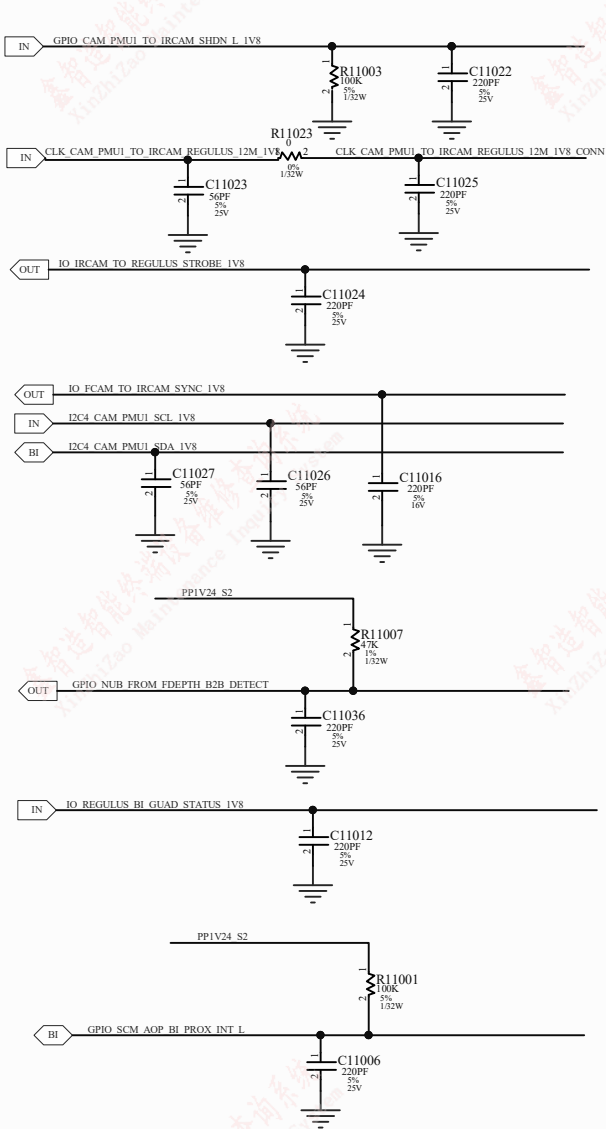
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MIC3



Title		
Size	Number	Revision
A4		
Date:	2024/8/16	Sheet of
File:	E:\CYX\原理图\项目5 15\56.SchDoc	Drawn By:

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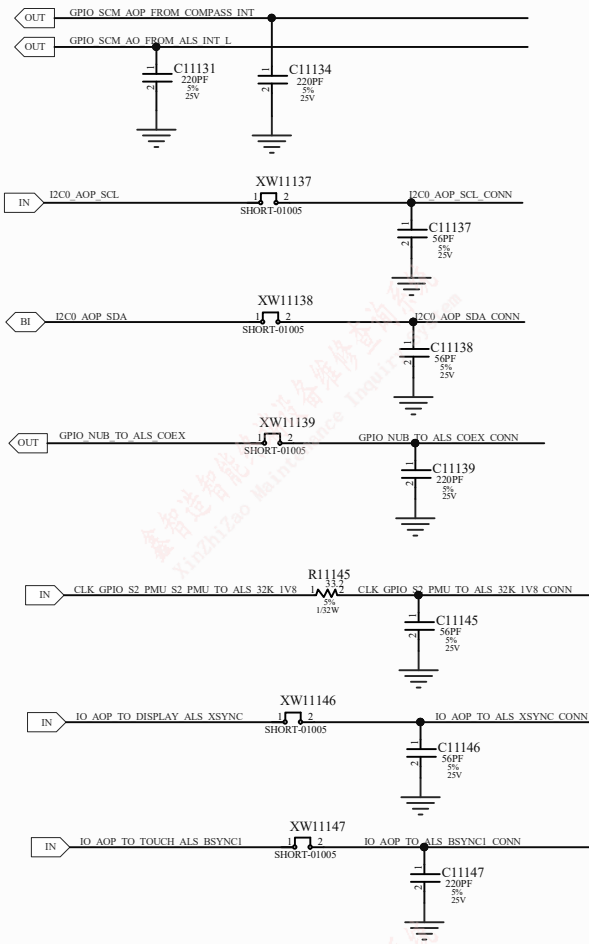
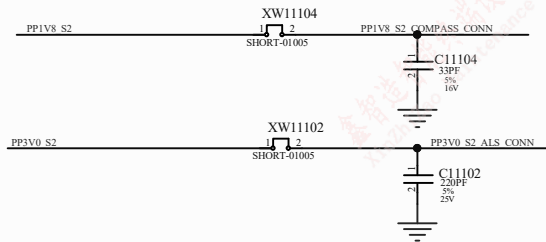
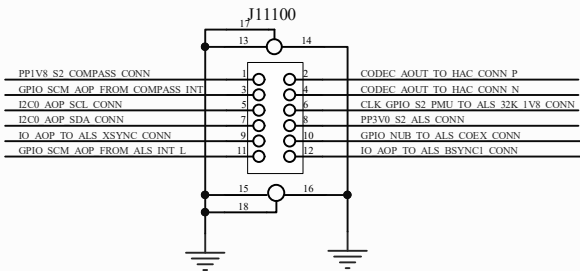
DD

1

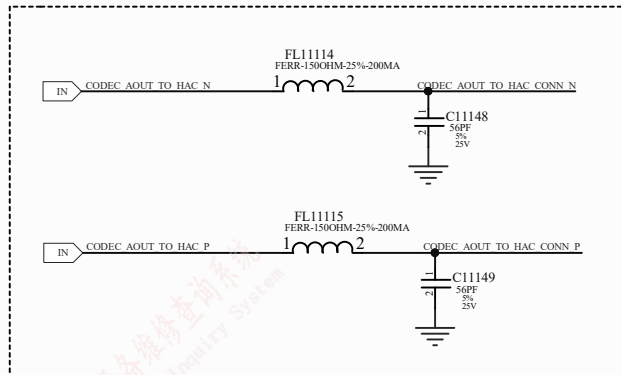
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Title		
Size	Number	Revision
A4		
Date:	2024/8/16	Sheet of
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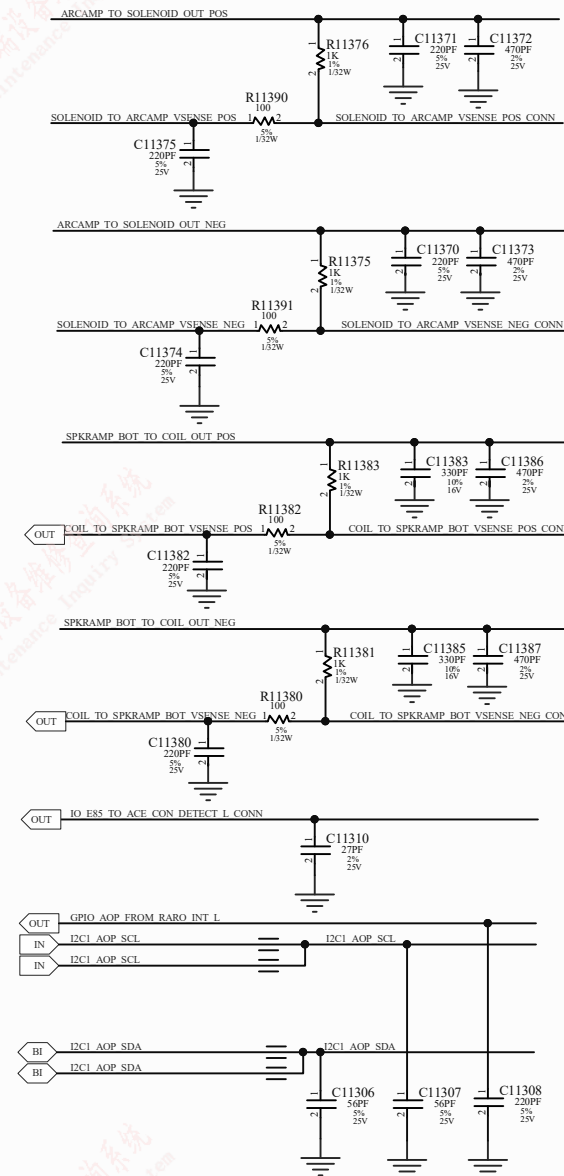
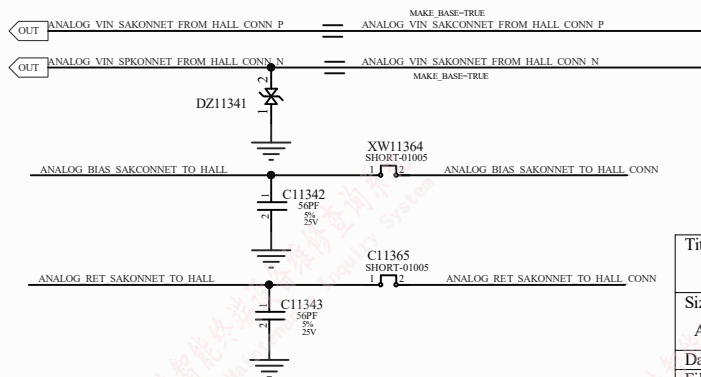
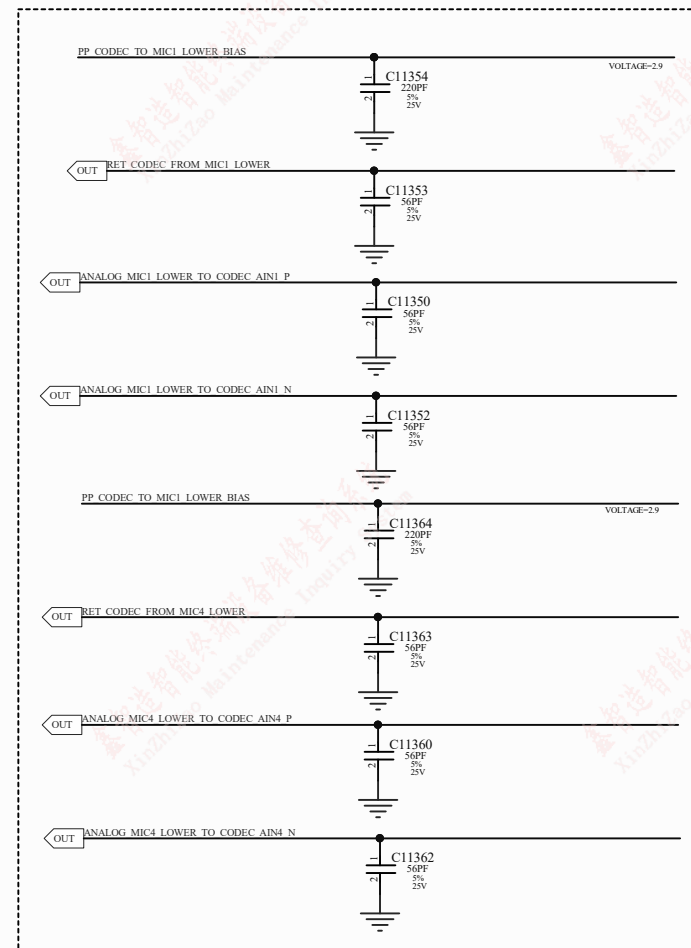
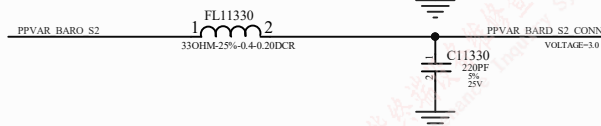
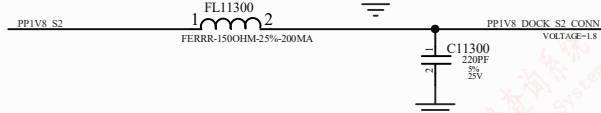
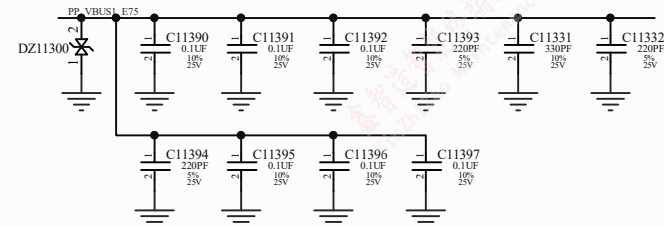
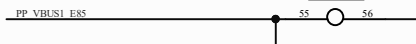
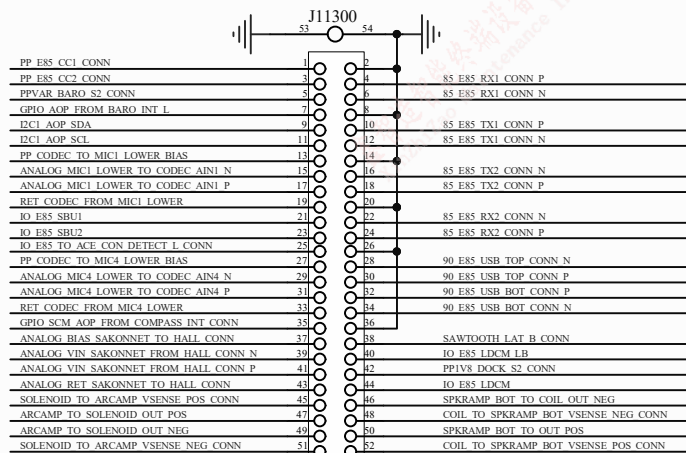
D

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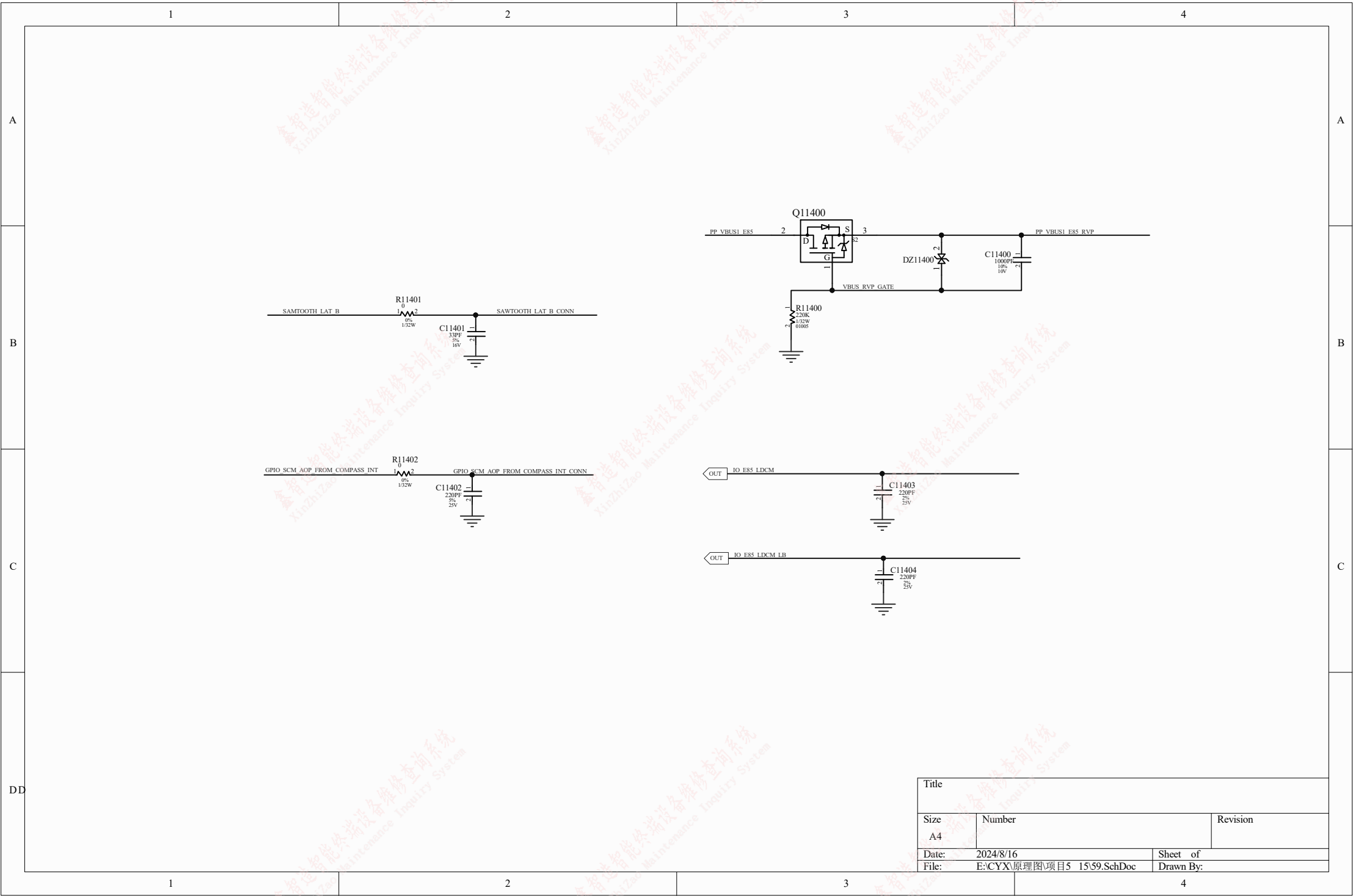
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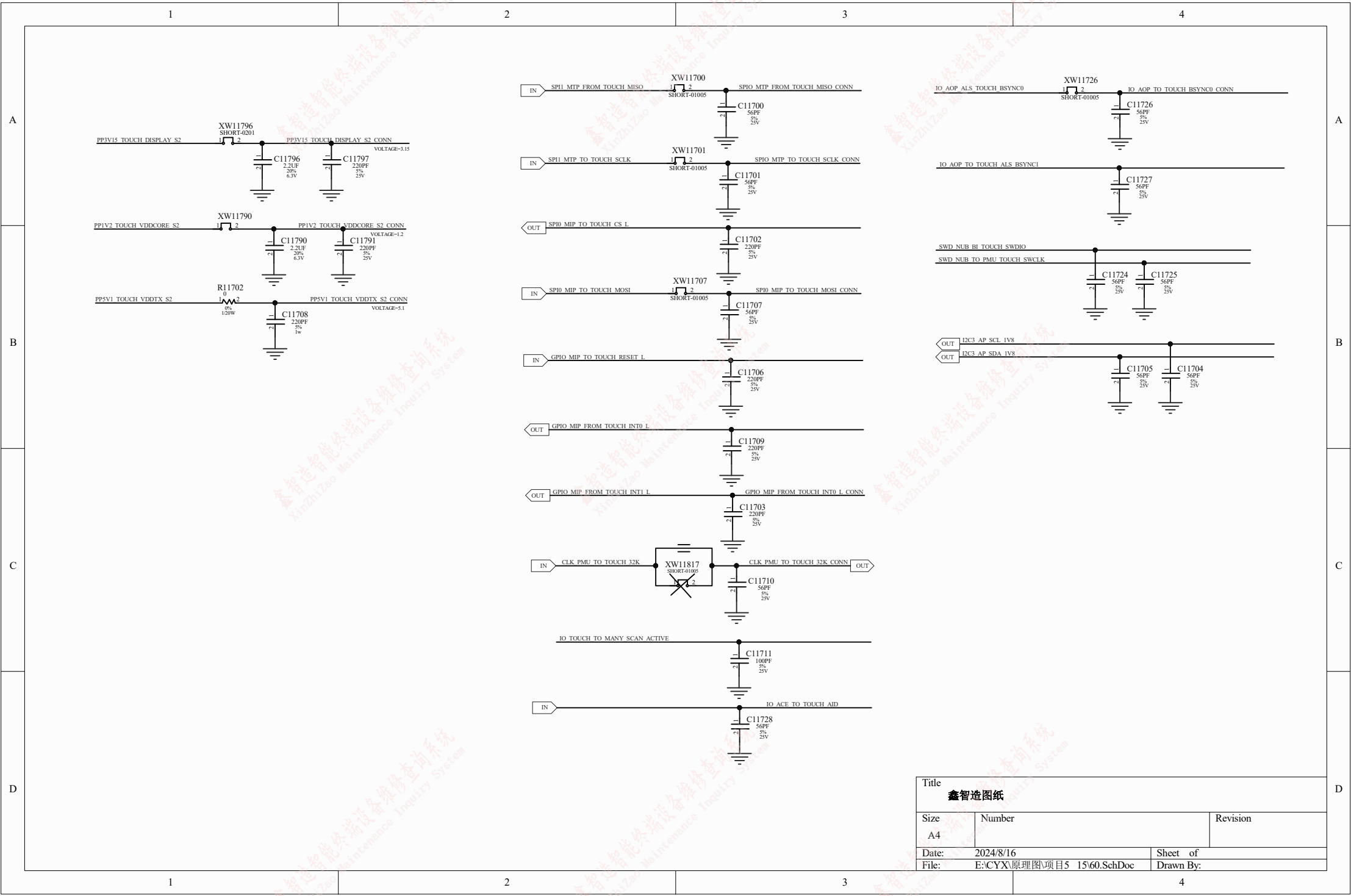
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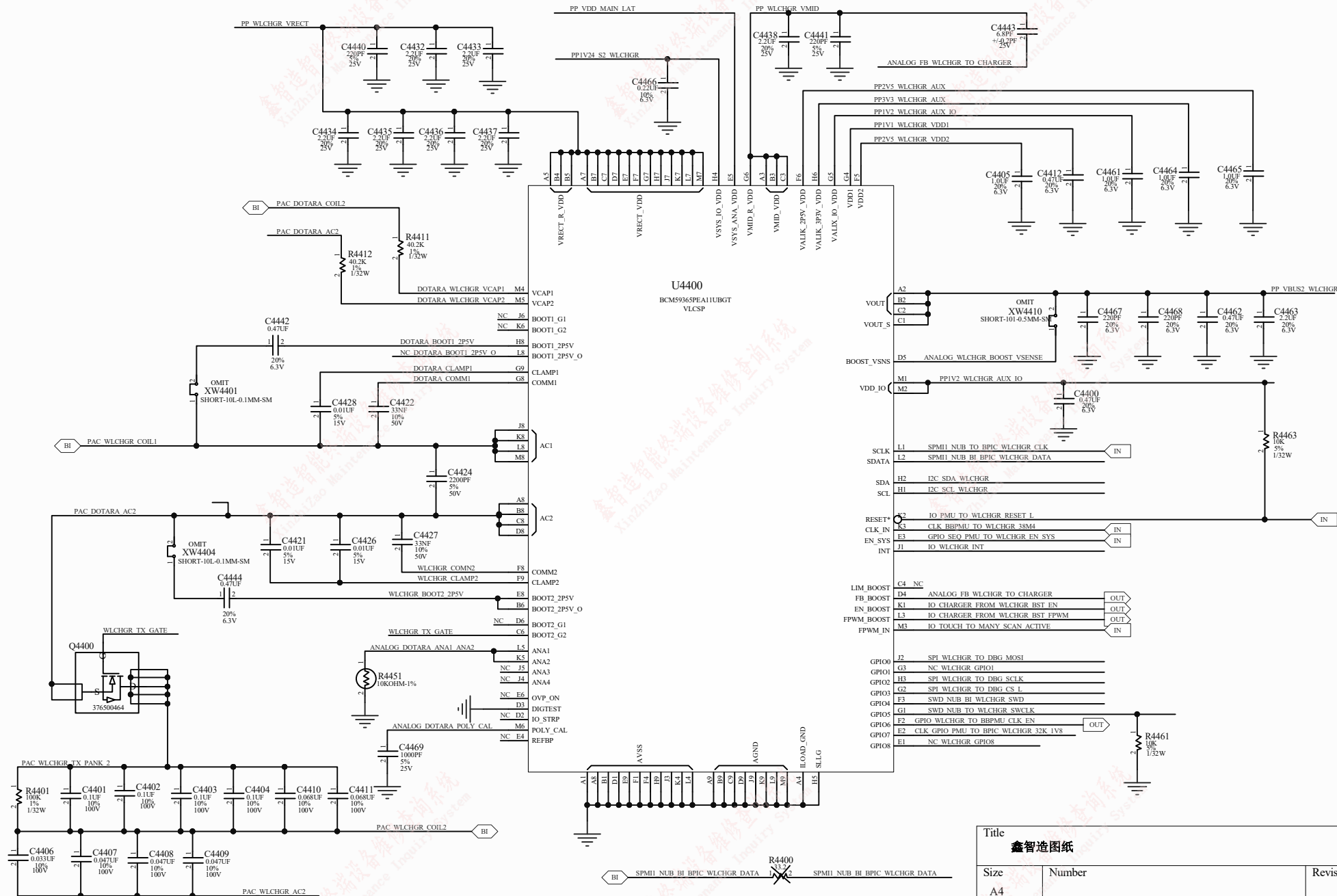
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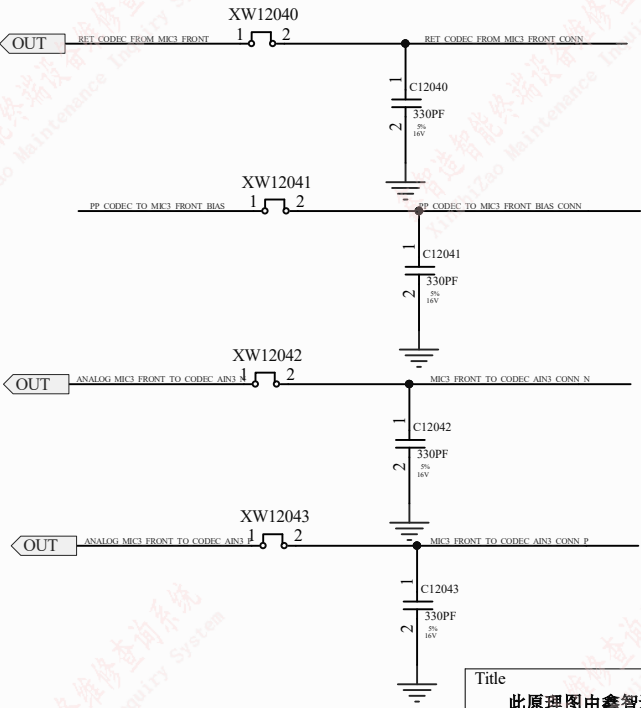
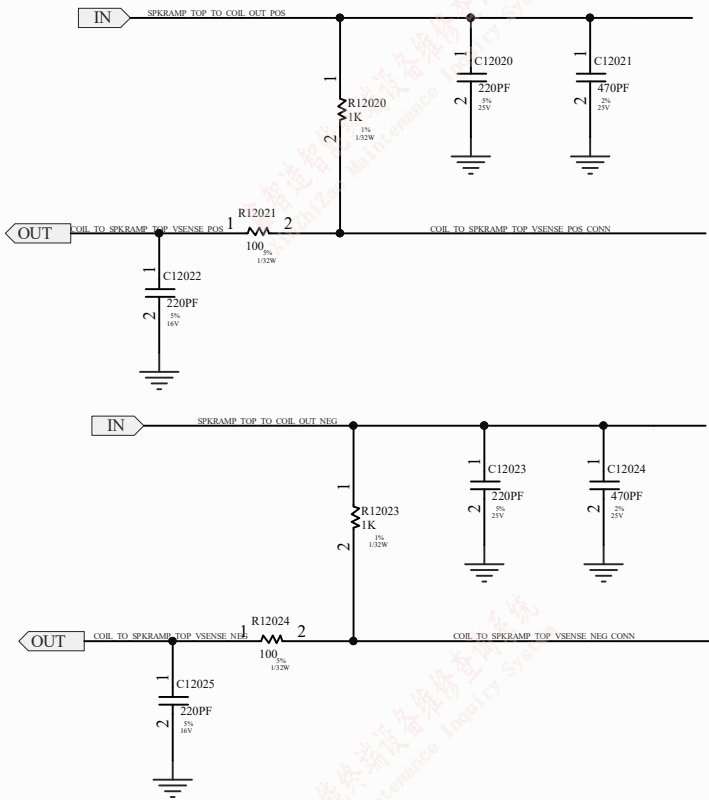
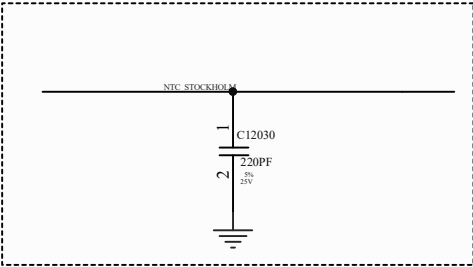
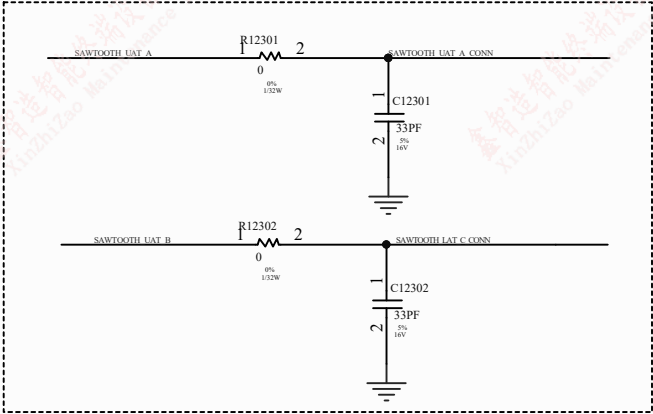
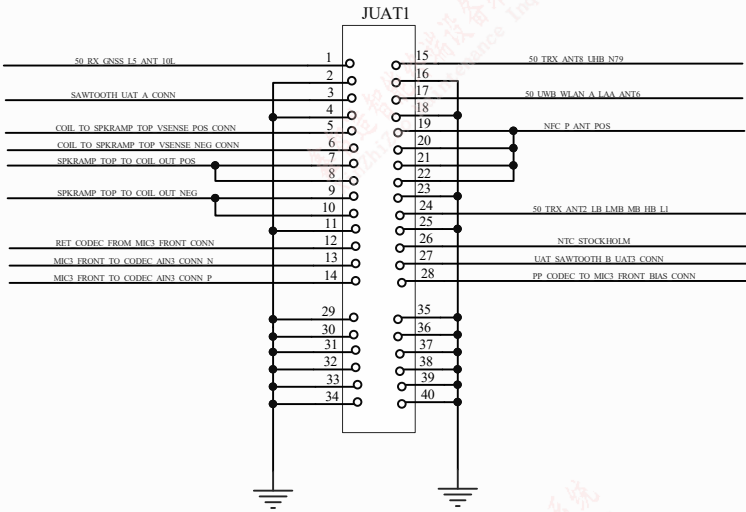
D

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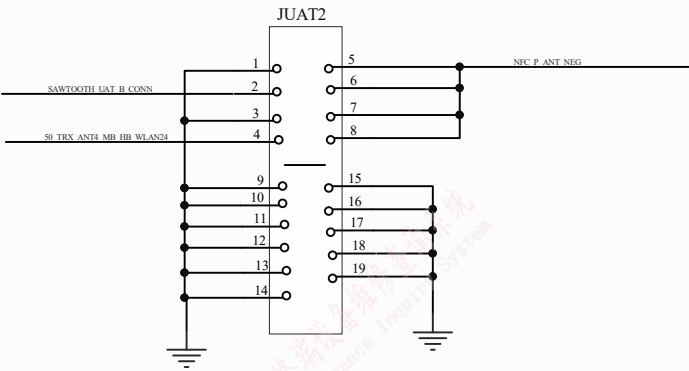
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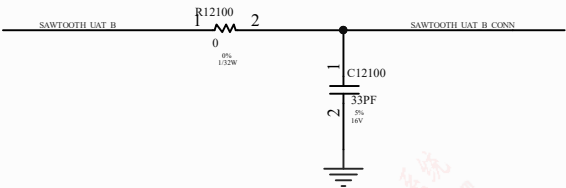
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CONNECTOR

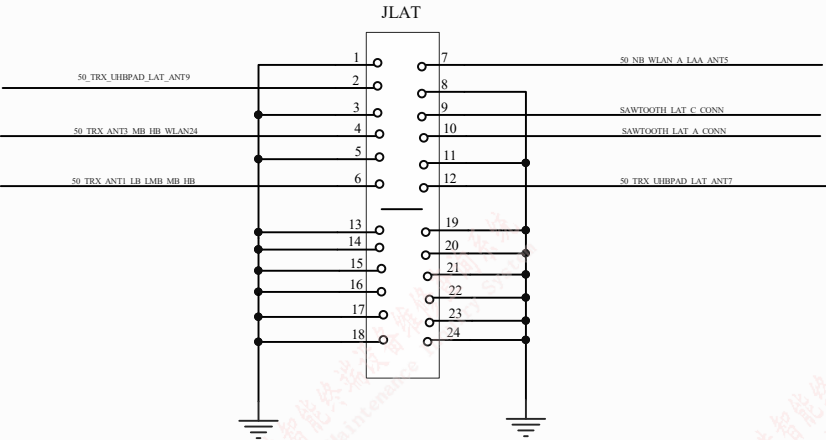


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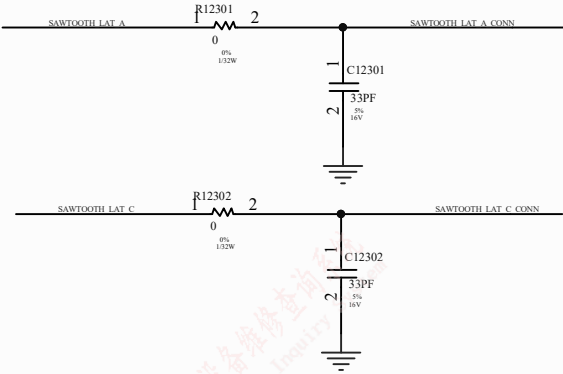


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File:	D:\HCP\...\73.SchDoc	Drawn By:

CONNECTOR



SUBUS FILTERS



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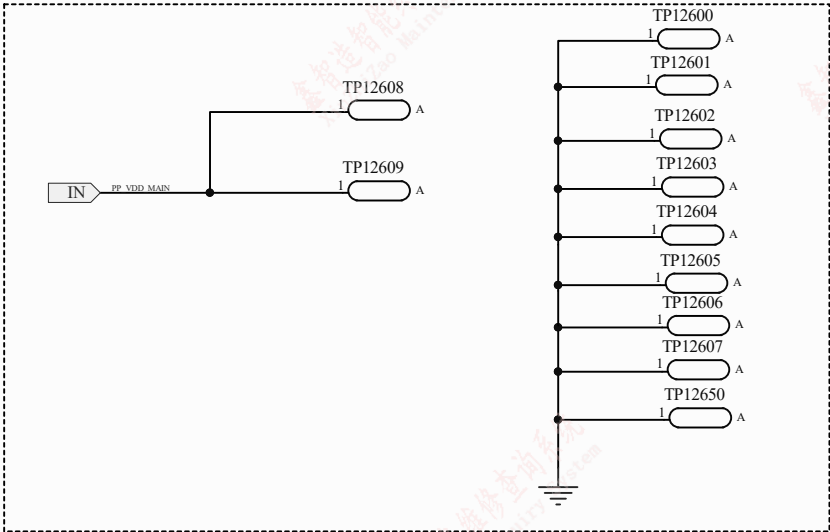
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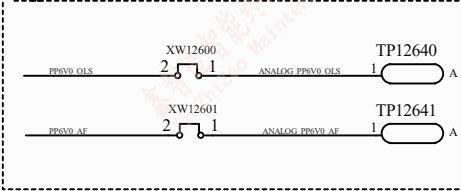
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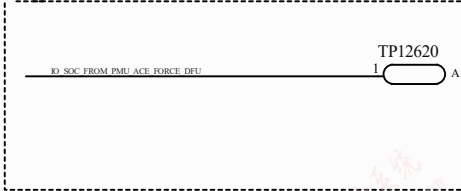
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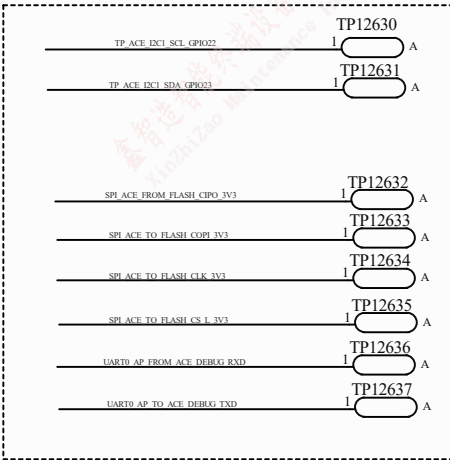
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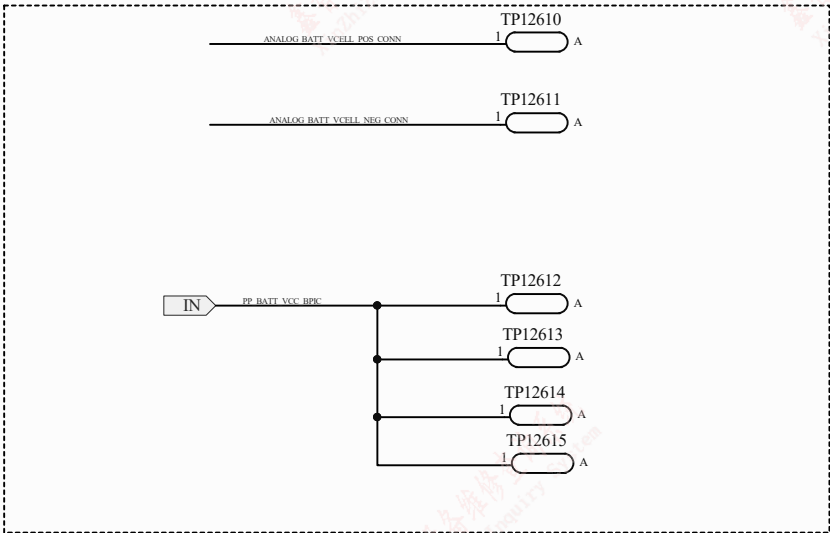
DFU



E85



BPIC



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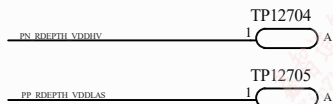
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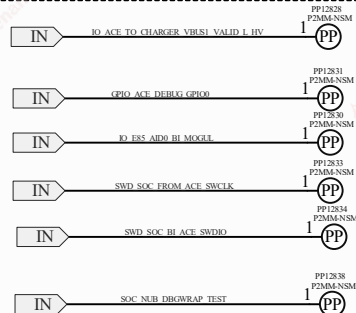
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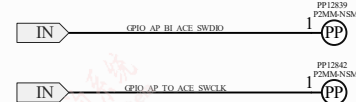
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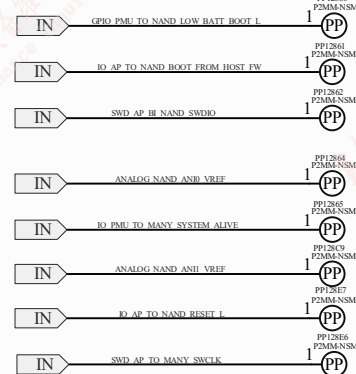
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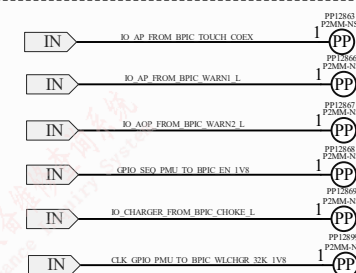
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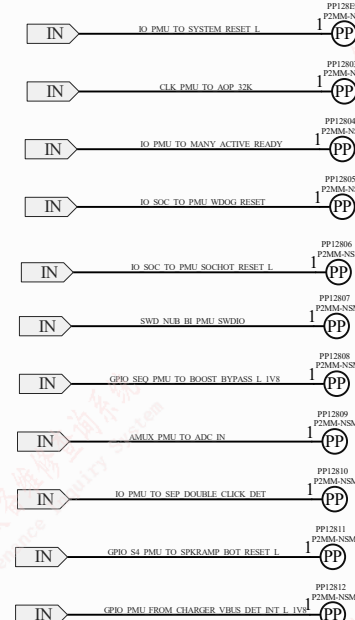
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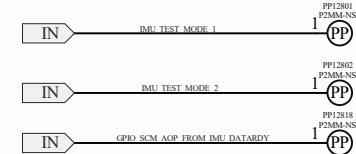
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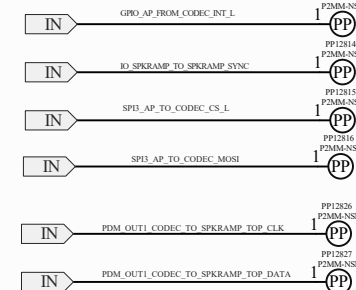
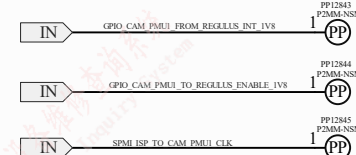
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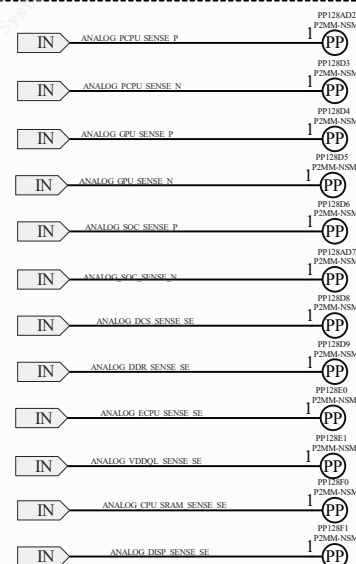
IMU



Camera PMU1

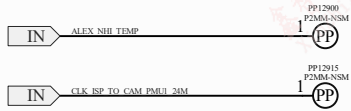


Bump Sense

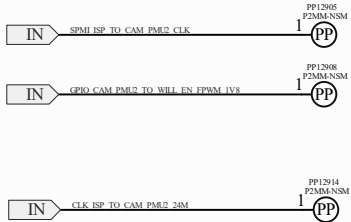


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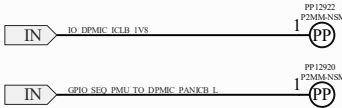
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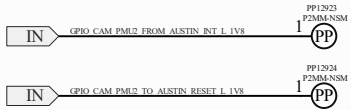
Camera PMU2



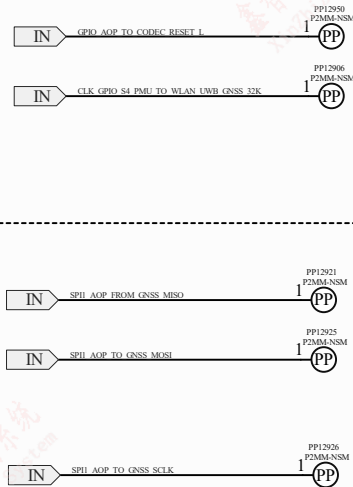
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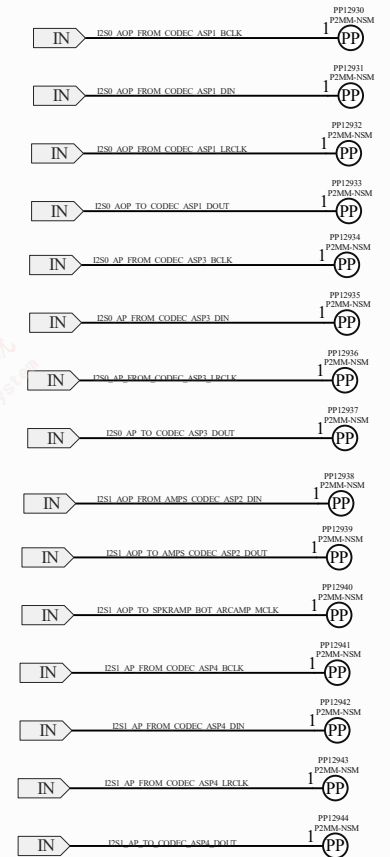
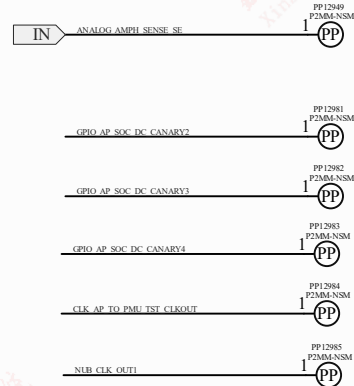
AUSTIN



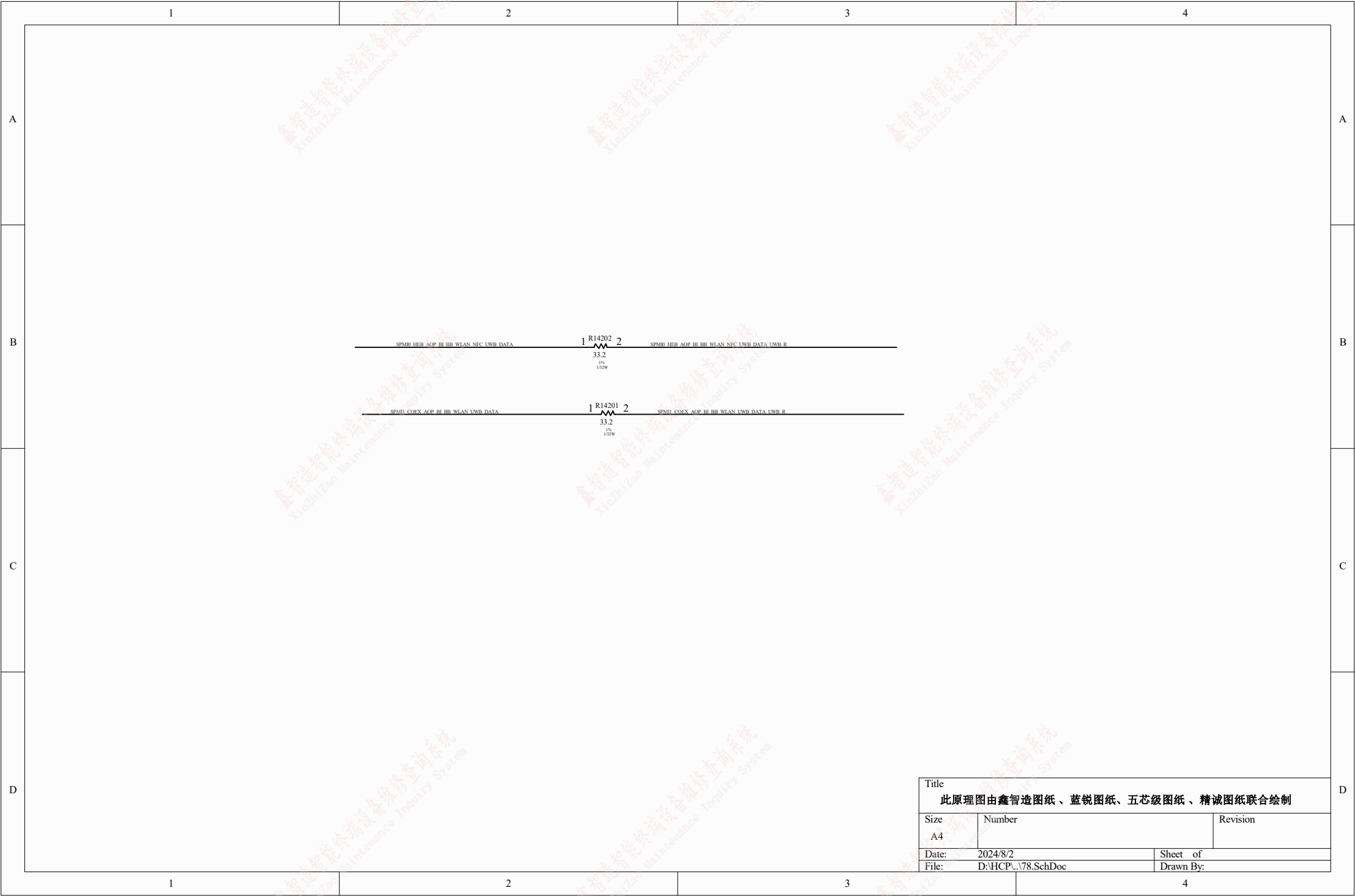
SPI



SOC



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Date:	2024/8/2	Sheet of
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SPMD_COEX_AOP_HL_HH_WLAN_UWB_DATA

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SPMD_COEX_AOP_HL_HH_WLAN_UWB_DATA_UWB_B

33.2

1%

1/32W

J_INT_BOT

INT-PAD
SYM 1 OF 4

GPIO AP INTERPOSER CANARY6	1	K01	91	GND
GND	2	K02	92	GND
GND	3	K03	93	ANALOG UAT 5G FEM TO WLAN PDT'S
GND	4	K04	94	GND
GND	5	K05	95	GND
IO TOUCH TO MANY SCAN ACTIVE	6	K06	96	SHIELD RFFE S4 WLAN TO UAT FEMS DATA
GND	7	K07	97	RFFE S4 WLAN TO UAT 5G FEM RST L
GND	8	K08	98	SHIELD RFFE S4 WLAN TO UAT FEMS CLK
GND	9	K09	99	GND
PP1V8 S2	10	K10	100	GND
GND	11	K101	101	50 NB TO 5G FEM LAT NORTH
GND	12	K102	102	GND
GND	13	K103	103	GND
GPIO SEQ S4 PMU TO WLAN REG ON L	14	K104	104	50 WLAN A C0 TXRX FEM M
GPIO SEQ PMU TO BBPMU RESET L	15	K105	105	GND
GND	16	K106	106	GND
GPIO PMU FROM WLAN HOST WAKE	17	K107	107	50 RX XCVR L D NA LAAFEM U LAA R
PP VDD BOOST	18	K108	108	GND
GND	19	K109	109	GND
GND	20	K110	110	50 TRX ANT8 UHB N79
GND	21	K111	111	GND
PP VDD MAIN	22	K112	112	GND
PP VDD MAIN	23	K113	113	SHIELD RFFE0 SDATA SDR0
PP VDD MAIN	24	K114	114	GPIO WLAN TO UAT 5G FEM LNA BYP_EN
GND	25	K115	115	SHIELD RFFE0 SCLK SDR0
PP VDD MAIN	26	K116	116	PP VIO RFFE IV8 UAT
GND	27	K117	117	GND
PP VDD MAIN	28	K118	118	GND
GND	29	K119	119	GPIO AP INTERPOSER CANARY3
PP VDD MAIN	30	K120	120	GPIO AP INTERPOSER CANARY4
GND	31	K121	121	GND
PP VDD MAIN	32	K122	122	GND
GND	33	K123	123	NFC P ANT POS
PP VDD MAIN	34	K124	124	NFC P ANT POS
GND	35	K125	125	NFC P ANT POS
GND	36	K126	126	GND
GPIO SEQ PMU TO NFC RST L	37	K127	127	GND
GND	38	K128	128	50 UWB9 TOF DIV ANT M
PCIE GP1 AP TO BB PERST L	39	K129	129	GND
GPIO AP TO BB COREDUMP	40	K130	130	GND
PCIE GP0 AP BI WLAN CLKREQ L	41	K131	131	50 TRX ANT2 LB LMB MB HB LI
PCIE GP0 AP TO WLAN PERST L	42	K132	132	GND
GND	43	K133	133	GND
GPIO AP FROM BT AUDIO SYNC	44	K134	134	GND
GND	45	K135	135	50 RX GNSS L5
GND	46	K136	136	GND
GND	47	K137	137	GND
GND	48	K138	138	GPIO AOP TO GNSS CS
GND	49	K139	139	CLK GPIO S4 PMU TO WLAN UWB GNSS 32K
GND	50	K140	140	GND
GND	51	K141	141	GNSS L5 ELNA EN
I2C0 AOP SCL	52	K142	142	GND
GND	53	K143	143	SP11 AOP TO GNSS SCLK
I2C0 AOP SDA	54	K144	144	GPIO AOP FROM GNSS HOST REQ
GND	55	K145	145	SP11 AOP TO GNSS MOSI
GPIO WLAN TO AP TIME SYNC IV26	56	K146	146	GND
ANALOG GRIMALDI IR TO CODEC AIN5 N	57	K147	147	SP11 AOP FROM GNSS MISO
ANALOG GRIMALDI IR TO CODEC AIN5 B	58	K148	148	NTC STOCKHOLM
ANALOG GRIMALDI VIS TO CODEC AIN6 N	59	K149	149	GPIO AOP TO GNSS ENABLE
ANALOG GRIMALDI VIS TO CODEC AIN6 B	60	K150	150	GPIO AOP TO BB GNSS TIME MARK
PP3V0 REAR LS S0	61	K151	151	PP1V24 S2
GND	62	K152	152	GND
GND	63	K153	153	PP1V8 S4
GND	64	K154	154	PP1V24 NFC S2
GND	65	K155	155	SPM10 HEB AOP BI BB WLAN NEC UWB DATA
GND	66	K156	156	IO S4 BT TO UWB WAKE
GPIO SCM AOP FROM MAGUARI INT	67	K157	157	SPM10 HEB AOP TO BB WLAN NEC UWB CLK
RET CODEC FROM MIC2 REAR	68	K158	158	IO S4 BT TO UWB TIME SYNC
ANALOG MIC2 REAR TO CODEC AIN2 P	69	K159	159	SAWTOOTH UAT A
GND	70	K160	160	I2C S4 UWB BI SE SDA
ANALOG MIC2 REAR TO CODEC AIN2 N	71	K161	161	SAWTOOTH UAT B
ANALOG MIC2 REAR TO CODEC AIN2 B	72	K162	162	I2C S4 UWB TO SE SCL
GND	73	K163	163	GND
PP CODEC TO MIC2 REAR BIAS	74	K164	164	GND
GND	75	K165	165	GND
GPIO AOP FROM NFC P IRQ	76	K166	166	GND
GND	77	K167	167	GND
GPIO AOP TO NFC IRONMAN EN	78	K168	168	GND
GND	79	K169	169	GND
GND	80	K170	170	GND
GND	81	K171	171	GND
PP VDD MAIN	82	K172	172	GND
PP VDD MAIN	83	K173	173	I2C2 CAM PMU2 SCL IV8
GND	84	K174	174	SPM11 COEX AOP TO BB WLAN UWB CLK
GND	85	K175	175	I2C2 CAM PMU2 SDA IV8
PP VDD MAIN	86	K176	176	SPM11 COEX AOP BI BB WLAN UWB DATA
GND	87	K177	177	GND
GND	88	K178	178	PP1V24 S4
GND	89	K179	179	GND
GPIO NUB TO BB FORCE PWM	90	K180	180	PP VDD MAIN

J_INT_BOT

INT-PAD
SYM 2 OF 4

GND	181	K0181	271	PNVAR DISPLAY VSSEL
PP VDD MAIN	182	K0182	272	GND
GND	183	K0183	273	PP1V8 S4
PP VDD MAIN	184	K0184	274	NTC RADIO BB P
GND	185	K0185	275	GND
PP VDD MAIN	186	K0186	276	GND
GND	187	K0187	277	BB SIM2 HALLEY DATA
GND	188	K0188	278	PP UIM2 LDO13
GND	189	K0189	279	BB SIM2 HALLEY RESET L
GND	190	K0190	280	BB SIM2 HALLEY CLK
GND	191	K0191	281	GPIO S4 NFC TO ARCAM RESET L TV8
GND	192	K0192	282	GPIO S4 NFC TO ARCAM TRIG IV8
GND	193	K0193	283	NTC RADIO PA P
AMUX DIV STROBE BOOST OUT	194	K0194	284	GND
GPIO ISP CAMS TO STROBE TRIGGER IV8	195	K0195	285	GND
GPIO CAM PMU2 TO STROBE HWEN IV8	196	K0196	286	GND
GND	197	K0197	287	AMUX PMU BY
GPIO NUB FROM BB RESET DETECT L	198	K0198	288	AMUX PMU AY
GPIO BB TO NUB GSM TXBURST IND IV8	199	K0199	289	GND
GPIO NUB TO BBPMU DEBUG EN IV8	200	K0200	290	PP3V0 S2
GND	201	K0201	291	GND
GND	202	K0202	292	I2C DISPLAY SDA IV8
GND	203	K0203	293	GND
GND	204	K0204	294	I2C DISPLAY SCL IV8
GND	205	K0205	295	GND
GND	206	K0206	296	GND
GND	207	K0207	297	GND
GND	208	K0208	298	GPIO S4 BT TO BOT SPKR TRIG
GND	209	K0209	299	GND
GND	210	K0210	300	GND
GND	211	K0211	301	GND
90 PCIE GP0 AP TO WLAN REFCLK N	212	K0212	302	GND
GND	213	K0213	303	GND
90 PCIE GP0 AP TO WLAN REFCLK P	214	K0214	304	GND
GND	215	K0215	305	GND
90 PCIE GP0 AP TO WLAN TX P	216	K0216	306	GND
GND	217	K0217	307	GND
90 PCIE GP0 AP TO WLAN TX N	218	K0218	308	GND
GND	219	K0219	309	GND
90 PCIE GP0 AP FROM WLAN RX N	220	K0220	310	GND
GND	221	K0221	311	GND
90 PCIE GP0 AP FROM WLAN RX P	222	K0222	312	GND
GND	223	K0223	313	GND
GND	224	K0224	314	ANALOG PP5V25 ACC SENSE
GND	225	K0225	315	GND
GND	226	K0226	316	GND
GND	227	K0227	317	GND
90 PCIE GP1 AP TO BB TX P	228	K0228	318	GND
GND	229	K0229	319	GND
90 PCIE GP1 AP TO BB TX N	230	K0230	320	GND
GND	231	K0231	321	GND
90 PCIE GP1 AP FROM BB RX N	232	K0232	322	GND
GND	233	K0233	323	PP VDD MAIN
90 PCIE GP1 AP FROM BB RX P	234	K0234	324	GND
GND	235	K0235	325	PP VDD MAIN
90 PCIE GP1 AP TO BB REFCLK N	236	K0236	326	GND
GND	237	K0237	327	PP VDD MAIN
90 PCIE GP1 AP TO BB REFCLK P	238	K0238	328	GND
GND	239	K0239	329	GND
GND	240	K0240	330	GND
GND	241	K0241	331	GND
GND	242	K0242	332	GND
PCIE GP1 AP BI BB CLKREQ L	243	K0243	333	IO PMU TO WLCHGR RESET L
SWD AP BI BB SWDIO	244	K0244	334	GND
GND	245	K0245	335	GND
SWD AP TO MANY SWCLK	246	K0246	336	GND
GND	247	K0247	337	PP VBUS2 WLCHGR
PP1V8 S2	248	K0248	338	GND
GPIO PMU FROM BB PCIE HOST WAKE L	249	K0249	339	PP VBUS2 WLCHGR
ANALOG GPU SENSE N	250	K0250	340	GND
GND	251	K0251	341	PP VBUS2 WLCHGR
ANALOG GPU SENSE P	252	K0252	342	GND
ANALOG ECU SENSE SE	253	K0253	343	PP VBUS2 WLCHGR
GND	254	K0254	344	GND
GND	255	K0255	345	GND
GND	256	K0256	346	GND
GND	257	K0257	347	GND
50 TRX ANT4 MB HB WLAN24	258	K0258	348	GND
GND	259	K0259	349	GND
NFC P ANT NEG	260	K0260	350	GND
GND	261	K0261	351	GND
GND	262	K0262	352	GND
NFC P ANT NEG	263	K0263	353	GND
GND	264	K0264	354	GND
GND	265	K0265	355	GND
ANALOG ECU SENSE N	266	K0266	356	GND
PNVAR DISPLAY VCL	267	K0267	357	GND
ANALOG PCPU SENSE P	268	K0268	358	GND
PP6V8 DISPLAY AVDDH	269	K0269	359	GND
ANALOG PNVAR DISPLAY VSSEL SENSE	270	K0270	360	GND

Title

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Size

A4

Number

Revision

Date: 2024/8/2

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Drawn By:

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J_INT BOT
SMT-PAD
SYM 3 OF 4

J_INT BOT
SMT-PAD
SYM 4 OF 4

GND	361	J0361	451	GND
GND	362	J0362	452	GND
GND	363	J0363	453	GND
GND	364	J0364	454	GND
GND	365	J0365	455	50 TRX ANT3 MB HB WLAN24
GND	366	J0366	456	GND
GND	367	J0367	457	GND
GND	368	J0368	458	GND
GND	369	J0369	459	PP VIO RFFE IV8 RX
GND	370	J0370	460	SHIELD RFFE5 SDATA SDR0
GND	371	J0371	461	SHIELD RFFE5 SCLK SDR0
GND	372	J0372	462	GND
GND	373	J0373	463	PP1V24 S2
GND	374	J0374	464	GND
GND	375	J0375	465	SHIELD RFFE WLAN TO LAT FEMS DATA
GND	376	J0376	466	RFFE WLAN TO LAT 5G FEM RST L
GND	377	J0377	467	SHIELD RFFE WLAN TO LAT FEMS CLK
GND	378	J0378	468	GND
GND	379	J0379	469	GND
GND	380	J0380	470	GND
GND	381	J0381	471	GND
GND	382	J0382	472	GND
GND	383	J0383	473	GND
GND	384	J0384	474	50 WLAN A C1 TXRX FEM M
GND	385	J0385	475	GND
GND	386	J0386	476	GND
GND	387	J0387	477	50 RX XCVR L P NA LAA FEM L LAA R
GND	388	J0388	478	GND
GND	389	J0389	479	GND
GND	390	J0390	480	50 NB TO 5G FEM LAT SOUTH
GND	391	J0391	481	GND
GND	392	J0392	482	GND
GND	393	J0393	483	GND
GND	394	J0394	484	GPIO WLAN TO LAT 5G FEM LNA BYP EN
GND	395	J0395	485	GND
GND	396	J0396	486	ANALOG LAT 5G FEM TO WLAN PD1'S
GND	397	J0397	487	90 USB BB R N
IO ACE TO CHARGER VBUS1 VALID L HW	398	J0398	488	GND
I2C2 SMC CHARGER TO GG SCL IV8	399	J0399	489	90 USB BB R P
GND	400	J0400	490	GND
I2C2 SMC CHARGER I2C SDA IV8	401	J0401	491	GND
GND	402	J0402	492	GND
SWD NUB BI WLCHGR SWD	403	J0403	493	GND
GND	404	J0404	494	GND
IO CHARGER FROM WLCHGR BST EN	405	J0405	495	GND
GND	406	J0406	496	GND
IO CHARGER FROM WLCHGR BST FPMW	407	J0407	497	GND
GND	408	J0408	498	GND
ANALOG FB WLCHGR TO CHARGER	409	J0409	499	GND
GND	410	J0410	500	GND
GND	411	J0411	501	GND
GND	412	J0412	502	GND
SPMII NUB TO BPIC WLCHGR CLK	413	J0413	503	GND
GPIO SEQ PMU TO WLCHGR EN SYS	414	J0414	504	GND
SPMII NUB BI BPIC WLCHGR DATA	415	J0415		
SWD NUB TO WLCHGR SWCLK	416	J0416		
GND	417	J0417		
CLK GPIO PMU TO BPIC WLCHGR 32K IV8	418	J0418		
GND	419	J0419		
GND	420	J0420		
GND	421	J0421		
GND	422	J0422		
GND	423	J0423		
GND	424	J0424		
GND	425	J0425		
SHIELD RFFE0 SDATA BB	426	J0426		
SHIELD RFFE0 SCLK SDR	427	J0427		
GND	428	J0428		
GND	429	J0429		
GND	430	J0430		
GND	431	J0431		
GND	432	J0432		
GND	433	J0433		
GND	434	J0434		
GND	435	J0435		
ANALOG PP2V8 DISPLAY VDDDEL SENSE	436	J0436		
PP2V8 DISPLAY VDDDEL	437	J0437		
GND	438	J0438		
PP5V1 TOUCH VDDTX S2	439	J0439		
GND	440	J0440		
GND	441	J0441		
GND	442	J0442		
GND	443	J0443		
50 TRX UHBPAD LAT ANT7	444	J0444		
GND	445	J0445		
GND	446	J0446		
50 TRX UHBPAD LAT ANT9	447	J0447		
GND	448	J0448		
GND	449	J0449		
50 TRX ANT1 LB LMB MB HB	450	J0450		

GND	H A1	H A1	H C3	GND
GND	H A2	H A2	H C4	GND
GND	H A3	H A3	H C5	GND
GND	H A4	H A4	H C6	GND
GND	H A5	H A5	H C7	GND
GND	H A6	H A6	H C8	GND
GND	H A7	H A7	H C9	GND
GND	H A8	H A8	H C10	GND
GND	H A9	H A9	H C11	GND
GND	H A10	H A10	H C12	GND
GND	H A11	H A11	H C13	GND
GND	H A12	H A12	H C14	GND
GND	H A13	H A13	H C15	GND
GND	H A14	H A14	H C16	GND
GND	H A15	H A15	H C17	GND
GND	H A16	H A16	H C18	GND
GND	H A17	H A17	H D1	GND
GND	H A18	H A18	H D2	GND
GND	H B1	H B1	H D3	GND
GND	H B2	H B2	H D4	GND
GND	H B3	H B3	H D5	GND
GND	H B4	H B4	H D6	GND
GND	H B5	H B5	H D7	GND
GND	H B6	H B6	H D8	GND
GND	H B7	H B7	H D9	GND
GND	H B8	H B8	H D10	GND
GND	H B9	H B9	H D11	GND
GND	H B10	H B10	H D12	GND
GND	H B11	H B11	H D13	GND
GND	H B12	H B12	H D14	GND
GND	H B13	H B13	H D15	GND
GND	H B14	H B14	H D16	GND
GND	H C1	H C1	H D17	GND
GND	H C2	H C2	H D18	GND

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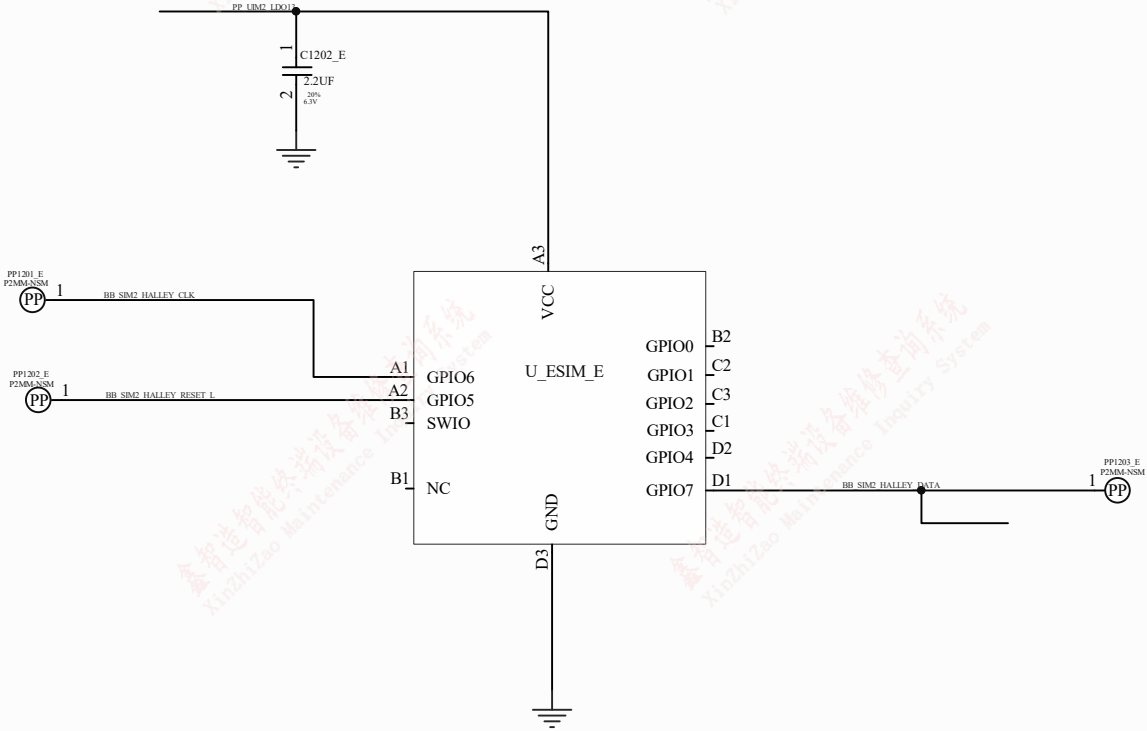
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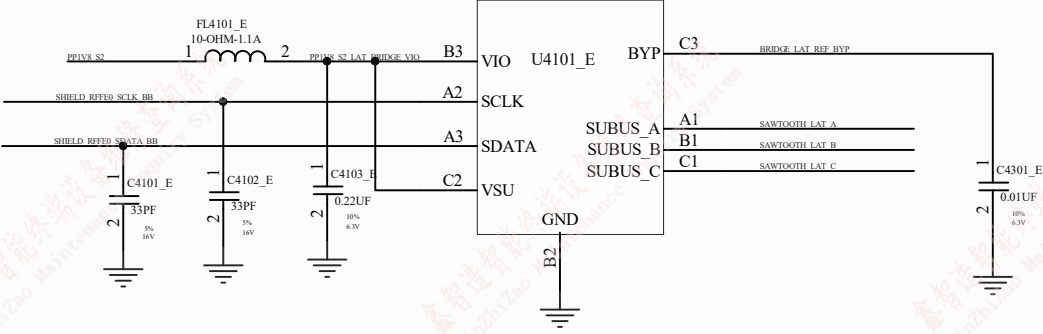
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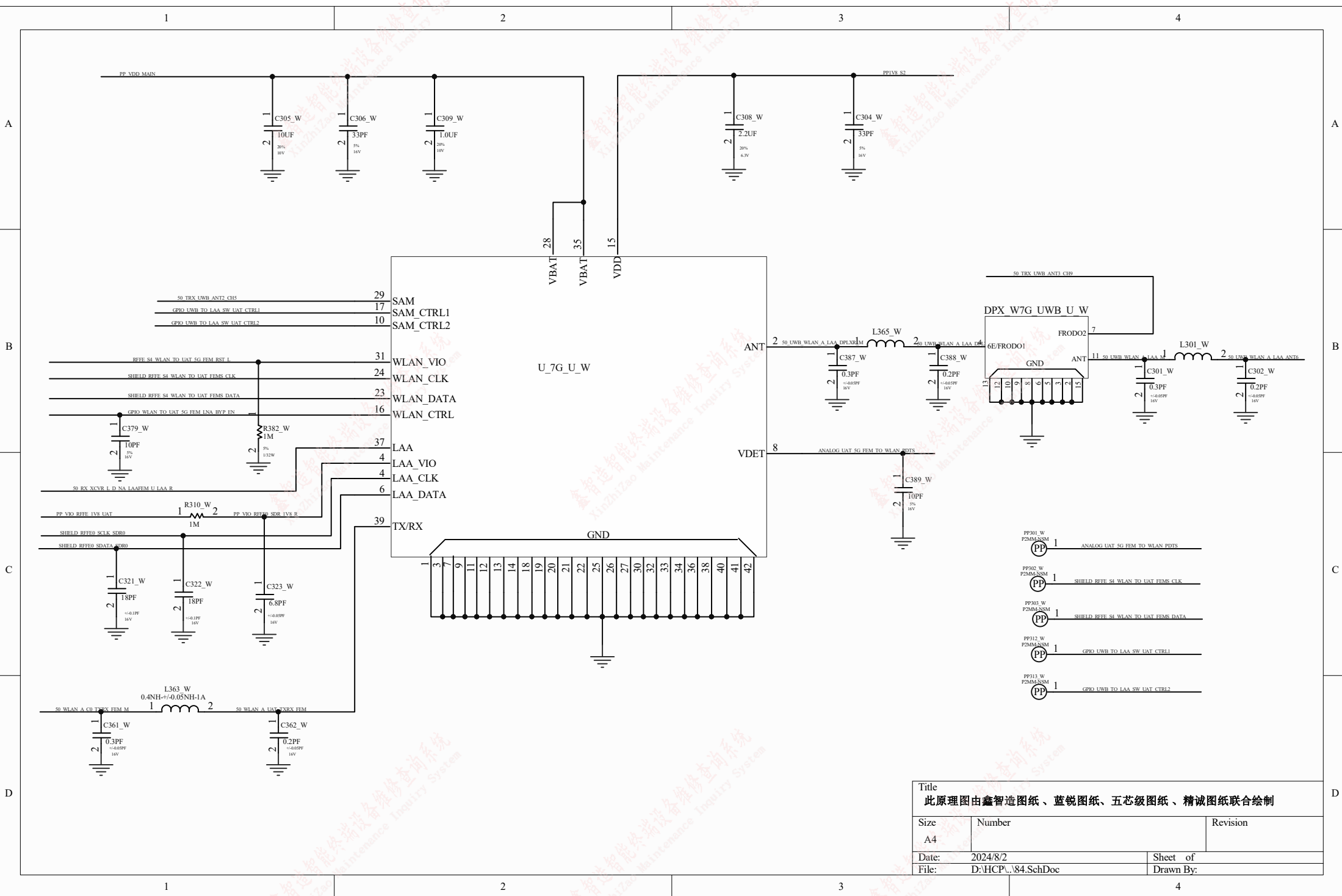
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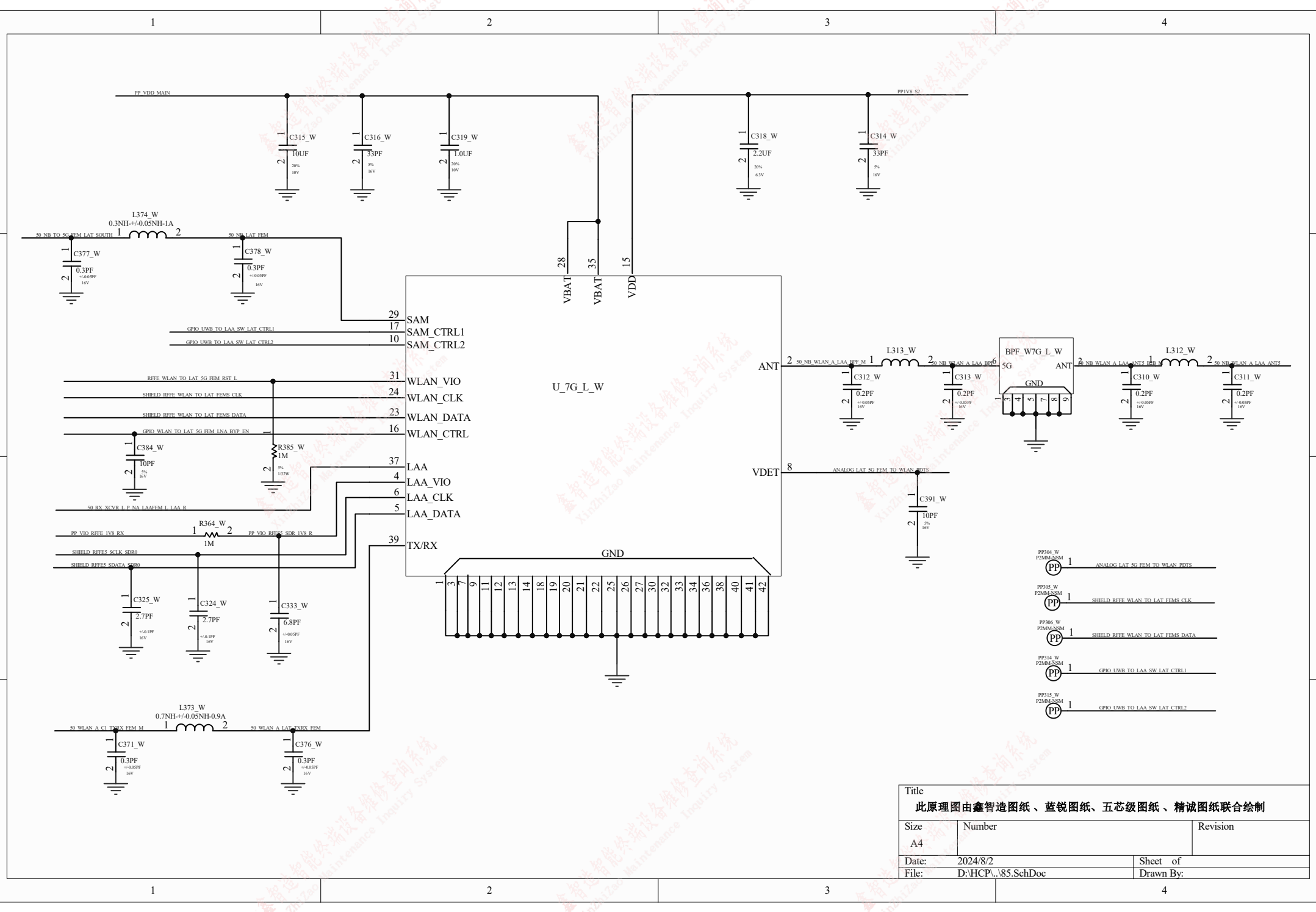
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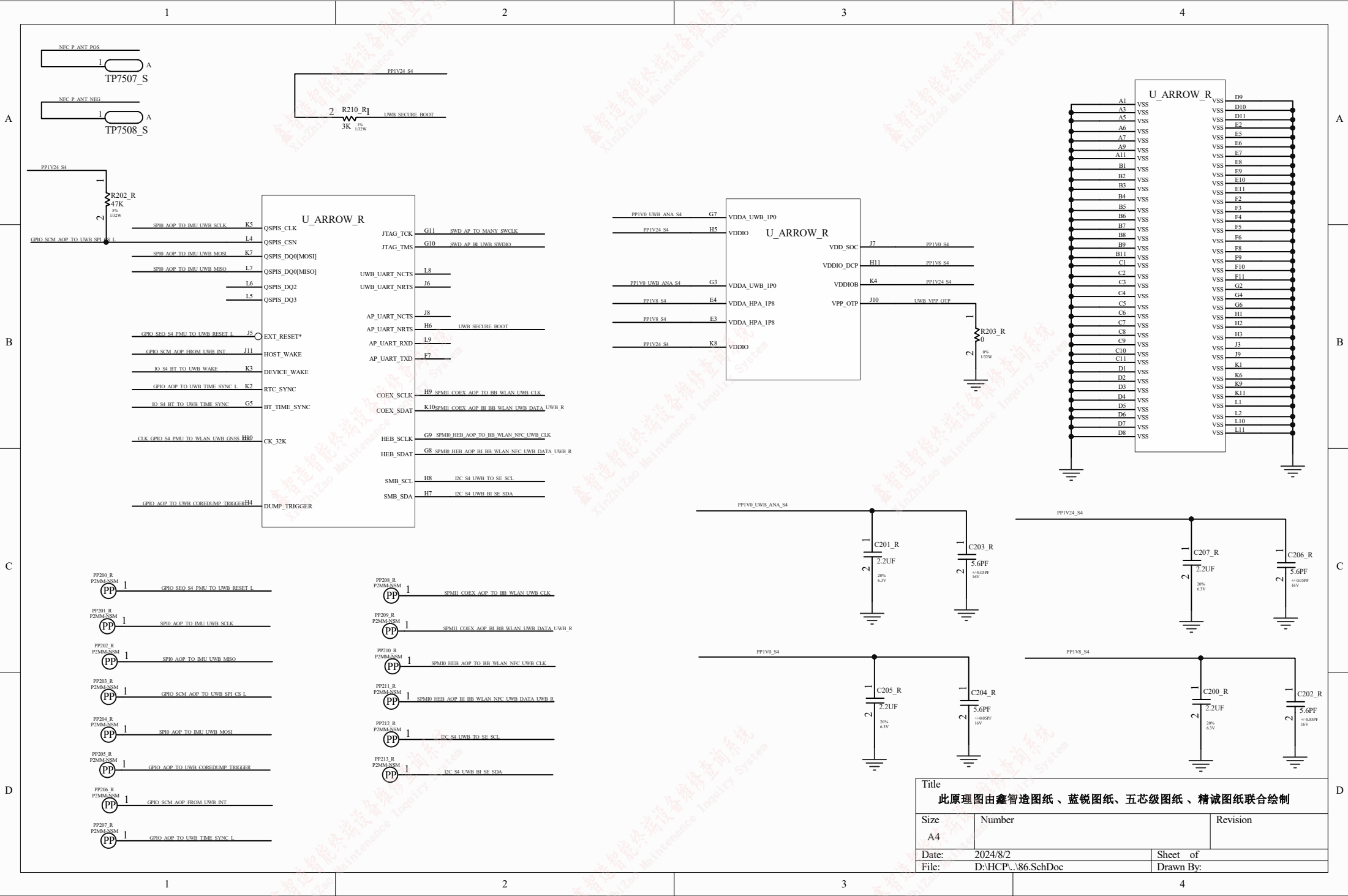
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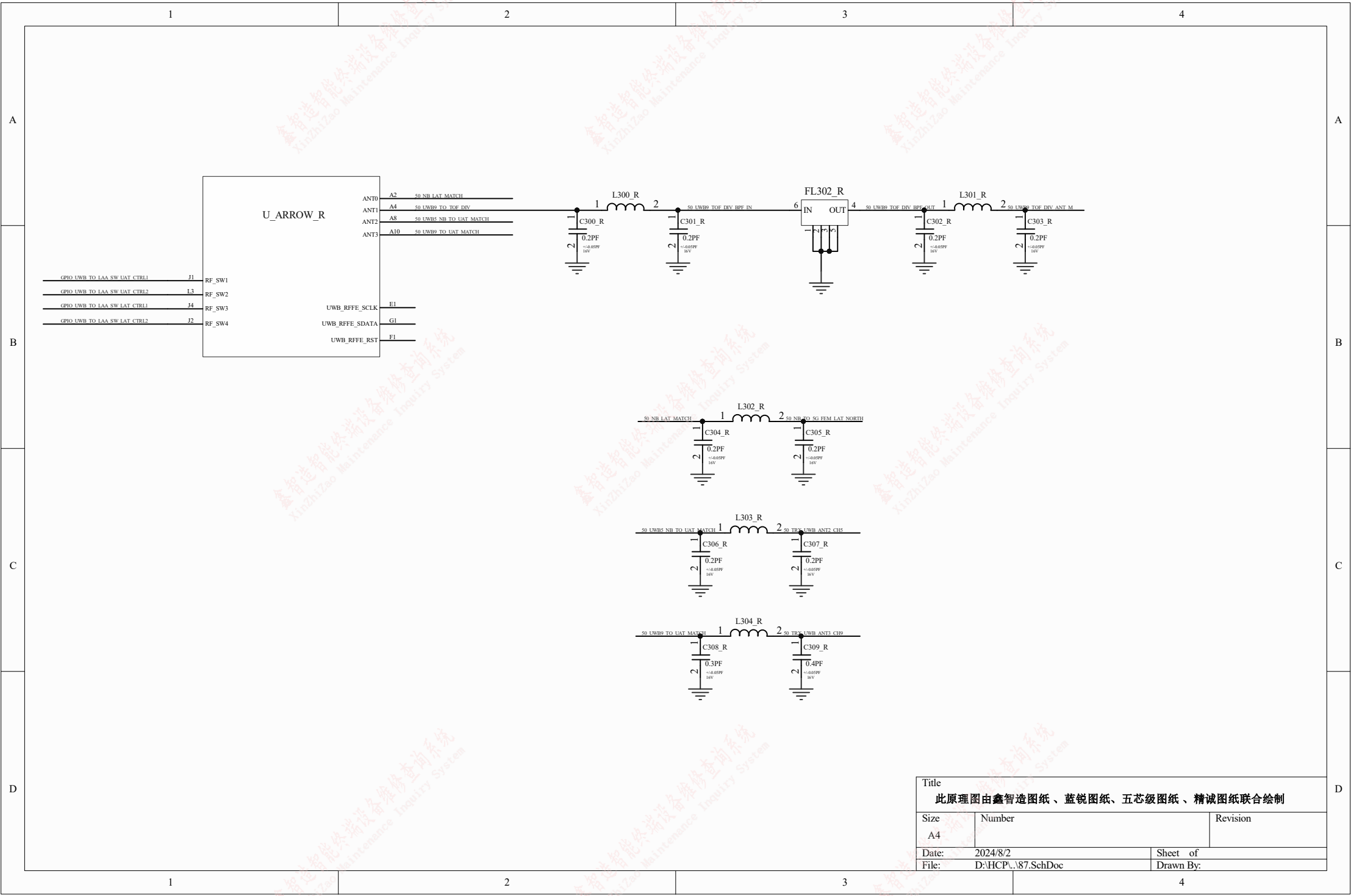
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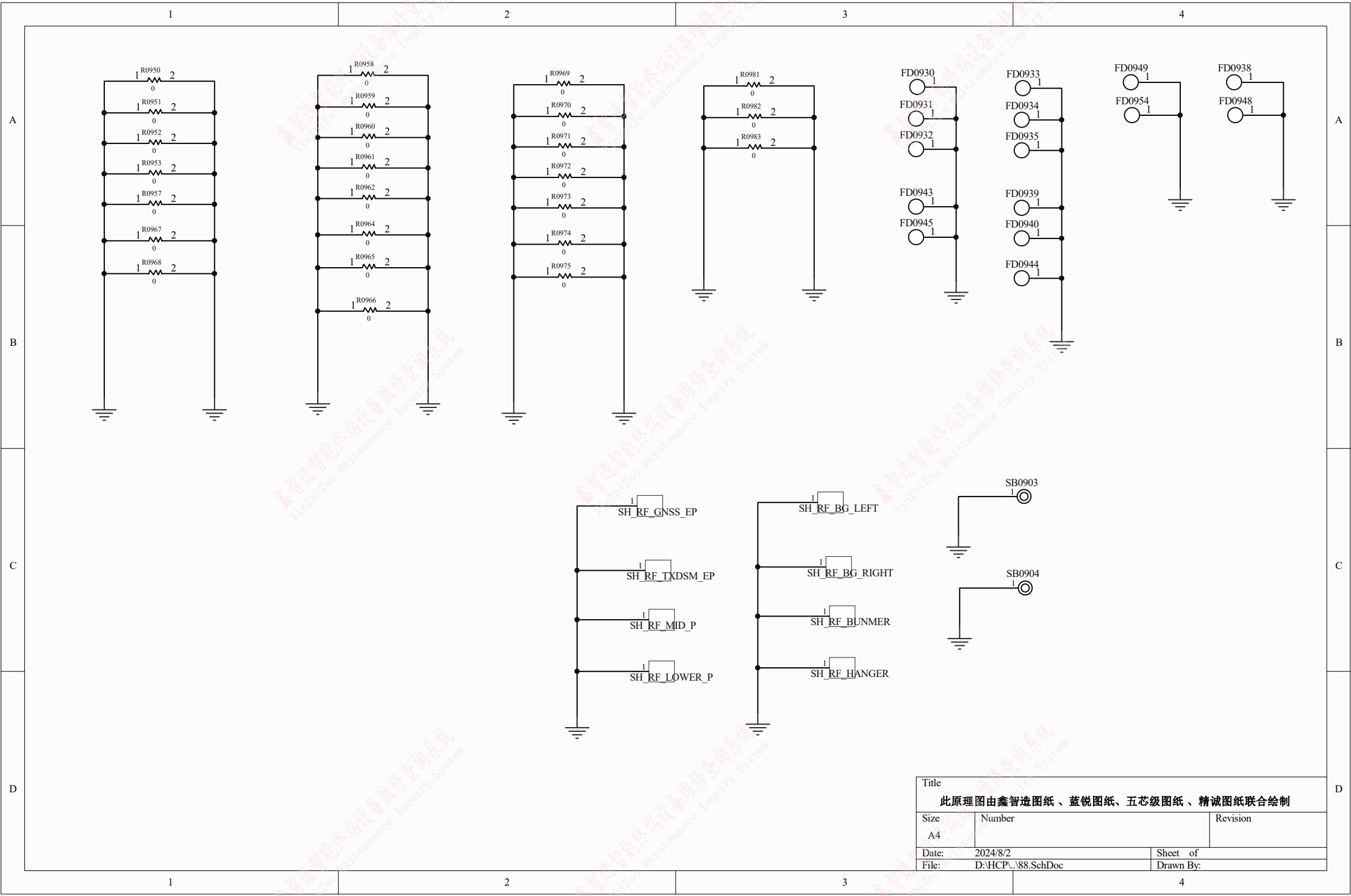


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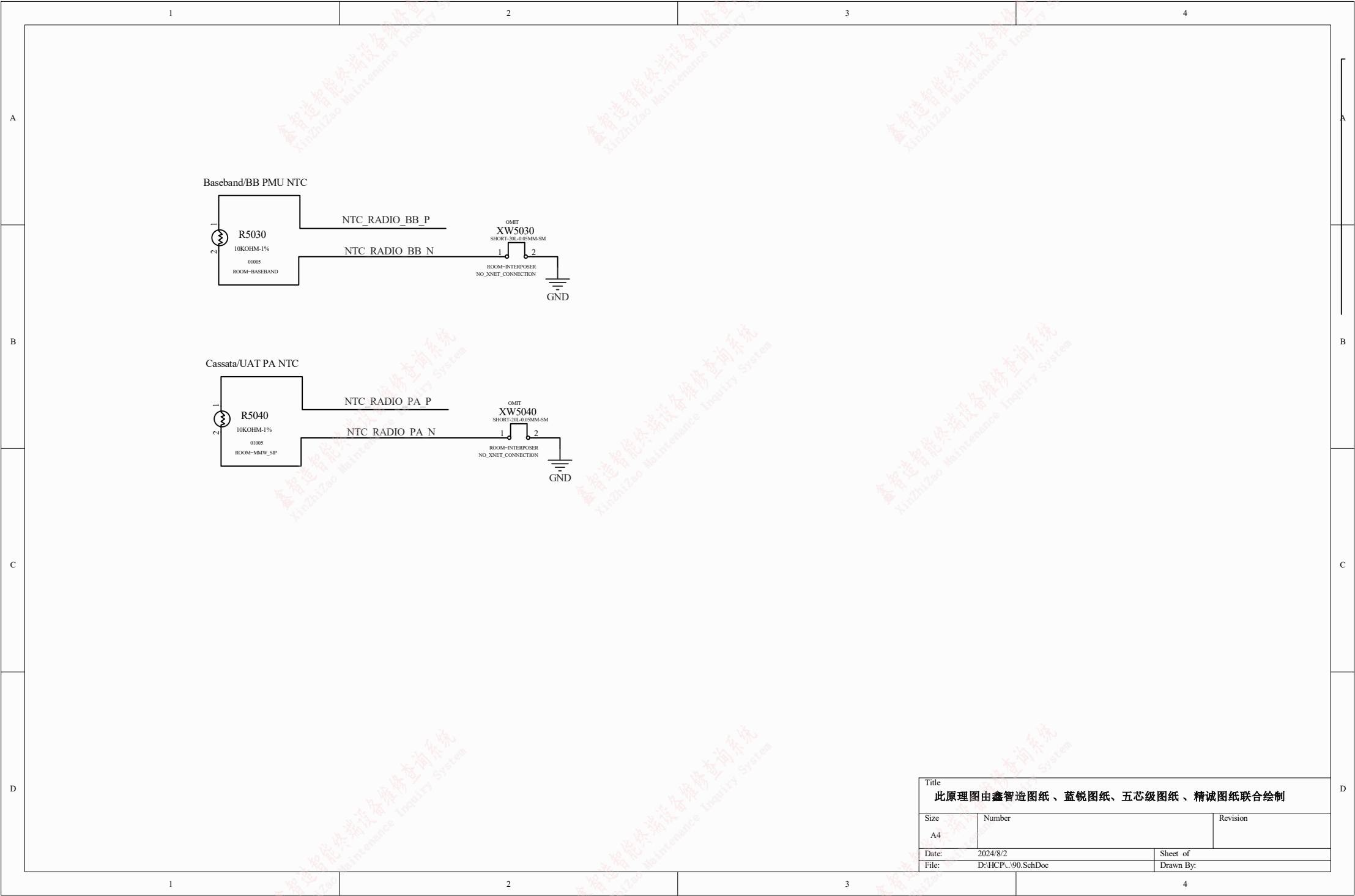


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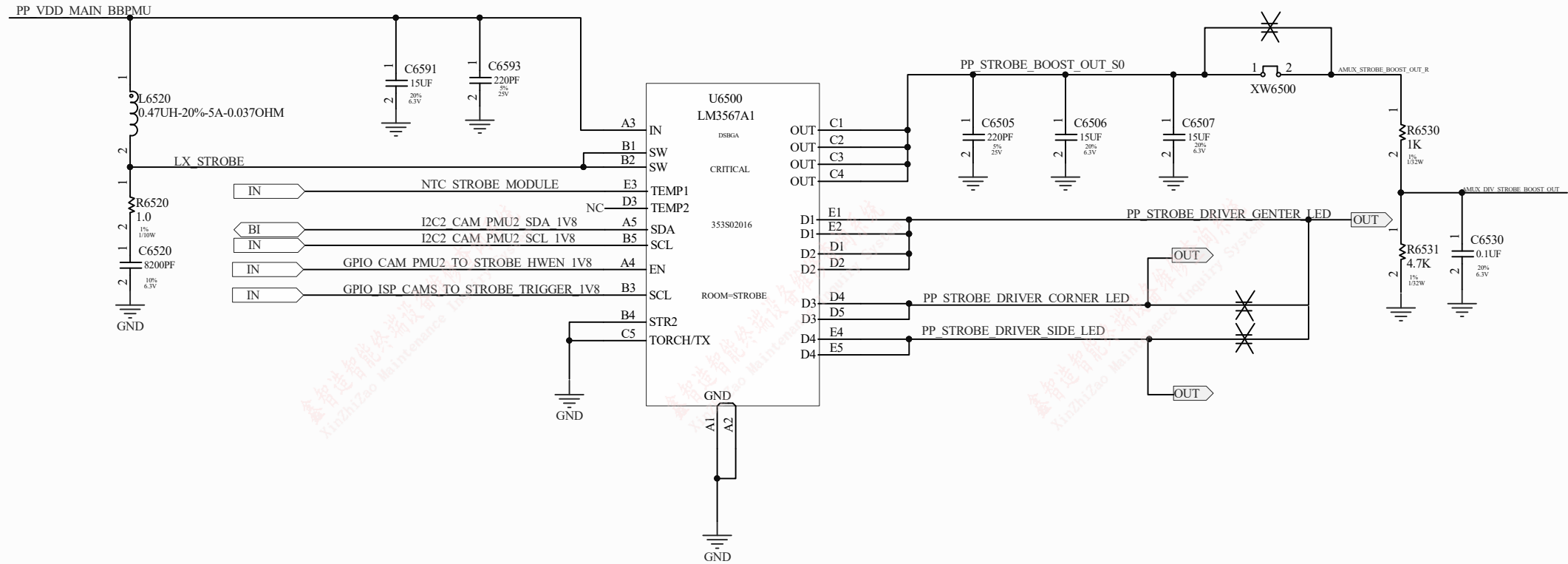




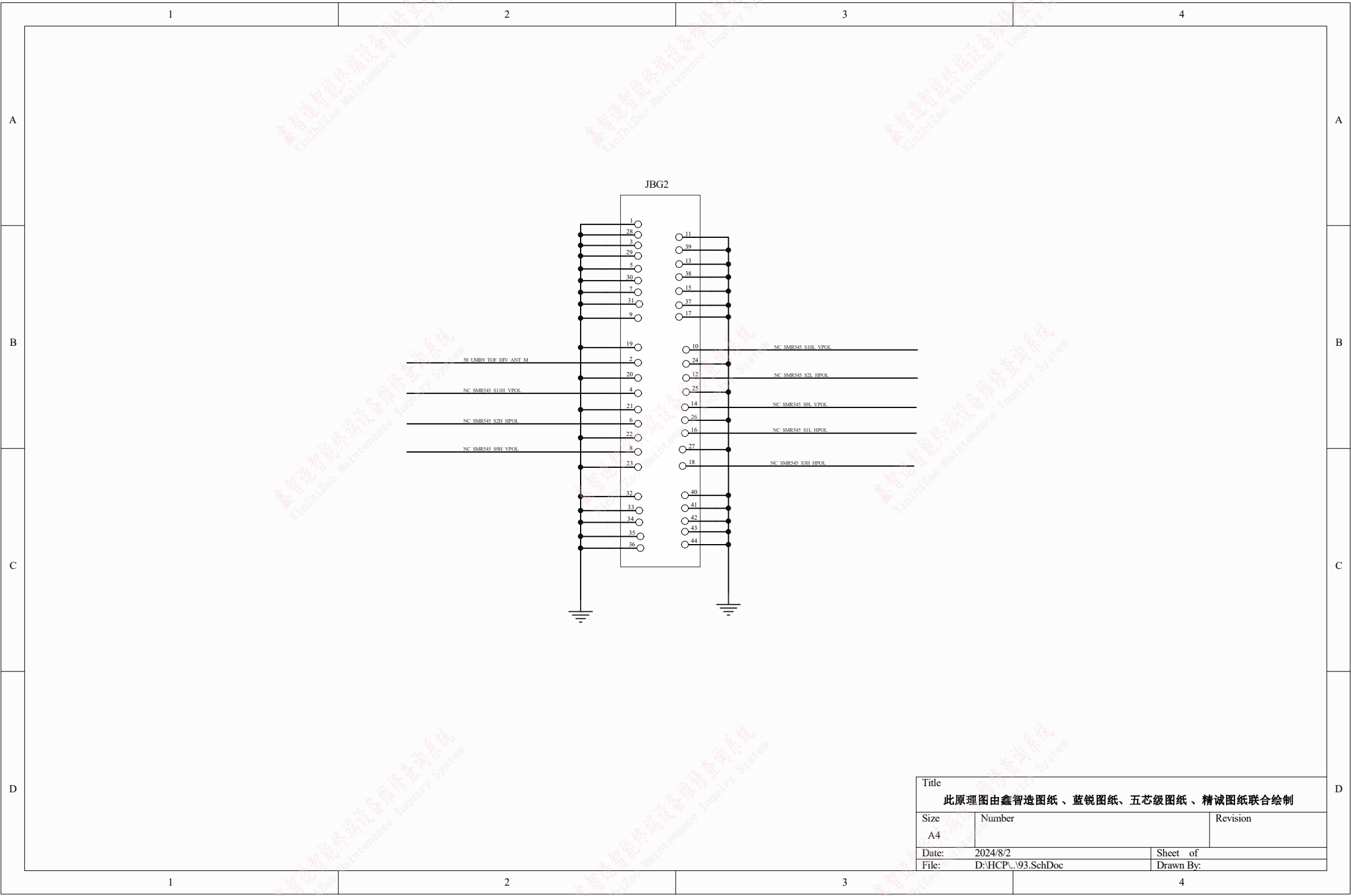
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	GND	1	K01	91	GND	K091
	GND	2	K02	92	GND	K092
	GND	3	K03	93	ANALOG UAT 5G FEM TO WLAN PDTS	K093
	GND	4	K04	94	GND	K094
	GND	5	K05	95	GND	K095
	IO TOUCH TO MANY SCAN ACTIVE	6	K06	96	SHIELD RFFE S4 WLAN TO UAT FEMS DATA	K096
	GND	7	K07	97	RFFE S4 WLAN TO UAT 5G FEM RST L	K097
	GND	8	K08	98	SHIELD RFFE S4 WLAN TO UAT FEMS CLK	K098
	GND	9	K09	99	GND	K099
	PP1V8 S2	10	K10	100	50 NB TO 5G FEM LAT NORTH	K100
	GND	11	K11	101	GND	K101
	GND	12	K12	102	GND	K102
	GND	13	K13	103	GND	K103
	GPIO SEQ S4 PMU TO WLAN REG ON	14	K14	104	50 WLAN A C0 TXRX FEM M	K104
	GPIO SEQ PMU TO BBPMU RESET L	15	K15	105	GND	K105
	GND	16	K16	106	GND	K106
	GPIO PMU FROM WLAN HOST WAKE	17	K17	107	50 RX XCVR L D NA LAA FEM U LAA R	K107
	PP VDD BOOST	18	K18	108	GND	K108
	GND	19	K19	109	GND	K109
	GND	20	K20	110	50 TRX ANT8 UHB N79	K110
	GND	21	K21	111	GND	K111
	PP VDD MAIN BBPMU	22	K22	112	GND	K112
	GND	23	K23	113	SHIELD RFFE0 SDATA SDR0	K113
	PP VDD MAIN BBPMU	24	K24	114	GPIO WLAN TO UAT 5G FEM LNA BYP_EN	K114
	GND	25	K25	115	SHIELD RFFE0 SCLK SDR0	K115
	PP VDD MAIN BBPMU	26	K26	116	PP VIO RFFE 1V8 RX	K116
	GND	27	K27	117	GND	K117
	PP VDD MAIN BBPMU	28	K28	118	GND	K118
	GND	29	K29	119	GND	K119
	PP VDD MAIN BBPMU	30	K30	120	GND	K120
	GND	31	K31	121	GND	K121
	PP VDD MAIN BBPMU	32	K32	122	GND	K122
	GND	33	K33	123	NFC P ANT POS	K123
	PP VDD MAIN BBPMU	34	K34	124	NFC P ANT POS	K124
	GND	35	K35	125	NFC P ANT POS	K125
	GND	36	K36	126	GND	K126
	GPIO SEQ PMU TO NFC RST L	37	K37	127	GND	K127
	GND	38	K38	128	50 UWB9 TOF DIV ANT M	K128
	PCIE GP1 AP TO BB PERST L	39	K39	129	GND	K129
	GPIO AP TO BB COREDUMP	40	K40	130	GND	K130
	PCIE GP0 AP BI WLAN CLKREQ L	41	K41	131	50 TRX ANT2 LB LMB MB HB L1	K131
	PCIE GP0 AP TO WLAN PERST L	42	K42	132	GND	K132
	GND	43	K43	133	GND	K133
	GPIO AP FROM BT AUDIO SYNC	44	K44	134	GND	K134
	GND	45	K45	135	50 RX GNSS L5	K135
	GND	46	K46	136	GND	K136
	GND	47	K47	137	GND	K137
	GND	48	K48	138	GPIO AOP TO GNSS CS	K138
	GND	49	K49	139	CLK GPIO S4 PMU TO WLAN UWR GNSS_32K	K139
	GND	50	K50	140	GND	K140
	GND	51	K51	141	GNSS L5 ELNA EN	K141
	I2C0 AOP SCL	52	K52	142	GND	K142
	GND	53	K53	143	SP11 AOP TO GNSS SCLK	K143
	I2C0 AOP SDA	54	K54	144	GPIO AOP FROM GNSS HOST REQ	K144
	GND	55	K55	145	SP11 AOP TO GNSS MOSI	K145
	GPIO WLAN TO AP TIME SYNC 1V26	56	K56	146	GND	K146
	GND	57	K57	147	SP11 AOP FROM GNSS MISO	K147
	ANALOG GRIMALDI IR TO CODEC AIN5	58	K58	148	NTC STOCKHOLM	K148
	GND	59	K59	149	GPIO AOP TO GNSS ENABLE	K149
	ANALOG GRIMALDI IR TO CODEC AIN5	60	K60	150	GPIO AOP TO BB GNSS TIME MARK	K150
	ANALOG GRIMALDI VIS TO CODEC AIN6	61	K61	151	PP1V24 S2	K151
	ANALOG GRIMALDI VIS TO CODEC AIN6	62	K62	152	GND	K152
	PP3V0 REAR LS S0	63	K63	153	PP1V8 S4	K153
	GND	64	K64	154	PP1V24 NFC S2	K154
	GND	65	K65	155	SPSPM0 HER AOP BI BB WLAN NFC UWB DATA	K155
	GND	66	K66	156	IO S4 BT TO UWB WAKE	K156
	GPIO SCM AOP FROM MAGUARI INT	67	K67	157	SPSPM0 HER AOP TO BB WLAN NFC UWB CLK	K157
	RET CODEC FROM MIC2 REAR	68	K68	158	IO S4 BT TO UWB TIME SYNC	K158
	GND	69	K69	159	GND	K159
	ANALOG MIC2 REAR TO CODEC AIN2	70	K70	160	EC S4 UWB BI SE SDA	K160
	GND	71	K71	161	SAWTOOTH UAT A	K161
	ANALOG MIC2 REAR TO CODEC AIN2	72	K72	162	EC S4 UWB TO SE SCL	K162
	GND	73	K73	163	GND	K163
	PP CODEC TO MIC2 REAR BIAS	74	K74	164	GND	K164
	GND	75	K75	165	GND	K165
	GPIO AOP FROM NFC P IRQ	76	K76	166	GND	K166
	GND	77	K77	167	GND	K167
	GPIO AOP TO NFC IRONMAN EN	78	K78	168	GND	K168
	GND	79	K79	169	GND	K169
	GND	80	K80	170	GND	K170
	GND	81	K81	171	GND	K171
	PP VDD MAIN BBPMU	82	K82	172	I2C2 CAM PMU2 SCL 1V8	K172
	GND	83	K83	173	GND	K173
	PP VDD MAIN BBPMU	84	K84	174	SPM11 COEX AOP TO BB WLAN UWB CLK	K174
	GND	85	K85	175	I2C2 CAM PMU2 SDA 1V8	K175
	PP VDD MAIN BBPMU	86	K86	176	SPM11 COEX AOP BI BB WLAN UWB DATA	K176
	GND	87	K87	177	GND	K177
	GND	88	K88	178	PP1V24 S4	K178
	GND	89	K89	179	GND	K179
	GPIO NUB TO BB FORCE PWM	90	K90	180	PP VDD MAIN UAT	K180

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	GND	181	K181	271	PNNVAR_DISPLAY_VSSEL	K10271
	PP VDD MAIN UAT	182	K182	272	GND	K10272
	GND	183	K183	273	PP1V8 S4	K10273
	PP VDD MAIN UAT	184	K184	274	NTC RADIO BB P	K10274
	GND	185	K185	275	GND	K10275
	PP VDD MAIN UAT	186	K186	276	GND	K10276
	GND	187	K187	277	BB SIM2 HALLEY DATA	K10277
	GND	188	K188	278	PP UIM2 ID013	K10278
	GND	189	K189	279	BB SIM2 HALLEY RESET L	K10279
	GND	190	K190	280	BB SIM2 HALLEY CLK	K10280
	GND	191	K191	281	GPIO S4 NFC TO ARCAM RESET L 1V8	K10281
	GND	192	K192	282	GPIO S4 NFC TO ARCAM TRIG 1V8	K10282
	GND	193	K193	283	NTC RADIO PA P	K10283
	AMUX DIV STROBE BOOST OUT	194	K194	284	GND	K10284
	GPIO ISP CAMS TO STROBE TRIGGER 1V8	195	K195	285	GND	K10285
	GPIO CAM PMU2 TO STROBE HWEN 1V8	196	K196	286	GND	K10286
	GND	197	K197	287	AMUX PMU BY	K10287
	GPIO NUB FROM BB RESET DETECT L	198	K198	288	AMUX PMU AY	K10288
	GPIO BB TO NUB GSM TXBURST IND 1V8	199	K199	289	GND	K10289
	GPIO NUB TO BBPMU DEBUG EN 1V8	200	K200	290	PP3V0 S2	K10290
	GND	201	K201	291	GND	K10291
	GND	202	K202	292	I2C DISPLAY SDA 1V8	K10292
	GND	203	K203	293	GND	K10293
	GND	204	K204	294	EC DISPLAY SCL 1V8	K10294
	GND	205	K205	295	GND	K10295
	GND	206	K206	296	GND	K10296
	GND	207	K207	297	GND	K10297
	GND	208	K208	298	GPIO S4 BT TO BOT SPKR TRIG	K10298
	GND	209	K209	299	GND	K10299
	GND	210	K210	300	GND	K10300
	GND	211	K211	301	GND	K10301
	90 PCIE GP0 AP TO WLAN REFCLK N	212	K212	302	GND	K10302
	GND	213	K213	303	GND	K10303
	90 PCIE GP0 AP TO WLAN REFCLK P	214	K214	304	GND	K10304
	GND	215	K215	305	GND	K10305
	90 PCIE GP0 AP TO WLAN TX P	216	K216	306	GND	K10306
	GND	217	K217	307	GND	K10307
	90 PCIE GP0 AP TO WLAN TX N	218	K218	308	GND	K10308
	GND	219	K219	309	GND	K10309
	90 PCIE GP0 AP FROM WLAN RX N	220	K220	310	GND	K10310
	GND	221	K221	311	GND	K10311
	90 PCIE GP0 AP FROM WLAN RX P	222	K222	312	GND	K10312
	GND	223	K223	313	GND	K10313
	GND	224	K224	314	ANALOG PP5V25 ACC SENSE	K10314
	GND	225	K225	315	GND	K10315
	GND	226	K226	316	GND	K10316
	GND	227	K227	317	GND	K10317
	90 PCIE GP1 AP TO BB TX P	228	K228	318	GND	K10318
	GND	229	K229	319	GND	K10319
	90 PCIE GP1 AP TO BB TX N	230	K230	320	GND	K10320
	GND	231	K231	321	GND	K10321
	90 PCIE GP1 AP FROM BB RX N	232	K232	322	GND	K10322
	GND	233	K233	323	PP VDD MAIN LAT	K10323
	90 PCIE GP1 AP FROM BB RX P	234	K234	324	GND	K10324
	GND	235	K235	325	PP VDD MAIN LAT	K10325
	90 PCIE GP1 AP TO BB REFCLK N	236	K236	326	GND	K10326
	GND	237	K237	327	PP VDD MAIN LAT	K10327
	90 PCIE GP1 AP TO BB REFCLK P	238	K238	328	GND	K10328
	GND	239	K239	329	GND	K10329
	GND	240	K240	330	GND	K10330
	GND	241	K241	331	GND	K10331
	GND	242	K242	332	GND	K10332
	PCIE GP1 AP BI BB CLKREQ L	243	K243	333	IO PMU TO WLCHGR RESET L	K10333
	SWD AP BI BB SWDIO	244	K244	334	GND	K10334
	GND	245	K245	335	GND	K10335
	SWD AP TO MANY SWCLK	246	K246	336	GND	K10336
	GND	247	K247	337	PP VBUS2 WLCHGR	K10337
	PP1V8 S2	248	K248	338	GND	K10338
	GPIO PMU FROM BB PCIE HOST WAKE L	249	K249	339	PP VBUS2 WLCHGR	K10339
	ANALOG GPU SENSE N	250	K250	340	GND	K10340
	GND	251	K251	341	PP VBUS2 WLCHGR	K10341
	ANALOG GPU SENSE P	252	K252	342	GND	K10342
	ANALOG EGPU SENSE SE	253	K253	343	PP VBUS2 WLCHGR	K10343
	GND	254	K254	344	GND	K10344
	GND	255	K255	345	GND	K10345
	GND	256	K256	346	GND	K10346
	GND	257	K257	347	GND	K10347
	50 TRX ANT4 MB HB WLAN24	258	K258	348	GND	K10348
	GND	259	K259	349	GND	K10349
	GND	260	K260	350	GND	K10350
	NFC P ANT NEG	261	K261	351	GND	K10351
	GND	262	K262	352	GND	K10352
	NFC P ANT NEG	263	K263	353	GND	K10353
	GND	264	K264	354	GND	K10354
	GND	265	K265	355	GND	K10355
	ANALOG PCPU SENSE N	266	K266	356	GND	K10356
	PNNVAR_DISPLAY_VGL	267	K267	357	GND	K10357
	ANALOG PCPU SENSE P	268	K268	358	GND	K10358
	PP6V8_DISPLAY_AVDDH	269	K269	359	GND	K10359
	ANALOG_PNNVAR_DISPLAY_VSSEL_SENSE	270	K270	360	GND	K10360

Title
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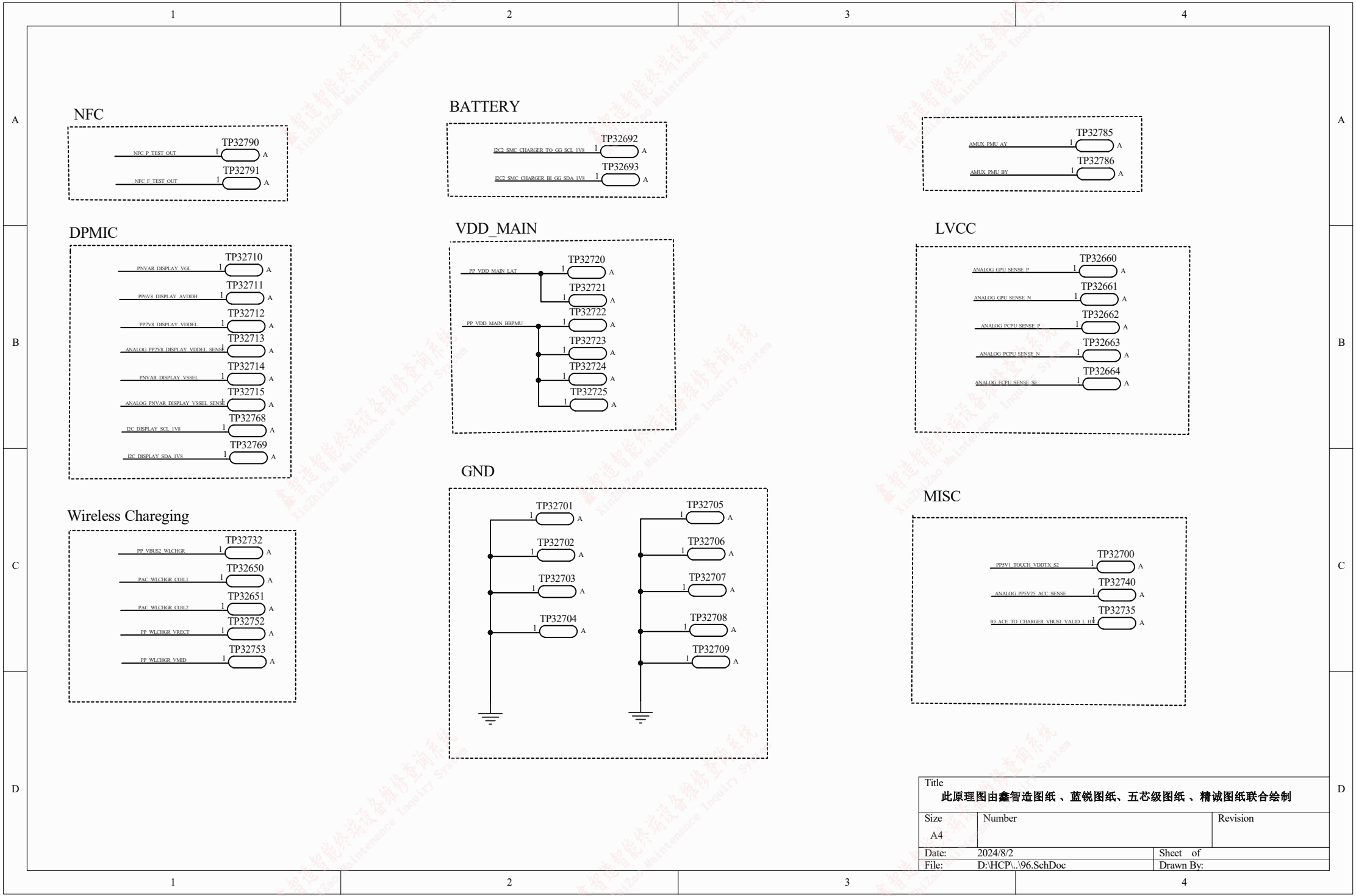
J_INT_TOP
SMT-PAD
SYM 3 OF 4

GND	361	K0361	451	GND
GND	362	K0362	452	GND
GND	363	K0363	453	GND
GND	364	K0364	454	GND
GND	365	K0365	455	50 TRX ANT3 MB HB WLAN24
GND	366	K0366	456	GND
GND	367	K0367	457	GND
GND	368	K0368	458	GND
GND	369	K0369	459	PP VIO RFEE 1V8 RX
GND	370	K0370	460	SHIELD RFEE5 SDATA SDR0
GND	371	K0371	461	SHIELD RFEE5 SCLK SDR0
GND	372	K0372	462	GND
GND	373	K0373	463	PP1V24 S2 WLCHGR
GND	374	K0374	464	GND
GND	375	K0375	465	SHIELD RFEE WLAN TO LAT FEMS DATA
GND	376	K0376	466	RFEE WLAN TO LAT 5G FEM RST L
GND	377	K0377	467	SHIELD RFEE WLAN TO LAT FEMS CLK
GND	378	K0378	468	GND
GND	379	K0379	469	GND
GND	380	K0380	470	GND
GND	381	K0381	471	GND
GND	382	K0382	472	GND
GND	383	K0383	473	GND
GND	384	K0384	474	50 WLAN A CT TXRX FEM M
GND	385	K0385	475	GND
GND	386	K0386	476	50 RX XCVR L P NA LAA FEM L LAA R
GND	387	K0387	477	GND
GND	388	K0388	478	GND
GND	389	K0389	479	GND
GND	390	K0390	480	50 NB TO 5G FEM LAT SOUTH
GND	391	K0391	481	GND
GND	392	K0392	482	GND
GND	393	K0393	483	GND
GND	394	K0394	484	GPIO WLAN TO LAT 5G FEM LNA BYP_EN
GND	395	K0395	485	GND
GND	396	K0396	486	ANALOG LAT 5G FEM TO WLAN P2T5
GND	397	K0397	487	90 USB BB R N
IO ACE TO CHARGER_VBUS1_VALID L HW	398	K0398	488	GND
IC2 SMC CHARGER TO GG_SCL 1V8	399	K0399	489	90 USB BB R P
GND	400	K0400	490	GND
IC2 SMC CHARGER BI_GG_SDA 1V8	401	K0401	491	GND
GND	402	K0402	492	GND
SWD_NUB BI_WLCHGR_SWD	403	K0403	493	GND
GND	404	K0404	494	GND
IO CHARGER FROM_WLCHGR_BST_EN	405	K0405	495	GND
GND	406	K0406	496	GND
IO CHARGER FROM_WLCHGR_BST_FPWMA7	407	K0407	497	GND
GND	408	K0408	498	GND
ANALOG_FB_WLCHGR_TO_CHARGER	409	K0409	499	GND
GND	410	K0410	500	GND
GND	411	K0411	501	GND
GND	412	K0412	502	GND
SPMIL_NUB TO BPIC_WLCHGR_CLK	413	K0413	503	GND
GPIO_SEQ_PMU TO_WLCHGR_EN_SYS	414	K0414	504	GND
SPMIL_NUB BI_BPIC_WLCHGR_DATA	415	K0415		
SWD_NUB TO_WLCHGR_SWCLK	416	K0416		
GND	417	K0417		
CLK_GPIO_PMU TO BPIC_WLCHGR_32K_1V8	418	K0418		
GND	419	K0419		
GND	420	K0420		
GND	421	K0421		
GND	422	K0422		
GND	423	K0423		
GND	424	K0424		
GND	425	K0425		
SHIELD_RFEE0_SDATA_BB	426	K0426		
SHIELD_RFEE0_SCLK_SDR	427	K0427		
GND	428	K0428		
GND	429	K0429		
GND	430	K0430		
GND	431	K0431		
GND	432	K0432		
GND	433	K0433		
GND	434	K0434		
GND	435	K0435		
ANALOG_PP2V8_DISPLAY_VDDSEL_SENSE	436	K0436		
PP2V8_DISPLAY_VDDSEL	437	K0437		
GND	438	K0438		
PP5V1_TOUCH_VDDTX_S2	439	K0439		
GND	440	K0440		
GND	441	K0441		
GND	442	K0442		
GND	443	K0443		
50_TRX_UHBPAD_LAT_ANT7	444	K0444		
GND	445	K0445		
50_TRX_UHBPAD_LAT_ANT9	446	K0446		
GND	447	K0447		
GND	448	K0448		
GND	449	K0449		
50_TRX_ANT1_LB_LMB_MB_HB	450	K0450		

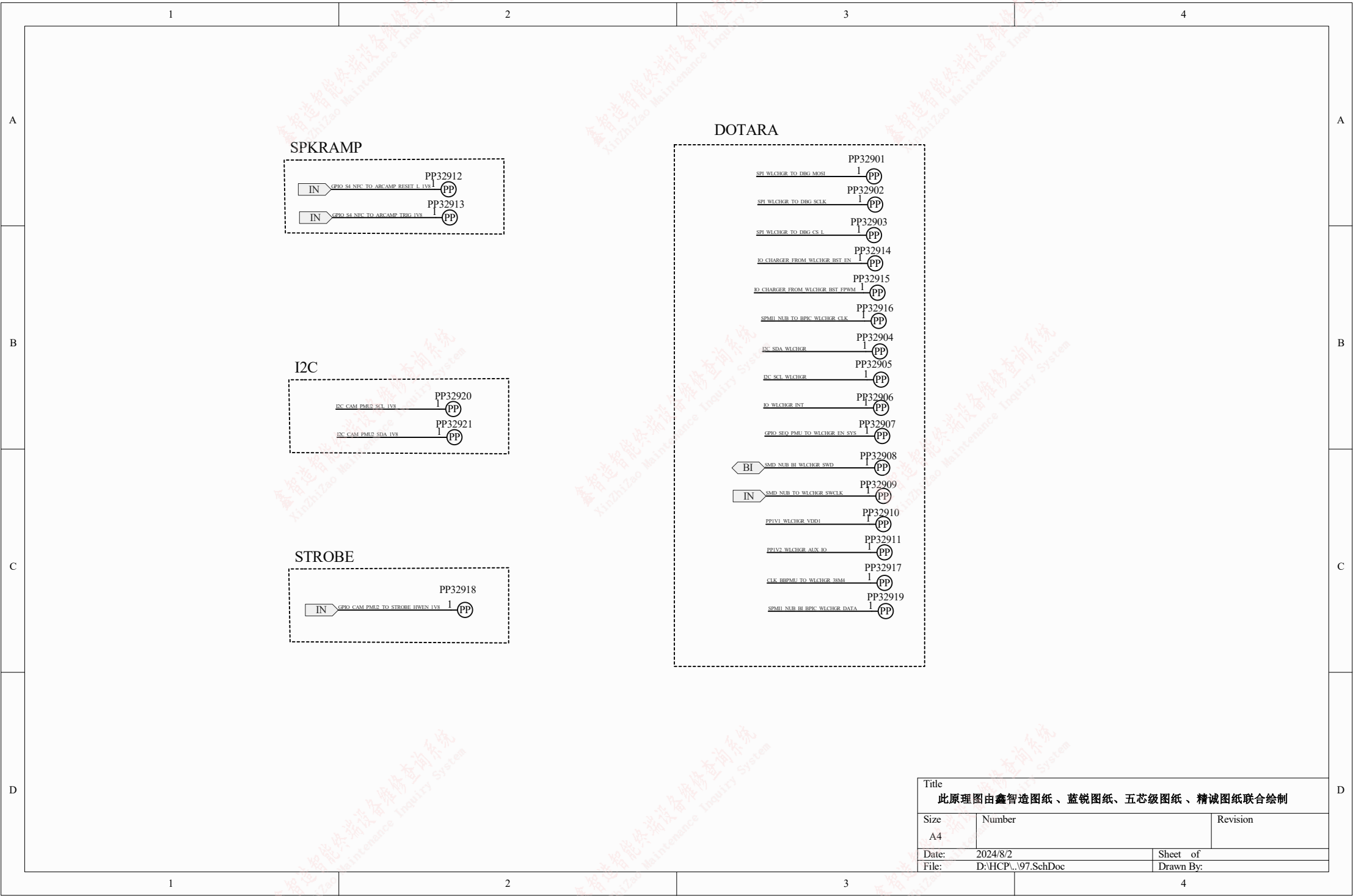
J_INT_TOP
SMT-PAD
SYM 4 OF 4

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GND	H_A2	H_A2	H_C2	GND
GND	H_A3	H_A3	H_C3	GND
GND	H_A4	H_A4	H_C6	GND
GND	H_A5	H_A5	H_C7	GND
GND	H_A6	H_A6	H_C8	GND
GND	H_A7	H_A7	H_C9	GND
GND	H_A8	H_A8	H_C10	GND
GND	H_A9	H_A9	H_C11	GND
GND	H_A10	H_A10	H_C12	GND
GND	H_A11	H_A11	H_C13	GND
GND	H_A12	H_A12	H_C14	GND
GND	H_A13	H_A13	H_C15	GND
GND	H_A14	H_A14	H_C16	GND
GND	H_A15	H_A15	H_C17	GND
GND	H_A16	H_A16	H_C18	GND
GND	H_A17	H_A17	H_D1	GND
GND	H_A18	H_A18	H_D2	GND
GND	H_B1	H_B1	H_D3	GND
GND	H_B2	H_B2	H_D4	GND
GND	H_B3	H_B3	H_D5	GND
GND	H_B4	H_B4	H_D6	GND
GND	H_B5	H_B5	H_D7	GND
GND	H_B6	H_B6	H_D8	GND
GND	H_B7	H_B7	H_D9	GND
GND	H_B8	H_B8	H_D10	GND
GND	H_B9	H_B9	H_D11	GND
GND	H_B10	H_B10	H_D12	GND
GND	H_B11	H_B11	H_D13	GND
GND	H_B12	H_B12	H_D14	GND
GND	H_B13	H_B13	H_D15	GND
GND	H_B14	H_B14	H_D16	GND
GND	H_C1	H_C1	H_D17	GND
GND	H_C2	H_C2	H_D18	GND

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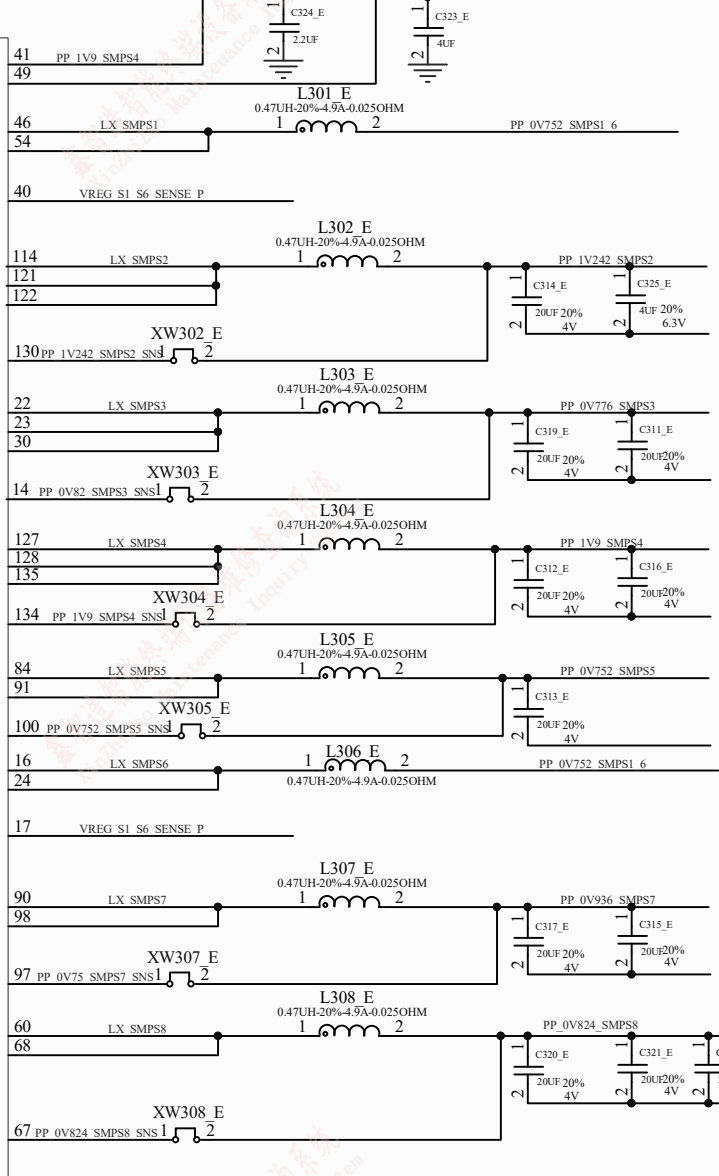
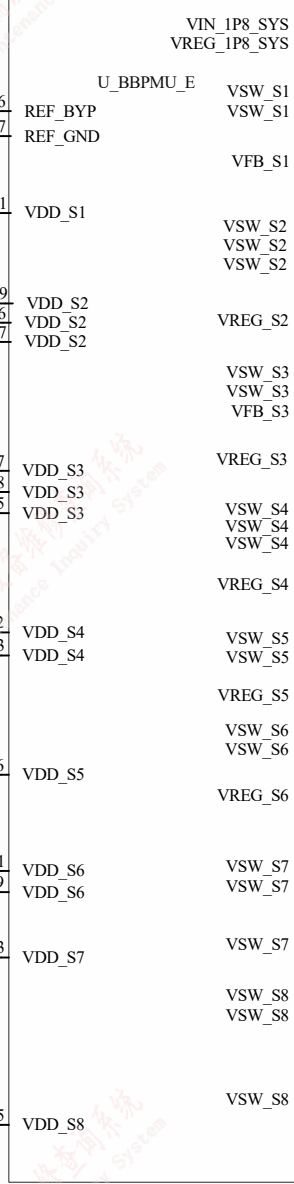
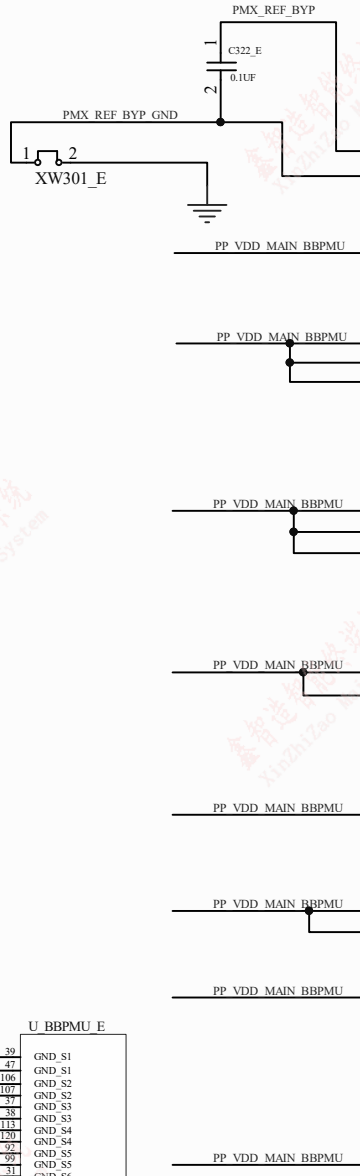
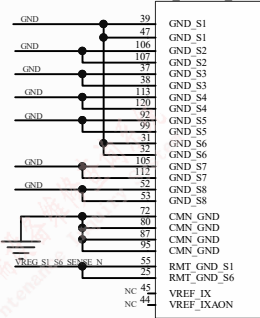
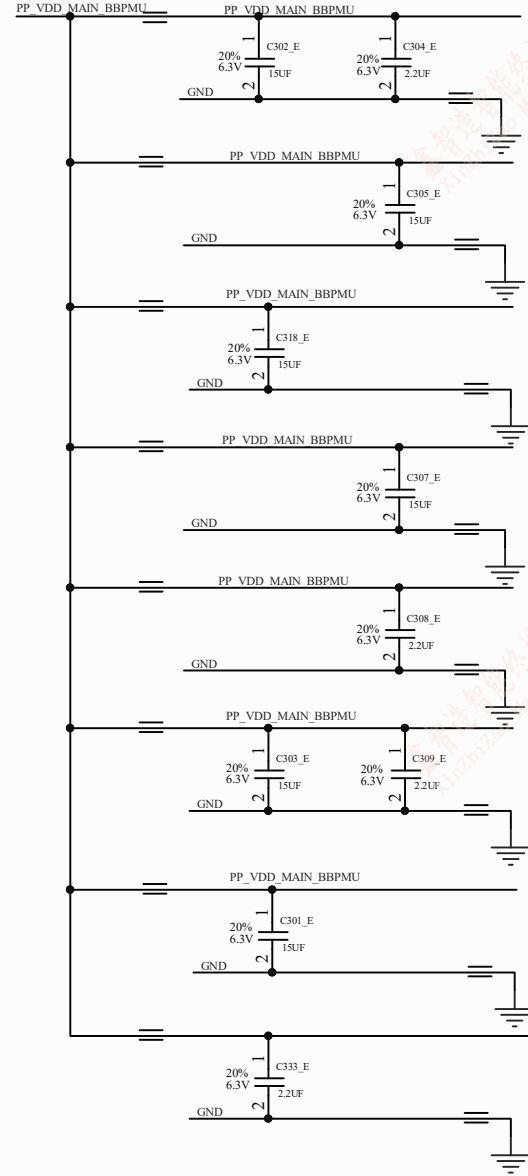
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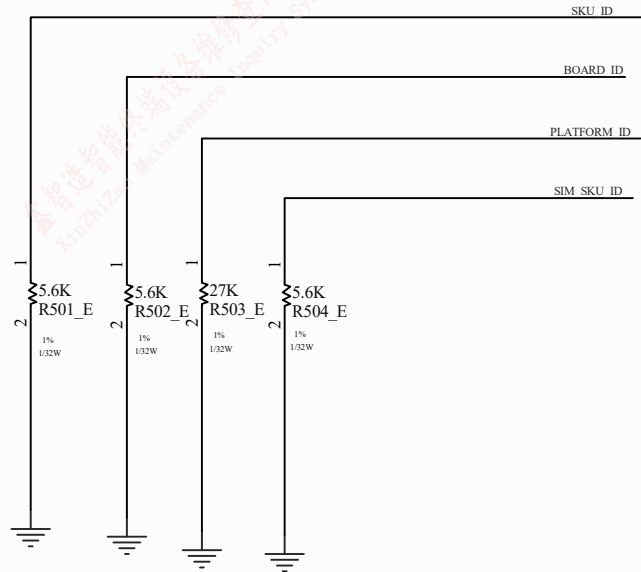
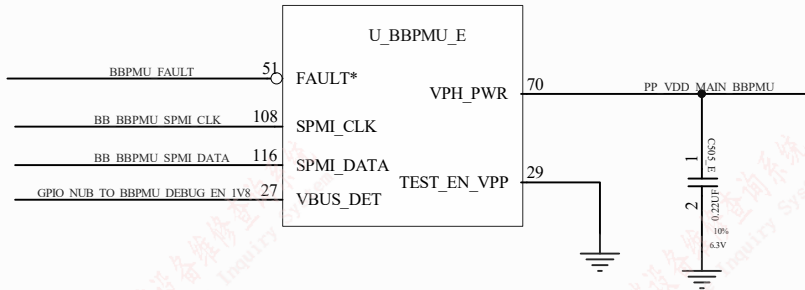
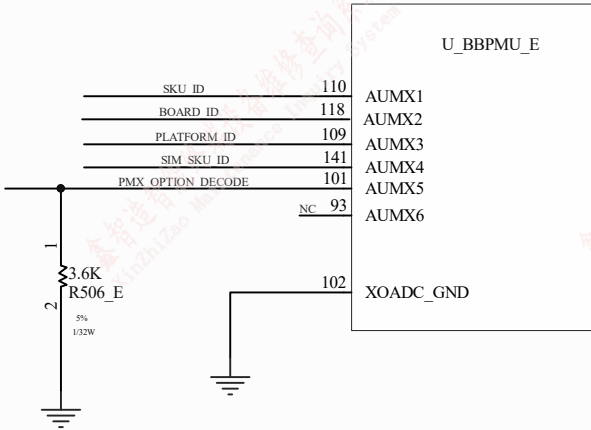
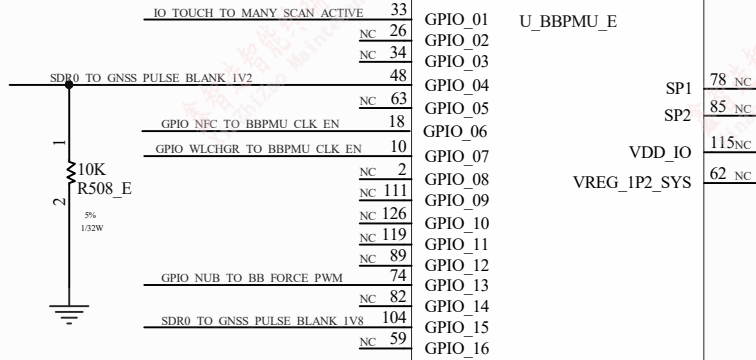
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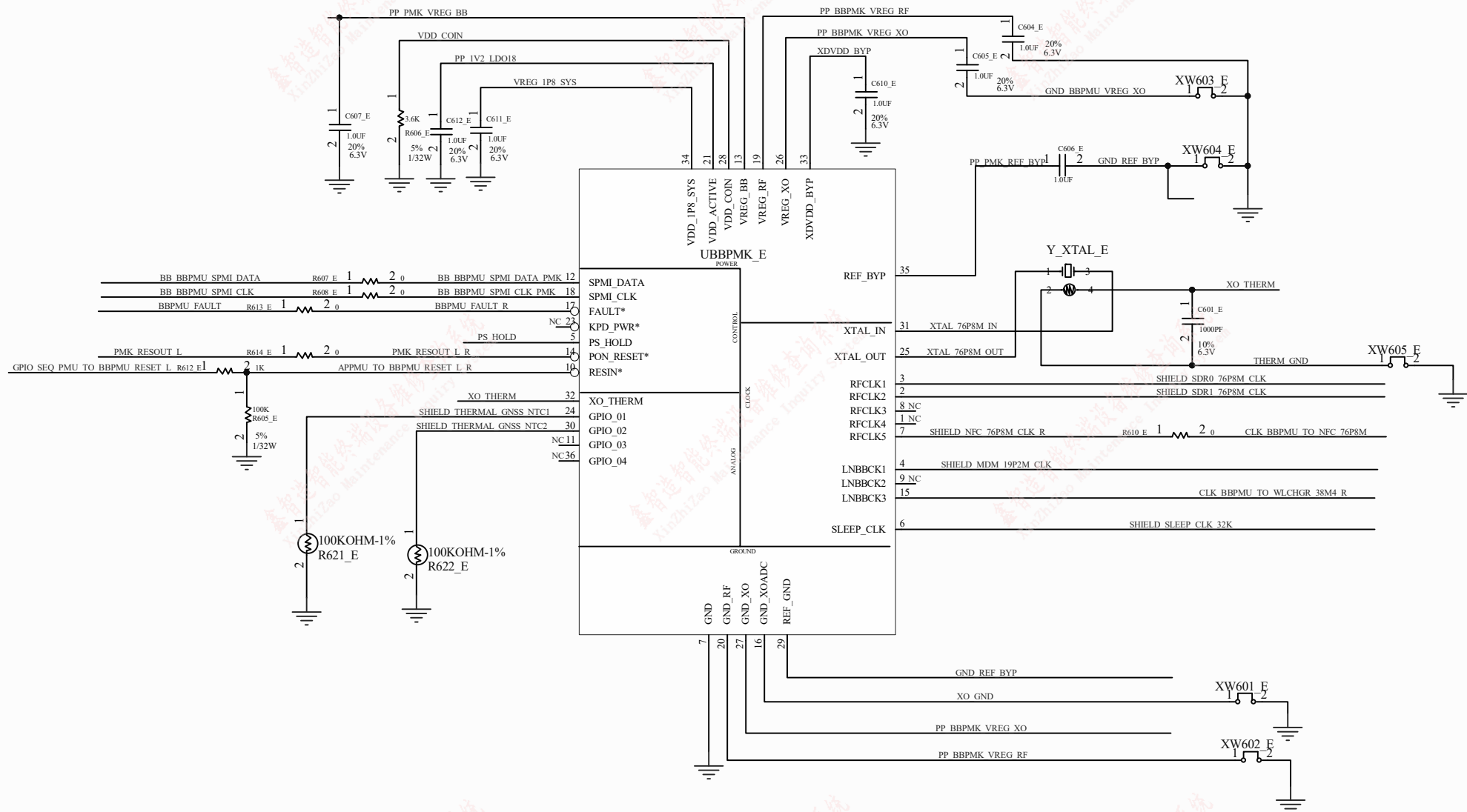
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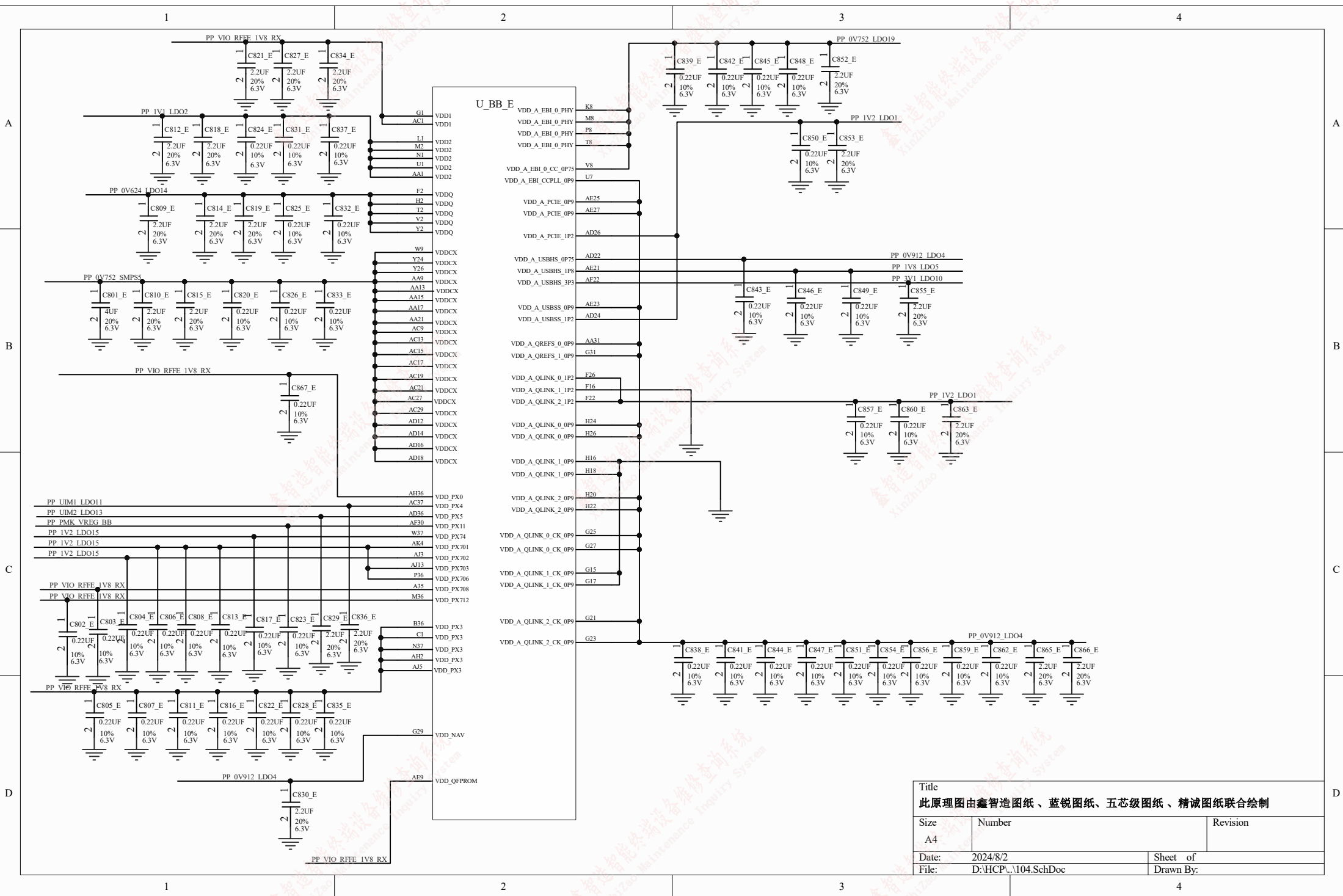
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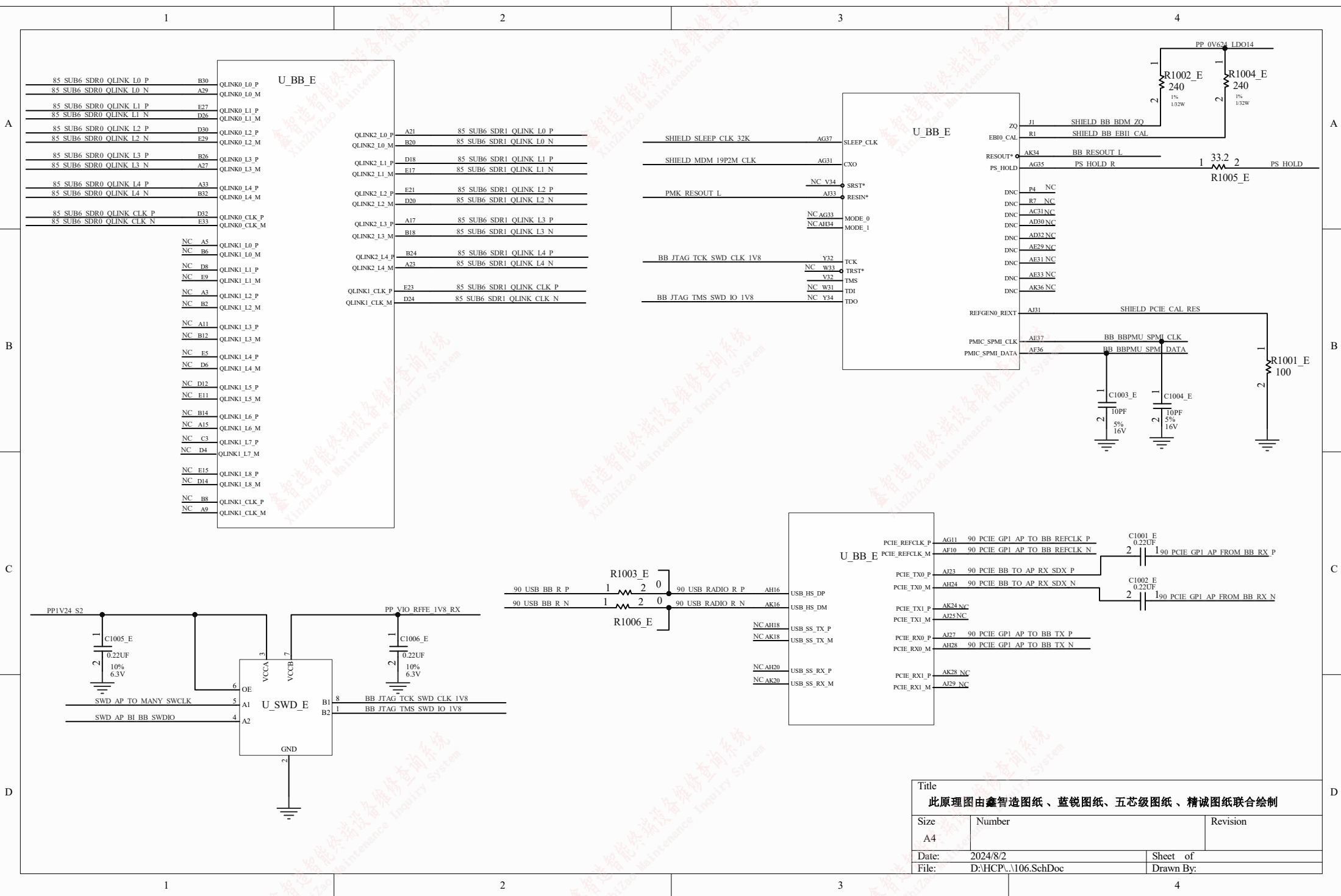
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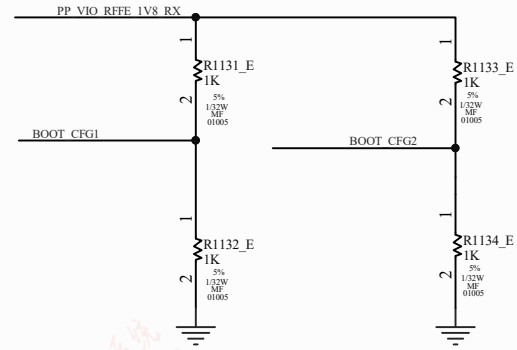
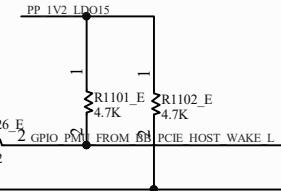
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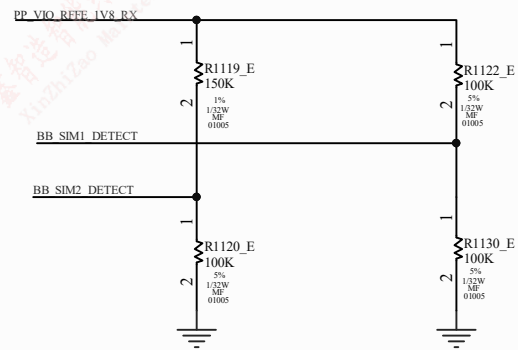
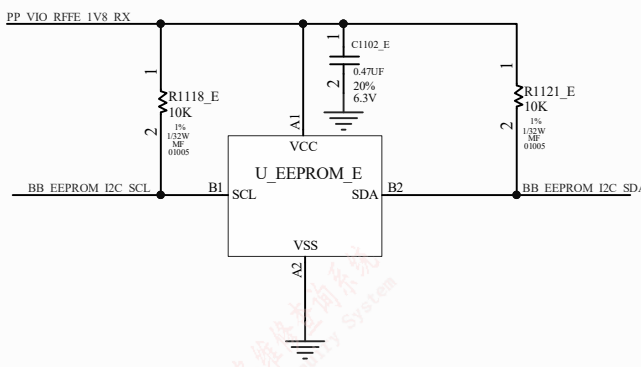
BB SIM2 DATA	AC33	GPIO_0
BB SIM2 DETECT	AC35	GPIO_1
BB SIM2 RESET L	AD34	GPIO_2
BB SIM2 CLK	AE35	GPIO_3
	NC_M32	GPIO_4
	NC_M34	GPIO_5
	NC_N33	GPIO_6
	NC_N35	GPIO_7
UART B2B FROM BB	AH6	GPIO_8
UART B2B TO BB	AJ7	GPIO_9
BB EEPROM I2C SDA	Y6	GPIO_10
BB EEPROM I2C SCL	AA7	GPIO_11
	NC_AH10	GPIO_12
	NC_AK10	GPIO_13
	NC_AG9	GPIO_14
	NC_AF9	GPIO_15
GPIO NUB FROM BB RESET DETECT L	AH8	GPIO_16
	NC_AF8	GPIO_17
	NC_AK8	GPIO_18
	NC_AG7	GPIO_19
	NC_AE7	GPIO_20
	NC_AE5	GPIO_21
	NC_AH12	GPIO_22
	NC_AF12	GPIO_23
BOOST CFG2	AE3	GPIO_24
	NC_AF2	GPIO_25
	NC_J35	GPIO_26
ANT4 TPLX GPIO BB	HB6	GPIO_27
SHIELD BB TO SDR0 WMSS RESET L	C35	GPIO_28
SHIELD RFFE4 SCLK BB	G35	GPIO_29
SHIELD RFFE4 SDATA BB	G37	GPIO_30
	NC_AH14	GPIO_31
	NC_AF14	GPIO_32
GPIO BB TO NUB GSM TXBURST IND IV8	J33	GPIO_33
QLINK EN BB TO SDR0	C37	GPIO_34
QLINK REQ SDR0 TO BB	D36	GPIO_35
SHIELD RFFE0 SDATA BB	K36	GPIO_36
SHIELD RFFE0 SCLK BB	J37	GPIO_37
SHIELD RFFE1 SDATA BB	L35	GPIO_38
SHIELD RFFE1 SCLK BB	K32	GPIO_39

U_BB_E

GPIO_40	K34	LB DPDT GRFC
GPIO_41	L33	NC
GPIO_42	AG11	NC
GPIO_43	AD4	NC
GPIO_44	AK14	NC
GPIO_45	AG13	NC
GPIO_46	AD2	NC
GPIO_47	AF6	NC
GPIO_48	AH4	GPIO AP TO BB COREDUMP
GPIO_49	AG5	NC
GPIO_50	AC7	NC
GPIO_51	AC5	NC
GPIO_52	AC3	NC
GPIO_53	AG1	PCIE BB TO PMU HOST WAKE IV2 L R
GPIO_54	AD6	NC
GPIO_55	AB4	NC
GPIO_56	AG3	PCIE GPI AP BI BB CLKREQ L
GPIO_57	AF4	PCIE GPI AP TO BB PERST L
GPIO_58	Y4	BOOT CFG1
GPIO_59	AB6	NC
GPIO_60	AA5	NC
GPIO_61	E35	SHIELD BB TO SDR1 WMSS RESET L
GPIO_62	AF10	NC
GPIO_63	T37	NC
GPIO_64	T36	NC
GPIO_65	U37	NC
GPIO_66	U35	NC
GPIO_67	AA33	BB SIM1 DATA
GPIO_68	AA35	BB SIM1 DETECT
GPIO_69	AB34	BB SIM1 RESET
GPIO_70	AB32	BB SIM1 CLK
GPIO_71	E37	QLINK EN BB TO SDR1
GPIO_72	H34	NC
GPIO_73	G33	NC
GPIO_74	H32	NC
GPIO_75	Y36	SPM11 COEX AOP BI BB WLAN UWB DATA BB R
GPIO_76	AA37	SPM11 COEX AOP TO BB WLAN UWB CLK
GPIO_77	F36	QLINK REQ SDR1 TO BB
GPIO_78	W35	SPM10 HEB AOP BI BB WLAN NFC UWB DATA BB R
GPIO_79	V36	SPM10 HEB AOP TO BB WLAN NFC UWB CLK



BB EEPROM



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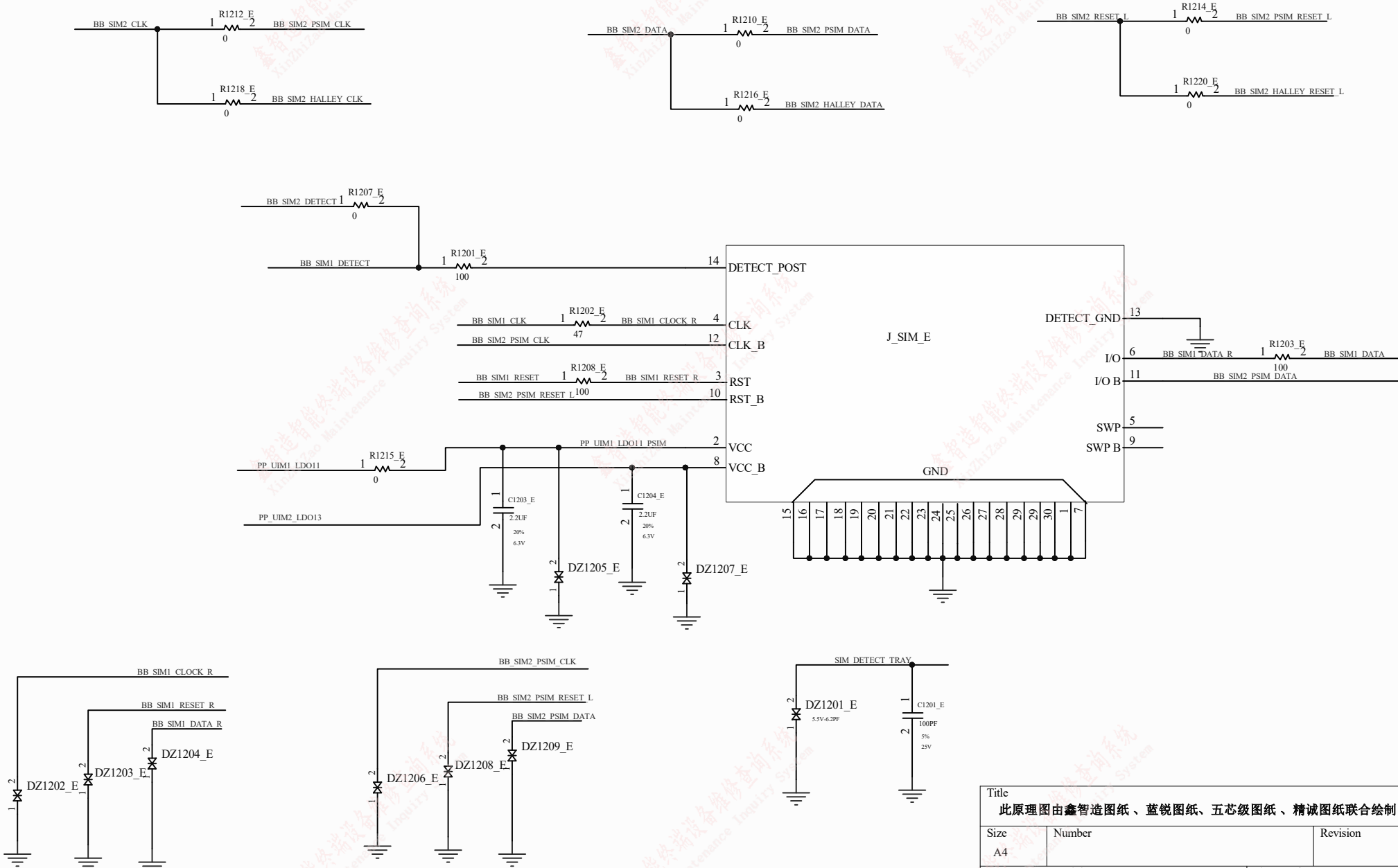
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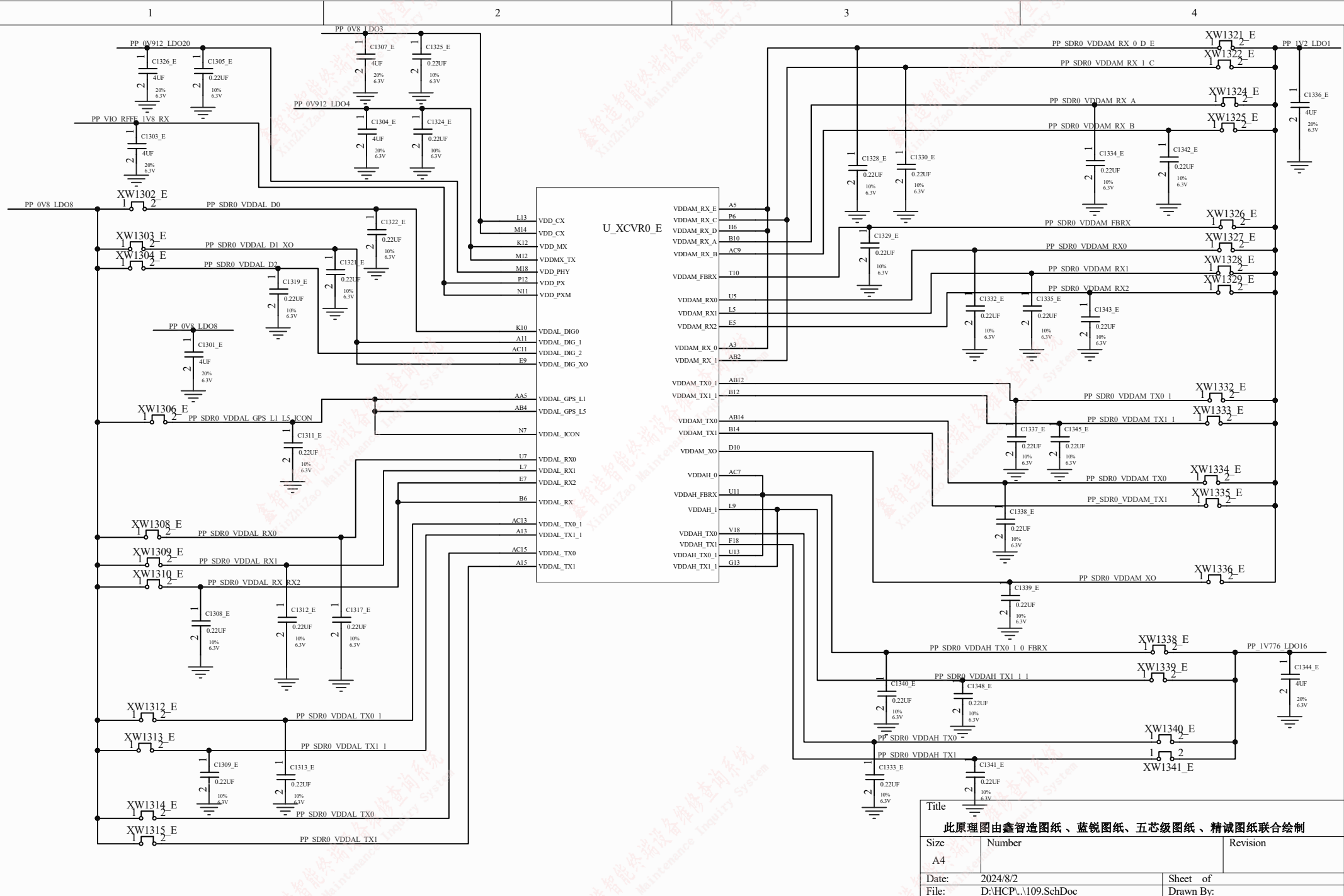
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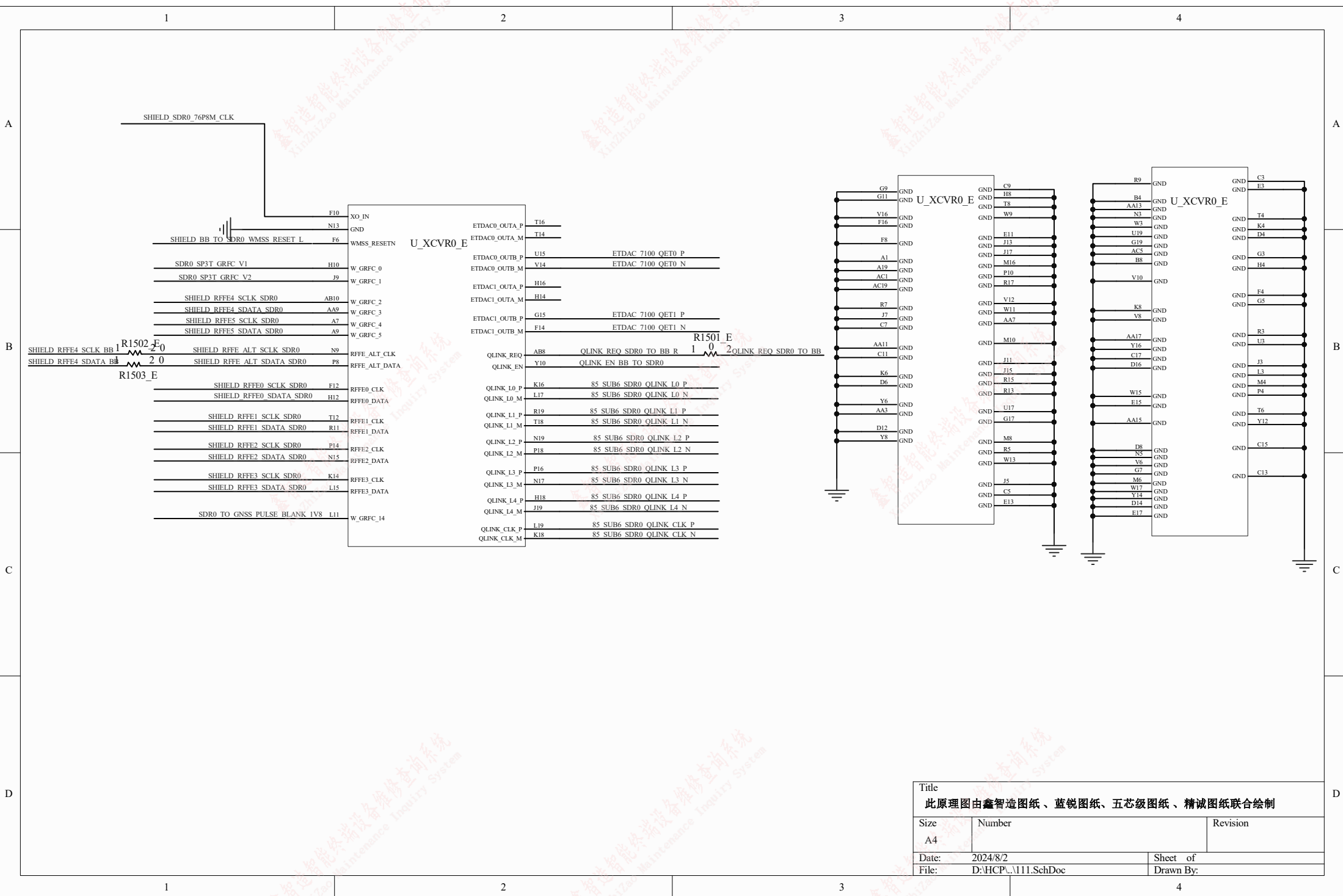
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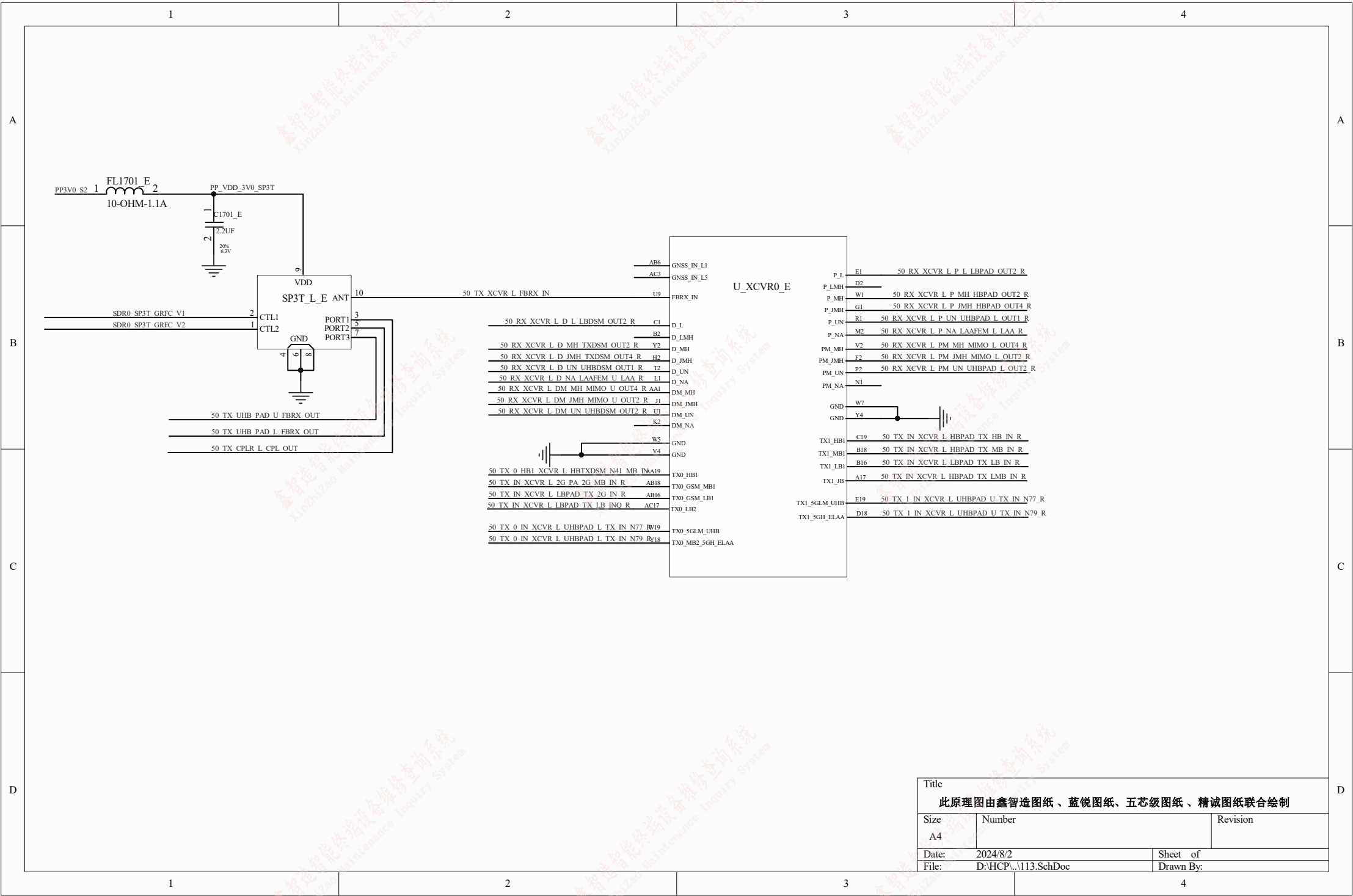
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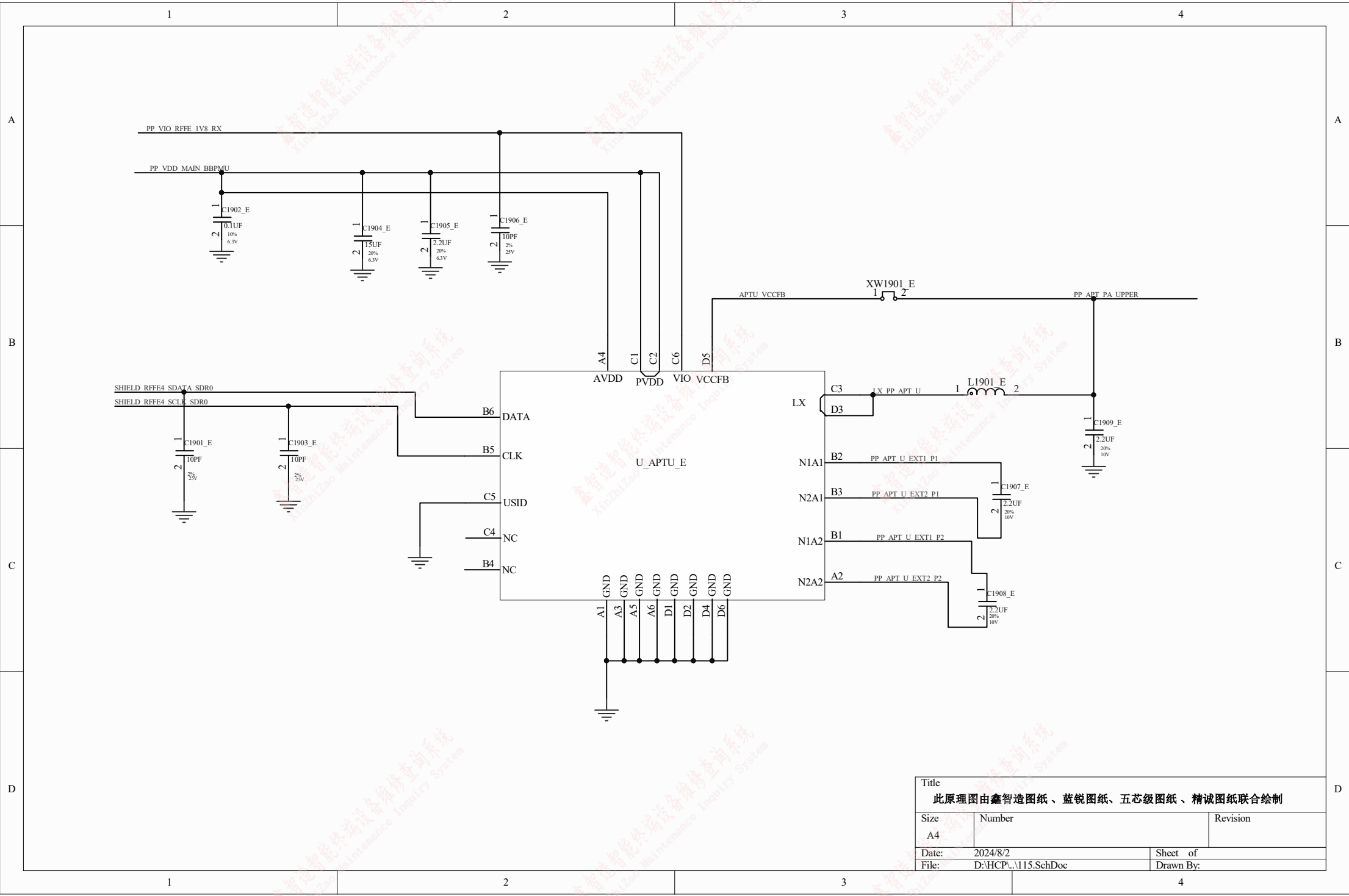
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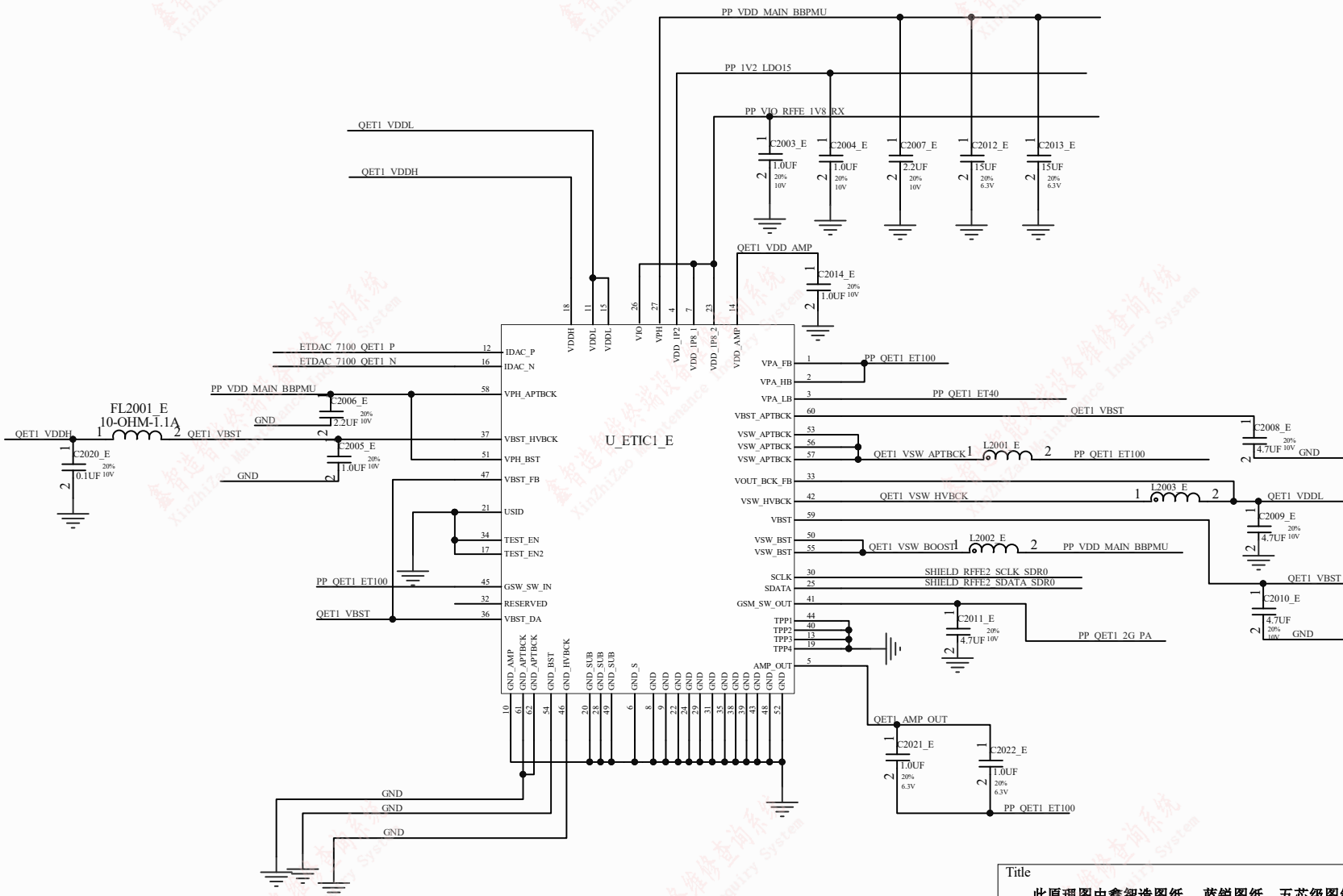
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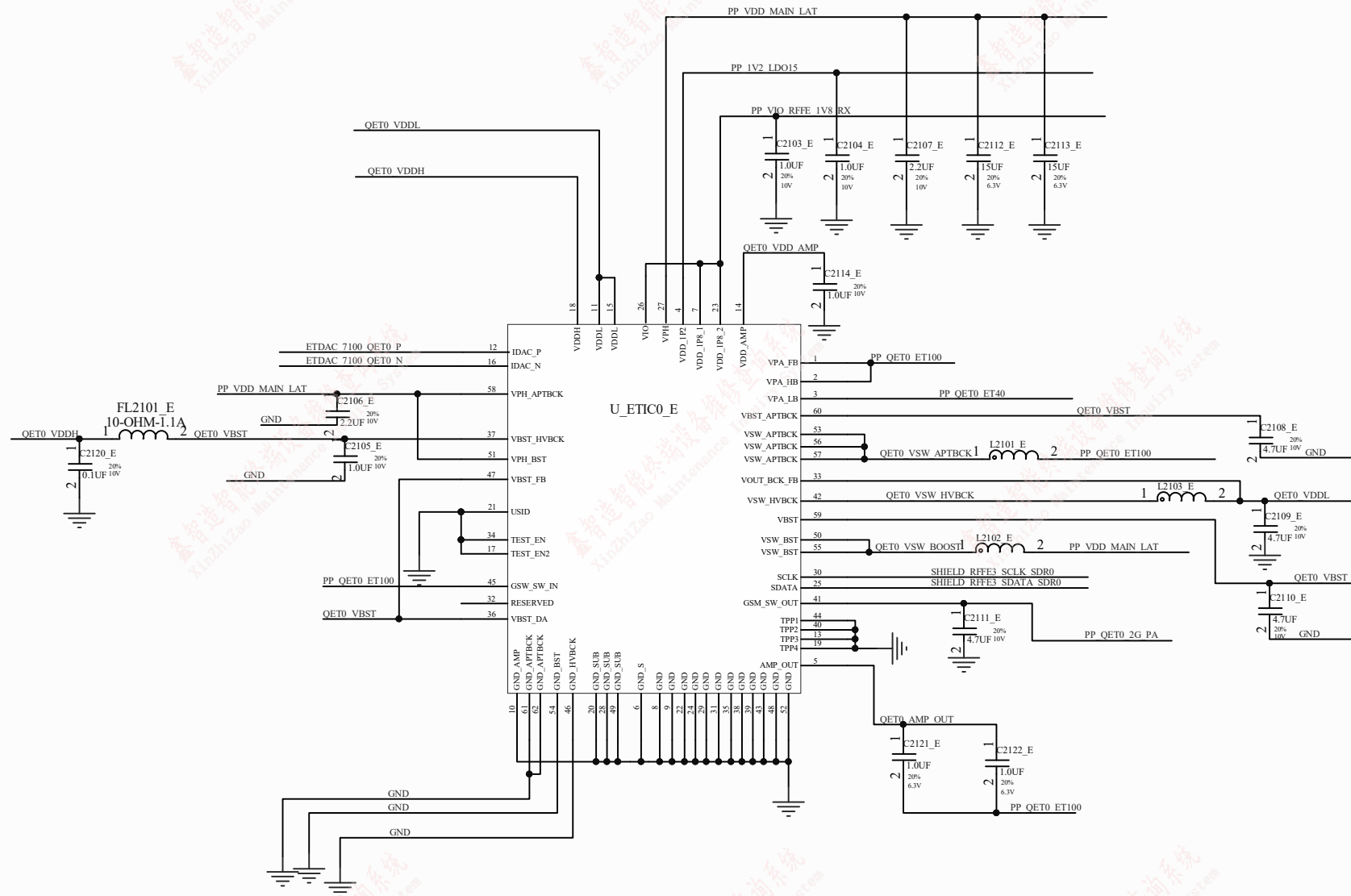
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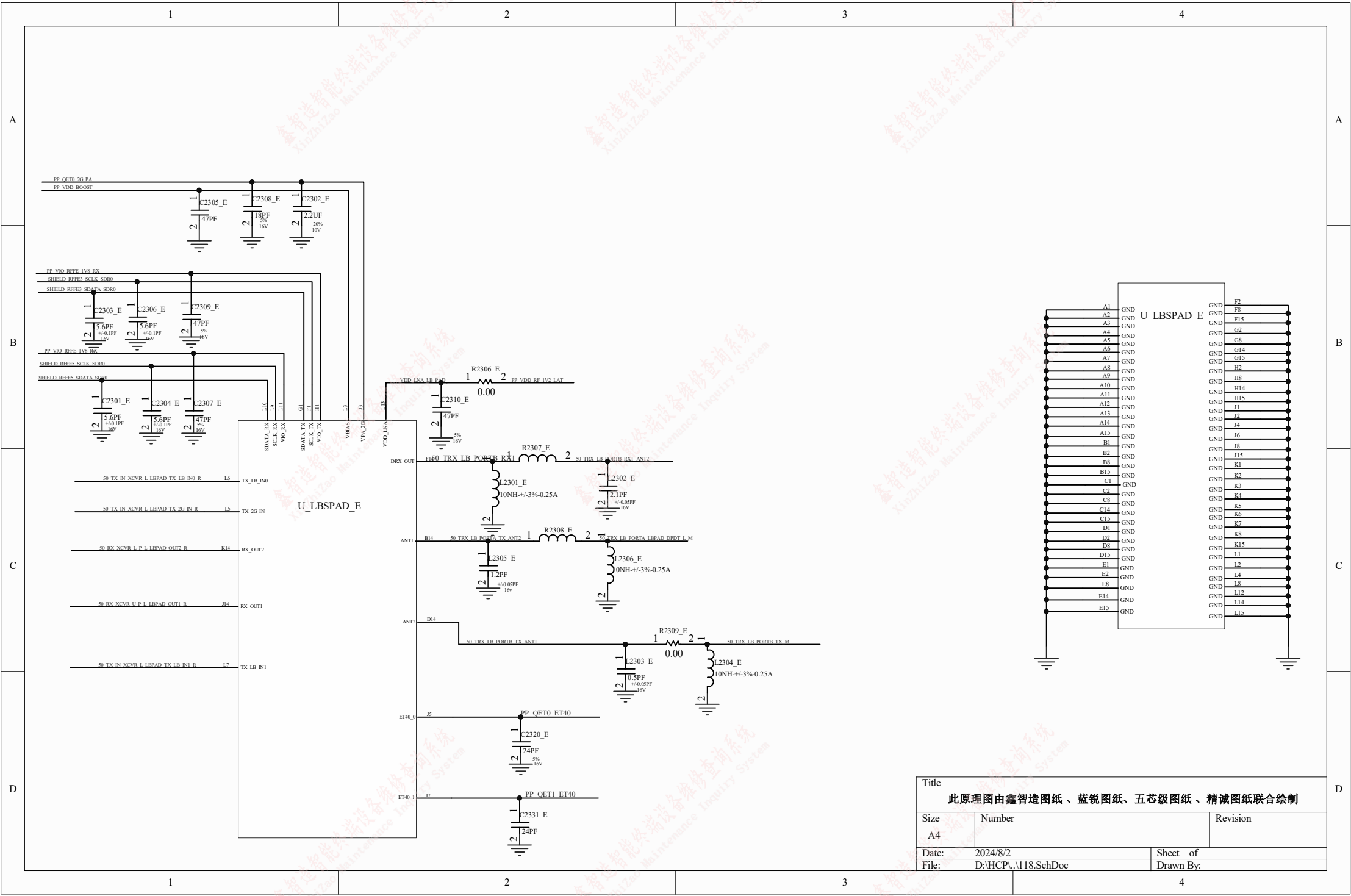
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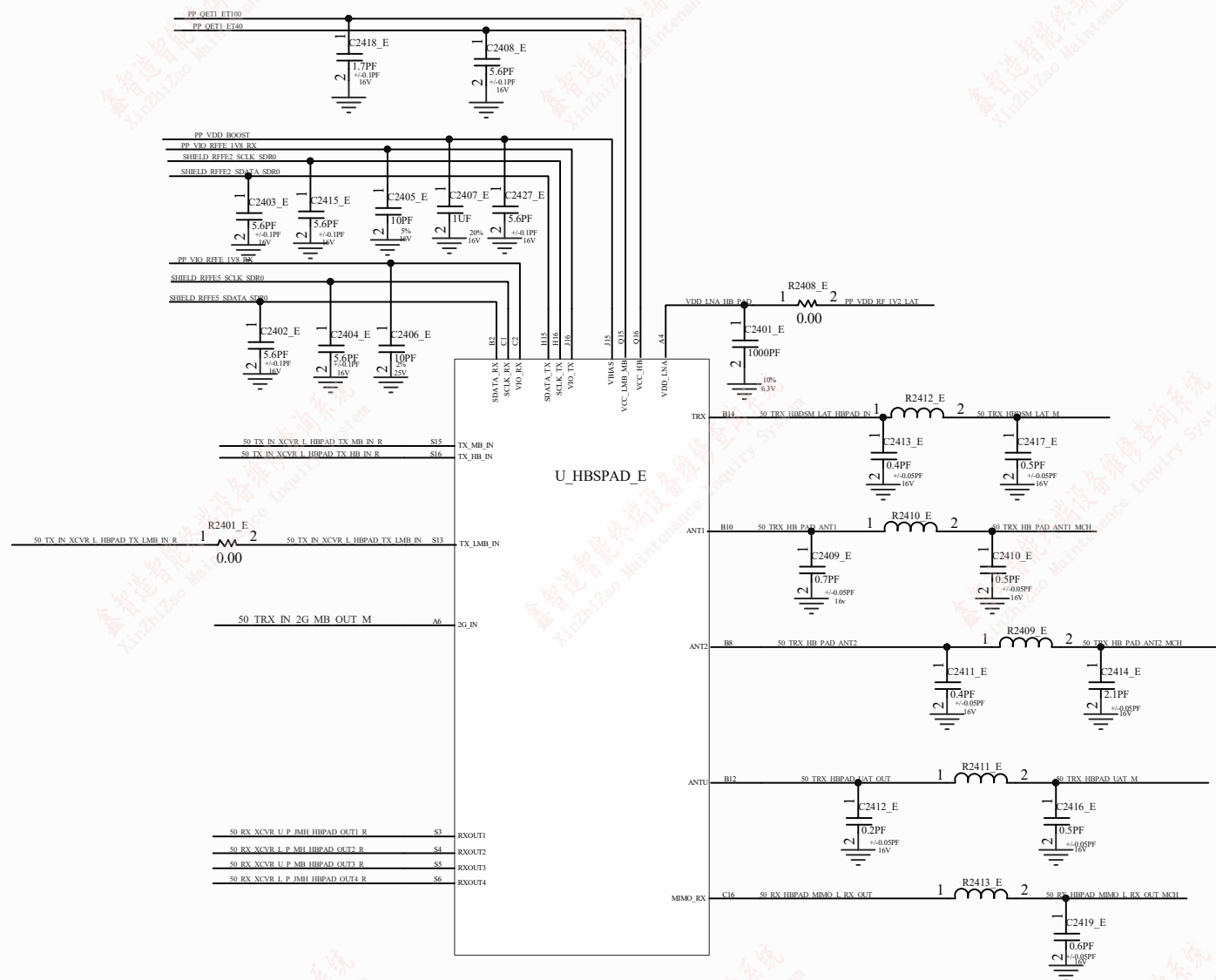
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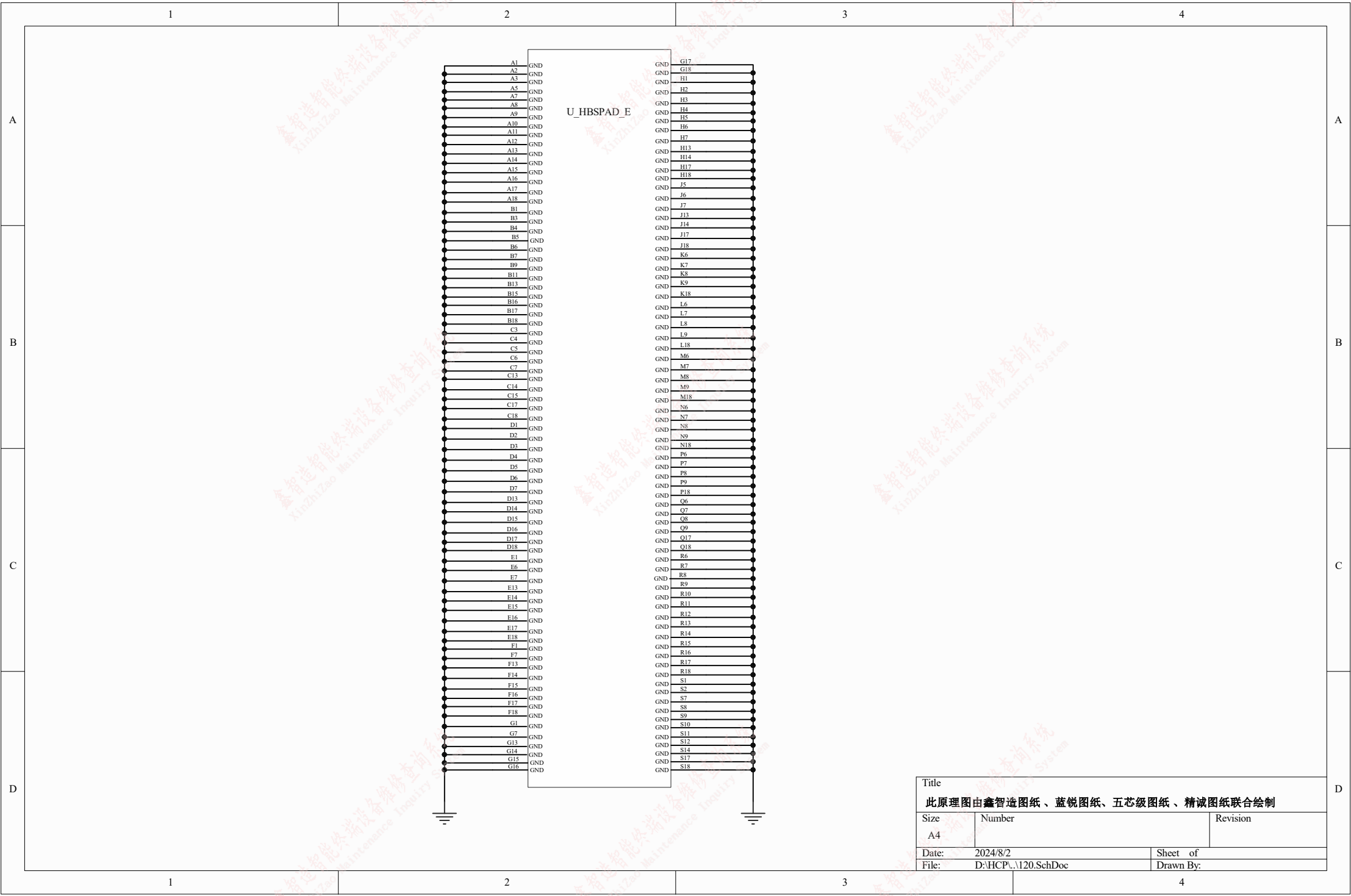
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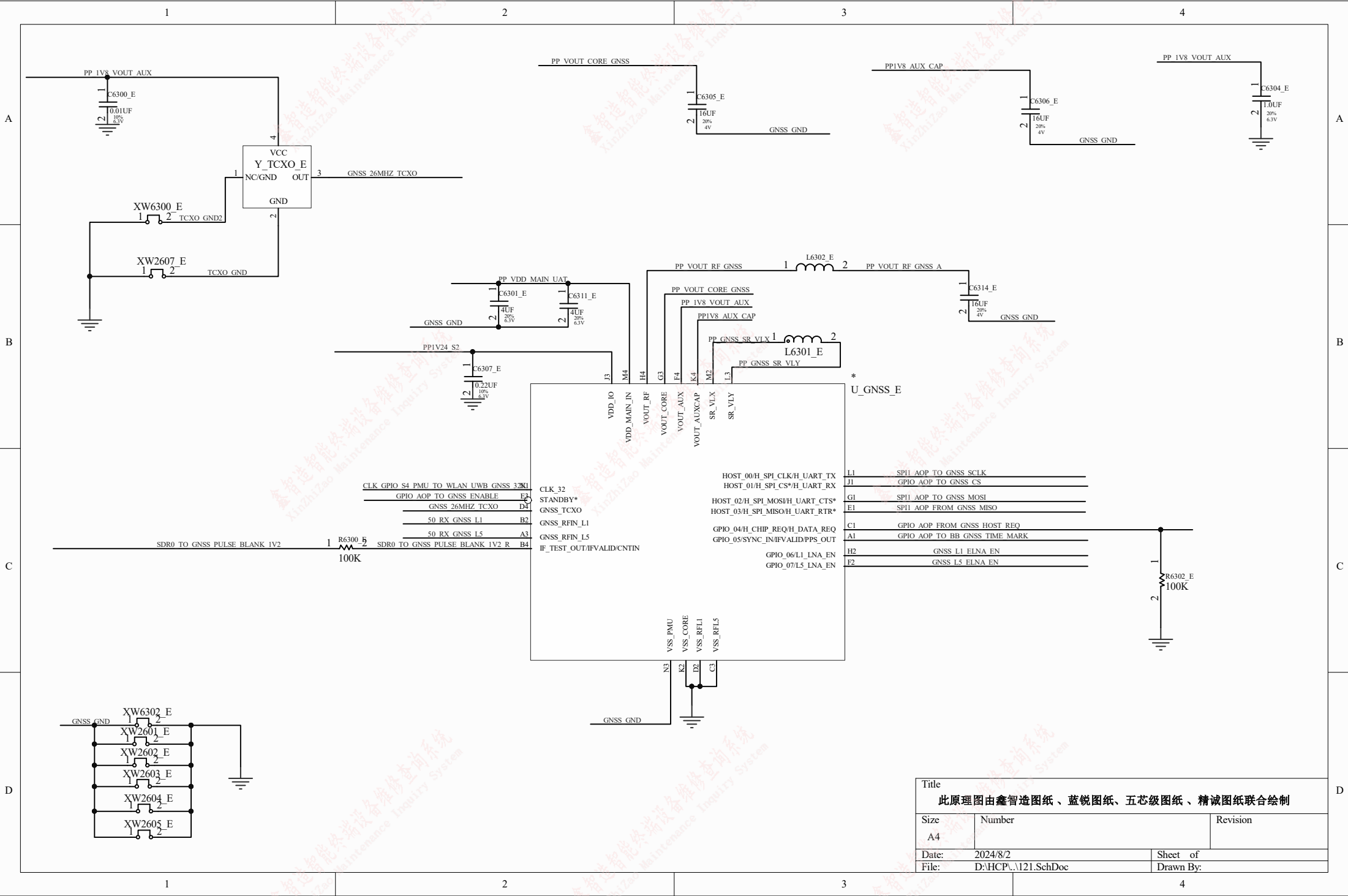
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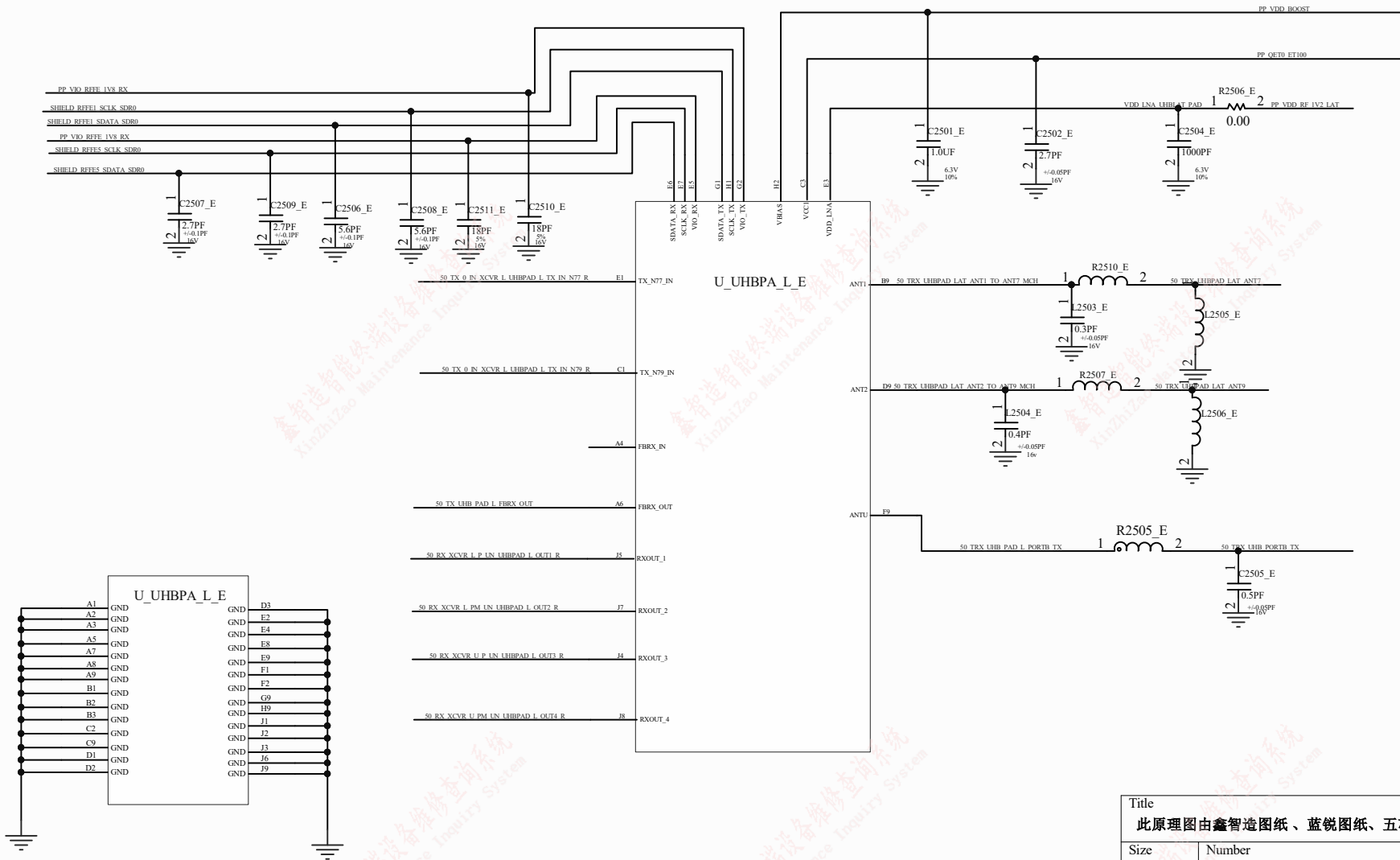
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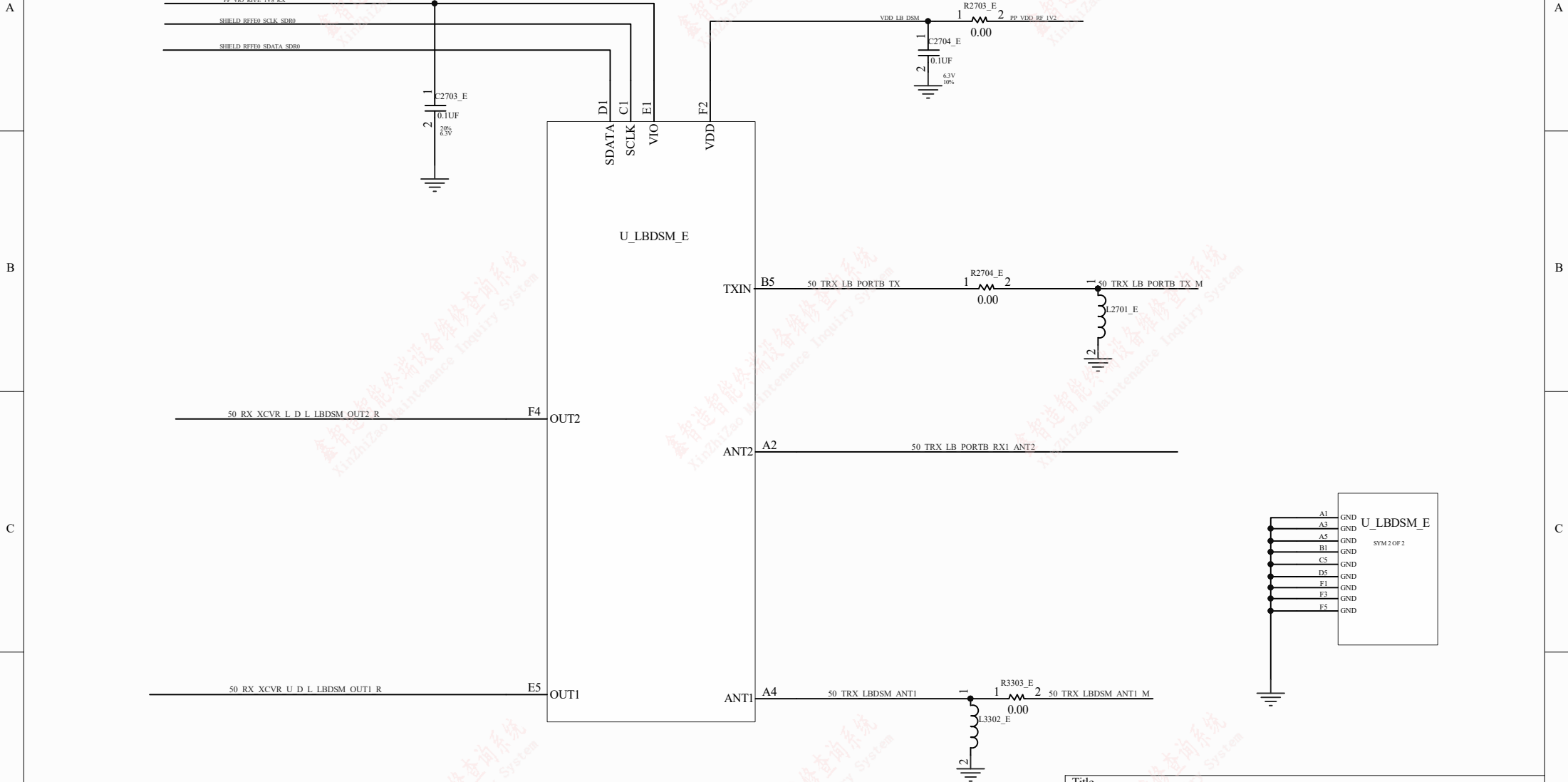
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A1	GND
A3	GND
A5	GND
B1	GND
C5	GND
D5	GND
E1	GND
F3	GND
F5	GND

Title		
此原理图由鑫智造图纸、蓝锐图纸、五芯级图纸、精诚图纸联合绘制		
Size	Number	Revision
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Date:	2024/8/2	Sheet of
File:	D:\HCP\1124.SchDoc	Drawn By:

1

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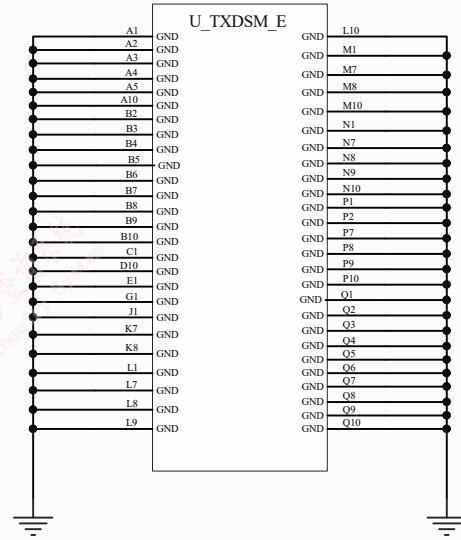
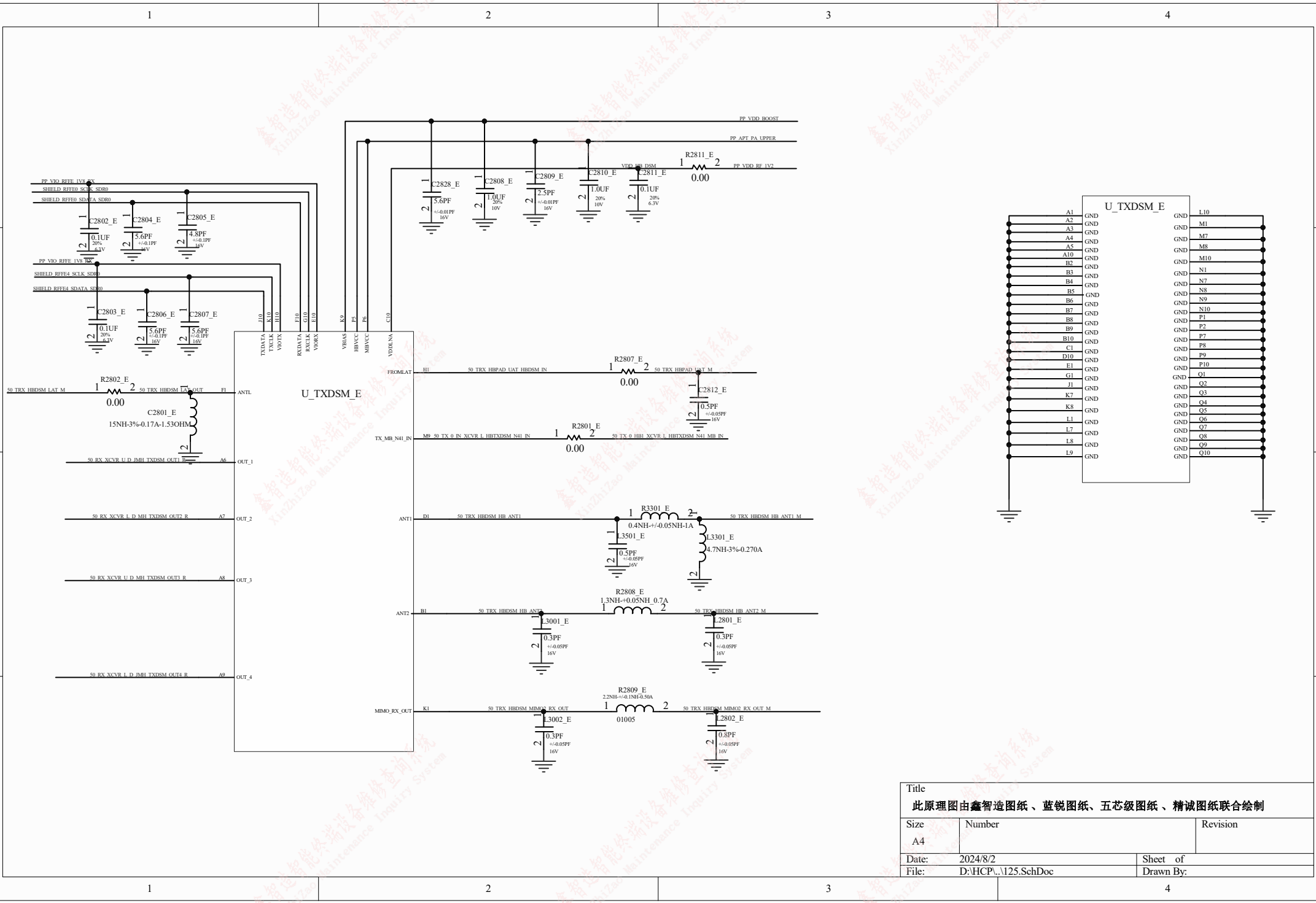
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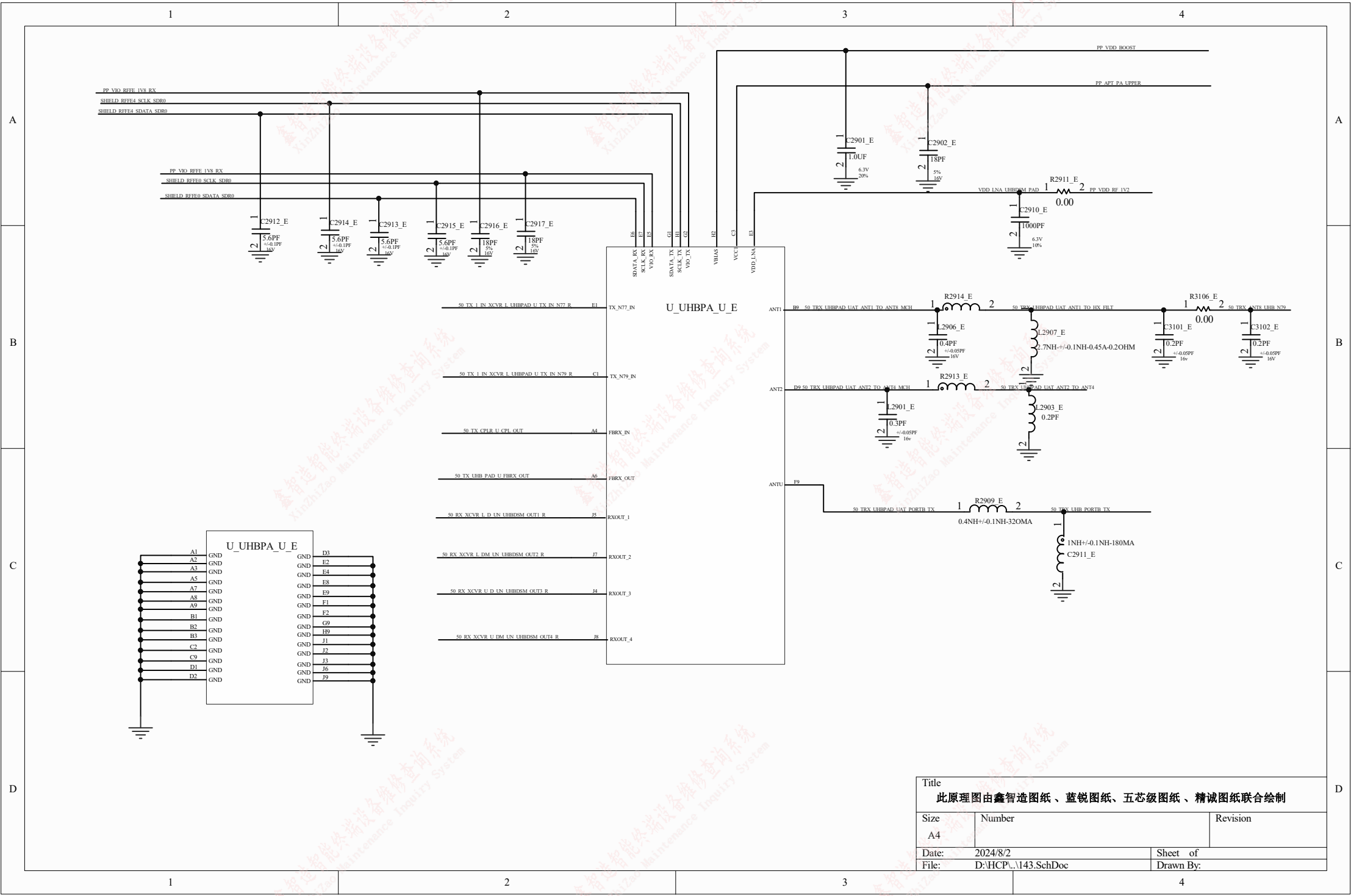
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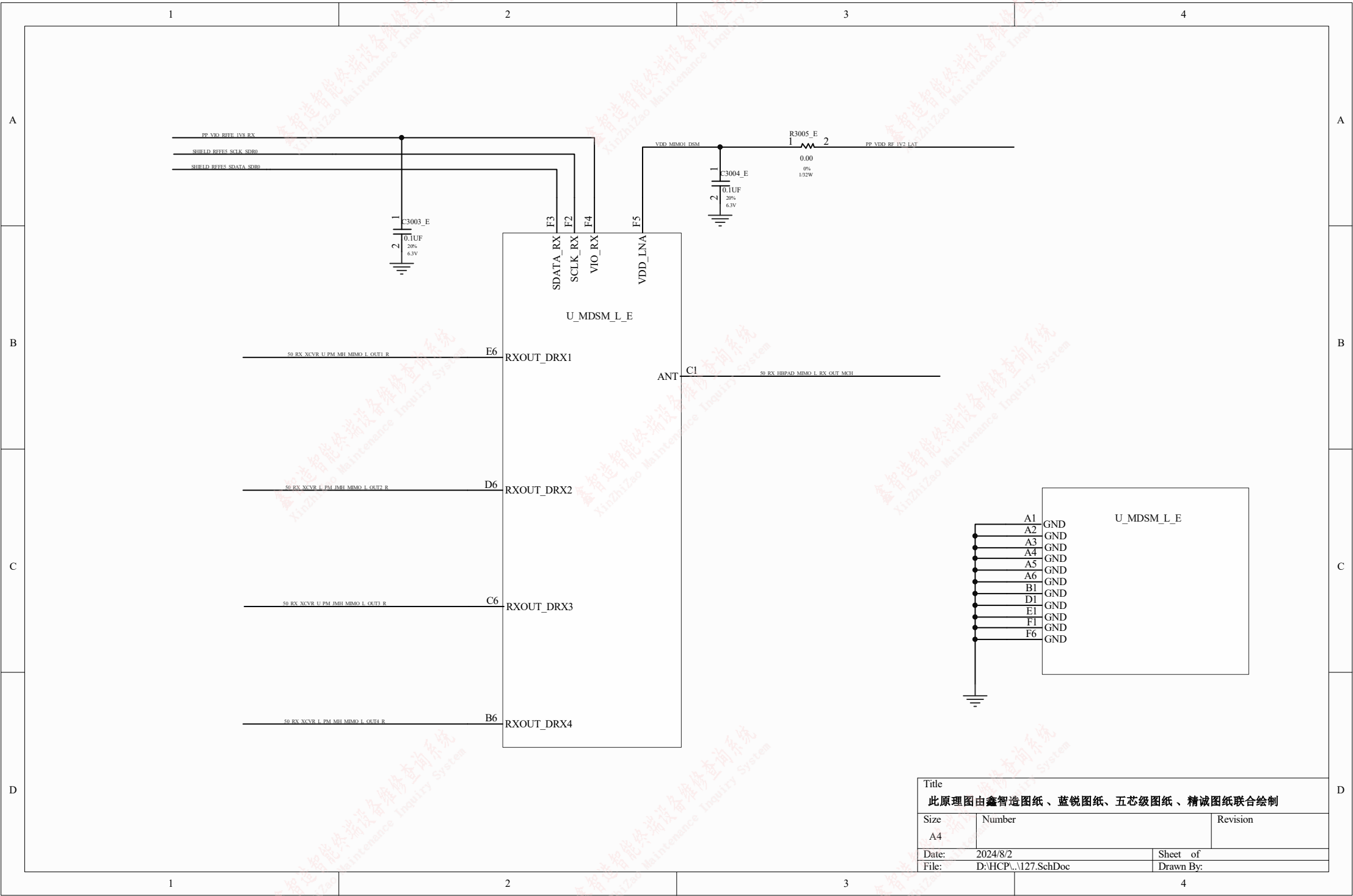
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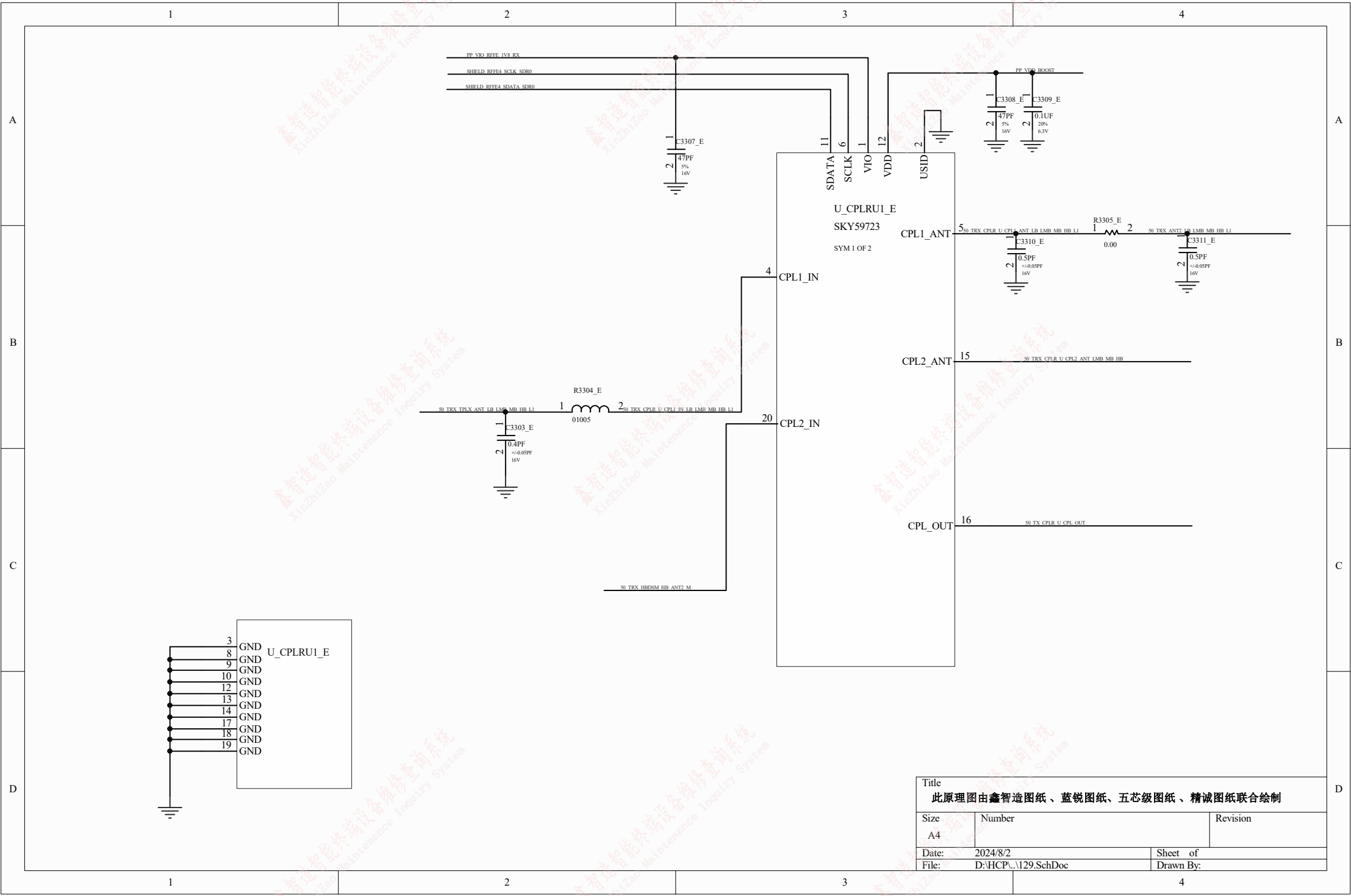
C

D









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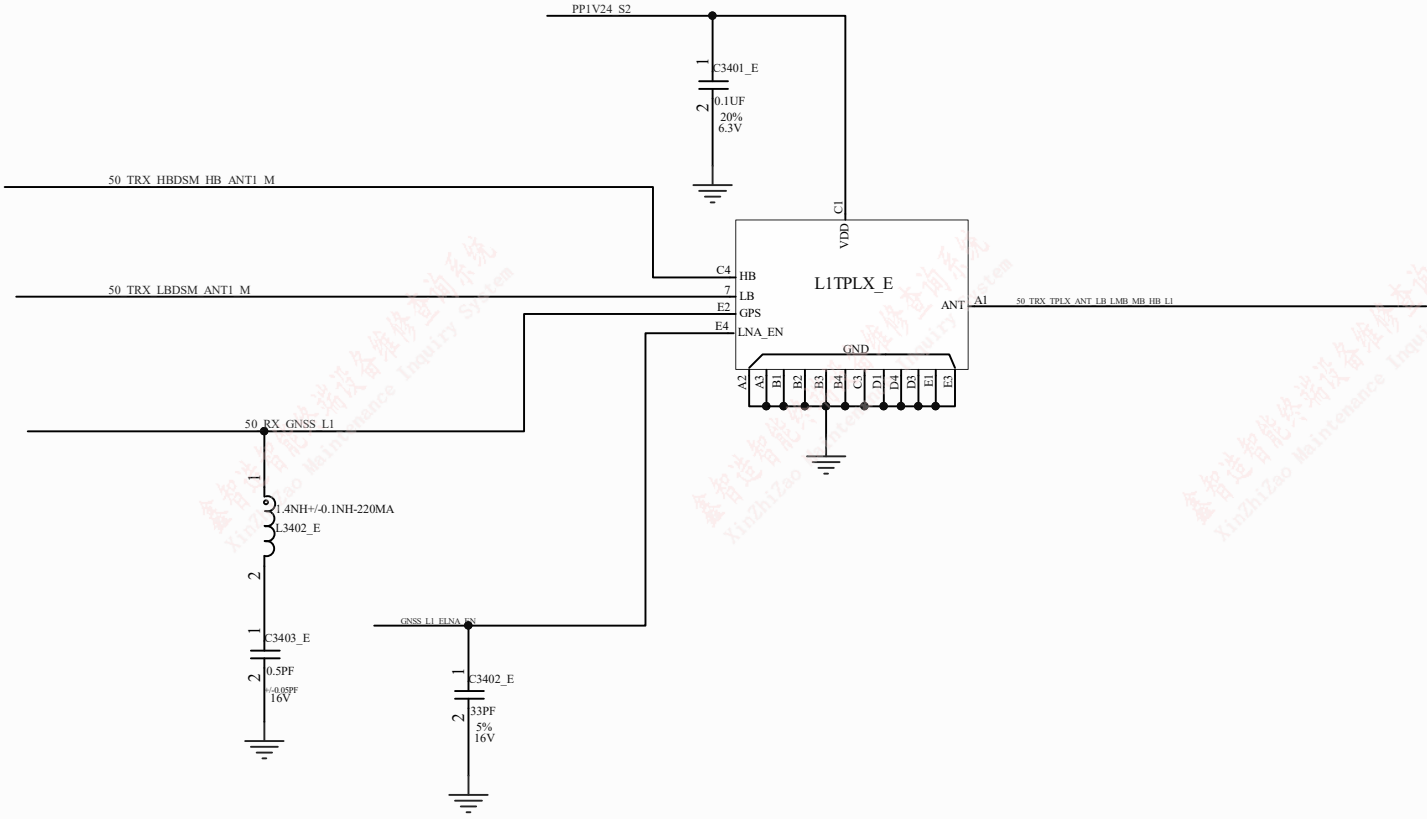
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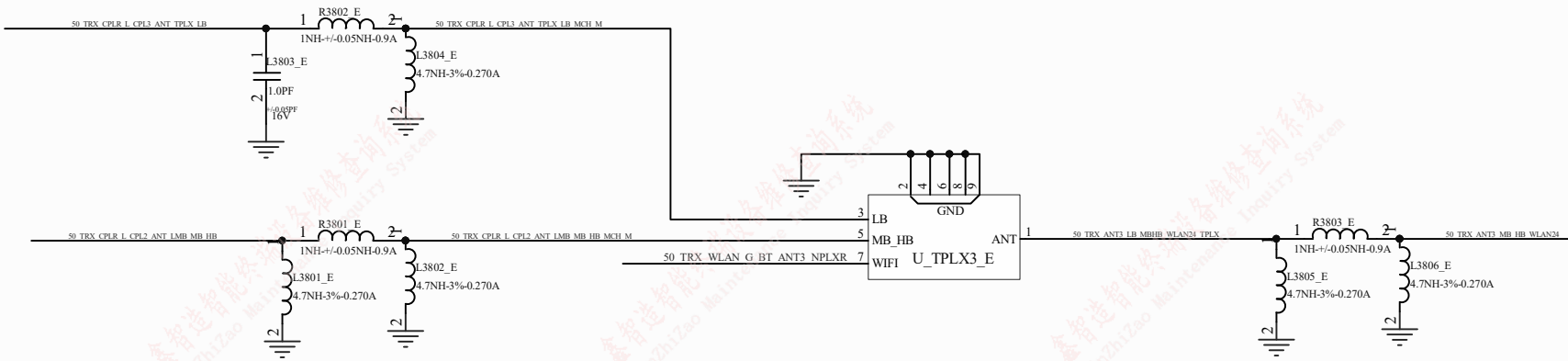
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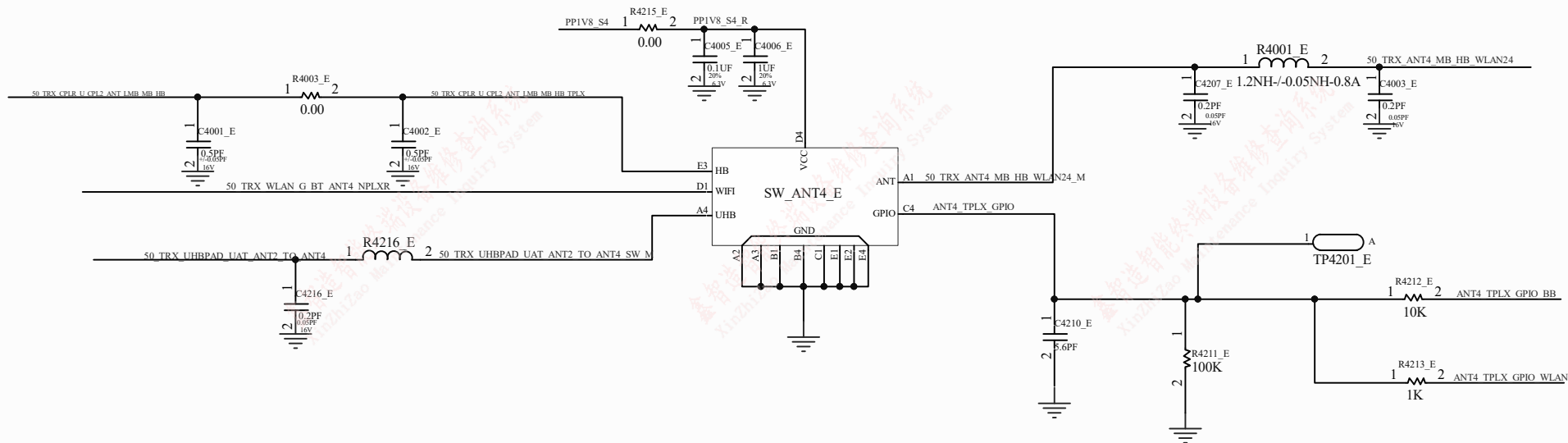
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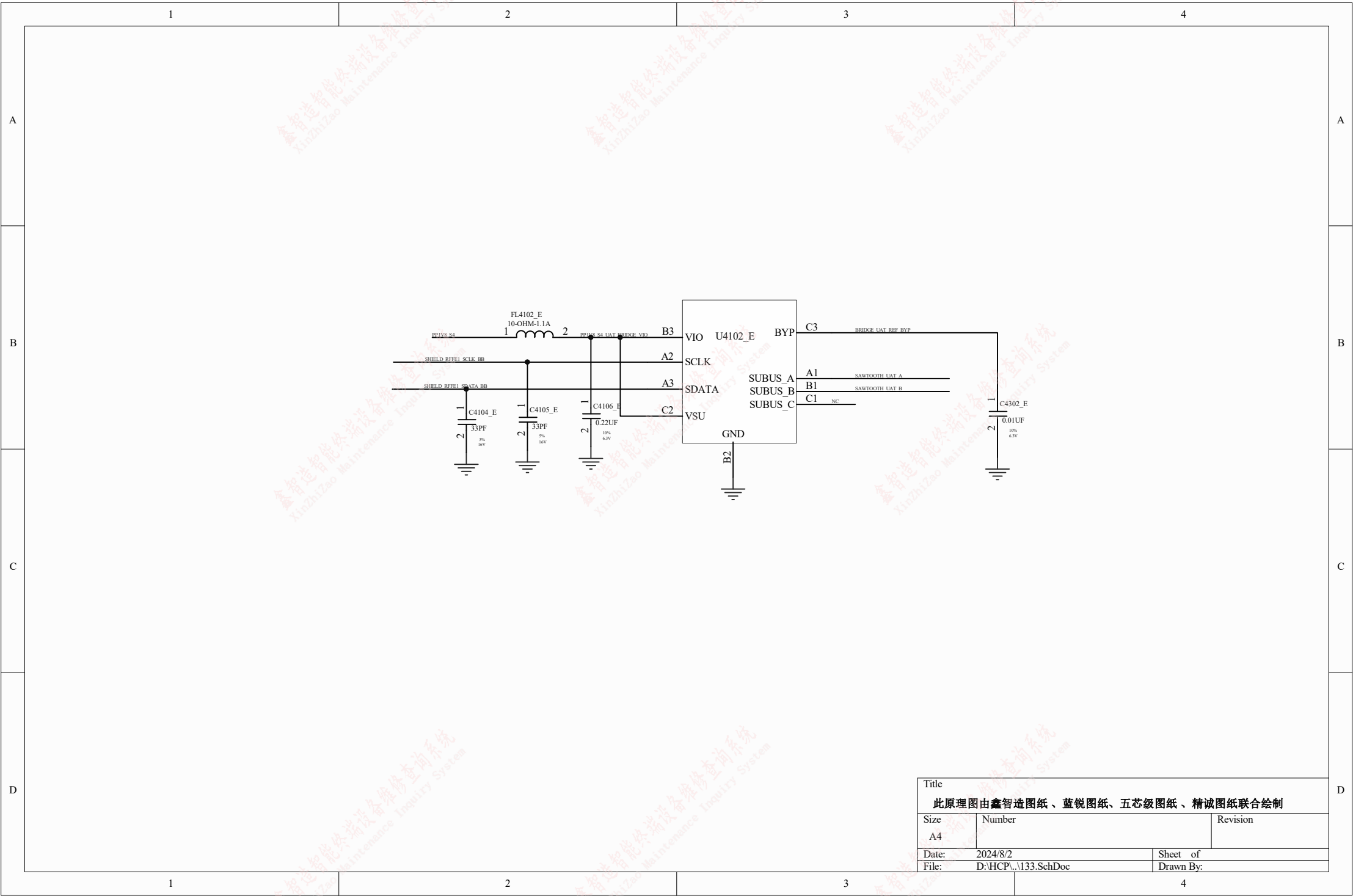
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Title			
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Title			
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Size	Number		Revision
A4			
Date:	2024/8/2		Sheet of
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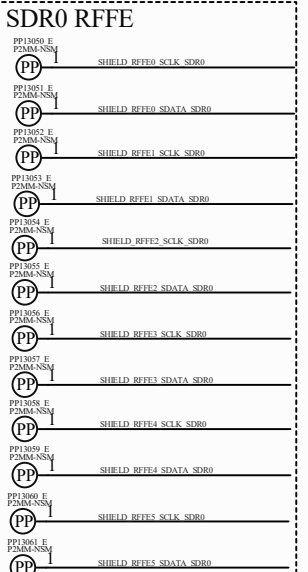
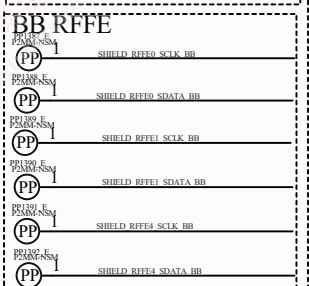
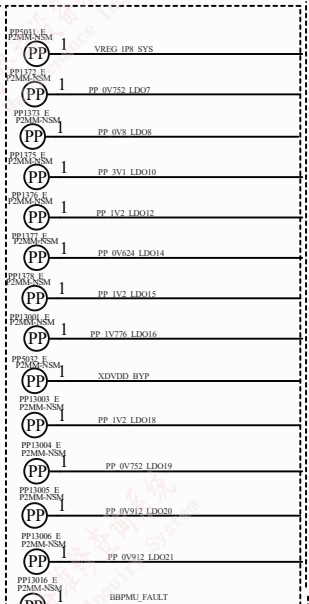
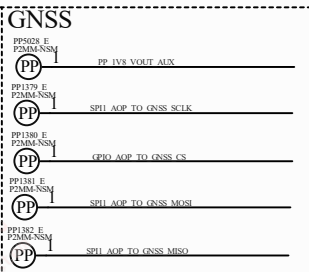
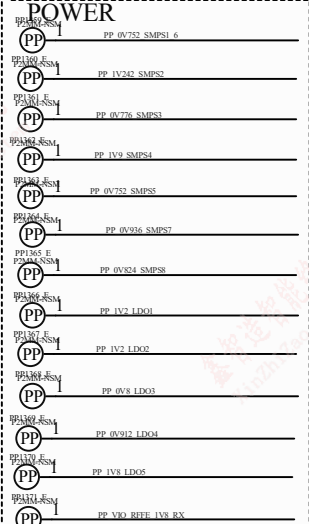
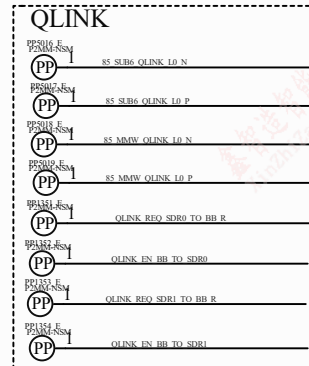
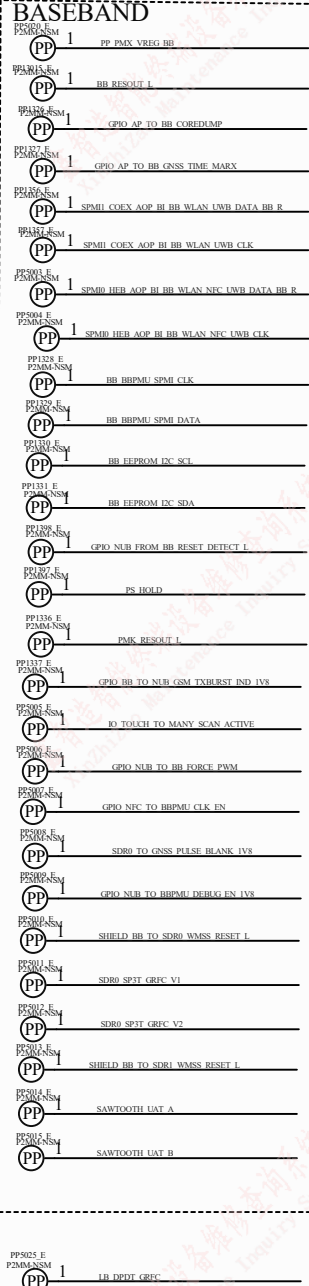
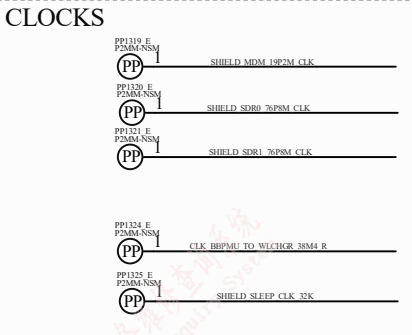
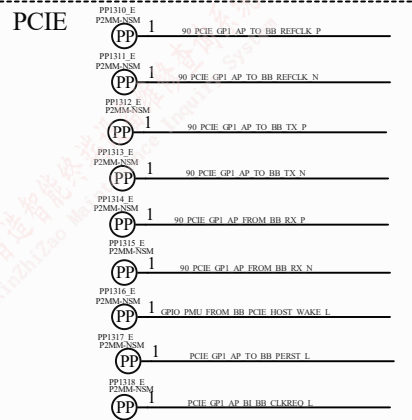
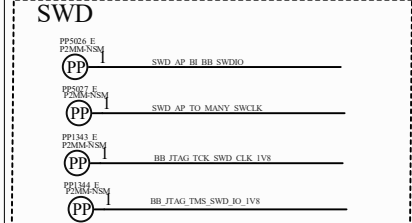
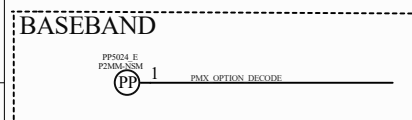
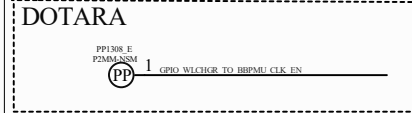
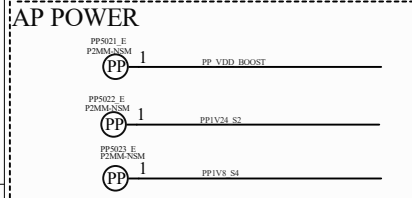
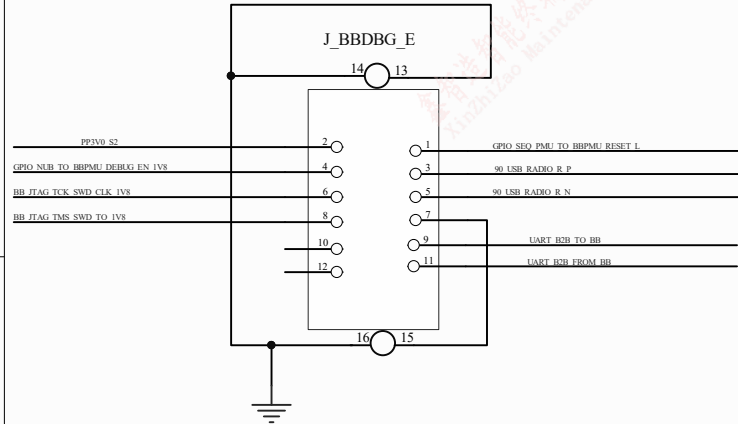
B

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Title		
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A

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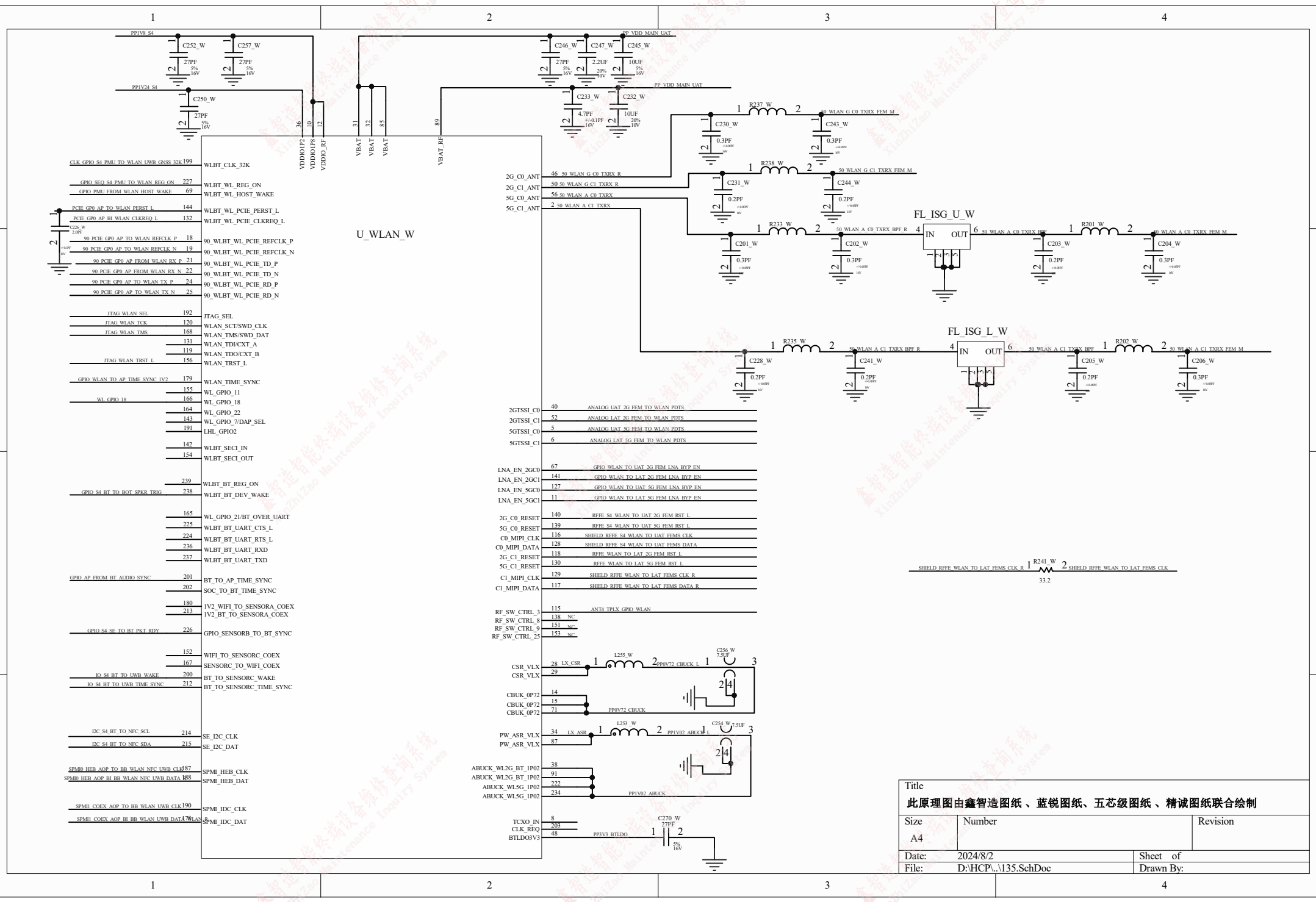
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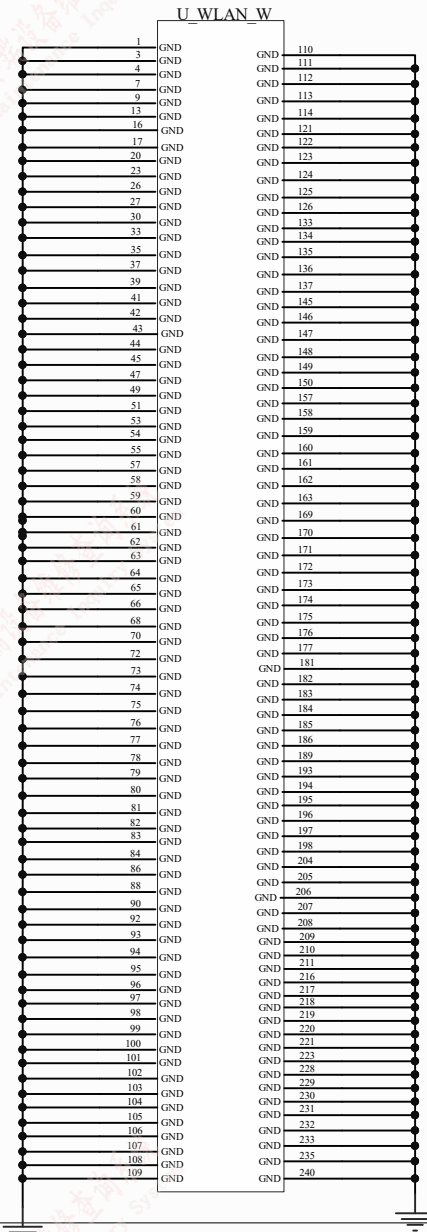
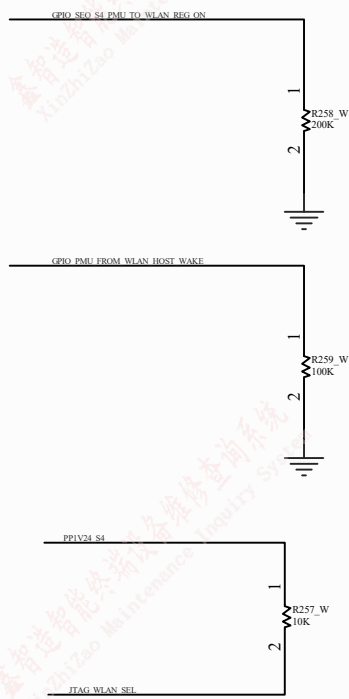
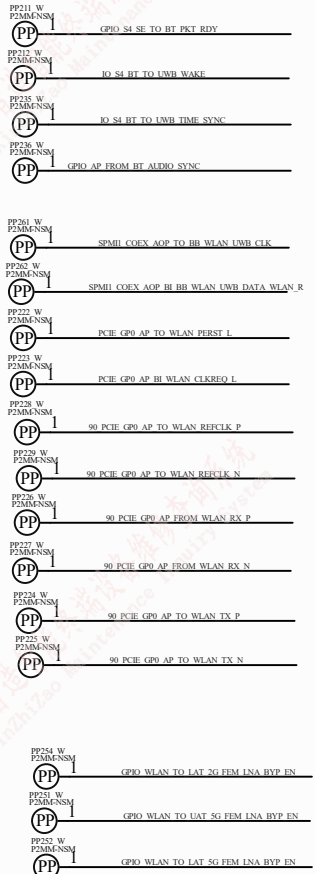
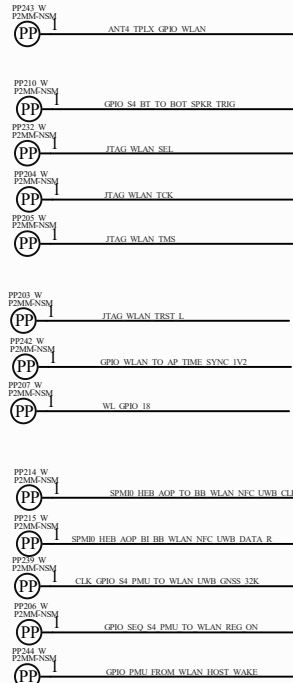
B

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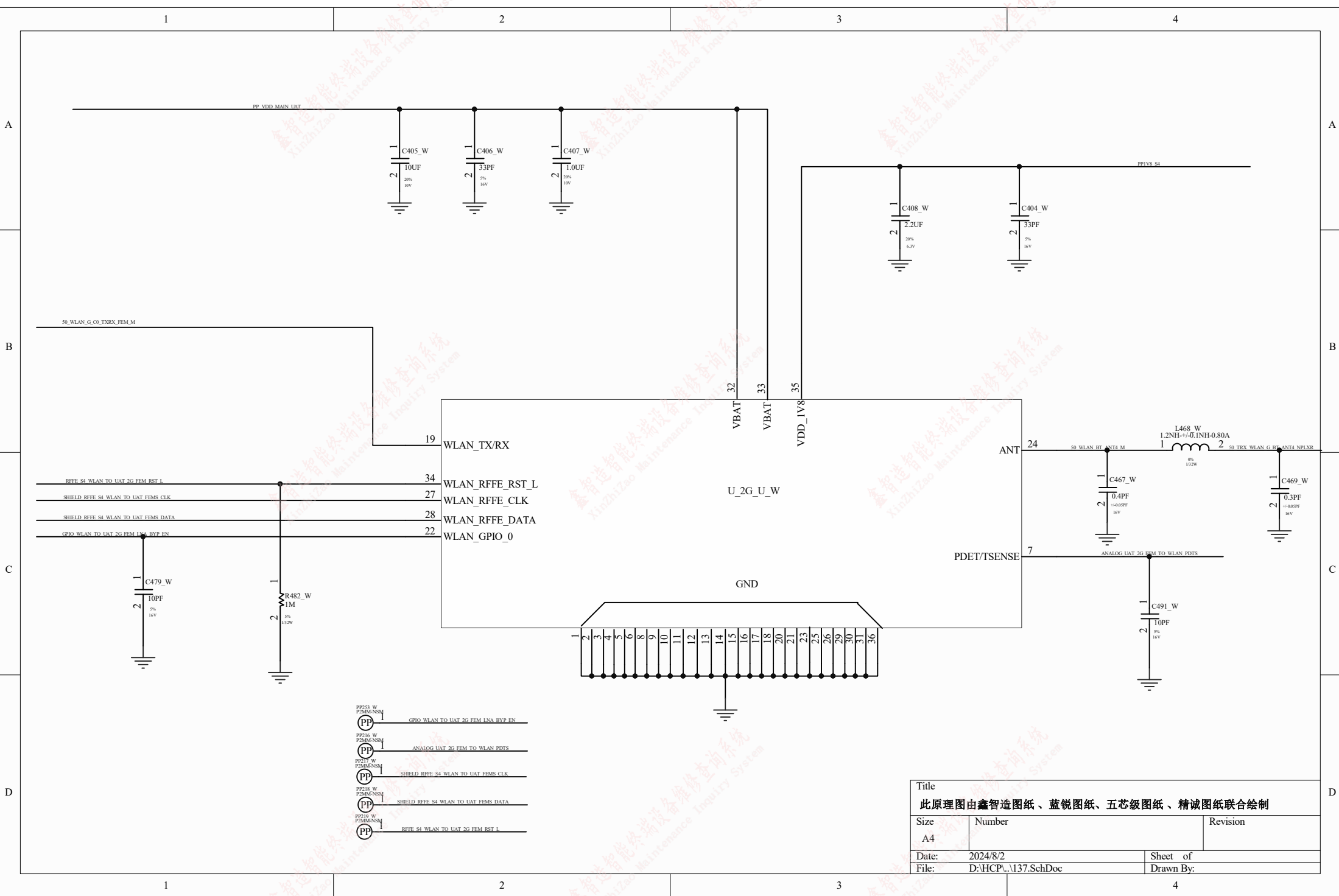
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Title		
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File:	D:\HCP\135.SchDoc	Drawn By:



Title		
此原理图由鑫智造图纸、蓝锐图纸、五芯级图纸、精诚图纸联合绘制		
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Date: 2024/8/2	Sheet of	
File: D:\HCP\...\136.SchDoc	Drawn By:	



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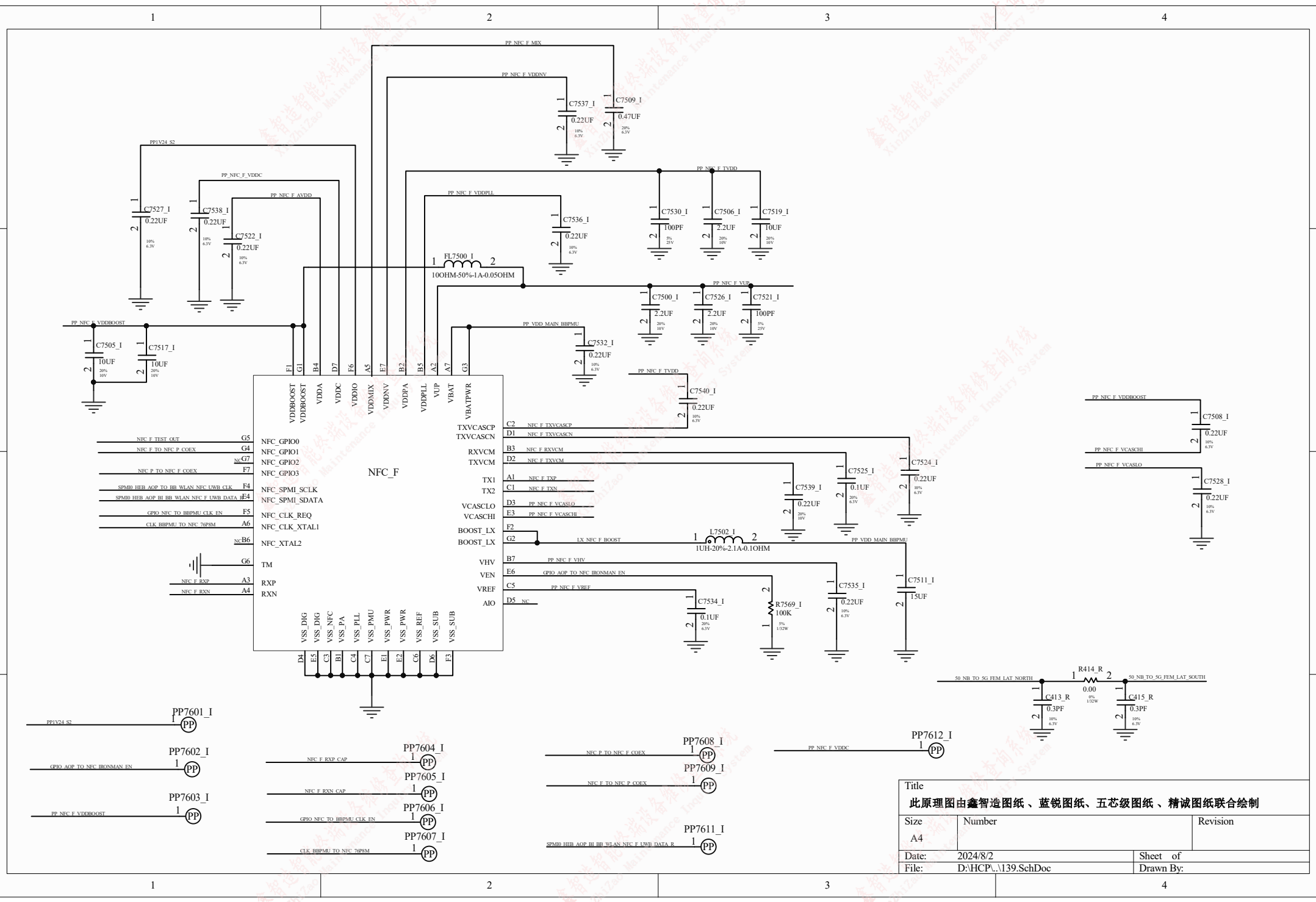
D

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Title		
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File:	D:\HCP\1139.SchDoc	Drawn By:

A

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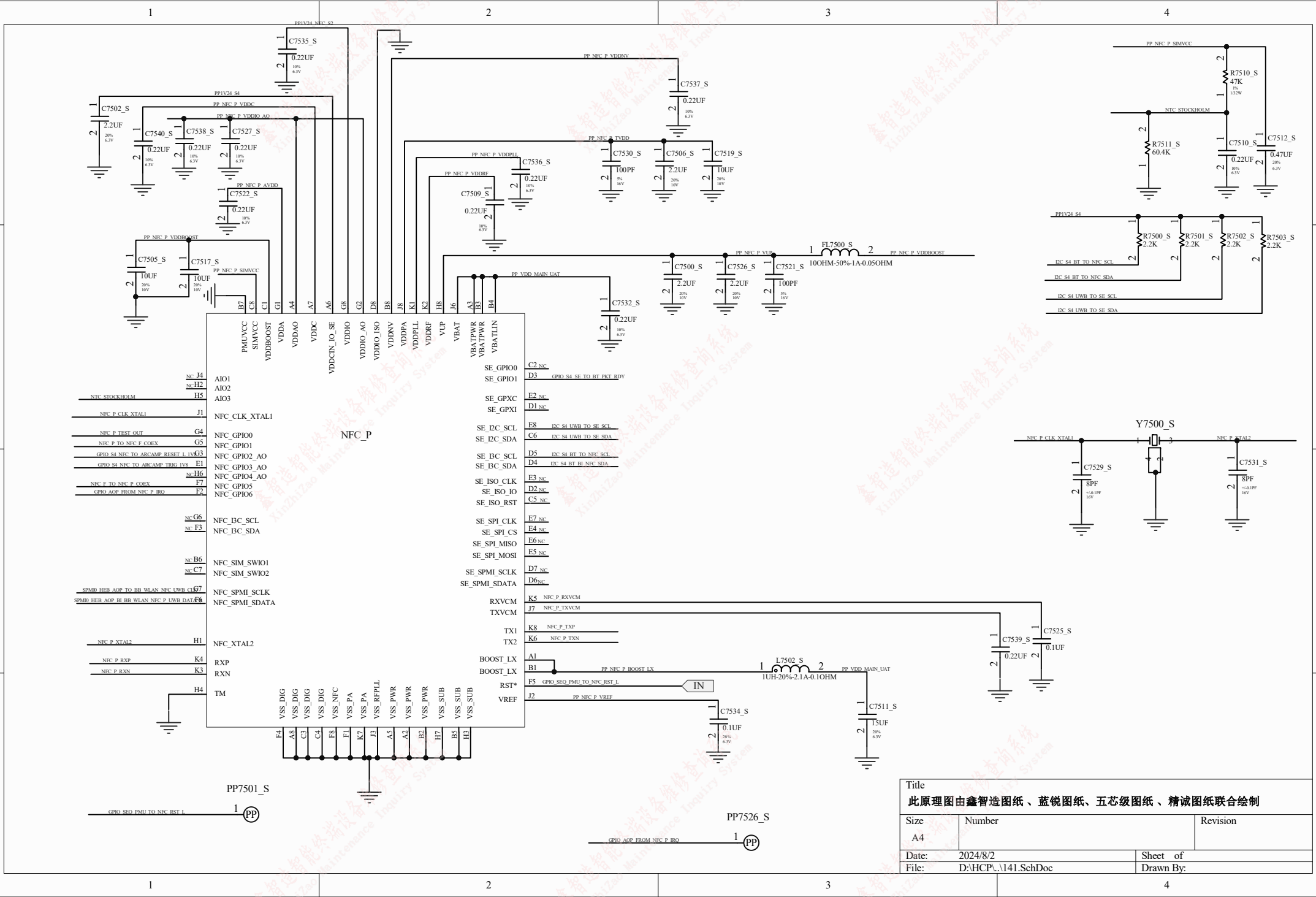
D

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Title		
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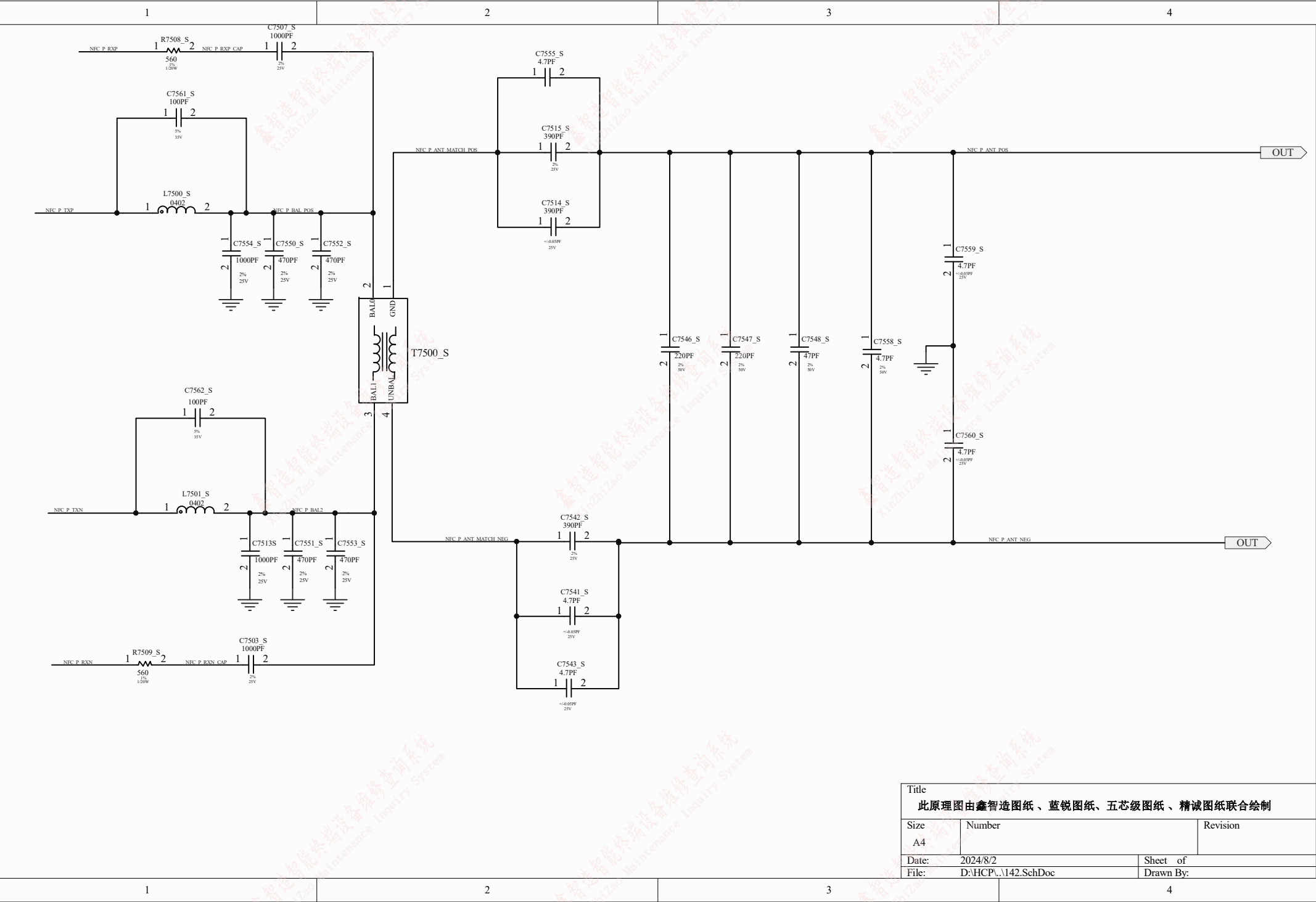
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Title		
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