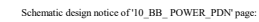


V0.2
1、增加HWD的电阻，R4706，R4707
2、增加R4923，默认NC

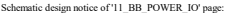
0801
1、更换esim为ST33J2M0

0805
1、交换spk P和N的线序

DVT1
1、增加QC3+，删除指纹预留1.8VLD0
2、删除闪光灯
3、增加C3480,C3481。
4、预留C6580
5、增加R1211
6、删除CC logic预留的并位电容（CC,IIC）
7、删除摄像头STC预留
8、增加VBUS电压采样
9、闪光灯增加二供



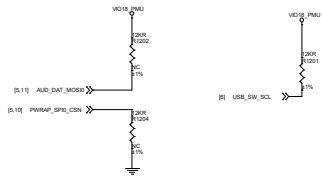
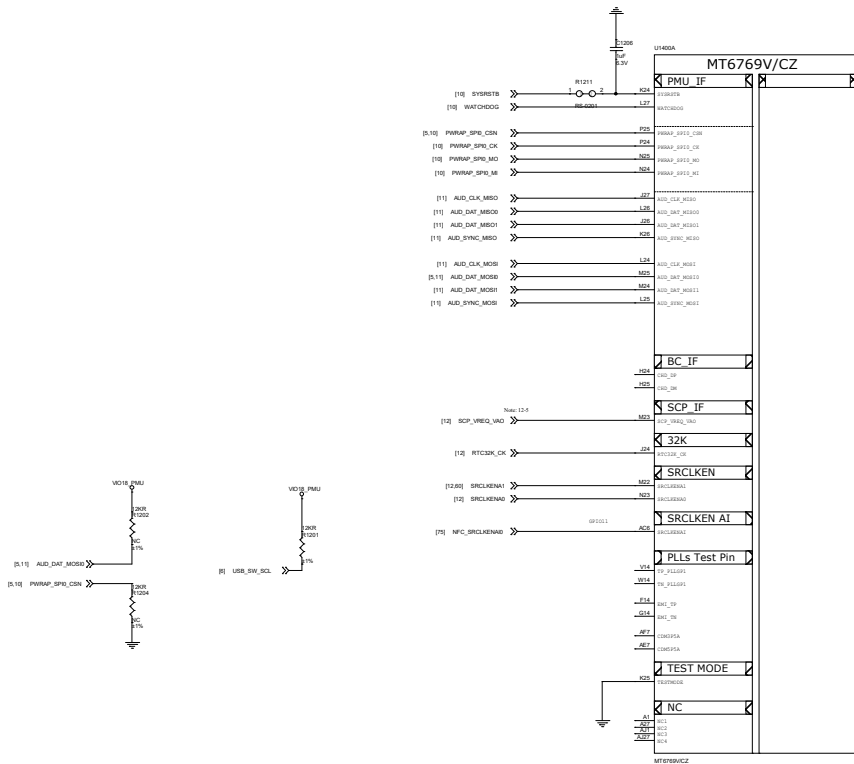
Note 10-1:	<p>must be close to HIFI's ball</p> <p>Differential pair of DVDD, GPU remote sense</p> <p>Remote sense trace with GND shielding to PMIC (Differential)</p>
Note 10-2:	<p>Remote sense trace with GND shielding to PMIC (Differential)</p> <p>Differential pair of DVDD, PROC remote sense</p> <p>must be close to HIFI's ball</p>
Note 10-3:	<p>Differential pair of DVDD, MODIEM remote sense must be close to HIFI's ball with GND shielding to PMIC (Differential)</p>
Note 10-4:	<p>Differential pair of DVDD, CORE remote sense must be close to HIFI's ball with GND shielding to PMIC (Differential)</p>



Note 11-1: C1111 closed DVDD18_MSDC0 150mV

Note 11-2: C1112 closed DVDD18 MSDC1 150mV

C1113 closed DVDD28_MSDC1 150mi



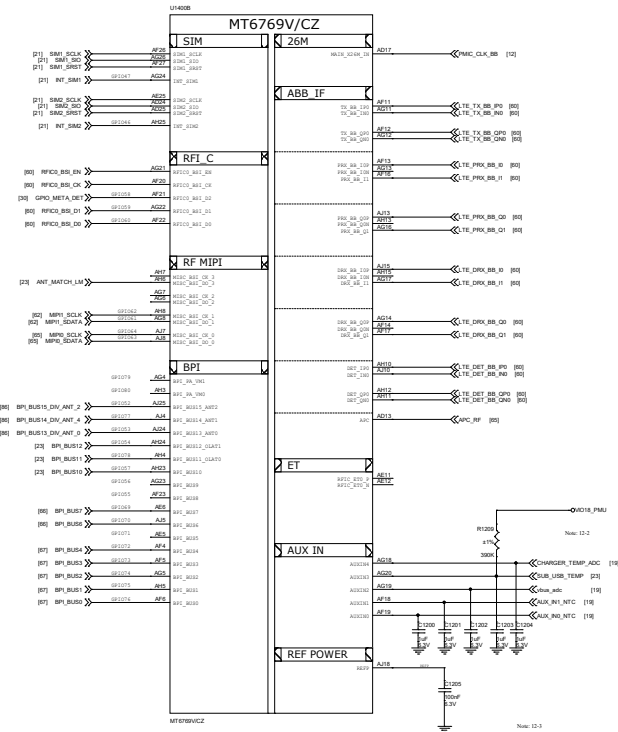
Schematic design notice of '12_BB_1' page:

Note 12-1: PWRAP_SPD_CSN and AUD_DAT_MOSIB are bootstrap pins to select which interface will be the JTAG pin out

PWRAP_SPD_CSN	AUD_DAT_MOSIB	JTAG Function	MD_JTAG
default	default	N/A	N/A
HI	LO	N/A	N/A
HI	HI	SPD + ENTS	SPD + SPD
LO	LO	SPD + ENTS	N/A
LO	HI	N/A	N/A

Note 12-2: To shut a half capacitor in the AUXIN ADC input to prevent noise coupling. It should be placed as close to BB as possible. Connect the unused AUX ADC input to GND.

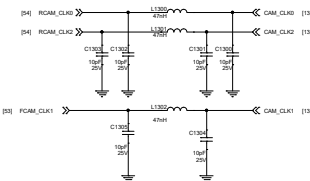
Note 12-3: The de-coupling cap. for REFP (A118 ball) have to be placed as close to BB as possible.

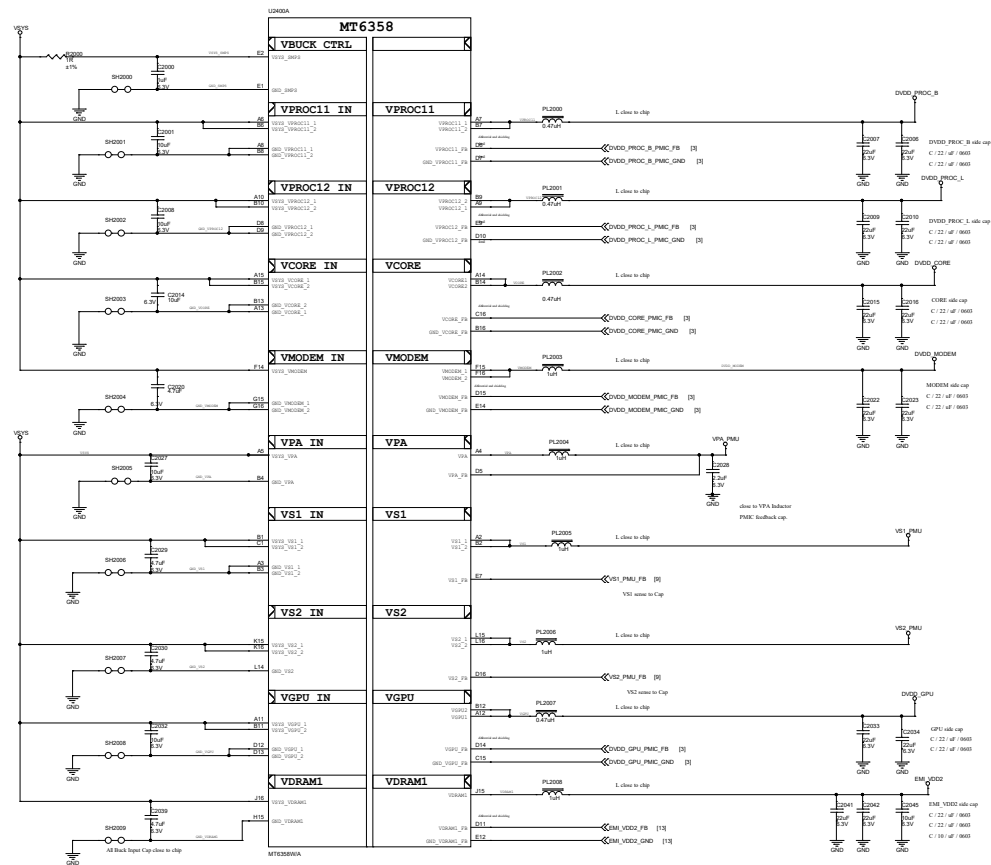


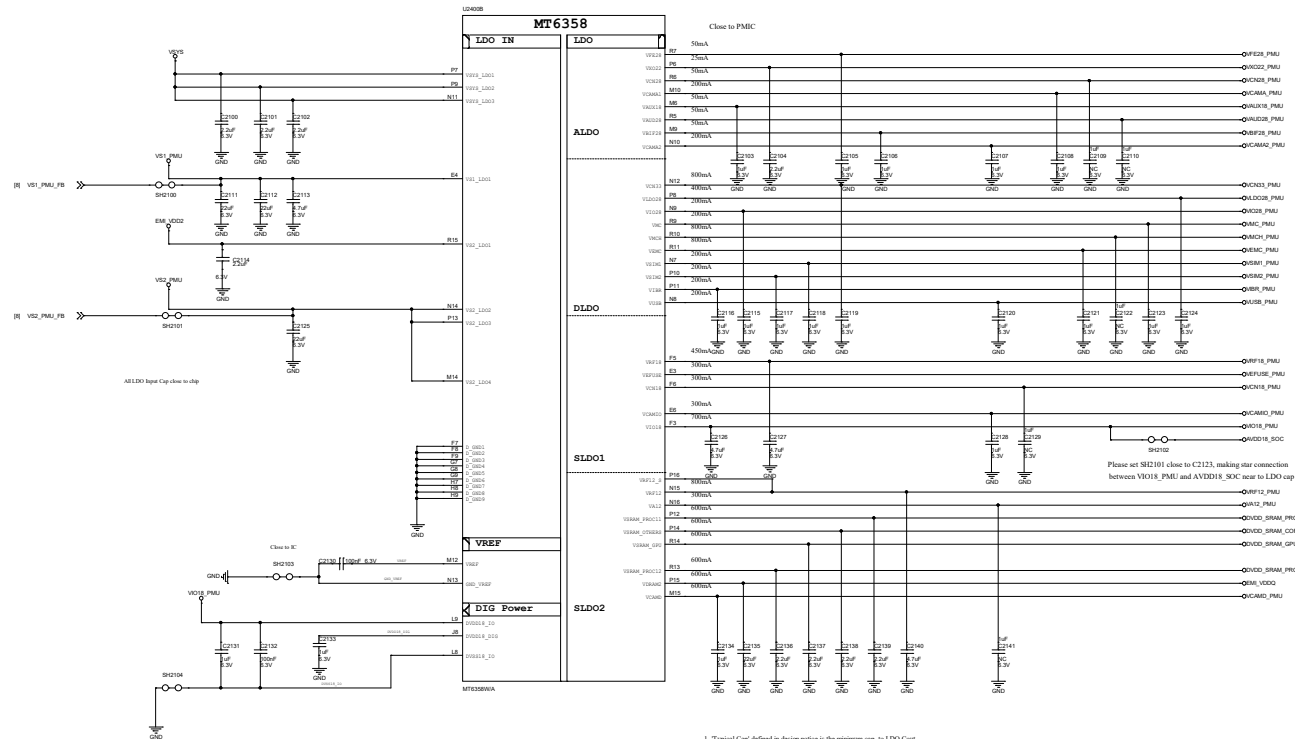
Note 12-4: HW pin for DOR type feature in bootstrap (refer to BB HW design Notice)

AUD_SYNC_MOSIB	AUD_CLK_MOSIB	CAM_PDN3	PMIC 635B voltage	DOR Type
default	default	default	VDDPM1 / VDDPM2 / 1.125V / 0.6V	DOR
No ext. pull	No ext. pull	No ext. pull	0V / 1.8V	LP4X
No ext. pull	12K pull to VDD18	No ext. pull	1.225V / 0.6V	LP4X
12K pull to VDD18	12K pull to VDD18	No ext. pull	1.125V / 1.8V	Reserved
No ext. pull	No ext. pull	12K pull to VDD18	1.125V / 0.6V	Discrete LP4X

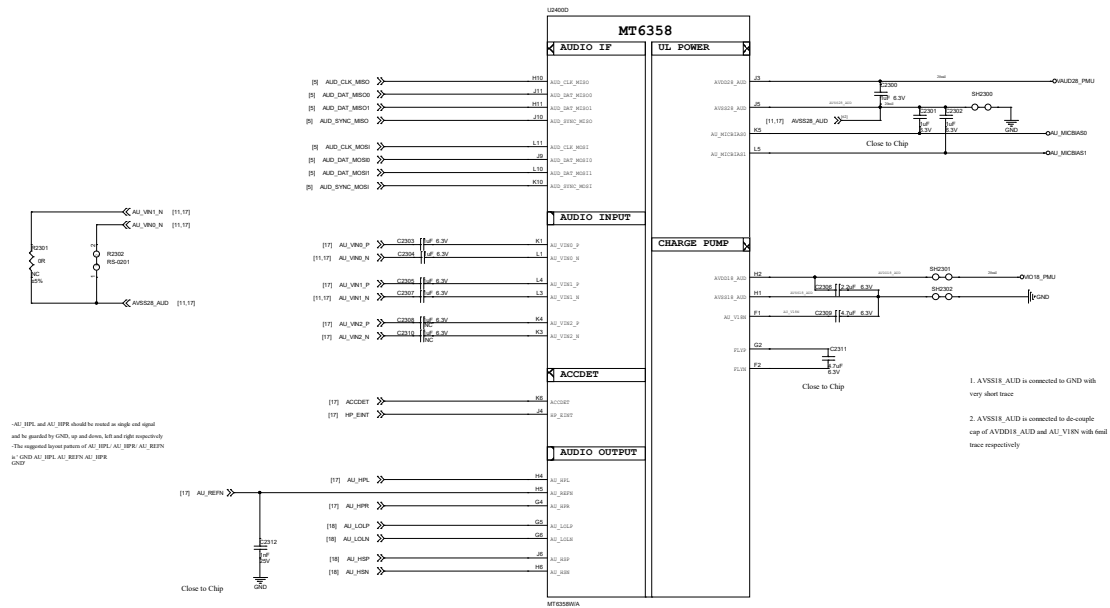
Note 12-5: Charger must have D+ D- pin for charger type USB detection. Charger must have at least 500mA USB current for All charger type.





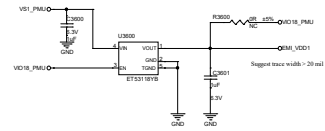


1. Typical Cap¹ defined in design notice is the minimum cap. to LDO Cost.
2. NC cap can move to application, if (PCB L<20mm, PCB R<0.2 ohm)

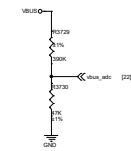
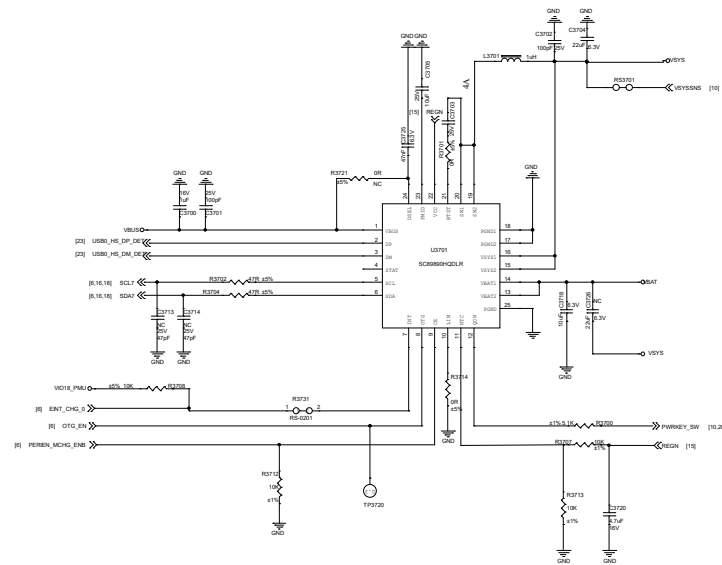




Ext. EMI_VDD1 for LPDDR4X VDD1

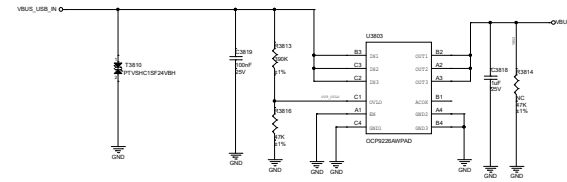


CHARGE_15W

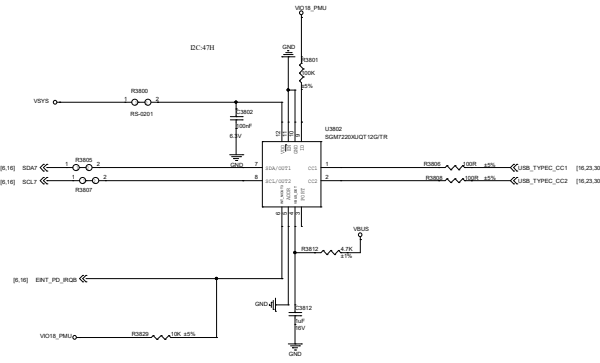


OVP

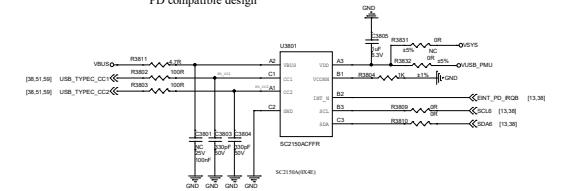
硬件门限8.96V,软件门限10.9V



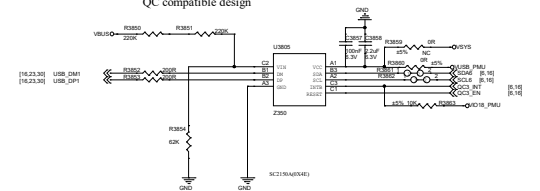
OVP	R3813	R3816	EQUATION
10.43V	300K	39K	$(300K+39K)/39K*1.2V$
11.16V	390K	47K	$(390K+47K)/47K*1.2V$
8.96V	330K	51K	$(330K+51K)/51K*1.2V$



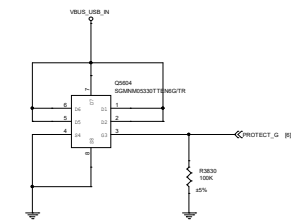
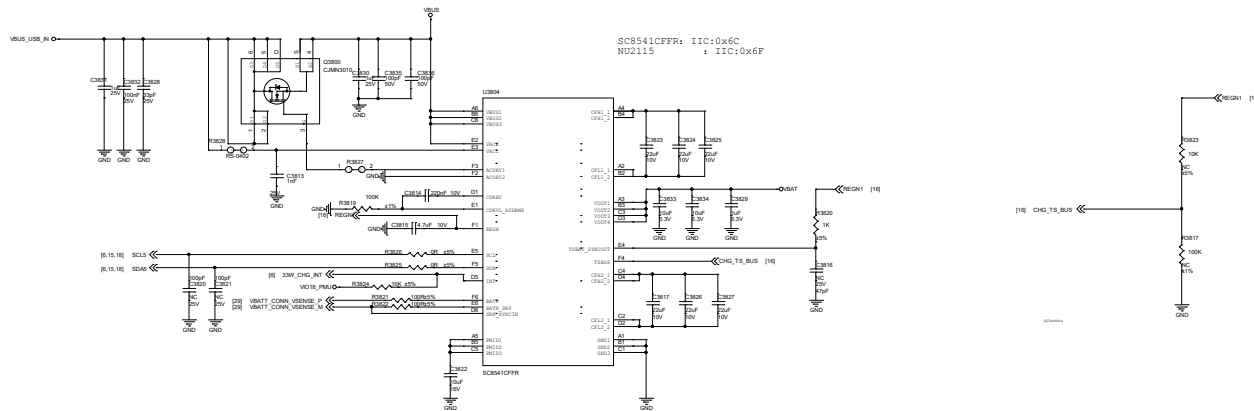
PD compatible design



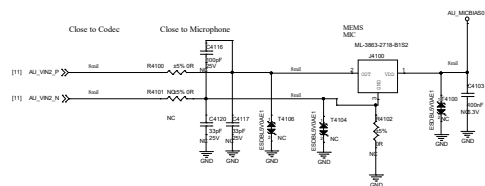
QC compatible design



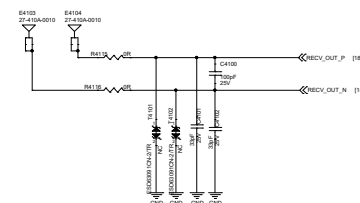
33W Charger



Sub Mic



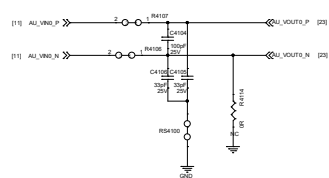
Handset Receiver



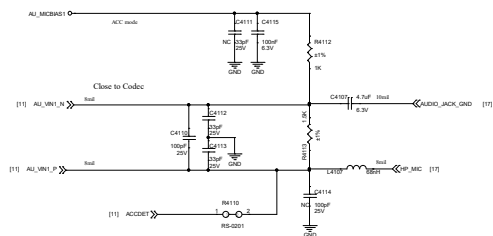
Main Mic

Main Mic

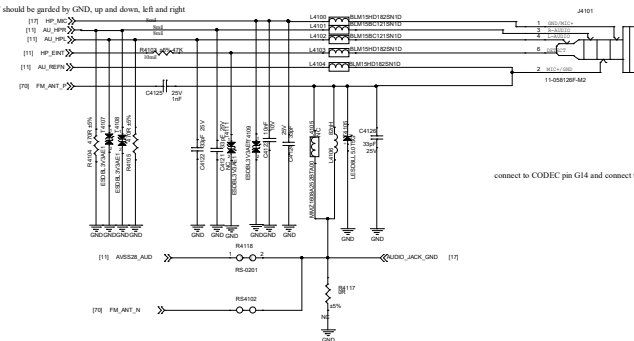
CLOSE TO
CODEC



Headset Mic

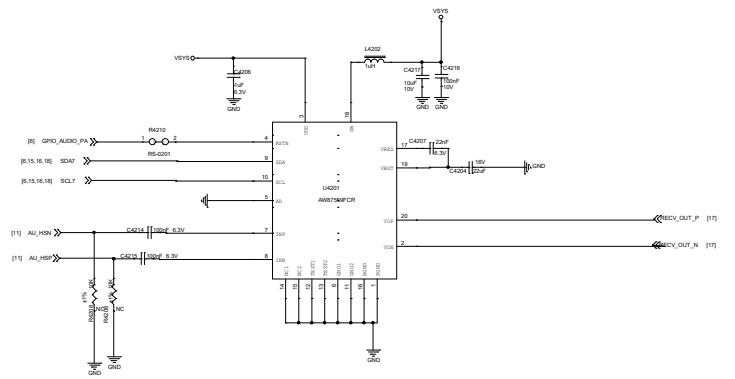
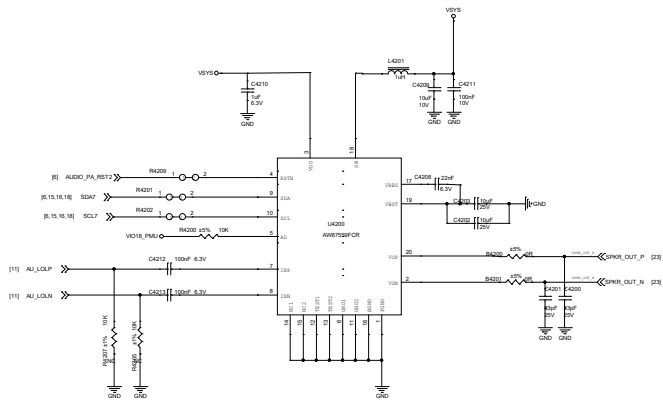


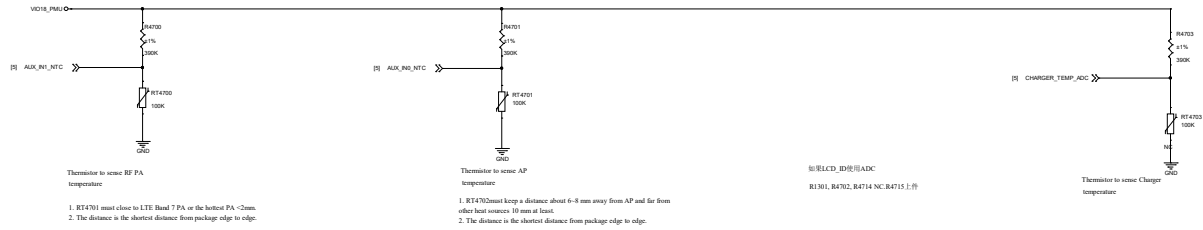
HP_EINT should be guarded by GND, up and down, left and right



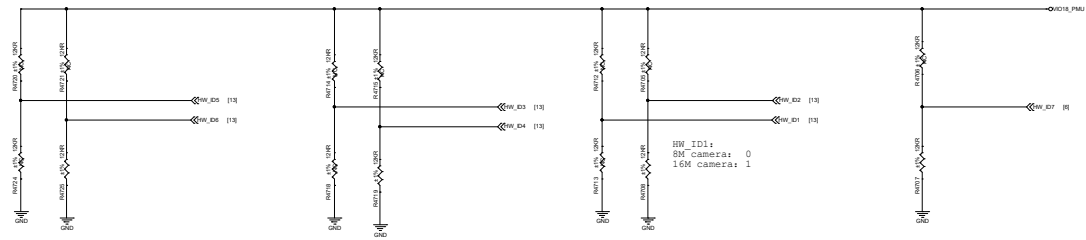
connect to CODEC pin G14 and connect to GND at CODEC

COBY-P352CE

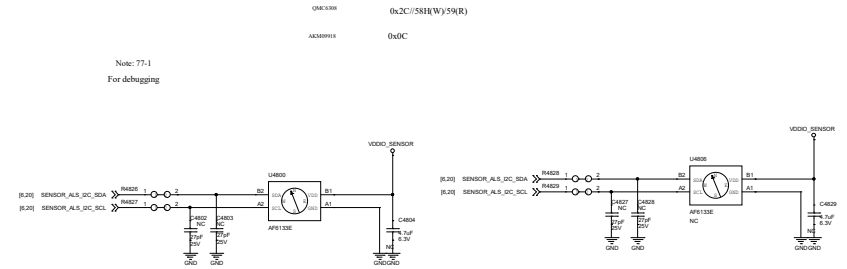
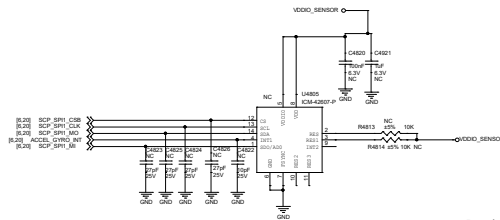




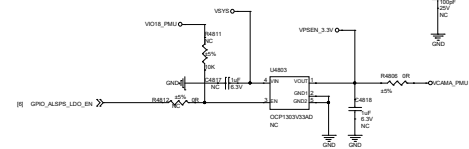
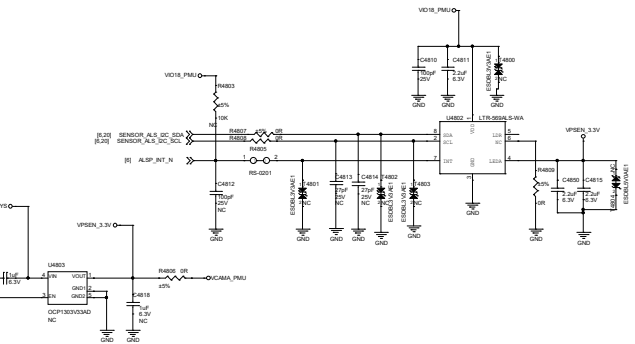
HW ID



Optional E-Compass

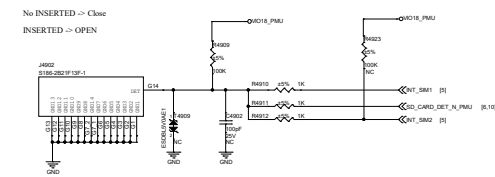
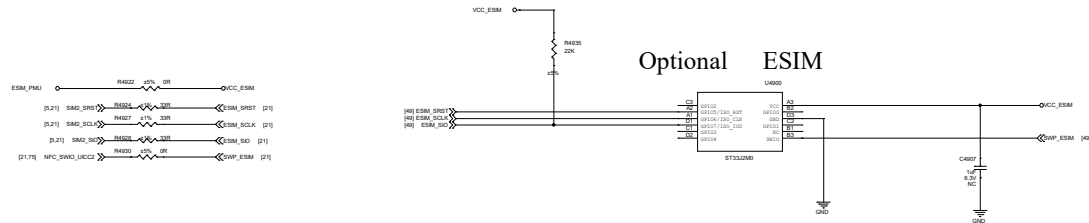
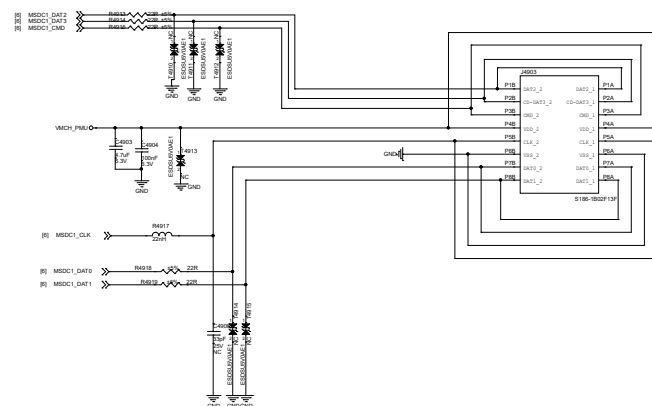
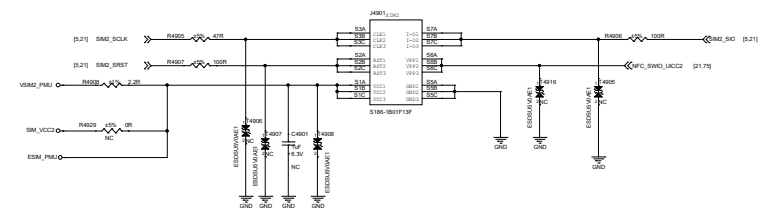
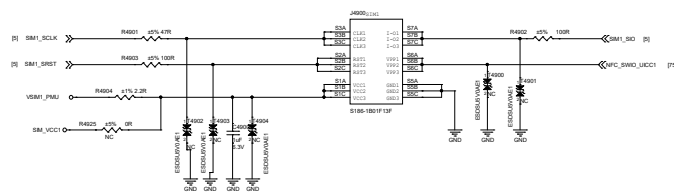


Proximity Sensor charge

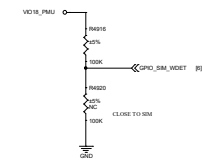


Bare(BMP380) I2C address : 76 (SDO pin tied to GND)

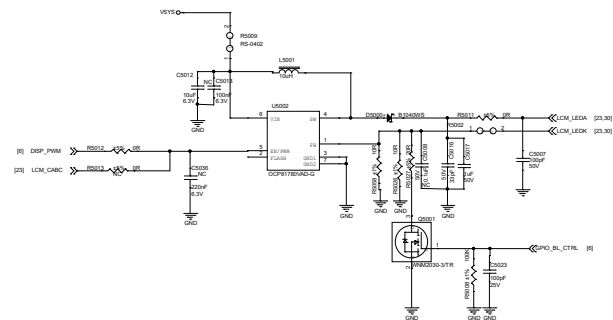
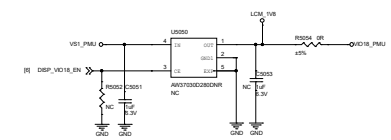
Note 77-7: Interrupt pin of MEMS sensor must be assign to ball EINT[12:0]



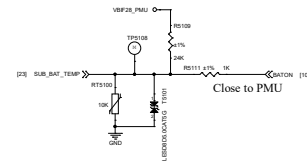
Normal:0
Insert:1



Optional ESIM



替换60PIN连接器, 新物料, NEW_2024360603



The schematic diagram shows the ETSS019B15 component connected to various inputs and outputs. The input signals are VSIS_PMA_0 and VSIS_0. The output signal is QCM_5SM_DVDD_1P05. The circuit includes two capacitors, C001 and C008, both rated at 1.0µF and 6.3V. The ground connections are labeled GND.

(AVDD) LDO for Rear Camera 2.8V

U5024

V5VS0

4

3

1

2

5

REAR_VCAMA_AVDD

EN

1.5V

10µF

0.1µF

NC

1

2

3

4

5

6

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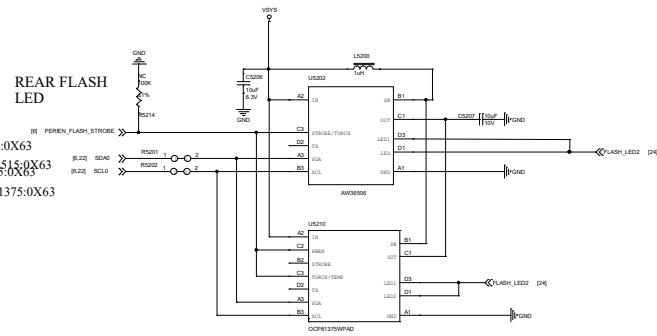
435

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437

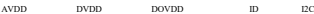
(AVDD) LDO for Front Camera 2.8V

AW36515:0X63
OCP81375:0X63
OCP81375:0X63



闪光灯放在闪光灯小板上

****前/后摄像头同时工作****



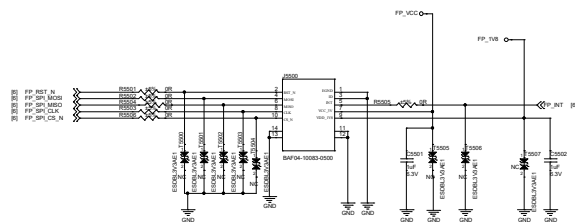
12C

50M/13M



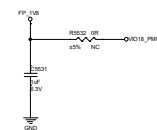
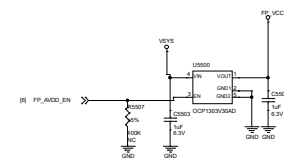
Optional

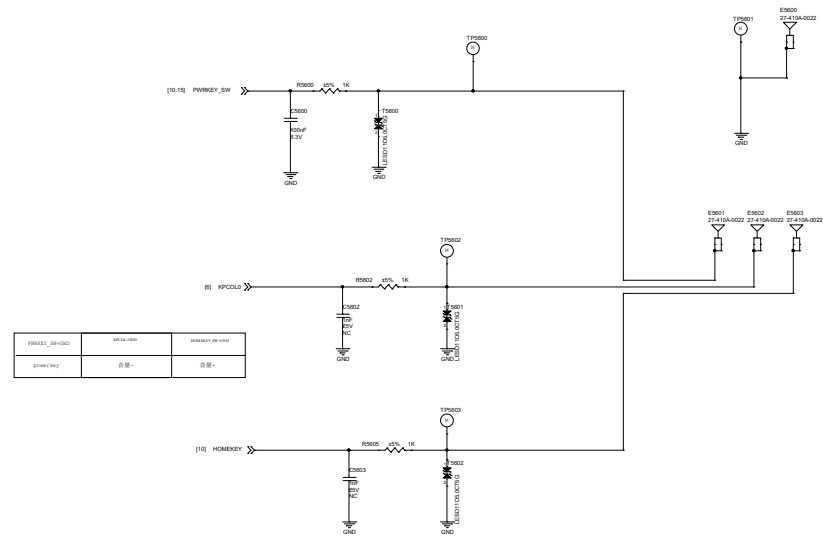




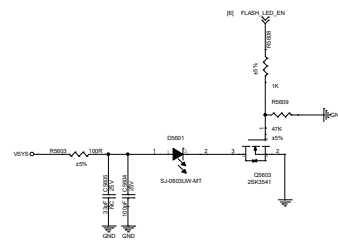
Fingerprint Connector

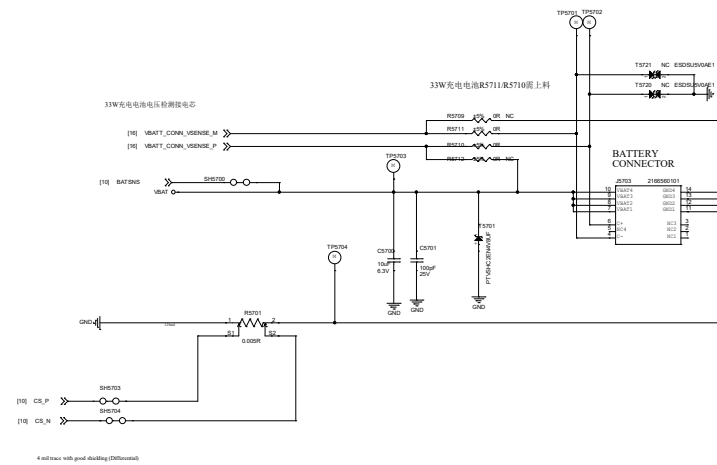
- 1 GND
- 2 RST
- 3 ID(GND)
- 4 MOSI
- 5 INT
- 6 MISO
- 7 VDD2V8
- 8 CLK
- 9 DOVDD1V8
- 10 CS
- 11 GND
- 12 GND
- 13 GND
- 14 GND



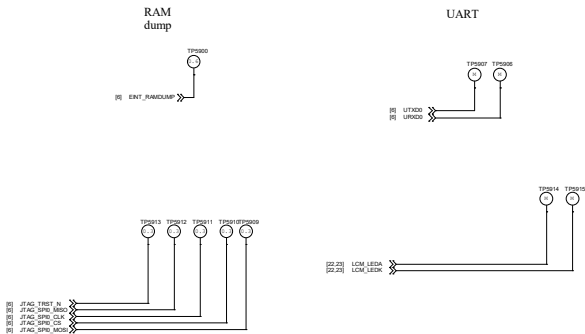


Charge Indicator

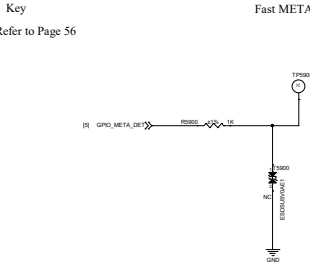
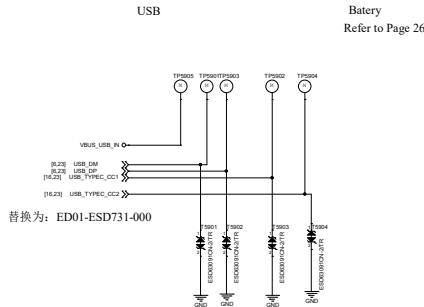




Debug Test Point



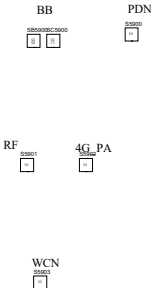
Fixture Test Point



Mark



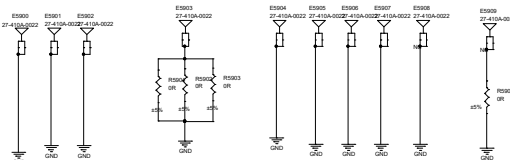
shielding can



Metal Grounding

Top

Bottom





D

D

C

C

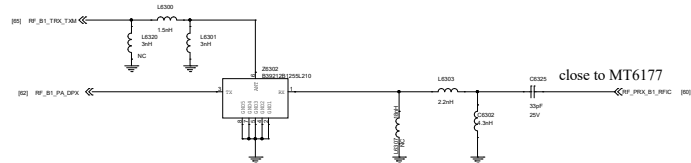
B

B

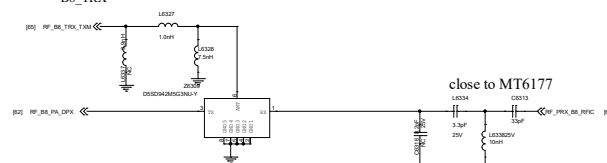
A

A

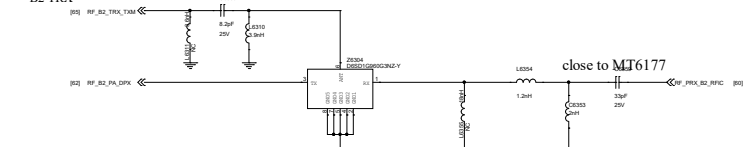
B1 TRX



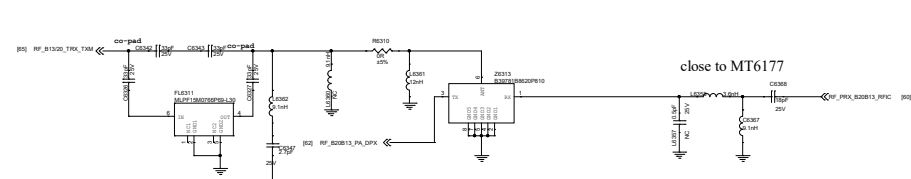
B8_TRX



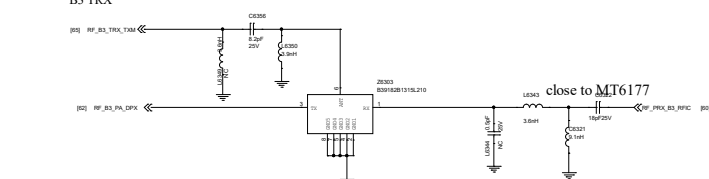
B2 TRX



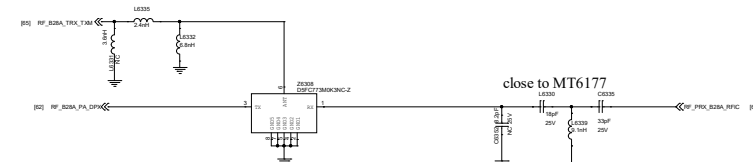
SKU1/2:B13 TRX.SKU3/4/5:B20 TRX



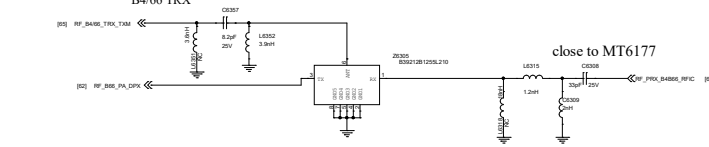
B3 TRX



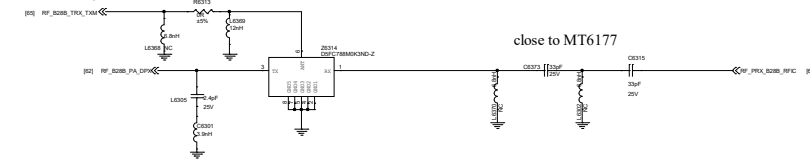
B28A TRX



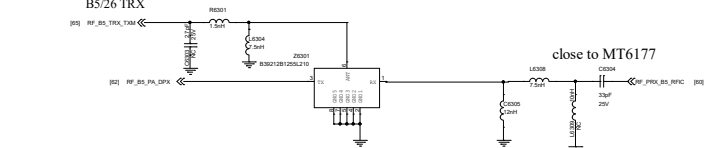
B4/66 TRX



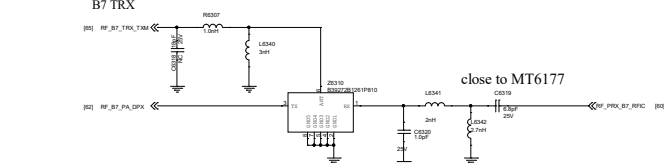
B28B TRX



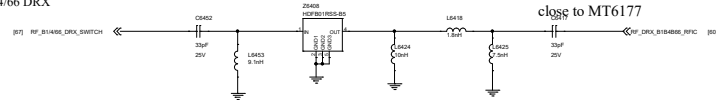
B5/26 TRX



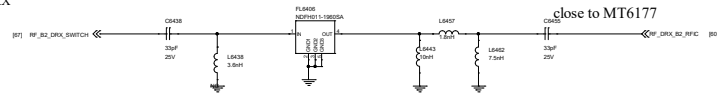
B7 TRX



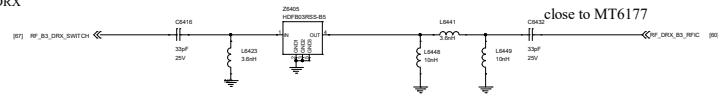
B1/4/66 DRX



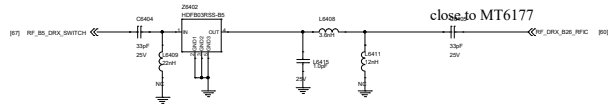
B2 DRX



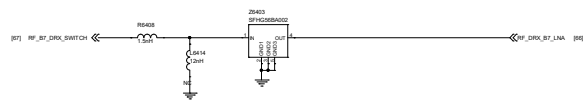
B3 DRX



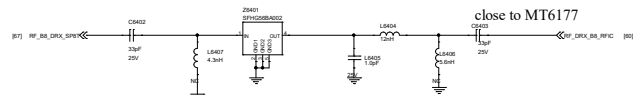
B5 DRX



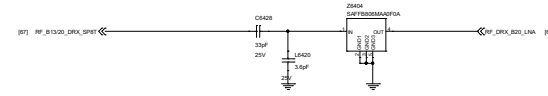
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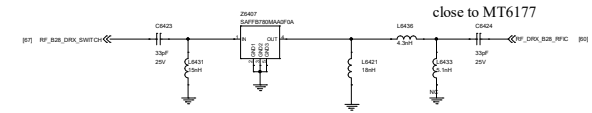
B8 DRX



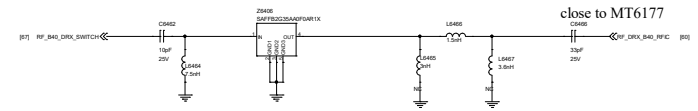
SKU1:B13 TRX,SKU2/3:B20 TRX



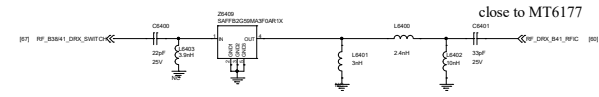
B28 DRX



B40 DRX

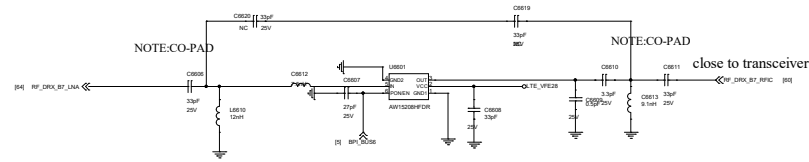


B41 DRX



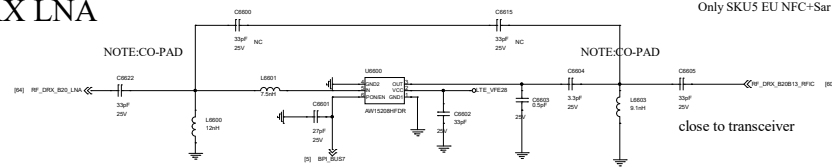
B7 DRX LNA

Only SKU5 EU NFC+Sar 带B41 DRX_LNA



B20_DRX LNA

Only SKU5 EU NFC+Sar 带B41 DRX_LNA



D

D

C

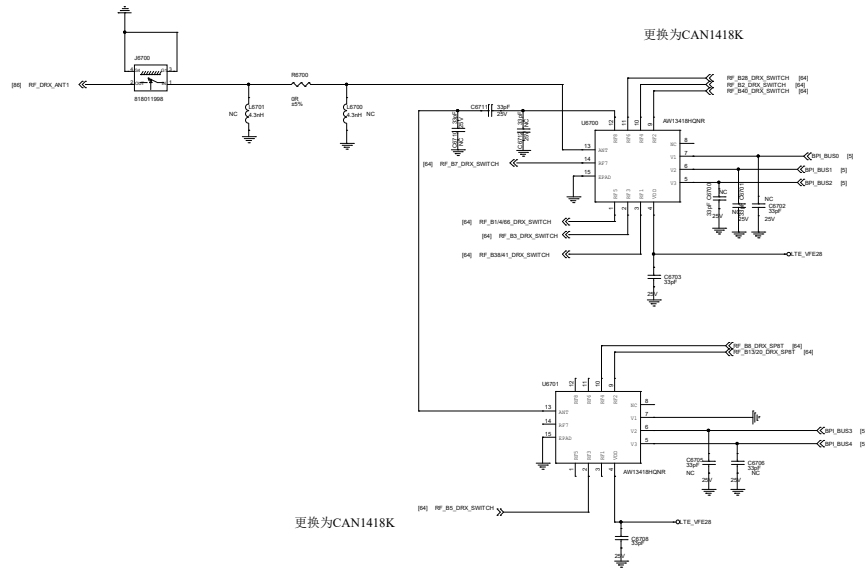
C

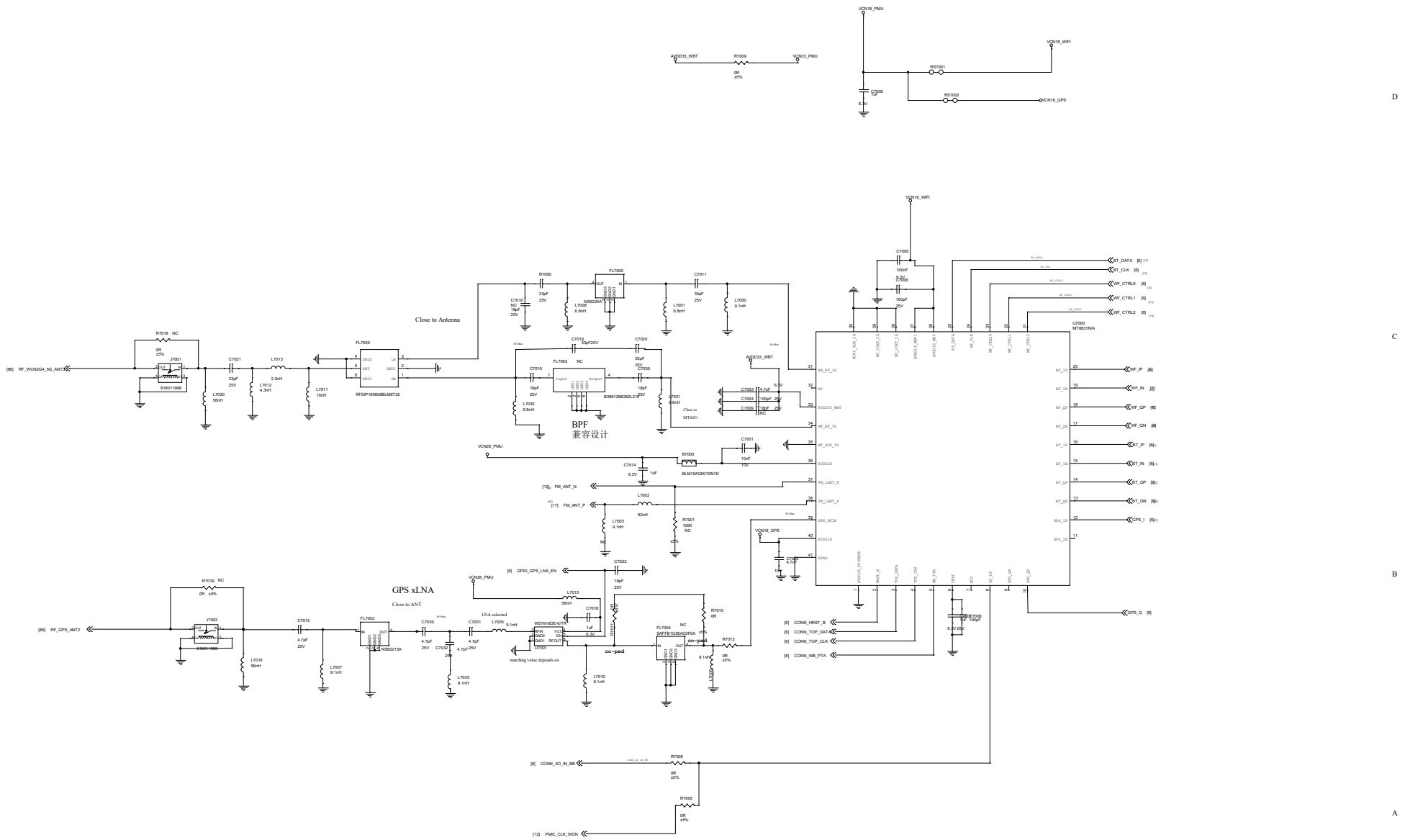
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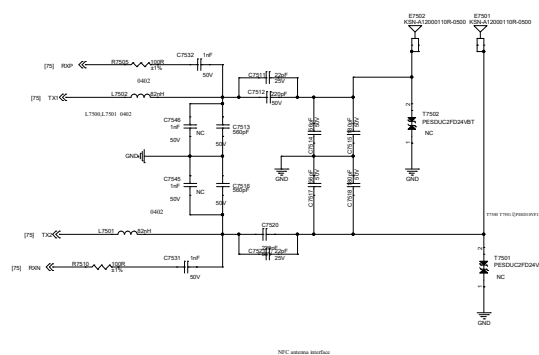
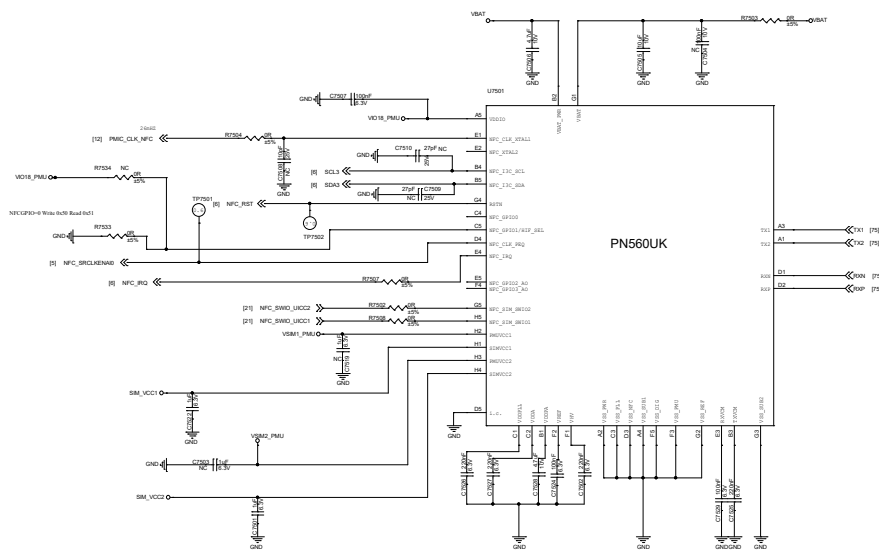
B

A

A



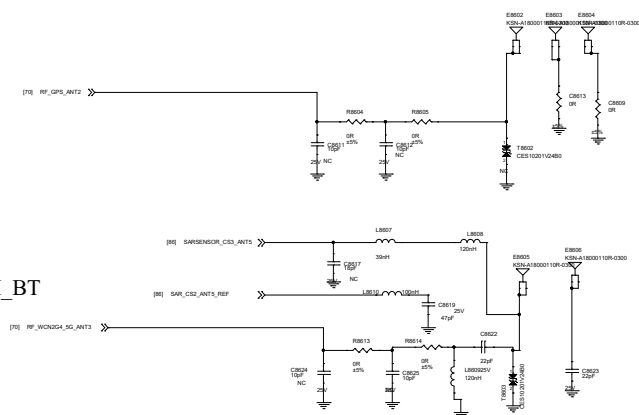




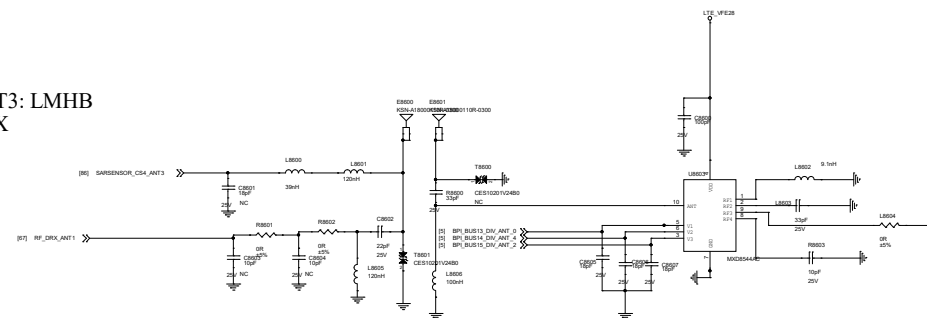
C

A

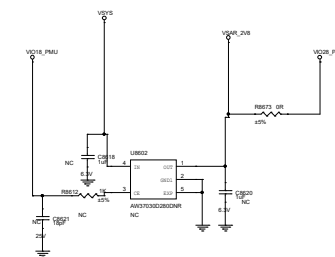
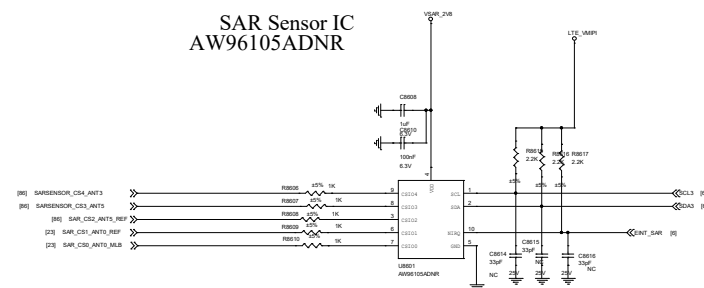
ANT5: WCN_2.4G/5GWIFI_BT



ANT3: LMHB
DRX



SAR Sensor IC
AW96105ADNR



2.8V LDO: RP114K281D