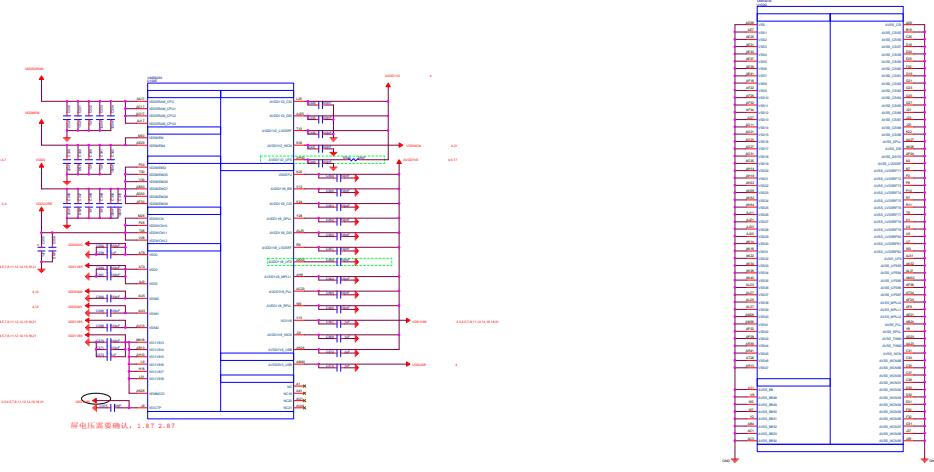
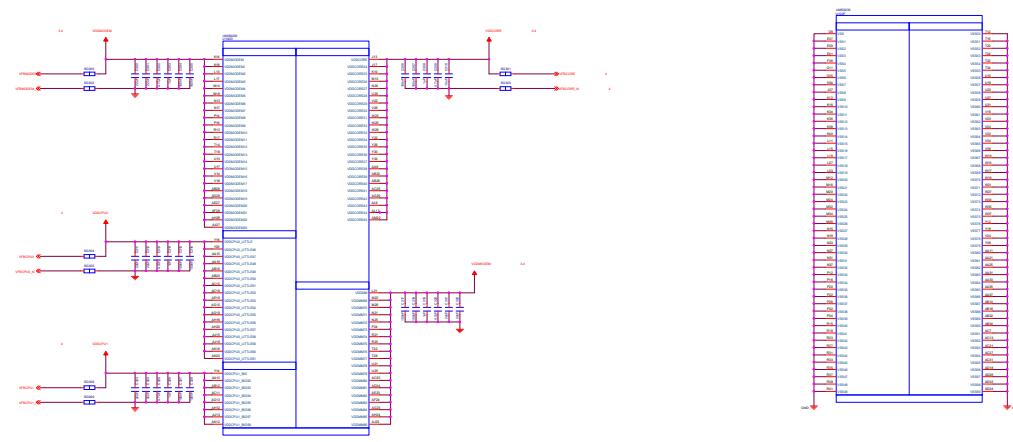


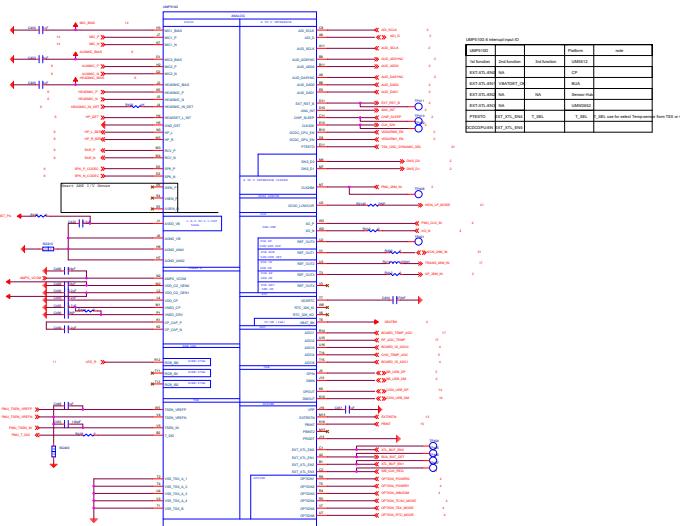
Ball name	Signal name in boot code	Value	Description	Default value
UI_XD	USB_LOAD_EN	5	LART download/no time out	0(1=1,0=0)
AUD_DAD3	AMH_BOOT_HD2	08	Reserved	
AUD_DAD8	AMH_BOOT_HD3	19	SD boot	1(1=1,0=0) (MSB-HD1,LSB-HD0)
AB1_SOK	AMH_BOOT_HD1	11	eMMC 5.1(B)	
AB1_LD	AMH_BOOT_HD2	0	Reserved	0(1=1,0=0)
CHG_TYPE	AMH_BOOT_HD4	0	NON KEY Download/With Schemed mode	0(1=1,0=0)
		0	Other(DCV/other)	0(1=1,0=0) (0x00000000,0x00000000,0x00000000,0x00000000)
		1	USB Insert/SDR(CDF)	0(1=1,0=0) (0x00000000,0x00000000,0x00000000,0x00000000)



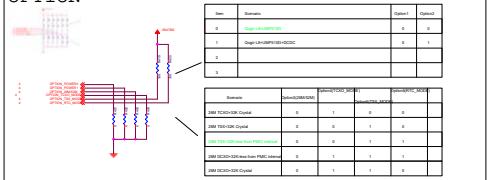


屏电压需要确认, 1.87 2.87

PMU_ANALOG

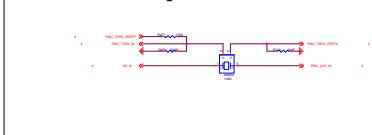


OPTION

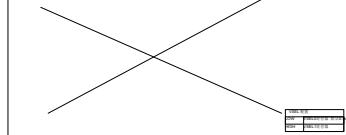


BKBT

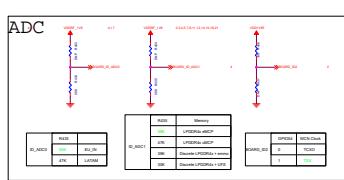
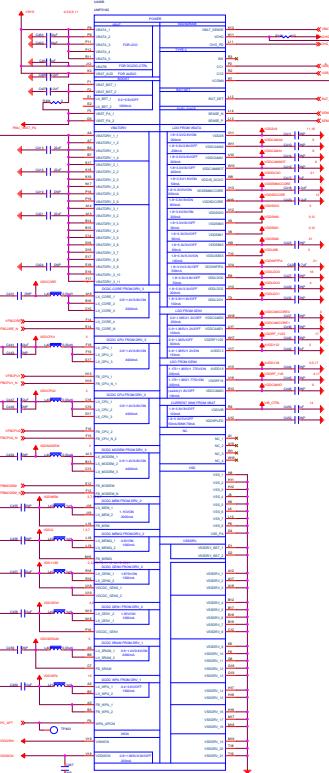
TSX (PMU) Only



EXT DCDC CPU1



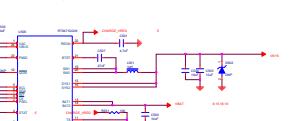
PMU POWER



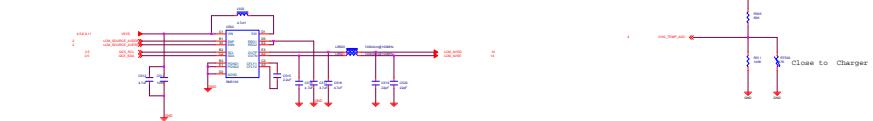
LCM Backlight LED driver



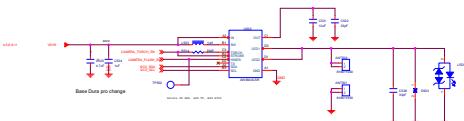
Charger



LCD Bias



Flash LED 5V Boost



Schematic design notice of "27_POWER_SubPMIC-HV powers" page:

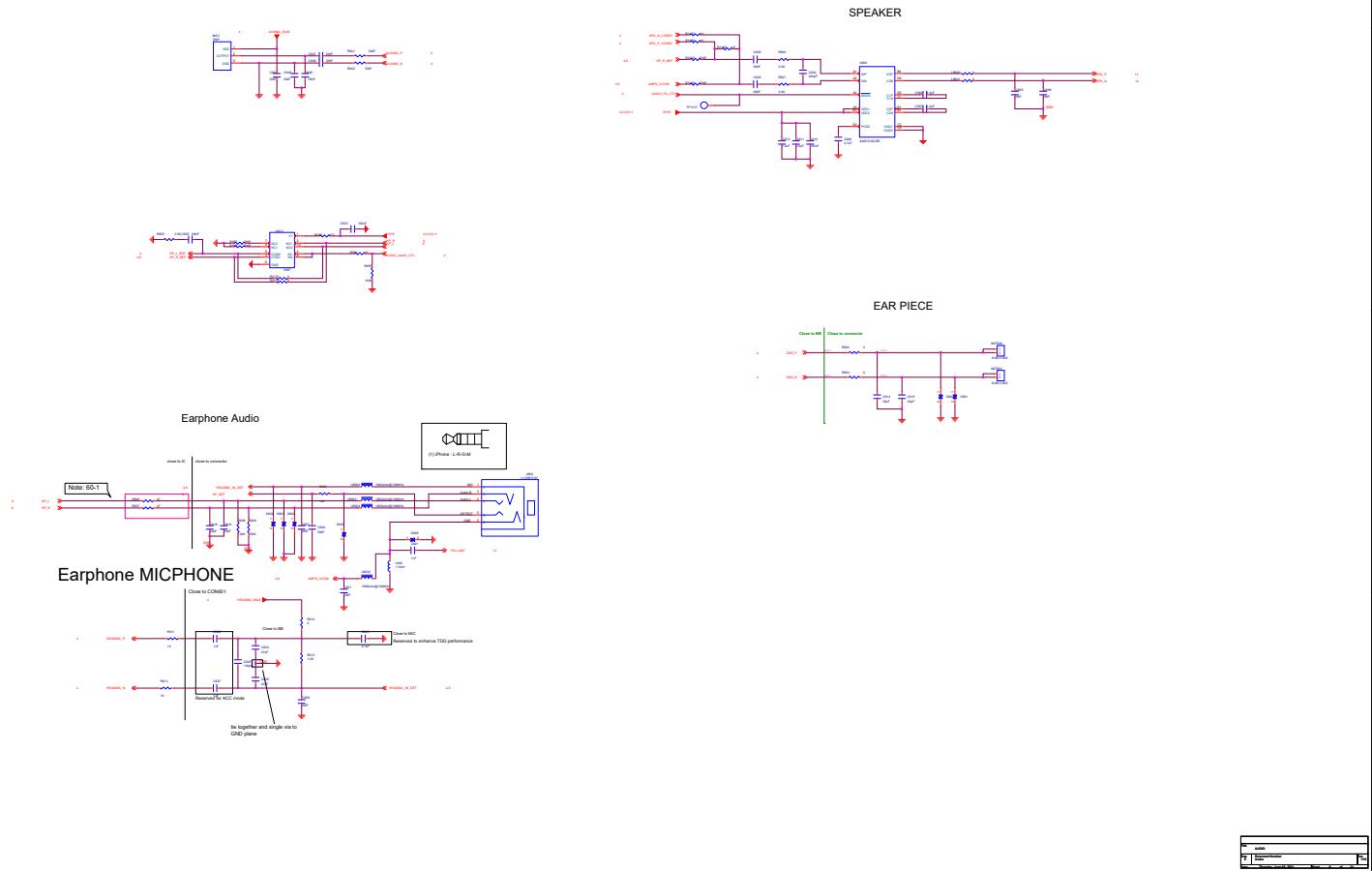
Note 27-1: To minimize RF de-sense, it is recommended to reserve 0-ohm and 0402 cap for BOM fine tuning.

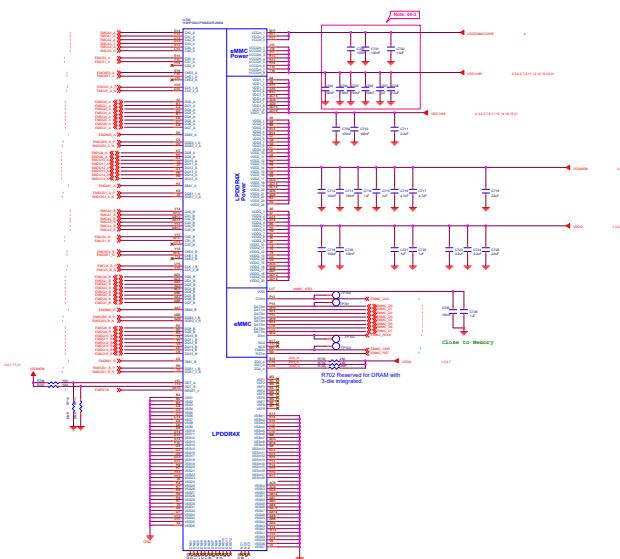
Note 27-2: To minimize RF de-sense, it is recommended to reserve 0-ohm and 0201 cap, for BOM fine tuning.

Note 27-3: C2705 could be replaced with C 1 / uF / 50V + C 1 / uF / 50V

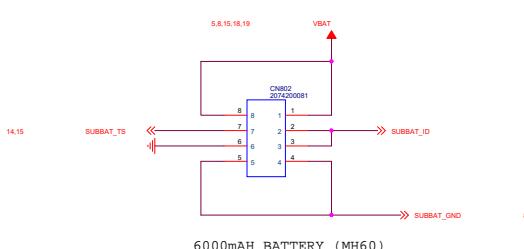
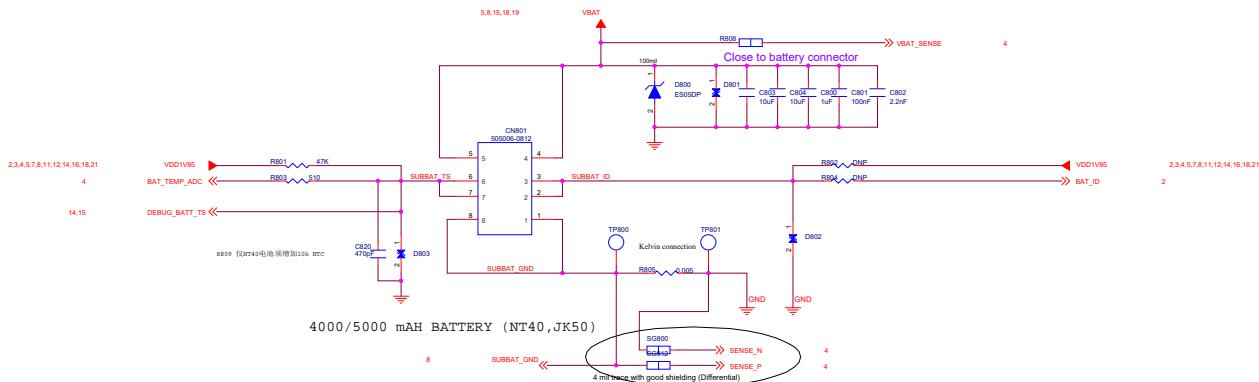


Handset 2nd Microphone

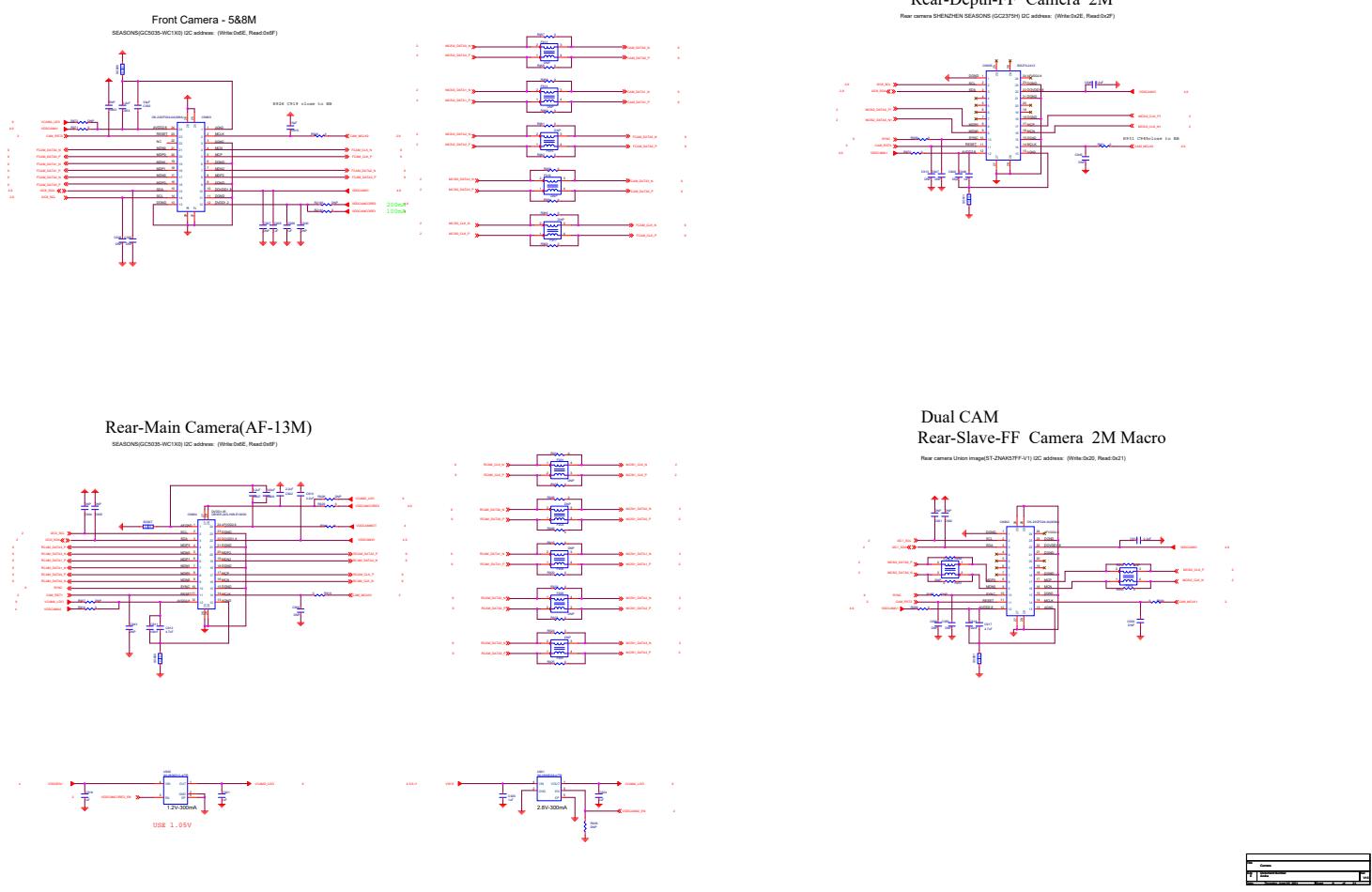




BATTERY CONNECTOR

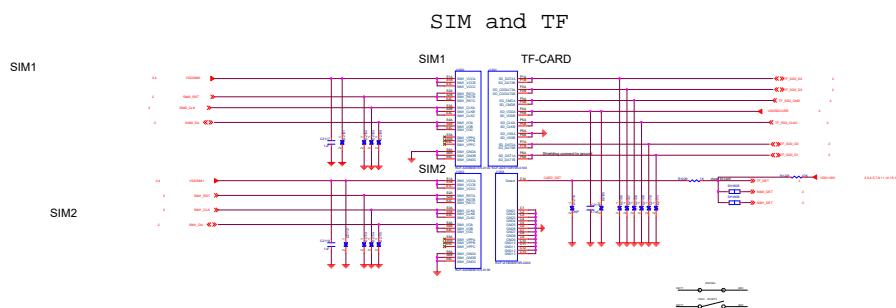


File		Rev
Battery		P0
Block C	Document Number Anba	
Date: Thursday, June 03, 2021	Sheet 8 of 21	



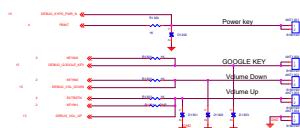


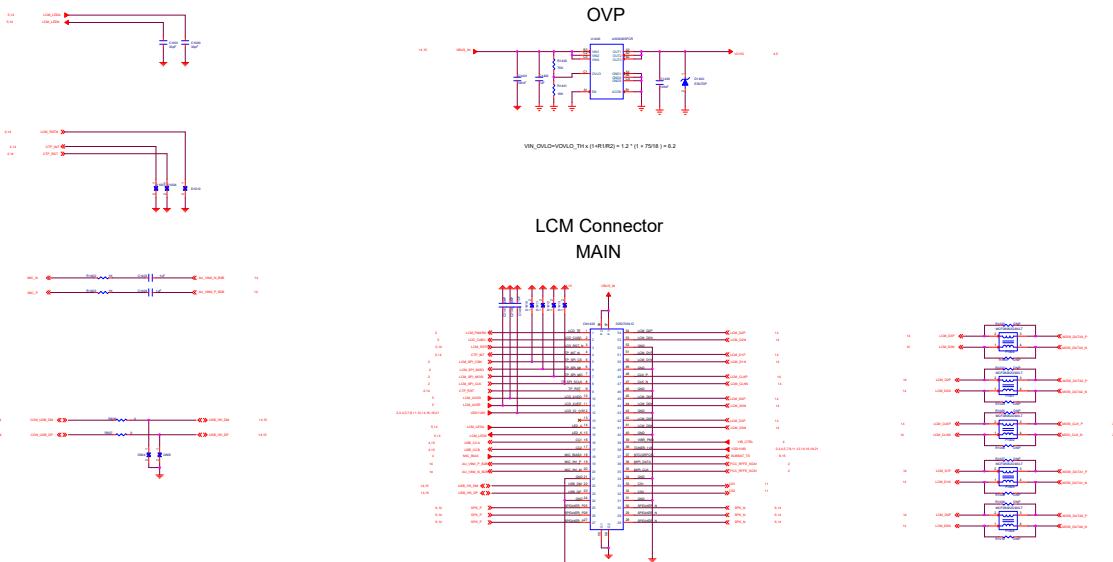




KEY

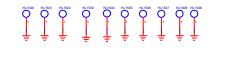
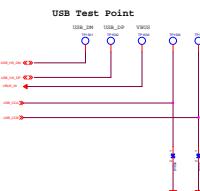
Volume Up : HOME Key/GND
Volume Down : KPCOL1/GND
DO NOT put pull-up resistor on PWRKEY



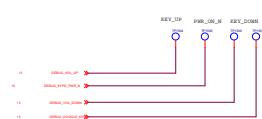


Download mode TP

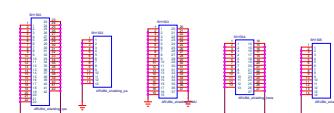
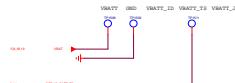
USB Test point



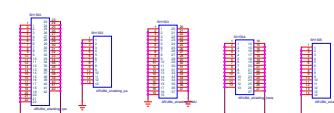
KEY Test point

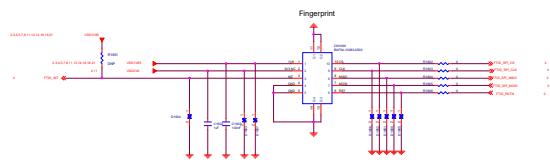


BATTERY Test point



UART





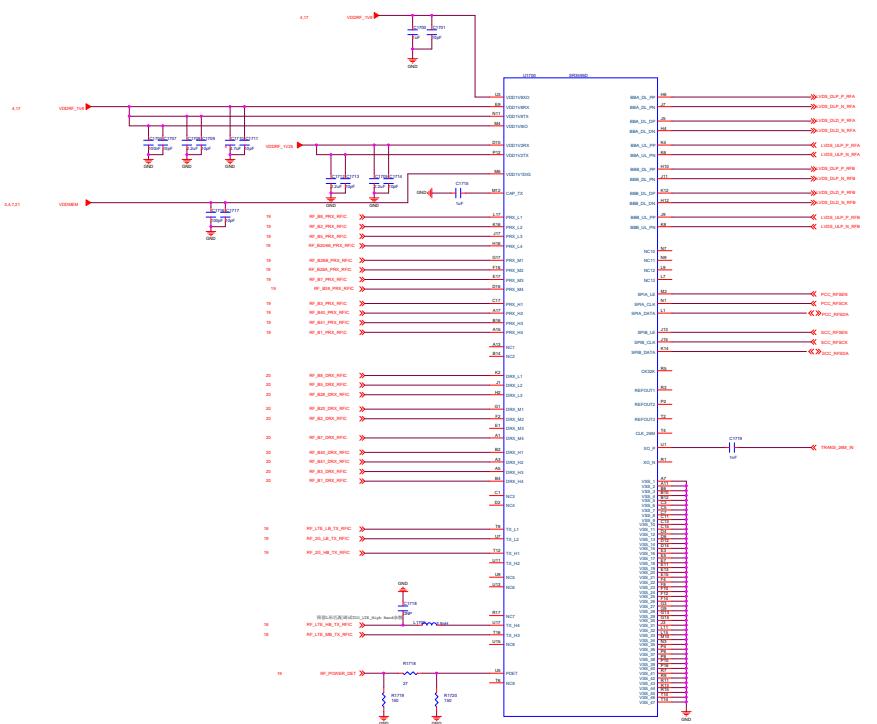
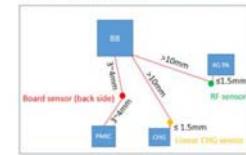


图4-2 NTC 电阻贴片与布局要求



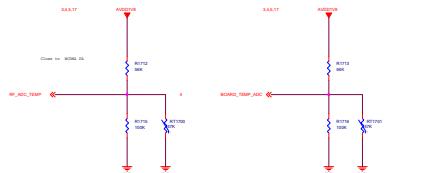
- NTC必须放置，温控策略是根据这个NTC运行的。禁止随便接个电阻，或者通过以降低热源温度。

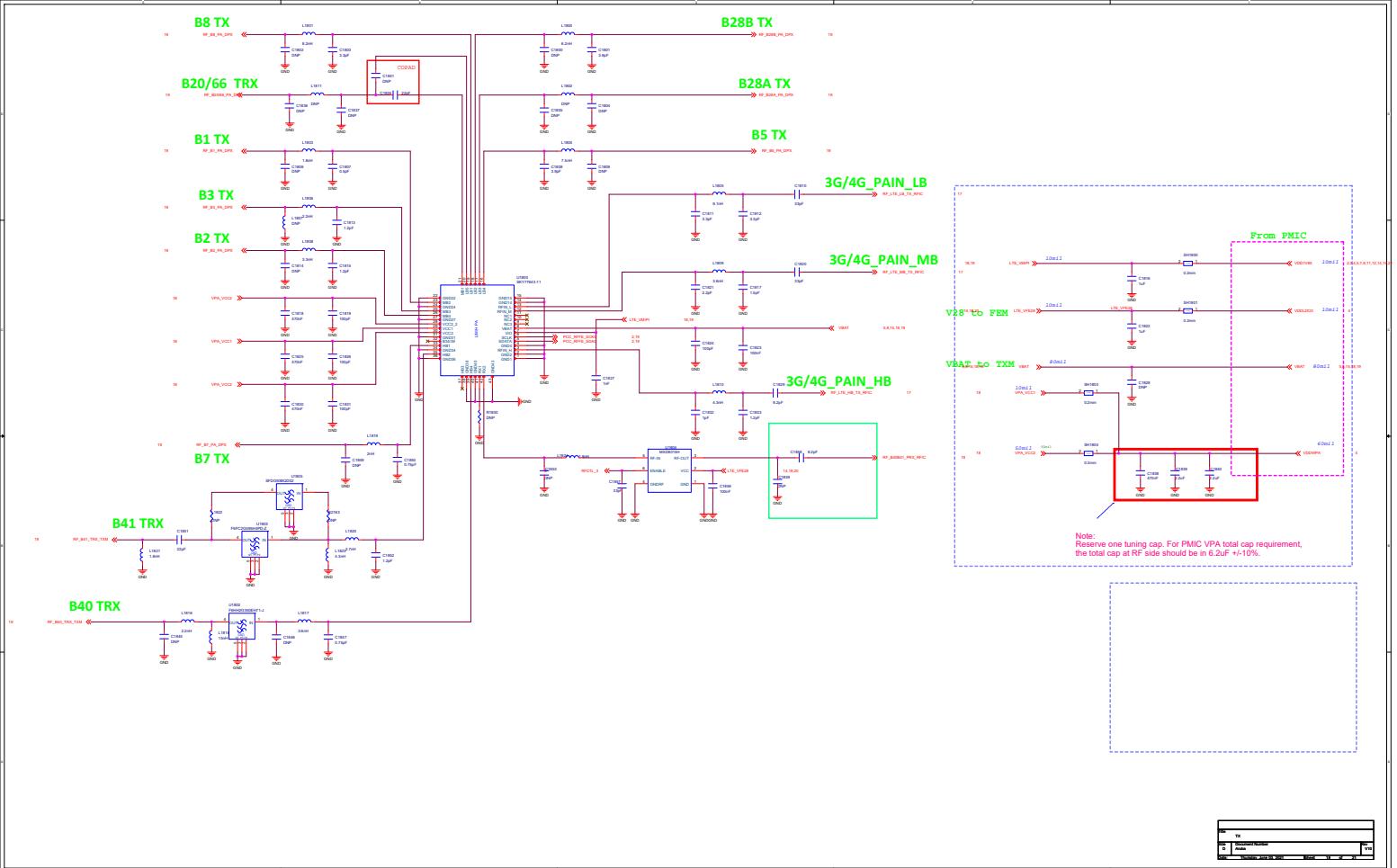
- NTC对于线性充电芯片必须放置，开关充电建议放置

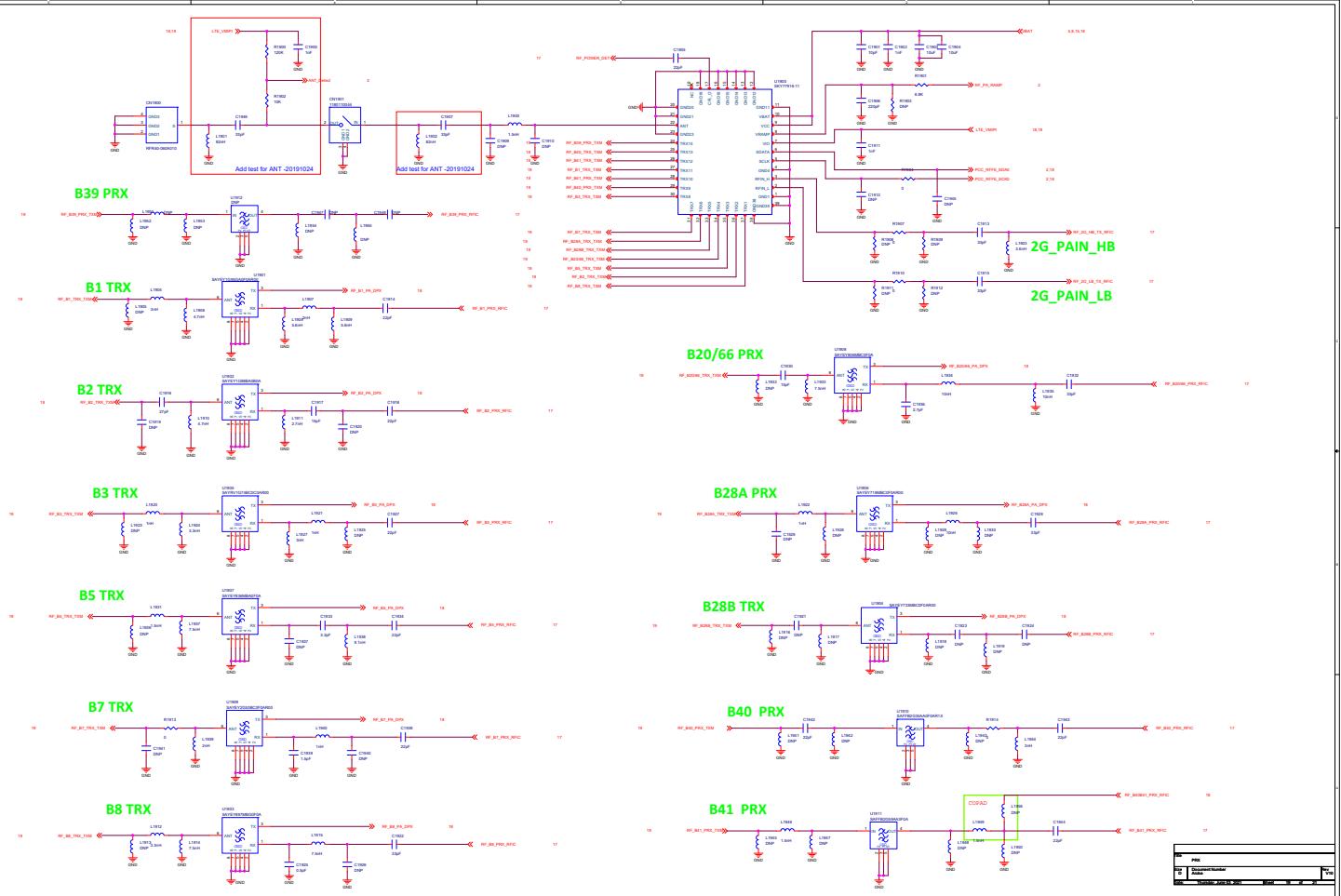
- NTC用于4G PA温度高时功率回退
如果需要这个功能需要放置

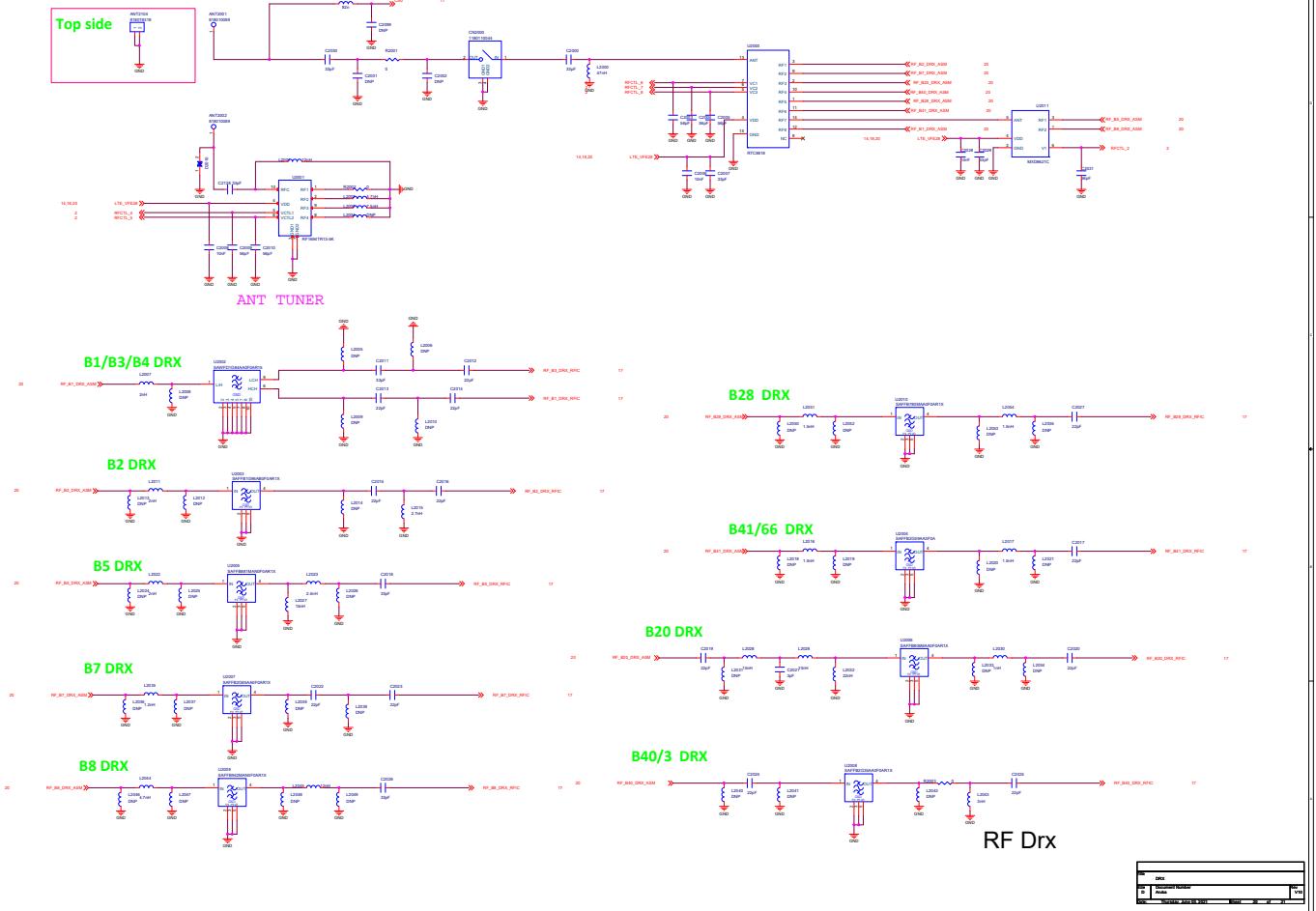
单面布网时，board sensor与BB
同面放置

内因外应









DRX	Antenna	Port
B1	ANT1	1
B1	ANT2	2
B1	ANT3	3
B1	ANT4	4
B3	ANT1	5
B3	ANT2	6
B3	ANT3	7
B3	ANT4	8
B4	ANT1	9
B4	ANT2	10
B4	ANT3	11
B4	ANT4	12
B2	ANT1	13
B2	ANT2	14
B2	ANT3	15
B2	ANT4	16
B5	ANT1	17
B5	ANT2	18
B5	ANT3	19
B5	ANT4	20
B7	ANT1	21
B7	ANT2	22
B7	ANT3	23
B7	ANT4	24
B8	ANT1	25
B8	ANT2	26
B8	ANT3	27
B8	ANT4	28
B28	ANT1	29
B28	ANT2	30
B28	ANT3	31
B28	ANT4	32
B41/66	ANT1	33
B41/66	ANT2	34
B41/66	ANT3	35
B41/66	ANT4	36
B20	ANT1	37
B20	ANT2	38
B20	ANT3	39
B20	ANT4	40
B40/3	ANT1	41
B40/3	ANT2	42
B40/3	ANT3	43
B40/3	ANT4	44

