

华勤通讯 Huaqin Telecom Technology Com.,Ltd		
Title 01_Block_Diagram		
Size D	Project AL6361A	Rev V1
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<Variant Name>

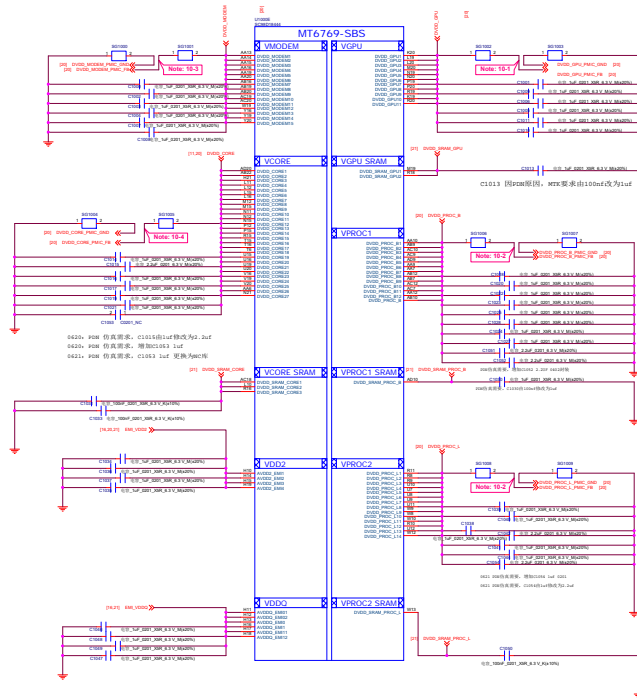
华勤通讯 Huaqin Telecom Technology Com.,Ltd

Title 04_Change_Notice

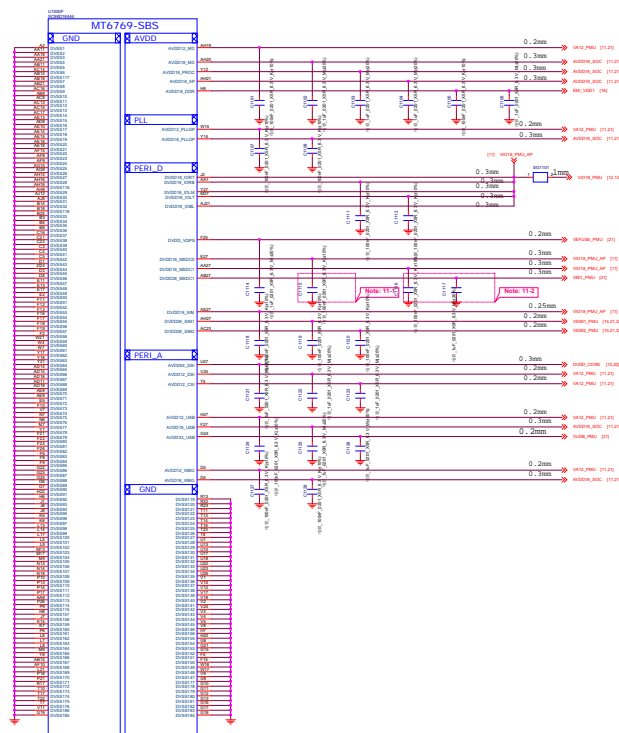
Size C	Project AL6361A	Rev V1
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Note 10-1:	Differential pair of DVD0_GPU remote sense must be close to BB's ball. Remote sense trace with GND shielding to PMIC (Differential)
Note 10-2:	Differential pair of DVD0_PROC remote sense must be close to BB's ball. Remote sense trace with GND shielding to PMIC (Differential)
Note 10-3:	Differential pair of DVD0_MODEM remote sense must be close to BB's ball. Remote sense trace with GND shielding to PMIC (Differential)
Note 10-4:	Differential pair of DVD0_CORE remote sense must be close to BB's ball. Remote sense trace with GND shielding to PMIC (Differential)



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Title 10_BB_POWER_PDN			
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Schematic design notice of "11_BB_POWER_IO" page:

Note 11-1: C1115 closed DVDD18_MSDC0 150mil

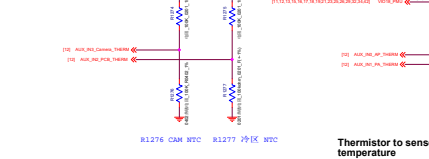
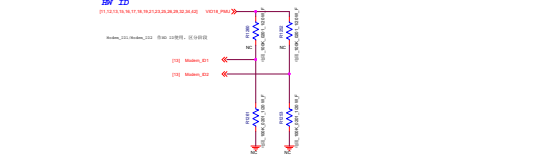
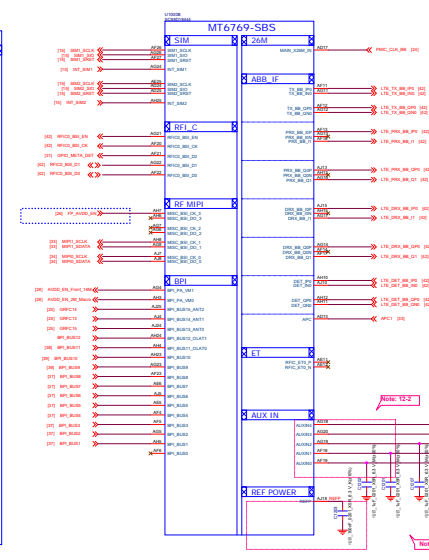
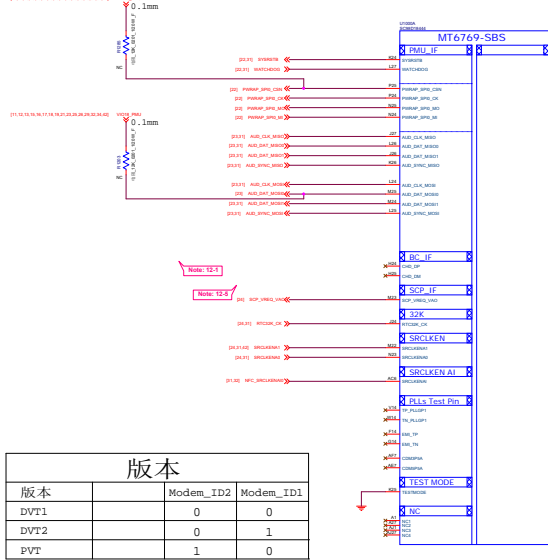
Note 11-2: C1116 closed DVDD18_MSDC1 150mil
C1117 closed DVDD28_MSDC1 150mil

AVDD12: 单个点走8mil, 两路分支各16mil, 总路径20mil

VDD18_PMD_AP: 总路径40mil

AVDD18_BDC: 总路径20mil

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Schematic design notice of "12_BB_1" page:

Note 12-1: PWRAP_SPI0_CSIN* and "AUD_DAT_MOSI0" are bootstrap pins to select which interface will be the JTAG pin out

PWRAP_SPI0_CSIN	AUD_DAT_MOSI0	AP_JTAG	MS_JTAG
HI	LO	TA0A	TA0A
LO	HI	SPB0 + ENTS	SPB0 + SPB3
LO	LO	TA0A	TA0A

Note 12-2: To shunt a 1uF capacitor in the AUXIN ADC input to prevent noise coupling. It should be placed as close to BB as possible. Connect the unused AUX ADC input to GND.

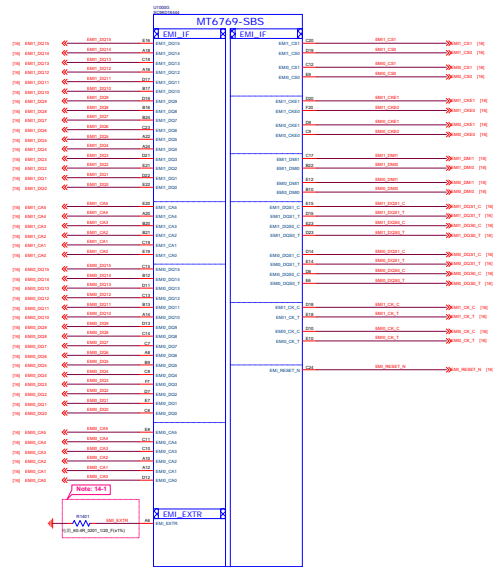
Note 12-3: The de-coupling cap. for REFP (A18 ball) have to be placed as close to BB as possible.

Note 12-4: HW pin for DDR type feature in bootstrap (refer to BB HW design Notices)

AUD_SPI0_CSIN	AUD_SPI0_CSIN	DDR_TYPE	DDR_TYPE
default=PD	default=PD	default=PD	default=PD
No ext. pull	No ext. pull	No ext. pull	No ext. pull
No ext. pull	No ext. pull	No ext. pull	No ext. pull
No ext. pull	No ext. pull	No ext. pull	No ext. pull

Note 12-5: Charger must have D+/D- pin for charger type USB detection. Charger must have at least 500mA USB current for AI charger type.

Title 12_BB_1			
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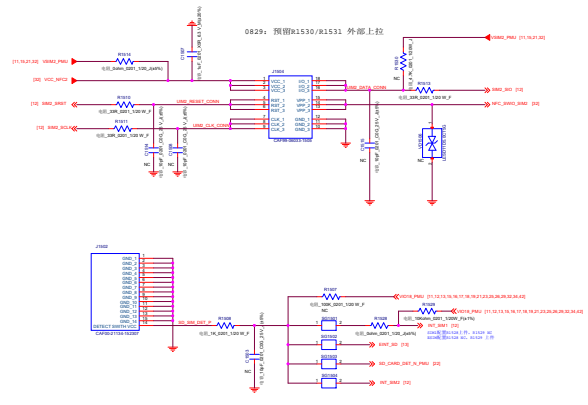
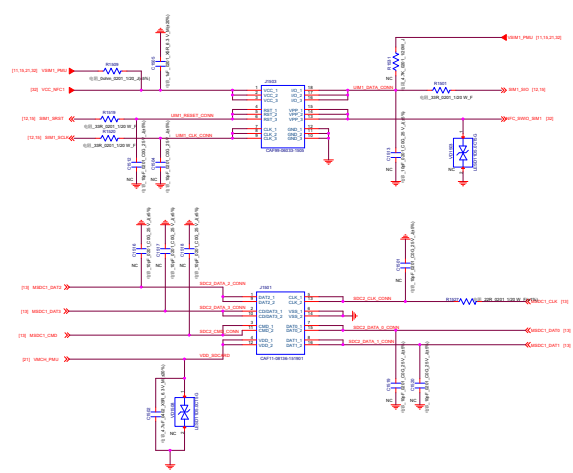


Schematic design notice of "14_BB_3" page.

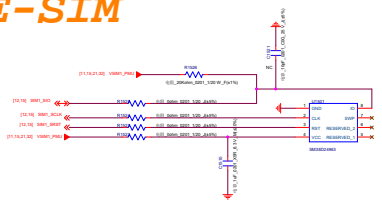
Note 14-1: R1401 please select 60.4 ohm (1%) resistor

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Title		14_BB_3	
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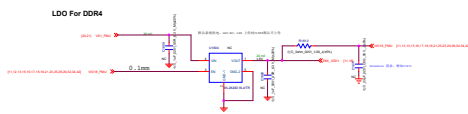
3IN3 SIM1&SIM2&SD



E-SIM



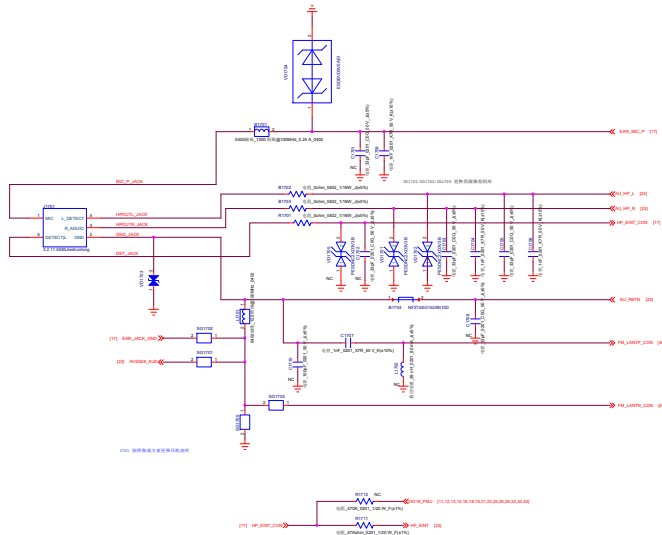
Title 15_Memory_SD SIM_Card		
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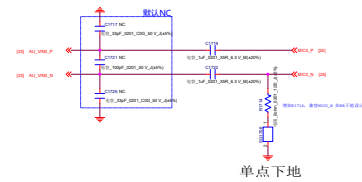
For other cap for PMIC (>10uF, at PMIC page):
please also refer to MMD and layout guide for placement.

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Title 16_Memory_EMMC_LPDDR4X		
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Earphone Audio

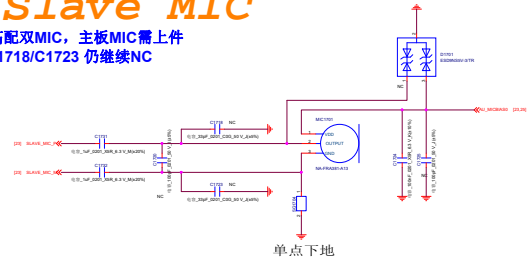


Main MIC

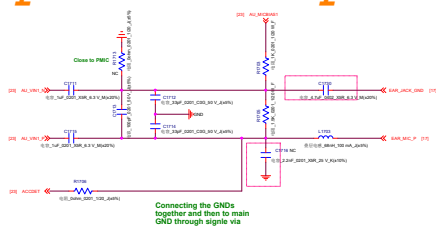


Slave MIC

低配单MIC，高配双MIC，主板MIC需上件
需要上件时C1718/C1723 仍继续NC

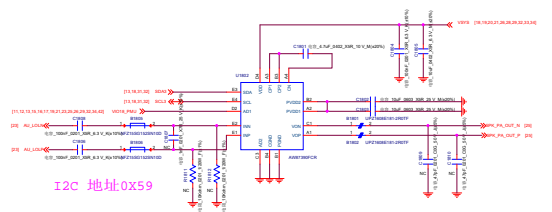


Earphone Microphone

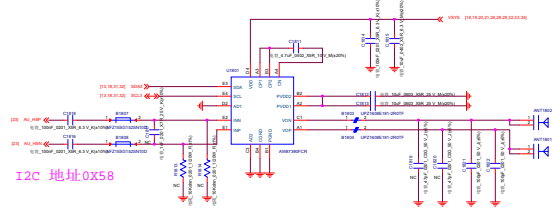


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Title 17_AUDIO_Earphone_MIC			
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SPEAKER



Receiver

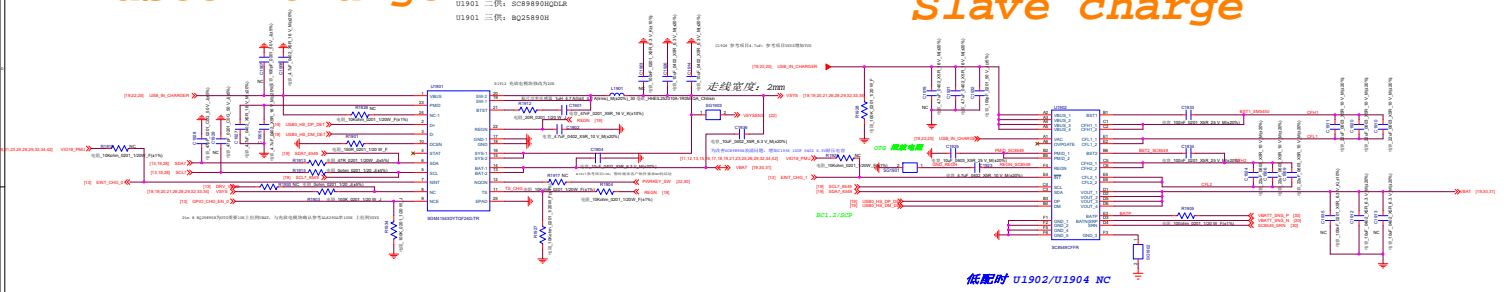


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Title		18_AUDIO_Speaker_Receiver	
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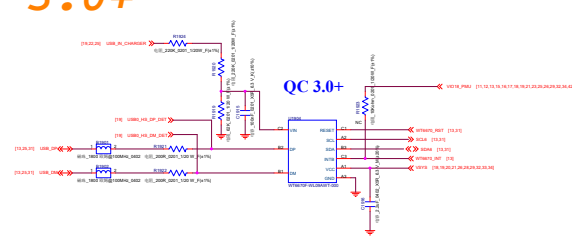
Master charge

U1901 主供: SQM415430VTP240/2R
U1901 二供: SC898908QDGA
U1901 三供: BQ25890H

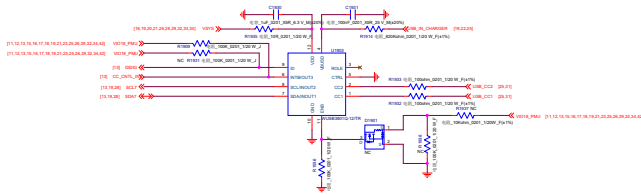
Slave charge



QC 3.0+



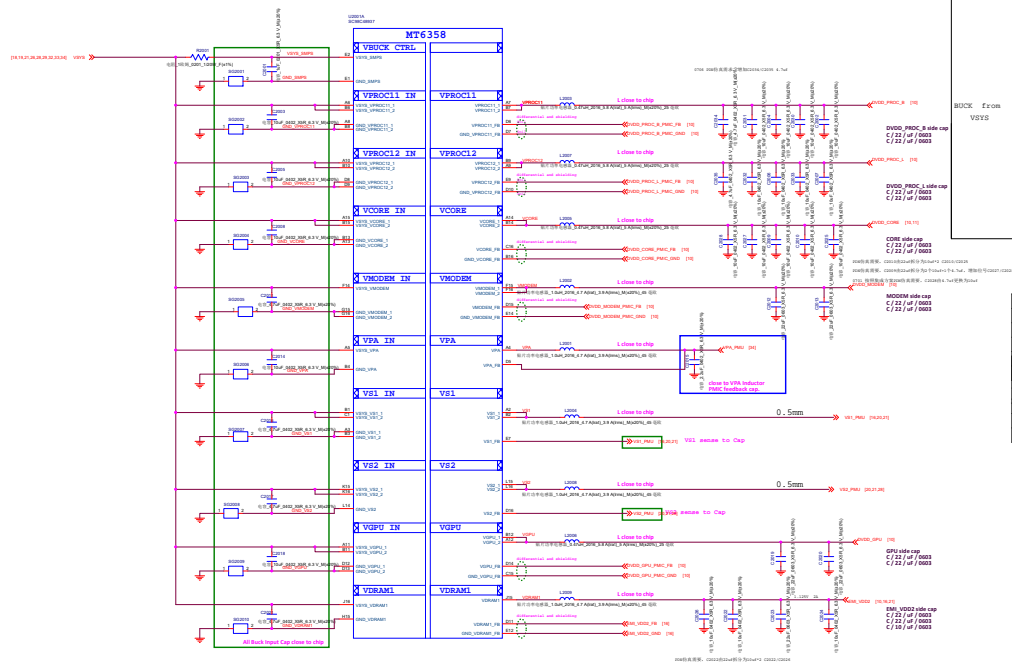
CC logic



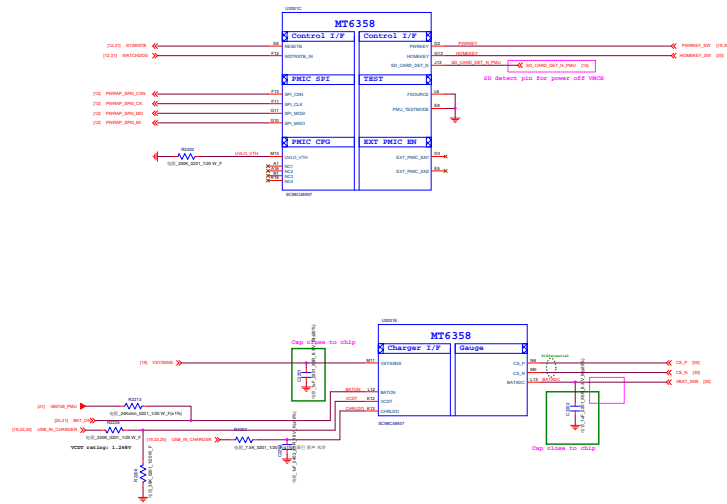
华勤通讯 Huaqin Telecom Technology Com.,Ltd		
Title 19_Charger		
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[illegible]

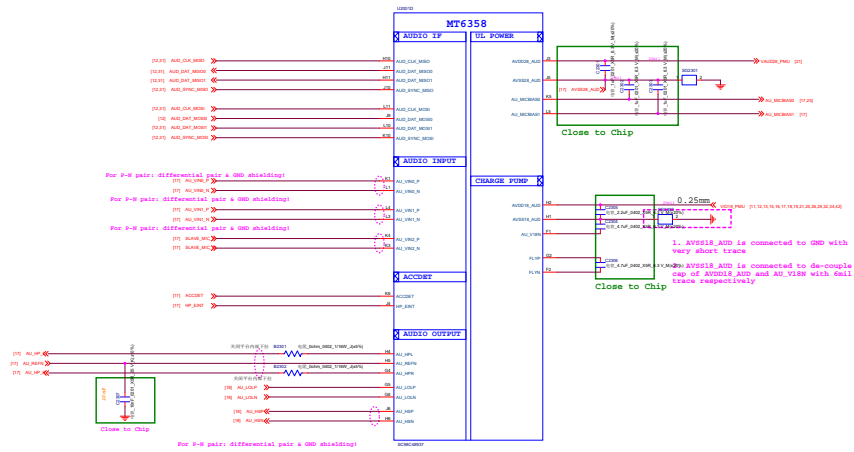
IP	Inductance	IBC(A)	
VS1	1	2.6	
VS2	1	2.5	
VDRAM	1	2.5	
VPA	1	4	
VMODEM	1	4	
VPROC11	0.47	5.3	
VPROC12	0.47	3.85	
VCORE	0.47	5.3	
VGPU	0.47	3.9	



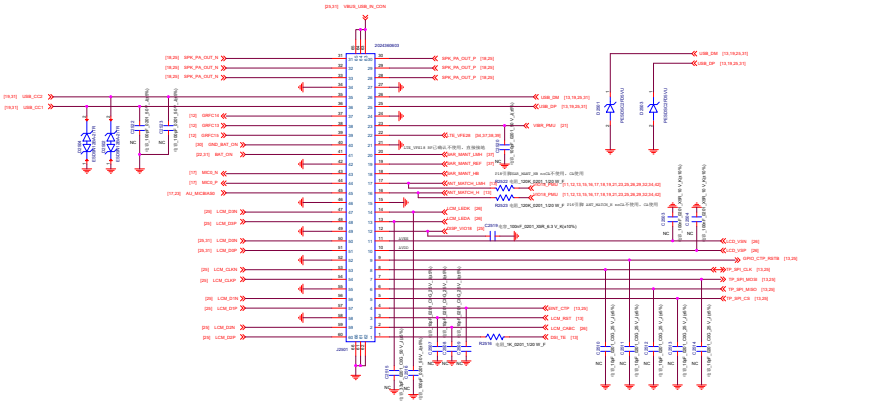
Title 20_POWER_MT6358_Buck		
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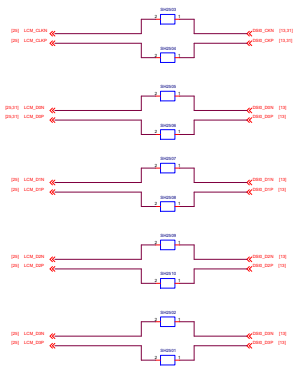
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Title 22_POWER_MT6358_IF			
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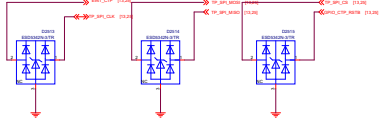
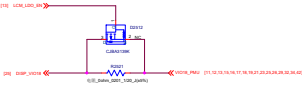
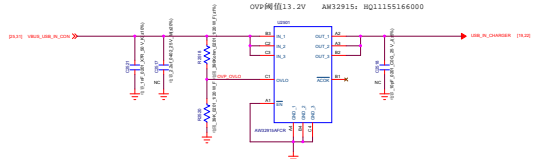
华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title 23_POWER_MT6358_Audio			
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LCD MIPI Common Mode Filter



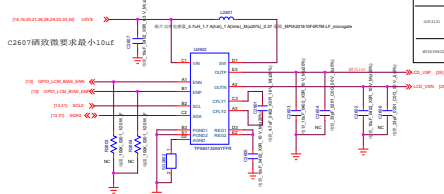
OVP OVP参考AL9260A AW32905 更换OVP库



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Title 25_LINK_OVP			
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偏压电路参考milan2

按照资源推荐型号更换U2602封装库, 然后结合SPC修改外围匹配。

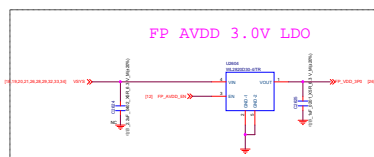


厂名	XXX 有限公司
00P2151	0111110 February (see 3)
WY655502	0111110 February (see 10)

C2631艾为建议先N

D2605 NC, R2615上件 正常模式BL_ISET为高阻态, 高亮模式 BL_ISET为低

D2605 上件, R2615 NC 正常模式 BL_ISET 为低, 高亮模式 BL_ISET 为高

[illegible]

◀ Gradient Norm

华勤通讯 Huaqin Telecom Technology Com.,Ltd

Title	26_LCD_BL_BIAS_Finger
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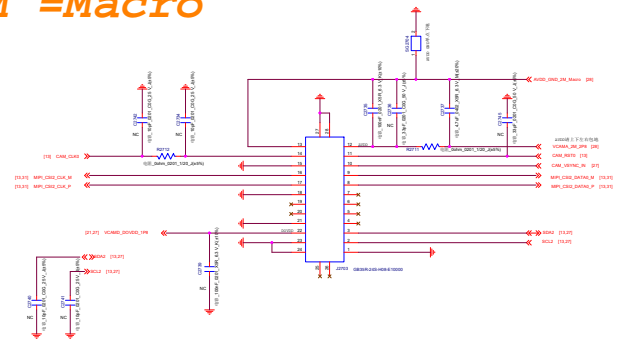
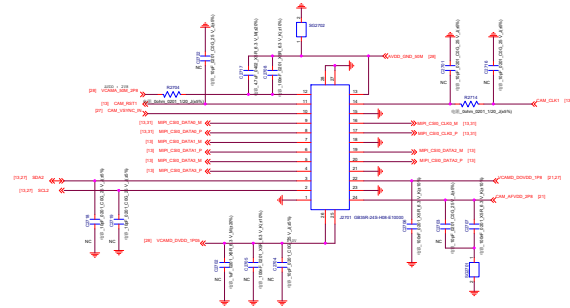
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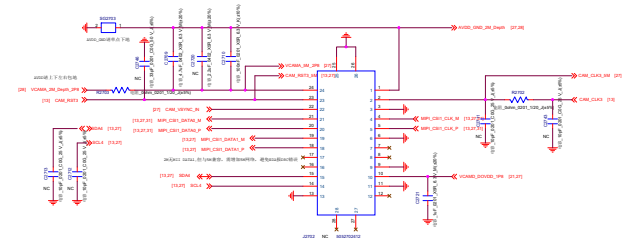
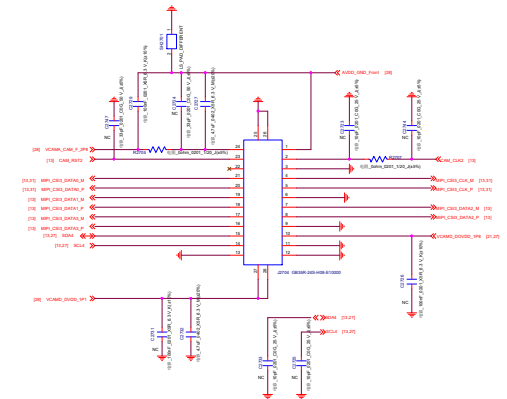
Main Camera 50M_OIS

2M =Macro

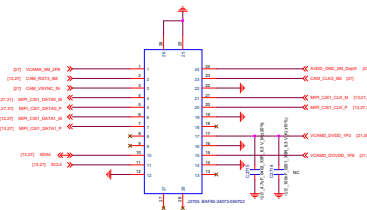


Front CAM 16M/8M

2M Depth ★NOTE:低配2M J2702上件

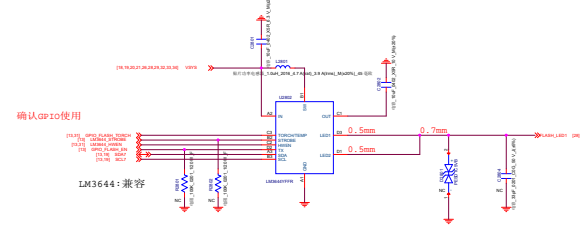


5M Wide ★NOTE:低配2M J2702上件

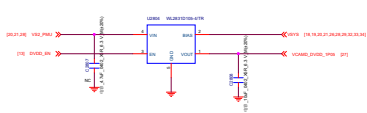


Huawei Logo 华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title 27_Camera			
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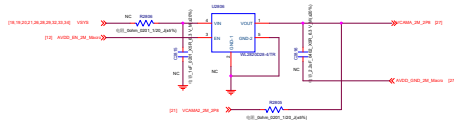
Rear CAM FLASH



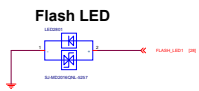
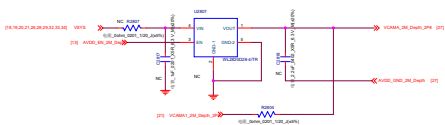
LDO For Camera DVDD 50M 1.05V



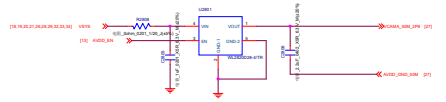
LDO For Camera AVDD 2M Macro 2.8V 预留外插LDO



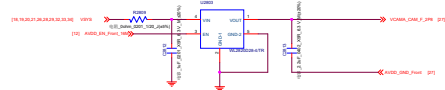
LDO For Camera AVDD 2M Depth 2.8V 预留外插LDO



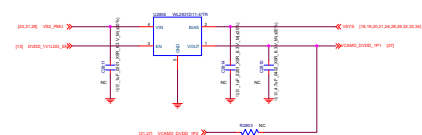
LDO For Camera AVDD 50M 2.8V



LDO For Camera AVDD Front 16M/8M 2.8V



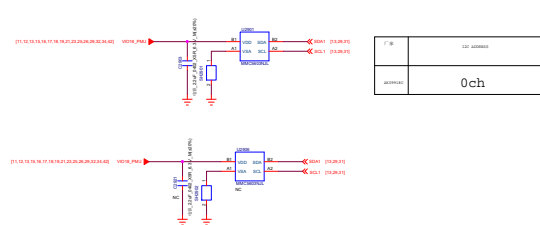
LDO For CAM DVDD Front 16M 1.1V HQ11154597000, LDO_1.1_500 mA



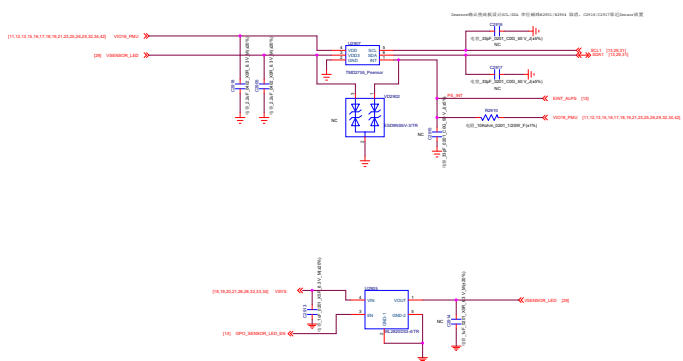
高配 16M R2803 MC, U2805 上件, C2814 MC
低配 8M R2803 上件, U2805及外插匹配MC

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Title 28_Camera_Flash_Powers_LDO		
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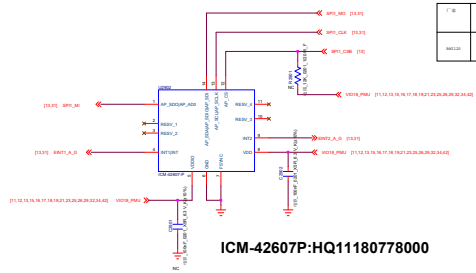
M-Sensor



ALPS SENSOR



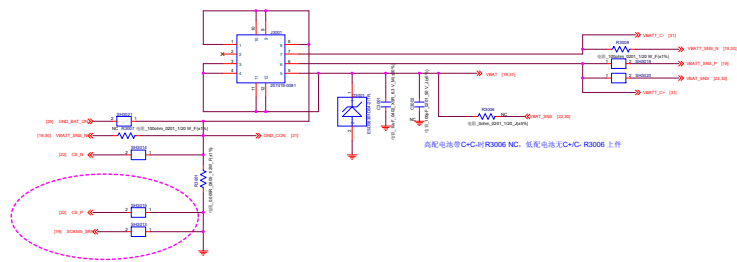
Accerometer + Gyro Sensor



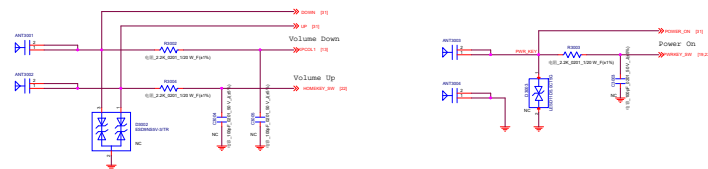
G Sensor OD0.7更新不需要再兼容，删除单 G sensor

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BATT CON



KEY



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Title 30_BAT_Key		
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[1X, 1M]	MMIO0_D0R6	7200.0.0	7200.0.0	7200.0.0
[1X, 1M]	MMIO0_DATA0	7200.0.0	7200.0.0	7200.0.0
[1X, 1M]	MMIO0_CS0K	7200.0.0	7200.0.0	7200.0.0
[1X, 1M]	MMIO0_C0ND	7200.0.0	7200.0.0	7200.0.0
[1X, 1M]	MMIO0_P0RTW	7200.0.0	7200.0.0	7200.0.0

[illegible]

[12,34] RTIC32M_OK >>> TSP0.3.1 TSP2100

[24] XIO_SIOC >>> TSP0.3.1 TSP2101

[24] XIO_CILS >>> TSP0.3.1 TSP2102

[13,29] OSH2_CXN >>> TPO.3.1 TP2103
 [13,29] OSH2_CXP >>> TPO.3.1 TP2104
 [29] LCM2_CXN >>> TPO.3.1 TP2105
 [29] LCM2_CXP >>> TPO.3.1 TP2106

Accession	Gene	Protein	Accession	Protein
U03.201	MPLI_C002	MPLI_C002A2_M	U03.201.1	1993116
U03.201	MPLI_C002	MPLI_C002A2_P	U03.201.2	1993117
U03.201	MPLI_C002	MPLI_C002A2_M	U03.201.3	1993118
U03.201	MPLI_C002	MPLI_C002A2_P	U03.201.4	1993119
U03.201	MPLI_C002	MPLI_C002A2_M	U03.201.5	1993120
U03.201	MPLI_C002	MPLI_C002A2_P	U03.201.6	1993121
U03.201	MPLI_C002	MPLI_C002A2_M	U03.201.7	1993122
U03.201	MPLI_C002	MPLI_C002A2_P	U03.201.8	1993123
U03.201	MPLI_C002	MPLI_C002A2_M	U03.201.9	1993124
U03.201	MPLI_C002	MPLI_C002A2_P	U03.201.10	1993125
U03.201	MPLI_C002	MPLI_C002A2_M	U03.201.11	1993126
U03.201	MPLI_C002	MPLI_C002A2_P	U03.201.12	1993127
U03.201	MPLI_C002	MPLI_C002A2_M	U03.201.13	1993128
U03.201	MPLI_C002	MPLI_C002A2_P	U03.201.14	1993129
U03.201	MPLI_C002	MPLI_C002A2_M	U03.201.15	1993130
U03.201	MPLI_C002	MPLI_C002A2_P	U03.201.16	1993131

[18,30] VBAT

[30] VBAT_C

[30] VBAT_C

[13,28] GPIO_FLASH_TORC → TP0.3.1 TP3166
[13,28] LM3666_HV5VIN → TP0.3.1 TP3167

[13.20] SPI0_M0

[13.20] SPI0_M02

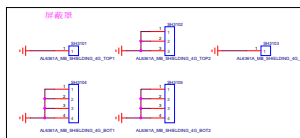
[13.20] SPI0_CLK

[13.20] SPI0_A_0

[13.20] SPI0_A_0

Figure 1-10 shows the power supply circuit for the ATmega16. It includes a 220V AC input, a 1000μF capacitor (C1), a 100kΩ resistor (R1), a 7805 voltage regulator (U1), and a 100kΩ resistor (R2). The circuit is designed to provide a stable 5V supply to the microcontroller.

[12.2] AUD_CLK_MISO >> TP0_3 1 TP0100
 [12.2] AUD_DAT_MISO >> TP0_3 1 TP0100

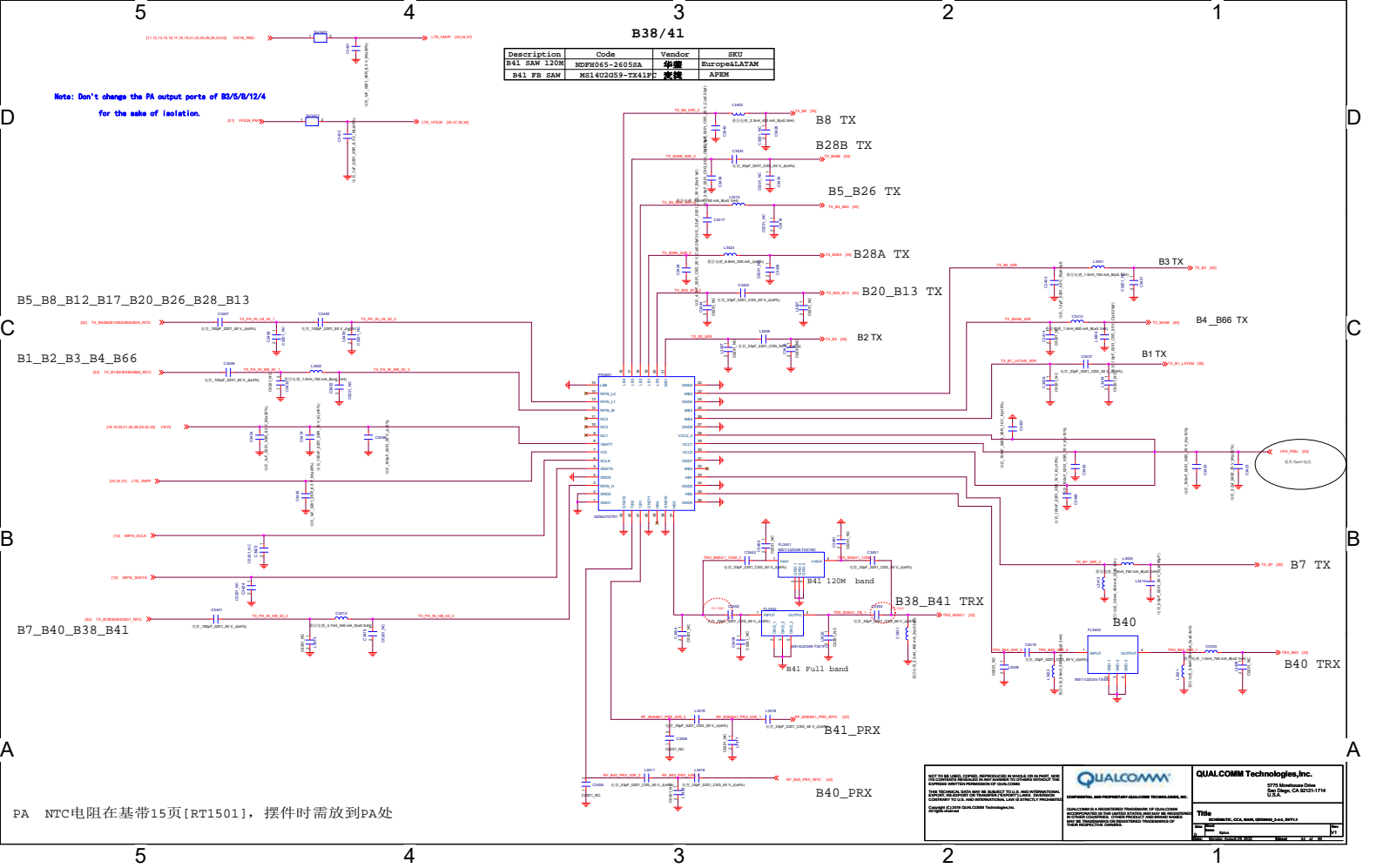


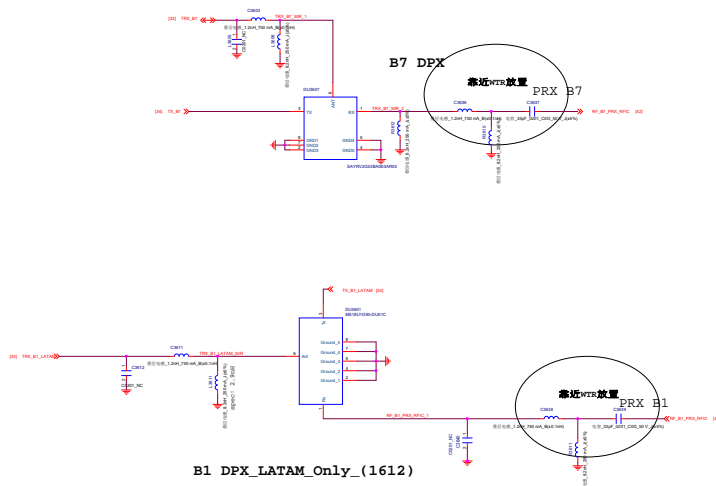
SH3101	RF	PA	屏蔽罩
SH3102	BB	CPU+DDR	屏蔽罩
SH3103	RF	WCN	屏蔽罩
SH3104	BB	PMU+EMMC	屏蔽罩
SH3105	RF	PRX/DRX	屏蔽罩

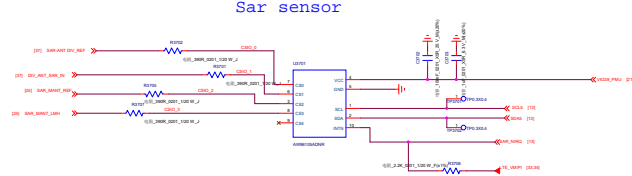
华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title 31_Testpoint_Debug			
Size D	Project AL6361A		Rev v1
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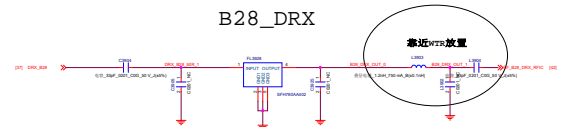
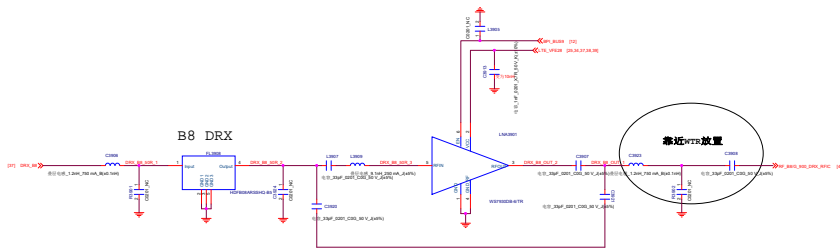
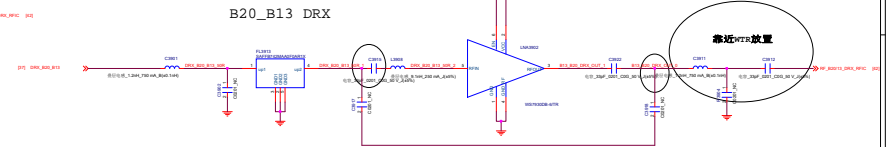
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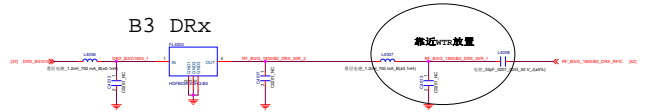
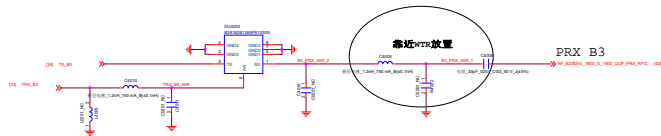
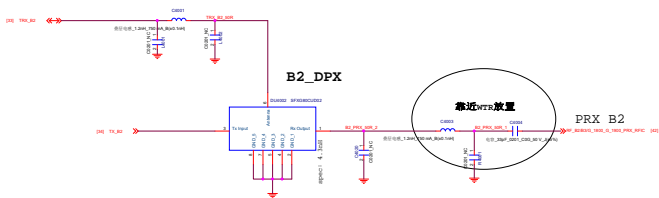




Description	Code	Vendor	SKU
B26 DRX	NDFH052-0876SA	华夏	LATAM & APAC
B5 DRX	NDFH052ARSSHQ-B5	好达	Europe

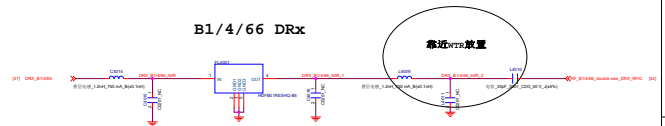
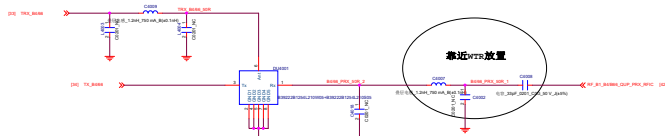


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		<p>Title</p> <p>QUALCOMM, CDMA, GSM, GPRS, J-AS, DTV-1</p>
	<p>Rev. 1.0</p> <p>Specs</p>	<p>Rev. 1.0</p>



B1/B4/B66

Description	Code	Vendor	SKU
B66 SAN	MS11U2G15-RX66B	爱模	LATAN
B1 SAN	MS11U2G15-RX66B	爱模	Europe&AFEM



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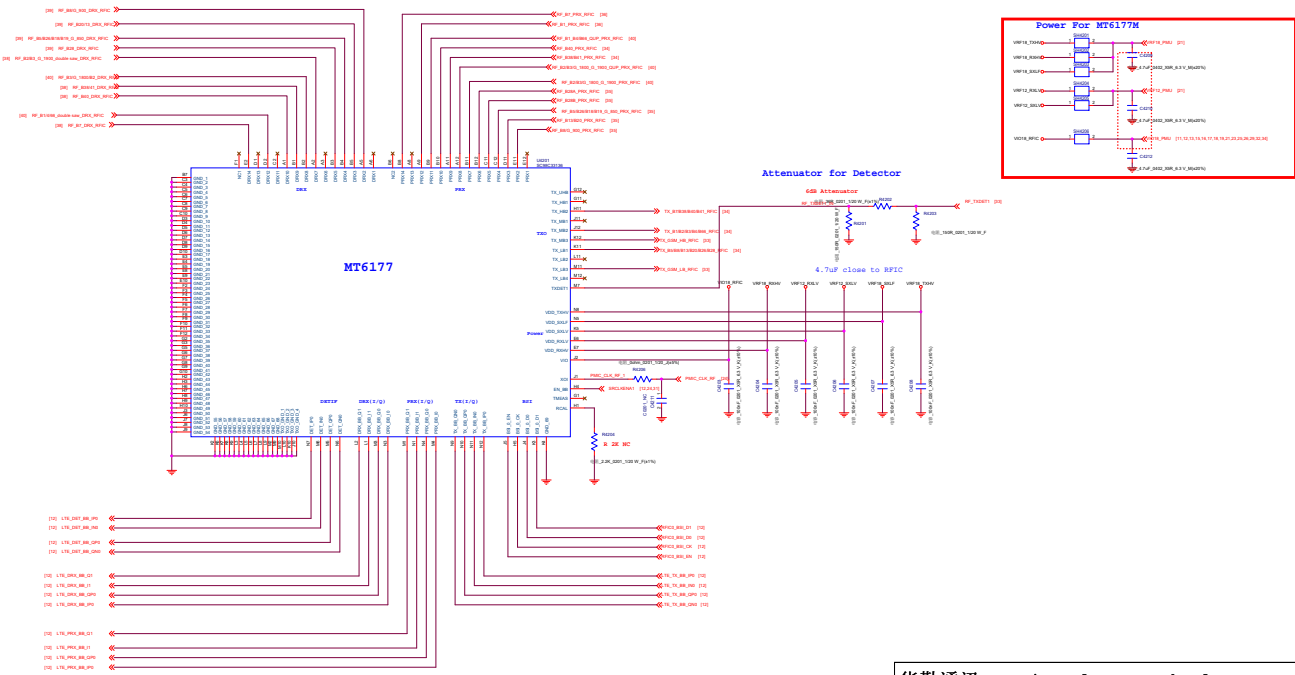
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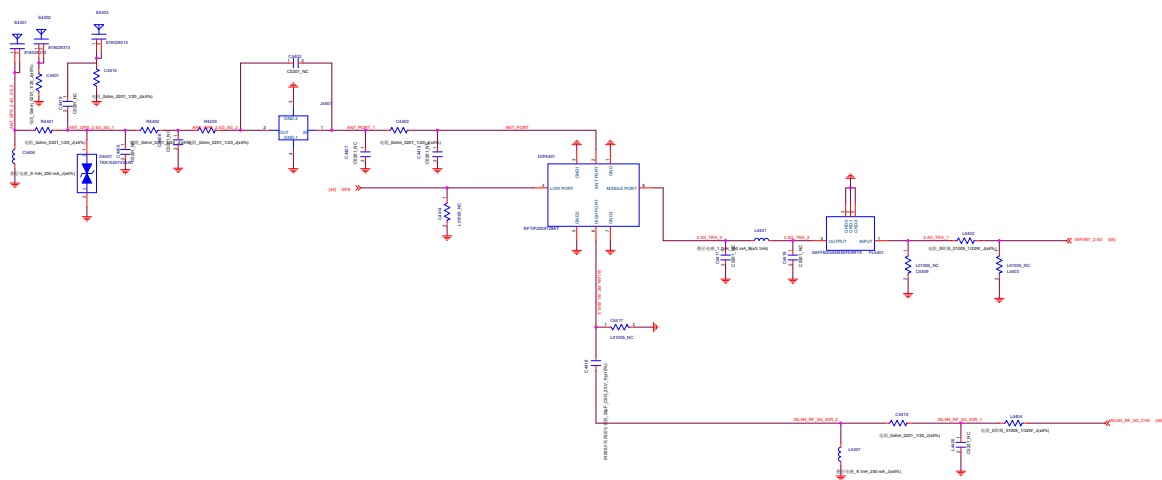


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Title 31_RF_MT6177_Pin_Out		
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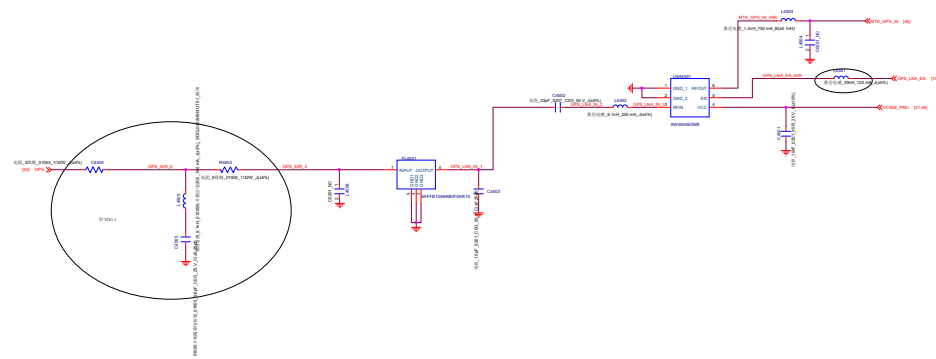
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Title SCHEMATIC, CCA, MMAL, QDSHOW, 3-4-2, DUTY-1	
Date 0	Description Schematic



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<p>Date</p> <p>_____</p>		<p>Page</p> <p>_____ of _____</p>	

需要从小板连接过来

Schematic design notice of "51_CONNECTIVITY_CONSYS_MT6631"

Note 51-1: For R5015 size, please select 0402 size or larger one

Note 51-2: Please refer to MT6785 Baseband design notice for VCN33 LDO selection guide

Note 51-3: If WiFi/SIG not support, connect pin 34(WF_RF_SIG) to GND

Note 51-4: Pin 36 (AVDD28_FM) must be connected to VCN28 even if FM not support

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Rev v4

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