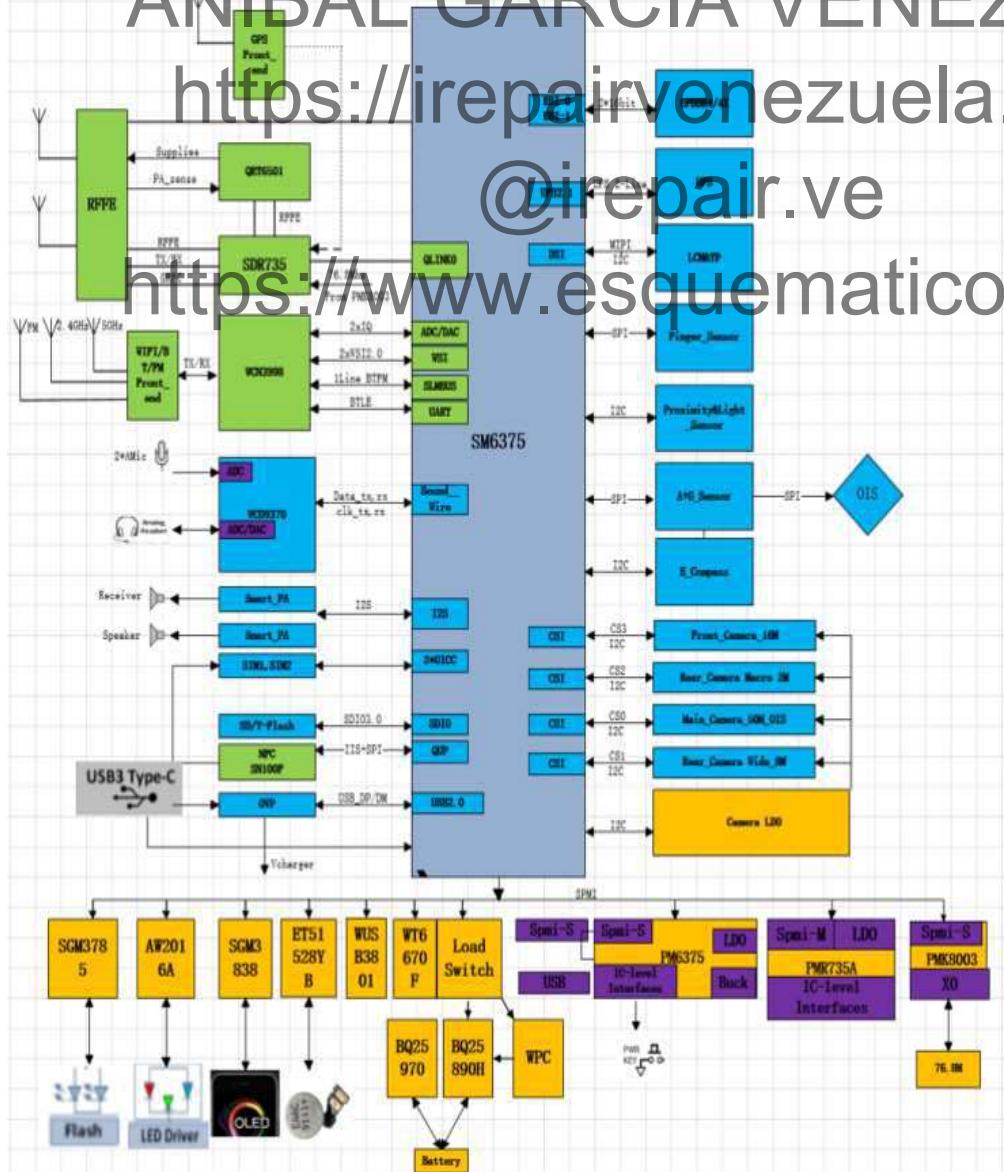


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Title  
Block Diagram

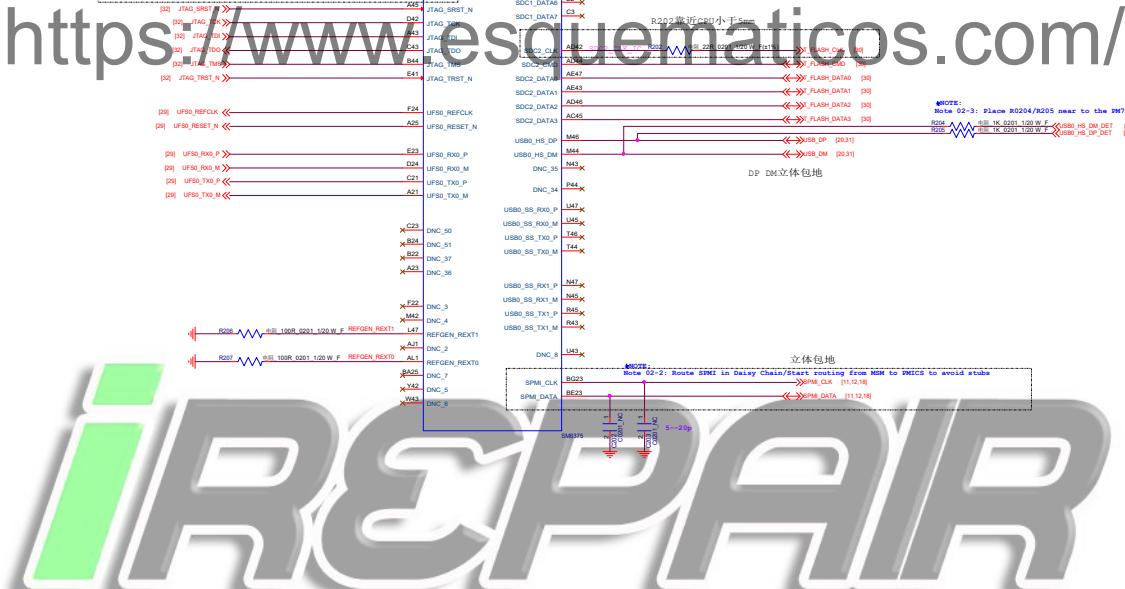
Size D	Project QN6139A_MB_V1	Rev V1
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Date: Thursday, January 13, 2022 | Sheet 1 of 60

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Title	SM6375_LPDDR4x
Size	Project
D	QN6139A_MB_V1
Date:	Thursday, January 13, 2022
Sheet	3 of 60

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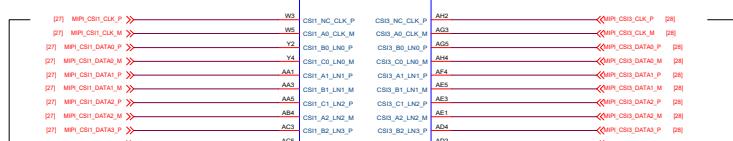
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REAR CAMERA\_MAIN\_50M



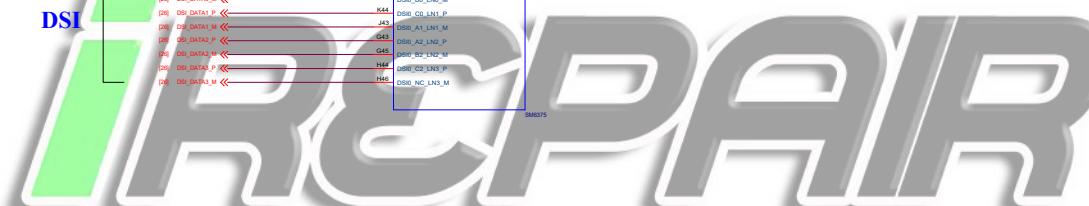
REAR CAMERA 8M WIDE



REAR CAMERA 2M

FRONT CAMERA 16M

DSI



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Title  
SM6375\_CSI/DSI

Size D	Project QN6139A_MB_V1	Rev V1
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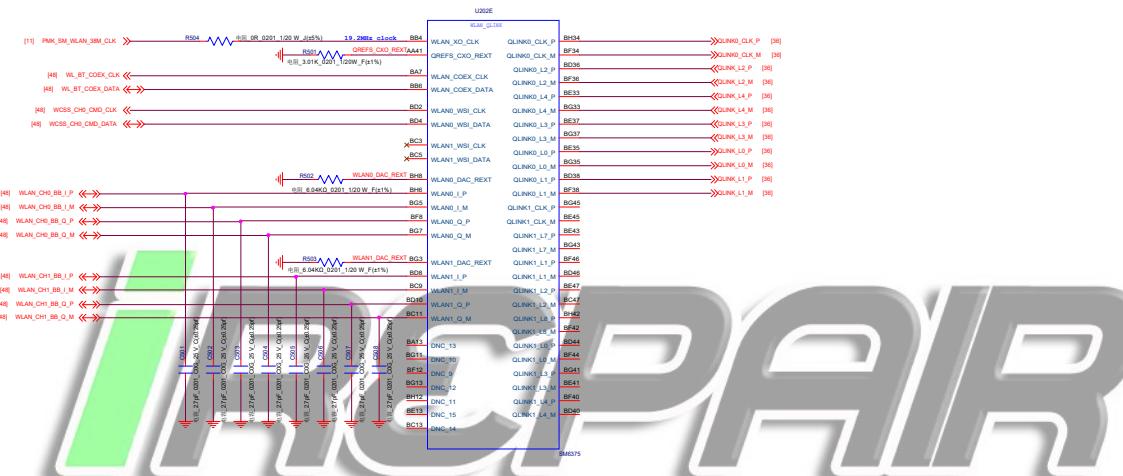
Date: Thursday, January 13, 2022 Sheet 4 of 60

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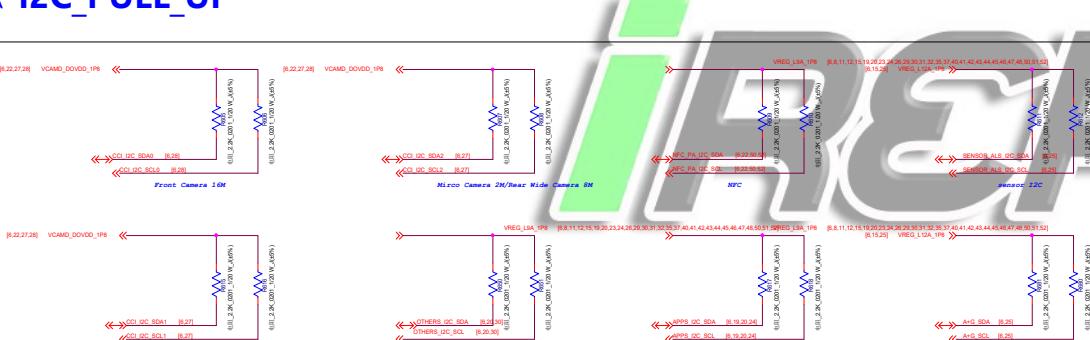
Title	SM6375_WLAN/QLINK
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Size D	Project QN6139A_MB_V1	Rev V1
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## ★ I2C\_PULL\_UP



### Schematic design notice of "06\_SM6375\_GPIO"

Note 06-1: NO external Pull-up on the GPIOs (GPIO\_117, GPIO\_119, GPIO\_120, GPIO\_103, GPIO\_106, GPIO\_113, GPIO\_51, GPIO\_50)

Note 06-2: FAST\_BOOT\_SEL [2: 0]

BOOT_CONFIG[3:1]/ FAST_BOOT_SEL[2:0]	SM6375 FAST BOOT OPTIONS
0b000	UFS HS G1 -> SD -> eDL (eDL path - USB only)
0b001	SD -> UFS HS G1 -> eDL (eDL path USB only)
0b010	USB -> eDL (eDL path - SD then USB)
0b011	eMMC -> SD -> eDL (eDL path - USB only)
0b100	SD -> eMMC -> eDL (eDL path USB only)
0b101	UFS HS G1 -> eDL (eDL path - USB only)
0b110	UFS HS G1 -> SD -> eDL (eDL path - USB only)
0b111	eMMC -> eDL (eDL path - USB only)

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Title  
**SM6375\_GPIO**

Size D Project QN6139A\_MB\_V1 Rev V1

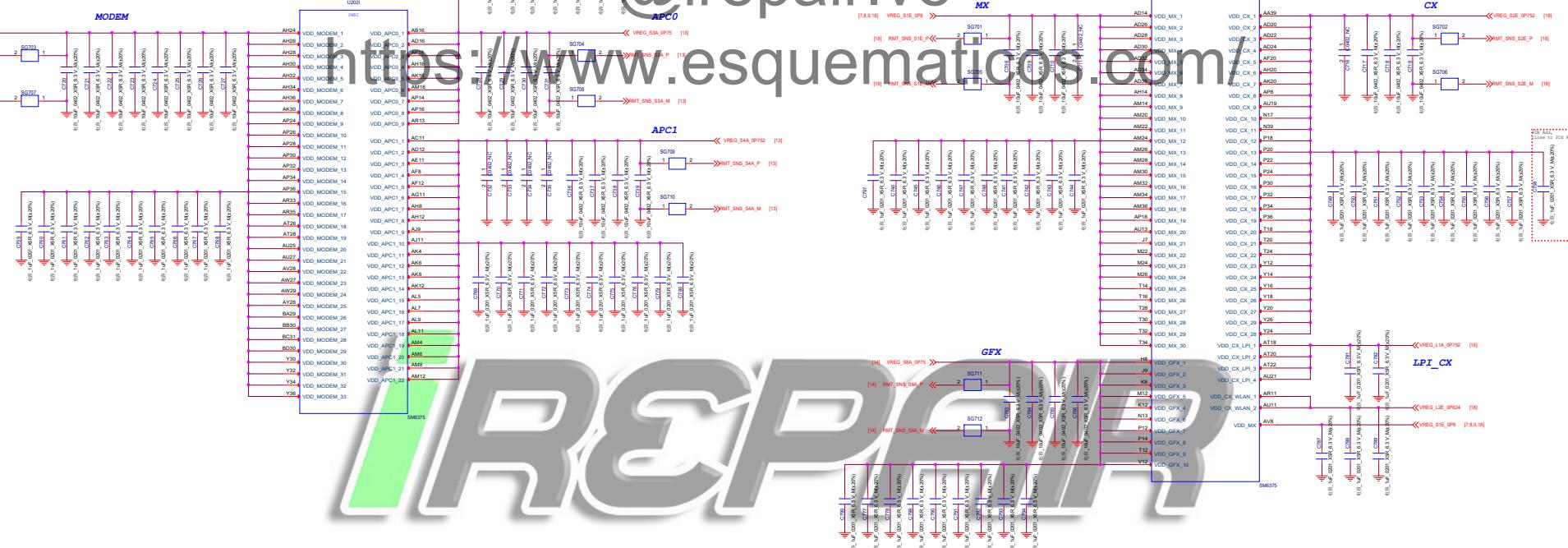
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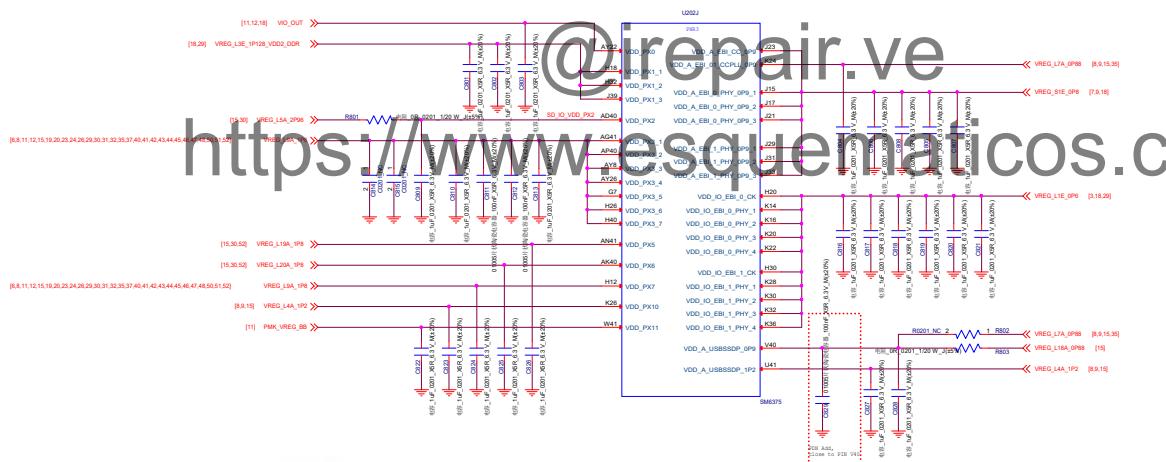
Title  
SM6375\_PWR1

Size D Project QN6139A\_MB\_V1 Rev V1

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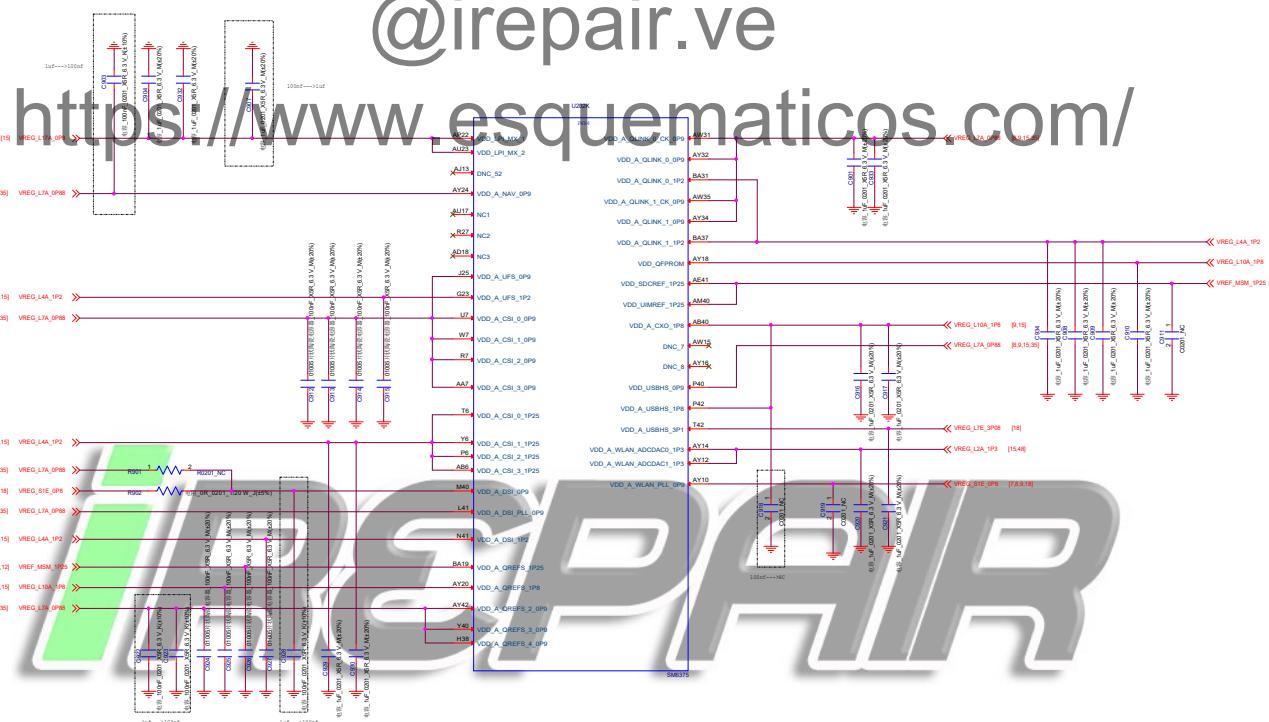
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Title	SM6375_PWR2
Size	Project QN6139A_MB_V1
D	Rev V1

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Title	SM6375_PWR3
Size	Project QN6139A_MB_V1
D	Rev V1

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Title  
**SM6375\_GND**

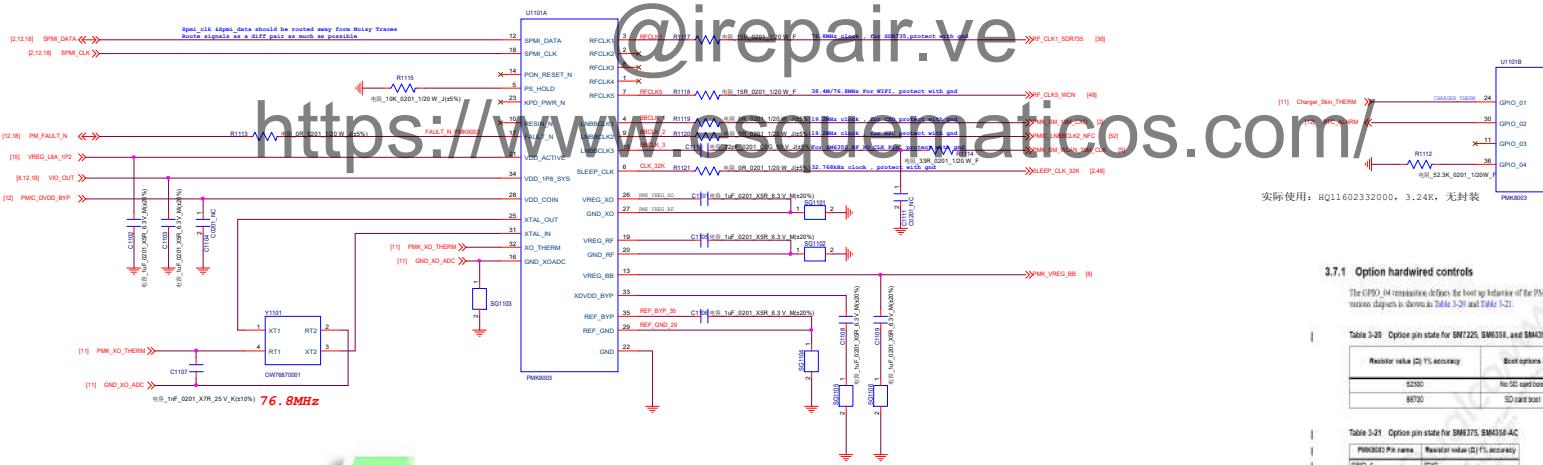
Size D	Project QN6139A_MB_V1	Rev V1
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实际使用: HQ11602332000, 3.24K, 无封装

## 3.7.1 Option hardwired controls

The GPIO\_04 termination defines the boot up behavior of the PMK8. The configuration for various changes is shown in Table 3-20 and Table 3-21.

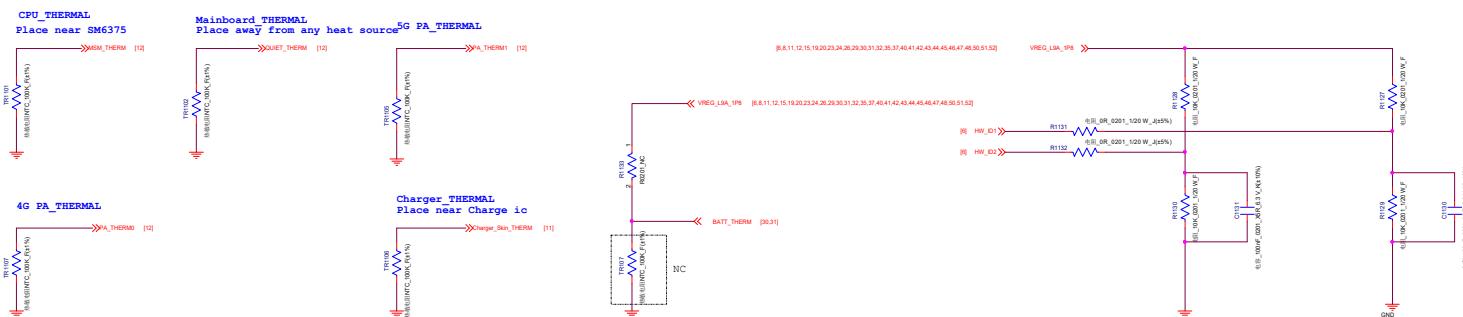
Table 3-20 Option pin state for SM8725, SM8338, and SM4350

Resistor value (2) 1% accuracy	Boot options
R2200	No SD card boot
R8700	SD card boot

Table 3-21 Option pin state for SM8375, SM4350-AC

PMK8003 Pin name	Resistor value (2) 1% accuracy
GPIO_4	SD4

NTC



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Title PMK8003\_CLK/NTC

Size D	Project QN6139A_MB_V1	Rev V1
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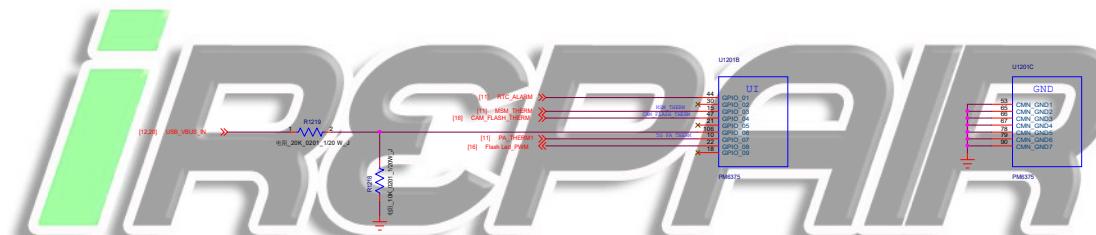
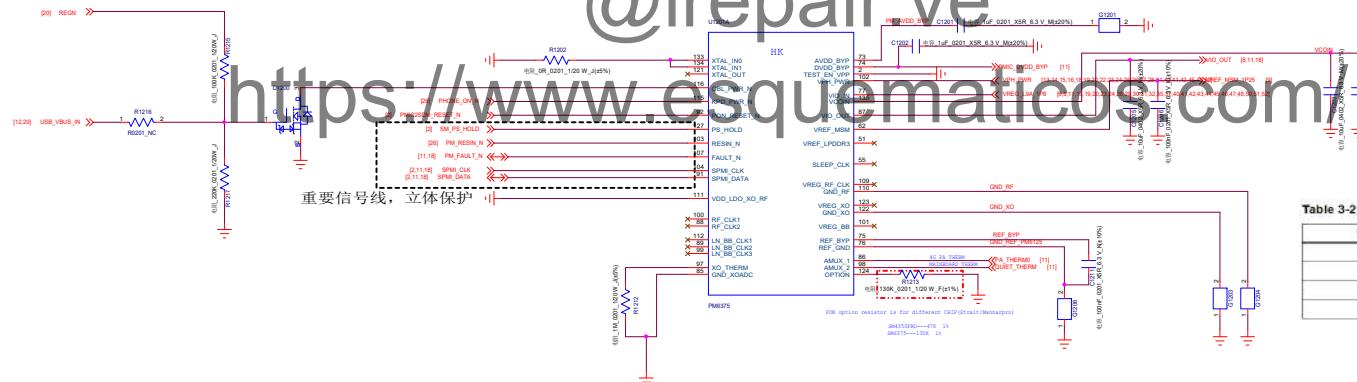


Table 3-21 PM6375 hardware configuration options for SM6375

Option pin	Configuration
130 K	SD card boot
240 K	No SD card boot
330 K	SD card boot
750 K	No SD card boot

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Title PM6375\_CTRL/AMUX/GPIO/GND

Size D Project QN6139A\_MB\_V1 Rev V1

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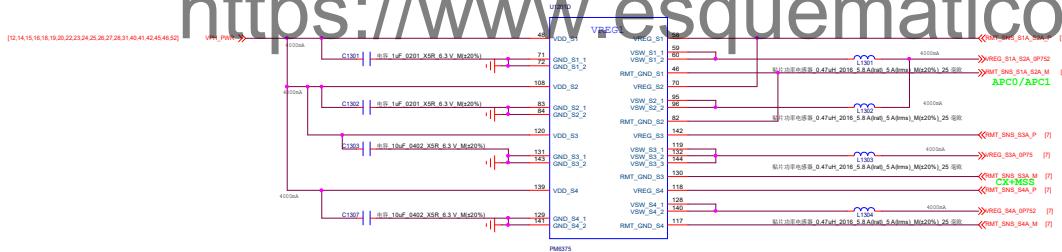


Table 3-6 PM6375 SMPS regulator summary for SM6375/SM4350

Regulator	Circuit type	I <sub>RATED</sub> (mA)	Default on booting voltage (V)	Specified range (V)	Programmable range (V)	Default on	Expected use (SM6375/SM4350)
S1A_S2A	FTS510	8000	0	0.32-1.352	N	Modem	
S3A	FTS510	4500	0.828	0.32-1.352	Y	APC0	
S4A	FTS510	4500	0	0.32-1.352	N	APC1	
S3A_S4A	FTS510	9000	0	0.32-1.352	N	APC1	
S5A	HFS510	4000	0.952	0.32-2.04	Y	LV Sub Reg	
S6A	HFS510	2000	1.352	0.32-2.04	Y	MV Sub Reg	
S7A	HFS510	2500	2.04	0.32-2.04	Y	HV Sub Reg	
S8A	FTS510	4500	0	0.32-1.352	N	GFX	

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Title  
PM6375\_BUCKS

Size D	Project QN6139A_MB_V1	Rev V1
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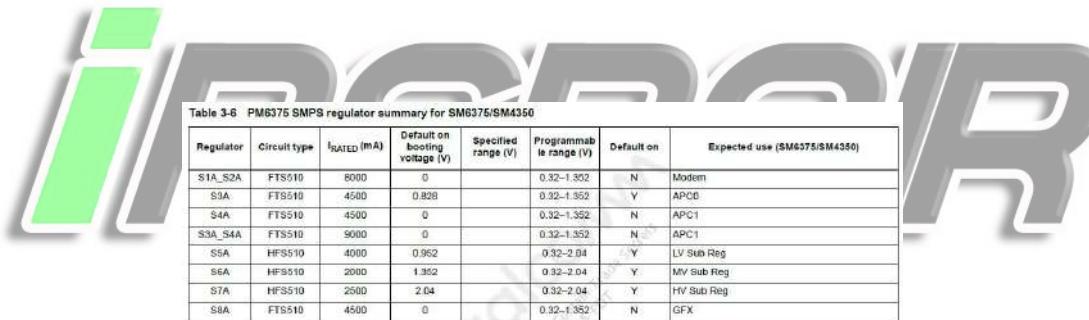
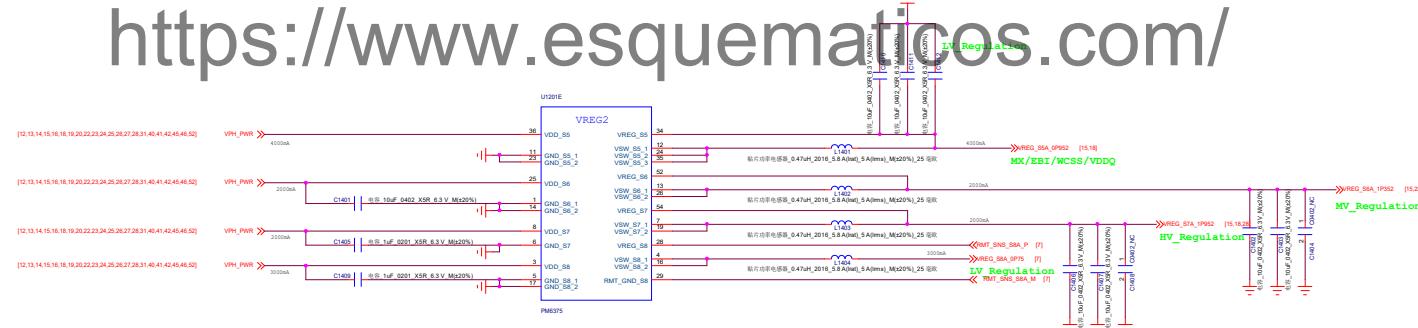
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PM6375 BUCK Circuits  
REF 1401-1499

<https://www.esquematicos.com/>



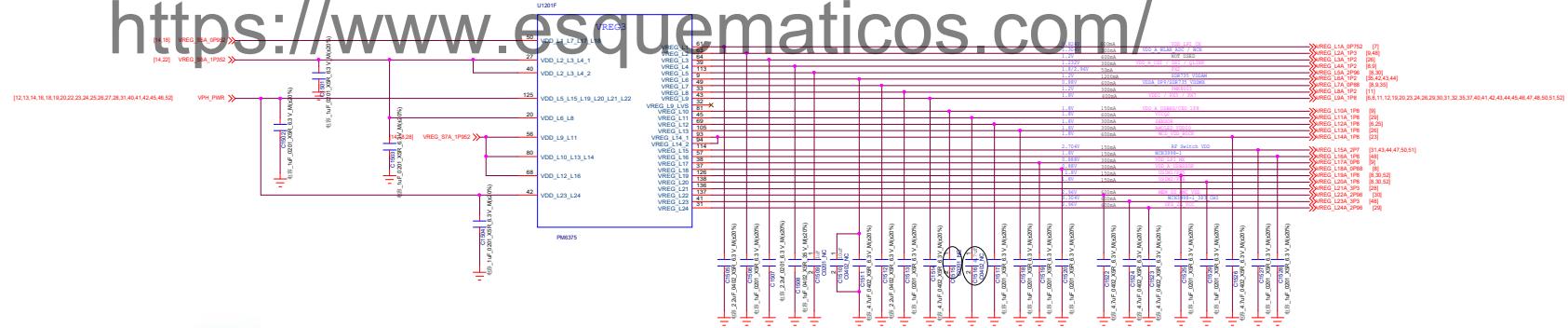
华勤通讯 Huaqin Telecom Technology Com., Ltd		
Title	PM6375_LDOS	
Size D	Project QN6139A_MB_V1	Rev V1
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Title PM6375\_CNTRL/GPIO/BOB/FLASH

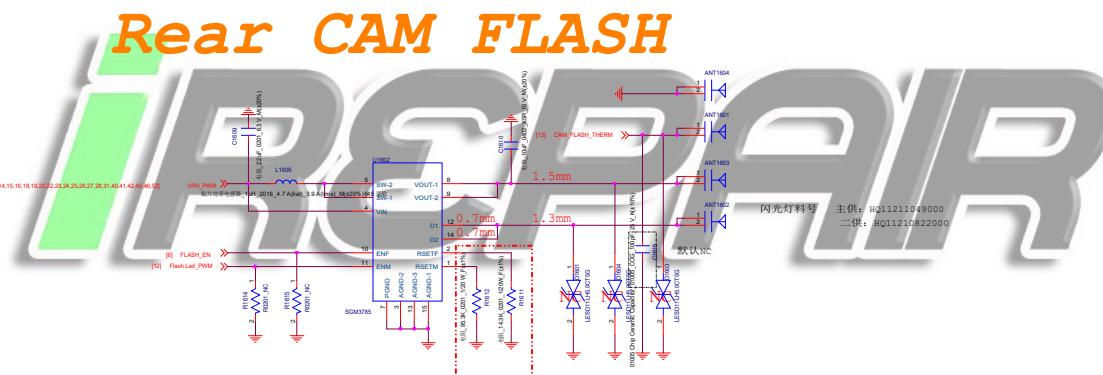
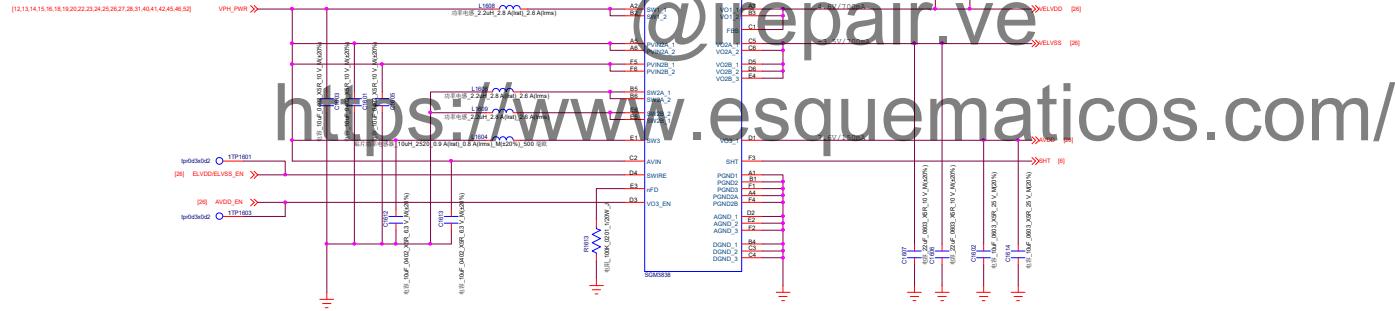
Size D Project QN6139A\_MB\_V1 Rev V1

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# ANIBAL GARCIA VENEZUELA

## AMOLED Deriver

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$I_{flash} = 8625 / 14.3 * 2 = 1206 \text{ mA}$   
 OCP8135B  
 $I_{movie} = 7100 / 95.3 * 2 = 149 \text{ mA}$   
  
 $I_{flash} = 8556 / 14.3 * 2 = 1196 \text{ mA}$   
 SGM3785  
 $I_{movie} = 7063 / 95.3 * 2 = 148.2 \text{ mA}$   
  
 $I_{flash} = 9000 / 14.3 * 2 = 1258 \text{ mA}$   
 AS6585BDRNC  
 $I_{movie} = 7500 / 95.3 * 2 = 157 \text{ mA}$

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Title 16.Amoled/Flash_LED/RGB Deriver		
Size D	Project QN6139A_MB_V1	Rev V1
Date: Thursday, January 13, 2022	Sheet 16 of 60	

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Title PM6150L\_LDOS

Size D	Project QN6139A_MB_V1	Rev v1
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# ANIBAL GARCIA VENEZUELA

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The diagram shows the connections for the VREG\_L18\_VDDQ\_DDR component. The connections are as follows:

- Pin 32: C102 (blue), C103 (green)
- Pin 31: C104 (blue), C105 (green)
- Pin 30: C106 (blue), C107 (green)
- Pin 29: C108 (blue), C109 (green)
- Pin 28: C110 (blue), C111 (green)
- Pin 27: C112 (blue), C113 (green)
- Pin 26: C114 (blue), C115 (green)
- Pin 25: C116 (blue), C117 (green)
- Pin 24: C118 (blue), C119 (green)
- Pin 23: C120 (blue), C121 (green)
- Pin 22: C122 (blue), C123 (green)
- Pin 21: C124 (blue), C125 (green)
- Pin 20: C126 (blue), C127 (green)
- Pin 19: C128 (blue), C129 (green)
- Pin 18: C130 (blue), C131 (green)
- Pin 17: C132 (blue), C133 (green)
- Pin 16: C134 (blue), C135 (green)
- Pin 15: C136 (blue), C137 (green)
- Pin 14: C138 (blue), C139 (green)
- Pin 13: C140 (blue), C141 (green)
- Pin 12: C142 (blue), C143 (green)
- Pin 11: C144 (blue), C145 (green)
- Pin 10: C146 (blue), C147 (green)
- Pin 9: C148 (blue), C149 (green)
- Pin 8: C150 (blue), C151 (green)
- Pin 7: C152 (blue), C153 (green)
- Pin 6: C154 (blue), C155 (green)
- Pin 5: C156 (blue), C157 (green)
- Pin 4: C158 (blue), C159 (green)
- Pin 3: C160 (blue), C161 (green)
- Pin 2: C162 (blue), C163 (green)
- Pin 1: C164 (blue), C165 (green)
- GND: C166 (blue), C167 (green)

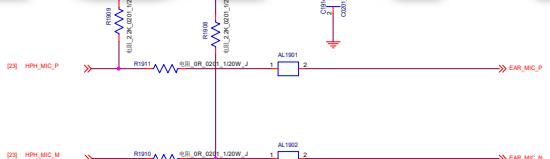
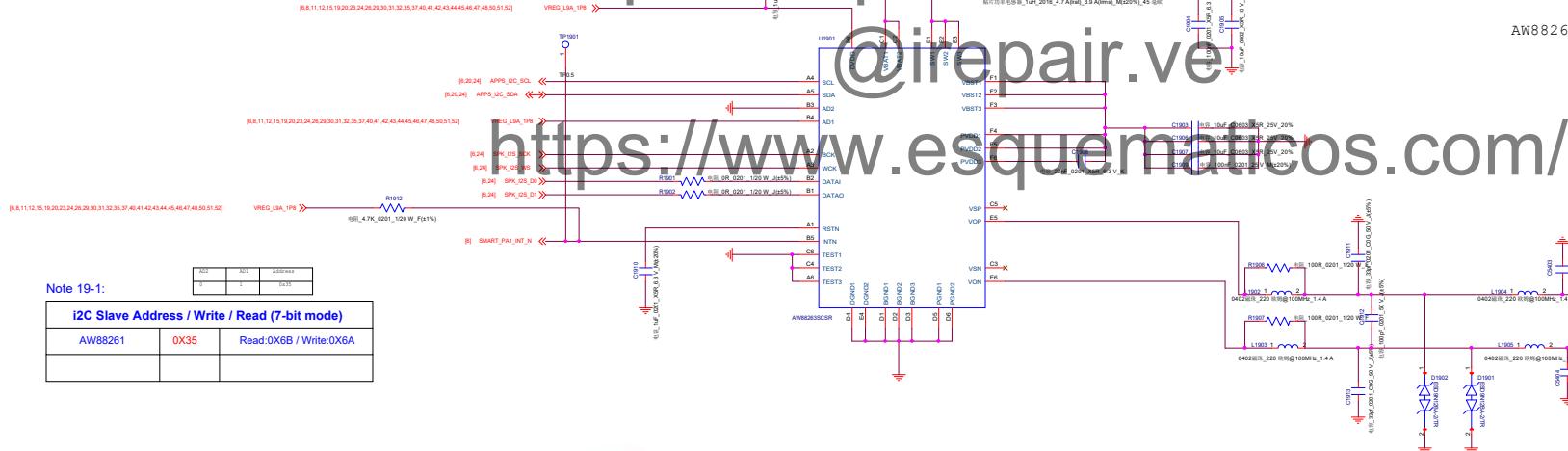
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Title	PMR735A_Power
Size	Project QN6139A_MB_V1
D	Rev V1
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# RECEIVER\_PA ANIBAL GARCIA VENEZUELA

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AW88261FCR

AW88261:R1903:NC, C1910:luF, C1908:22nF, R1904/R1905:NC



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Title PM6150A\_DISPLAY

Size D	Project QN6139A_MB_V1	Rev V1
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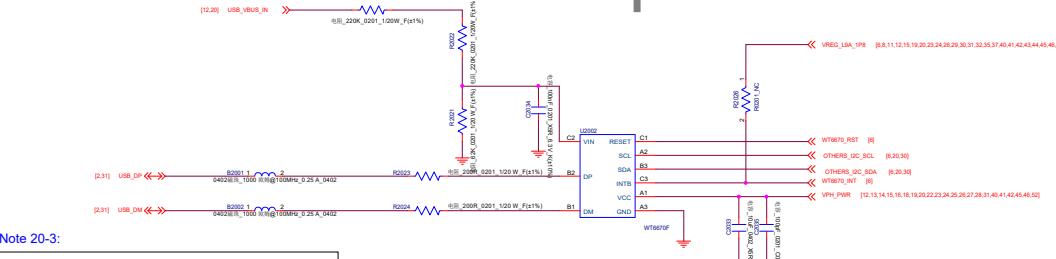
Date: Thursday, January 13, 2022 Sheet 19 of 60

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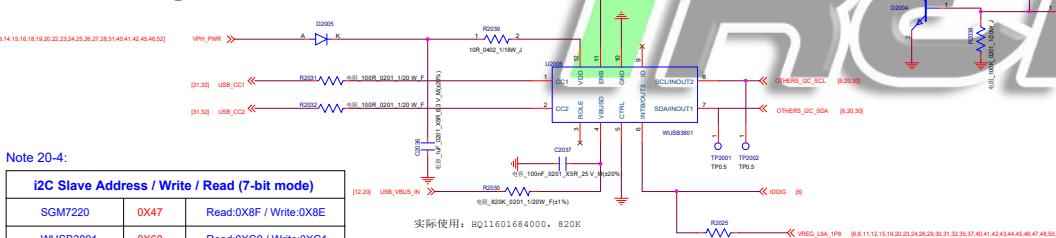
## QC logic



Note 2-3:

I2C Slave Address / Write / Read (7-bit mode)		
WT6670F		
Z350	0X35	Read:0X6B / Write:0X6A

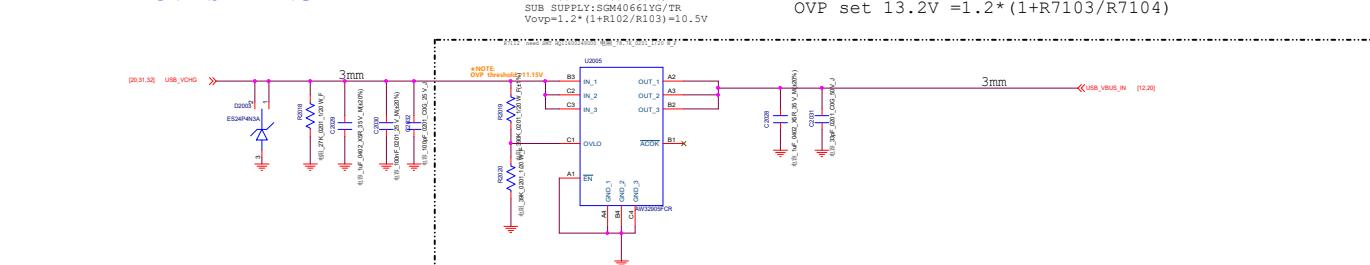
## CC logic



Note 20-4:

I2C Slave Address / Write / Read (7-bit mode)		
SGM720	0X47	Read:0X8F / Write:0X8E
WUSB3801	0X60	Read:0XC0 / Write:0XC1

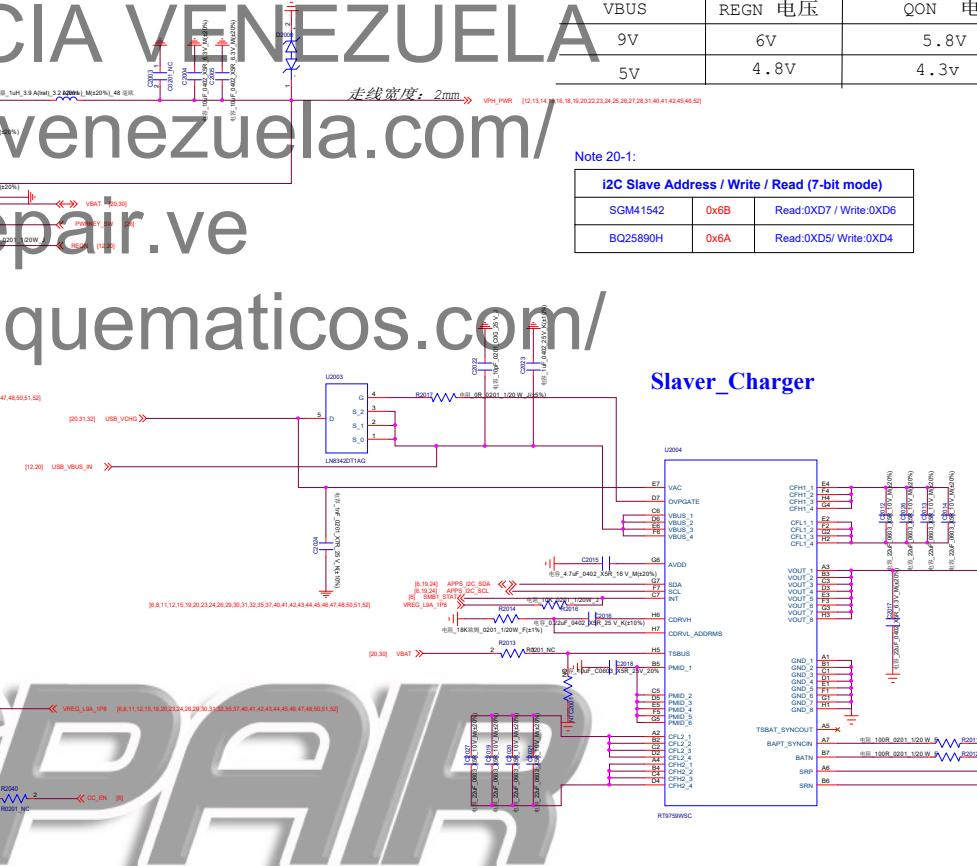
## OVP SETTING



Note 20-1:

I2C Slave Address / Write / Read (7-bit mode)		
SGM4154	0x6B	Read:0XD7 / Write:0XD6
BQ25890H	0x6A	Read:0XD5 / Write:0XD4

## Slaver\_Charger



Note 20-2:

I2C Slave Address / Write / Read (7-bit mode)		
SC8551	0x65	Read:0xC8 / Write:0XA
BQ25970		

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Title PM7250B\_CHARGE/FG

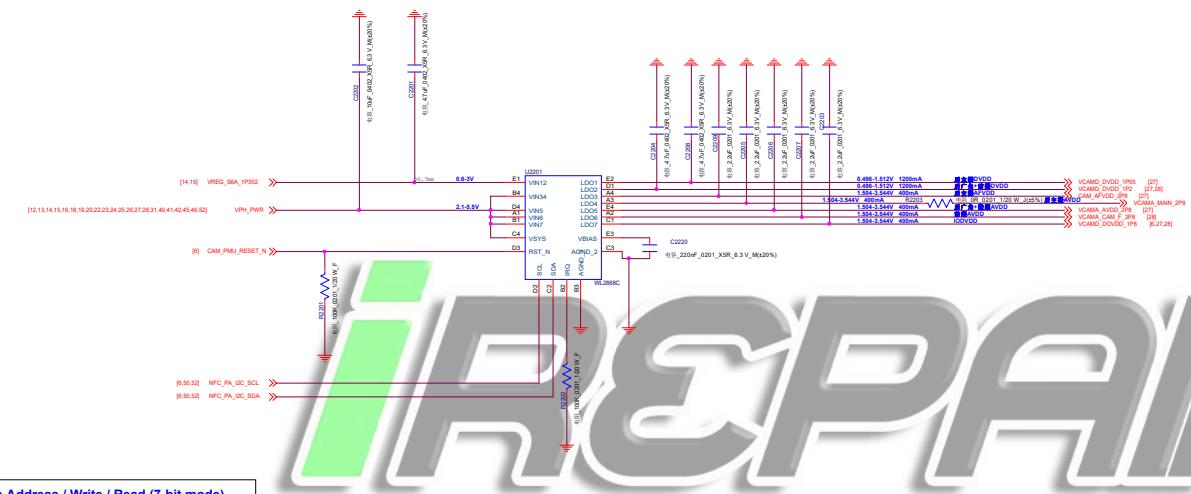
Size D Project QN6139A\_MB\_V1 Rev V1

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## Note 22-1:

I <sup>2</sup> C Slave Address / Write / Read (7-bit mode)		
ET5907MV	0X35	Read:0X6B / Write:0X6A
FAN53870UC00X	0X35	Read:0X6B / Write:0X6A
WL2868C-20/TR	0X2F	Read:0XF5 / Write:0XE5

CAMERA 供电电压分布

	<b>AVDD</b>	<b>DVDD</b>	<b>DOVDD</b>	<b>AFVDD</b>
<b>FCAM13M</b>	2.8V	<b>1.1V</b>	1.8V	NA
<b>FCAM16M</b>	2.8V	1.2V	1.8V	NA
<b>FCAM8M</b>	2.8V	1.2V	1.8V	NA
<b>RCAM50MAF</b>	2.8V	<b>1.05V</b>	1.8V	2.8V
<b>RCAM50MOIS</b>	2.8V	<b>1.05V</b>	1.8V	2.8V
<b>微距2M</b>	2.8V	NA	1.8V	NA
<b>广角8M</b>	2.8V	1.2V	1.8V	NA
<b>景深2M</b>	2.8V	NA	1.8V	NA

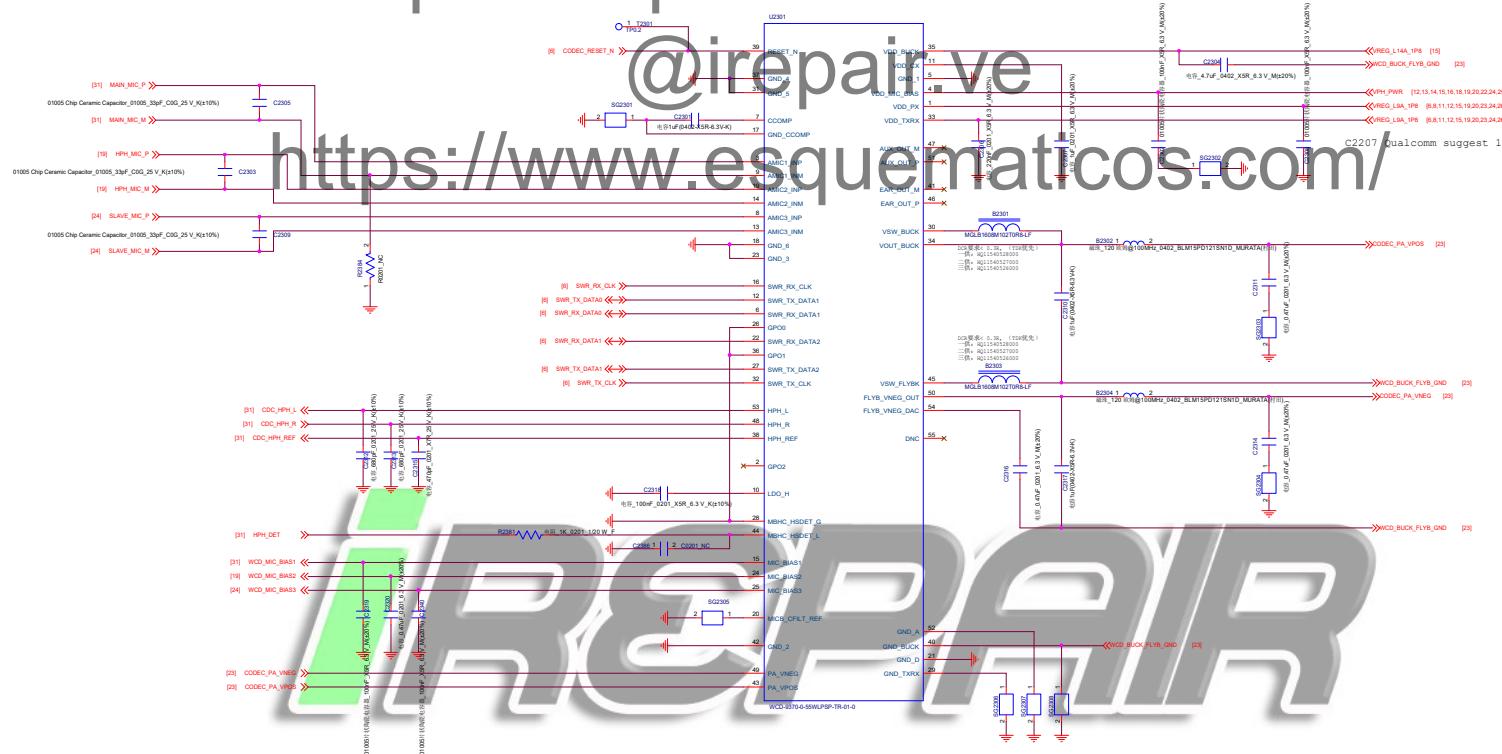
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## Title Camera LDO

Size D	Project QN6139A_MB_V1	Rev V1
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Title **AUDIO\_CODEC\_WCD9370**

Size D	Project QN6139A_MB_V1	Rev V1
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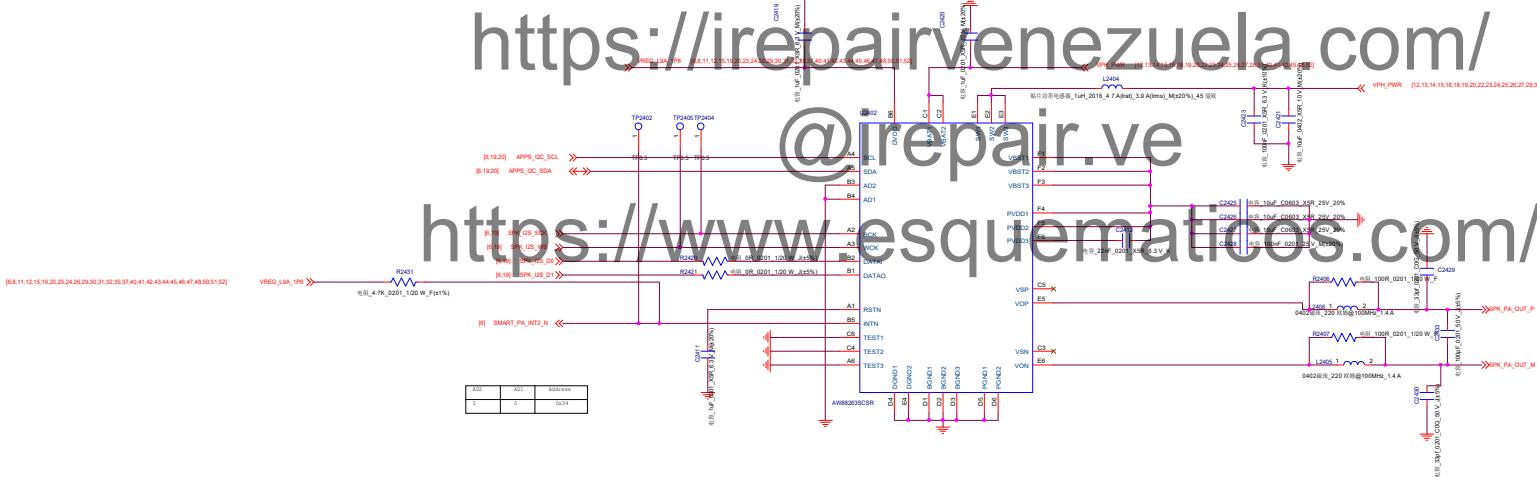
Date: Thursday, January 13, 2022 Sheet 23 of 60

# SPEAKER

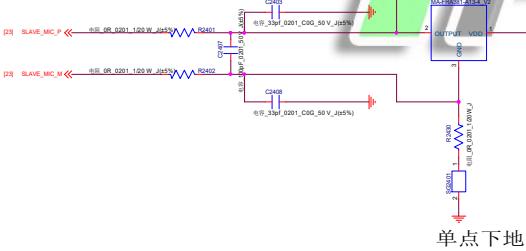
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The diagram illustrates the internal connections of the TI2402 and TI2404 integrated circuits. Key connections include:

- TI2402:** Pin 19 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>1</sub>. Pin 18 (GND) connects to GND. Pin 17 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>2</sub>. Pin 16 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>3</sub>. Pin 15 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>4</sub>. Pin 14 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>5</sub>. Pin 13 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>6</sub>. Pin 12 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>7</sub>. Pin 11 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>8</sub>. Pin 10 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>9</sub>. Pin 9 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>10</sub>. Pin 8 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>11</sub>. Pin 7 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>12</sub>. Pin 6 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>13</sub>. Pin 5 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>14</sub>. Pin 4 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>15</sub>. Pin 3 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>16</sub>. Pin 2 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>17</sub>. Pin 1 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>18</sub>.
- TI2404:** Pin 19 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>19</sub>. Pin 18 (GND) connects to GND. Pin 17 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>20</sub>. Pin 16 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>21</sub>. Pin 15 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>22</sub>. Pin 14 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>23</sub>. Pin 13 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>24</sub>. Pin 12 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>25</sub>. Pin 11 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>26</sub>. Pin 10 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>27</sub>. Pin 9 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>28</sub>. Pin 8 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>29</sub>. Pin 7 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>30</sub>. Pin 6 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>31</sub>. Pin 5 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>32</sub>. Pin 4 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>33</sub>. Pin 3 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>34</sub>. Pin 2 (V<sub>DD</sub>) connects to V<sub>DD</sub> and C<sub>35</sub>. Pin 1 (V<sub>SS</sub>) connects to V<sub>SS</sub> and C<sub>36</sub>.
- External Components:** A 100Ω resistor (R<sub>1</sub>) is connected between Pin 19 of TI2402 and Pin 19 of TI2404. A 100nF capacitor (C<sub>1</sub>) is connected between Pin 19 of TI2402 and ground. A 100nF capacitor (C<sub>2</sub>) is connected between Pin 17 of TI2402 and ground. A 100nF capacitor (C<sub>3</sub>) is connected between Pin 16 of TI2402 and ground. A 100nF capacitor (C<sub>4</sub>) is connected between Pin 15 of TI2402 and ground. A 100nF capacitor (C<sub>5</sub>) is connected between Pin 14 of TI2402 and ground. A 100nF capacitor (C<sub>6</sub>) is connected between Pin 13 of TI2402 and ground. A 100nF capacitor (C<sub>7</sub>) is connected between Pin 12 of TI2402 and ground. A 100nF capacitor (C<sub>8</sub>) is connected between Pin 11 of TI2402 and ground. A 100nF capacitor (C<sub>9</sub>) is connected between Pin 10 of TI2402 and ground. A 100nF capacitor (C<sub>10</sub>) is connected between Pin 9 of TI2402 and ground. A 100nF capacitor (C<sub>11</sub>) is connected between Pin 8 of TI2402 and ground. A 100nF capacitor (C<sub>12</sub>) is connected between Pin 7 of TI2402 and ground. A 100nF capacitor (C<sub>13</sub>) is connected between Pin 6 of TI2402 and ground. A 100nF capacitor (C<sub>14</sub>) is connected between Pin 5 of TI2402 and ground. A 100nF capacitor (C<sub>15</sub>) is connected between Pin 4 of TI2402 and ground. A 100nF capacitor (C<sub>16</sub>) is connected between Pin 3 of TI2402 and ground. A 100nF capacitor (C<sub>17</sub>) is connected between Pin 2 of TI2402 and ground. A 100nF capacitor (C<sub>18</sub>) is connected between Pin 1 of TI2402 and ground.
- Other Components:** A 100Ω resistor (R<sub>2</sub>) is connected between Pin 19 of TI2404 and Pin 19 of TI2402. A 100nF capacitor (C<sub>19</sub>) is connected between Pin 19 of TI2404 and ground. A 100nF capacitor (C<sub>20</sub>) is connected between Pin 17 of TI2404 and ground. A 100nF capacitor (C<sub>21</sub>) is connected between Pin 16 of TI2404 and ground. A 100nF capacitor (C<sub>22</sub>) is connected between Pin 15 of TI2404 and ground. A 100nF capacitor (C<sub>23</sub>) is connected between Pin 14 of TI2404 and ground. A 100nF capacitor (C<sub>24</sub>) is connected between Pin 13 of TI2404 and ground. A 100nF capacitor (C<sub>25</sub>) is connected between Pin 12 of TI2404 and ground. A 100nF capacitor (C<sub>26</sub>) is connected between Pin 11 of TI2404 and ground. A 100nF capacitor (C<sub>27</sub>) is connected between Pin 10 of TI2404 and ground. A 100nF capacitor (C<sub>28</sub>) is connected between Pin 9 of TI2404 and ground. A 100nF capacitor (C<sub>29</sub>) is connected between Pin 8 of TI2404 and ground. A 100nF capacitor (C<sub>30</sub>) is connected between Pin 7 of TI2404 and ground. A 100nF capacitor (C<sub>31</sub>) is connected between Pin 6 of TI2404 and ground. A 100nF capacitor (C<sub>32</sub>) is connected between Pin 5 of TI2404 and ground. A 100nF capacitor (C<sub>33</sub>) is connected between Pin 4 of TI2404 and ground. A 100nF capacitor (C<sub>34</sub>) is connected between Pin 3 of TI2404 and ground. A 100nF capacitor (C<sub>35</sub>) is connected between Pin 2 of TI2404 and ground. A 100nF capacitor (C<sub>36</sub>) is connected between Pin 1 of TI2404 and ground.



## *Slave MIC*



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Title	MIC/RCV/SPK
Size D	Project QN6139A_MB_V1
	Rev v1

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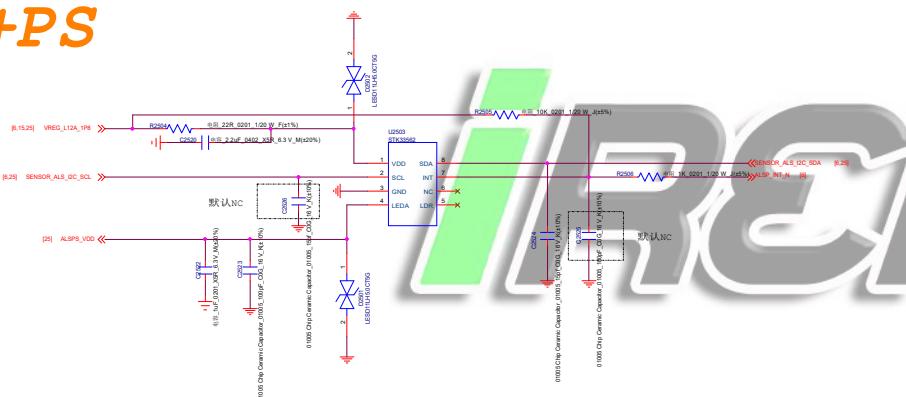
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Note 25-1:

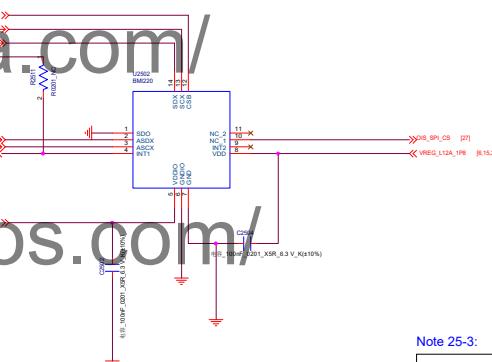
I2C Slave Address / Write / Read (7-bit mode)		
AK09919C-L	0x0E	Write:0x1C / Read:0x1D
MMCS603NJL	0x30	Write:0x60 / Read:0x61

## ALS+PS



Note 25-2:

I2C Slave Address / Write / Read (7-bit mode)		
STK33562	0x46	Write:0x8C / Read:0x8D



Note 25-3:

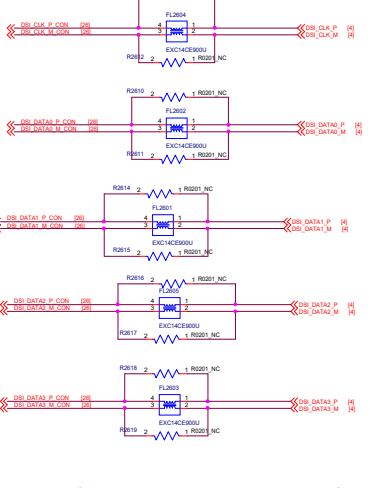
I2C Slave Address / Write / Read		
ICM42602	AD0=0.X68	Write:0xD0 / Read:0xD1

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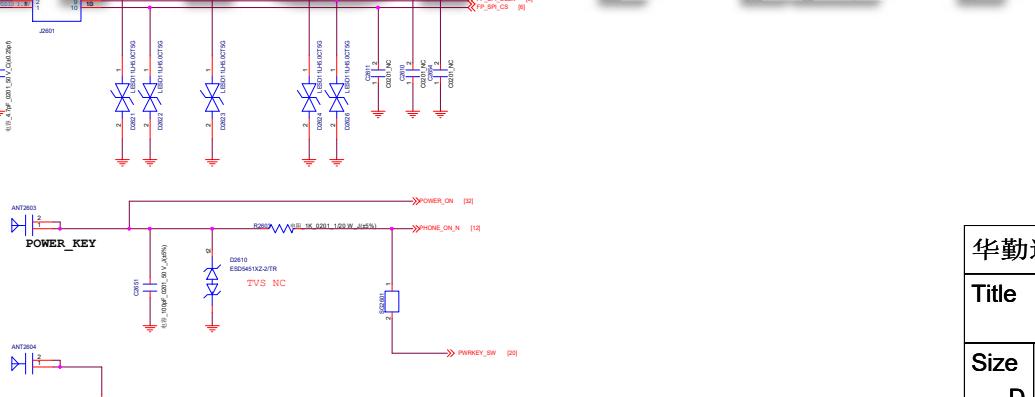
Title  
SENSOR

Size D	Project QN6139A_MB_V1	Rev V1
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First using OR ,Second using EMI Filter.



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Title  
AMOLED/TP/FPS/KEY

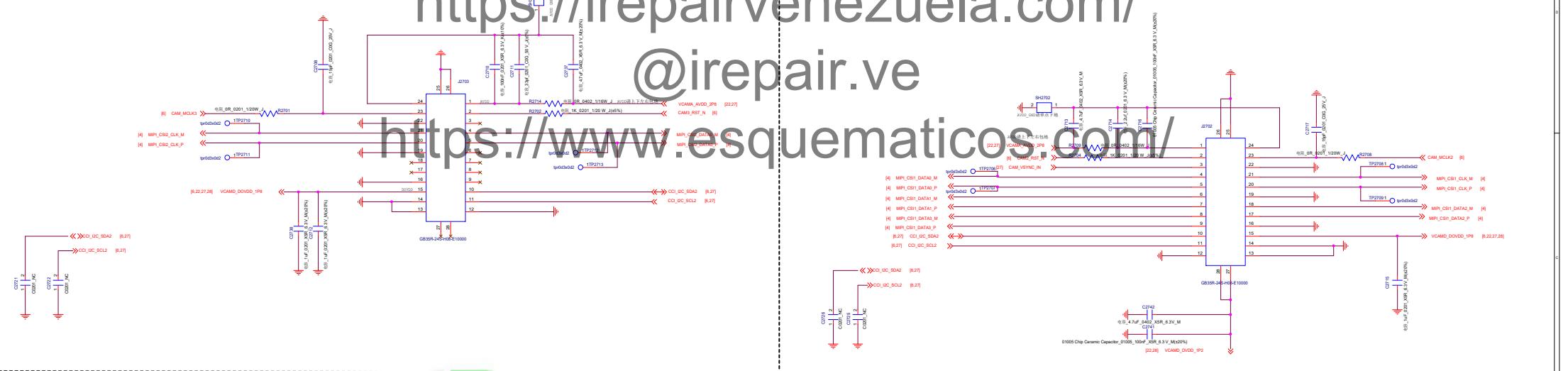
Size	Project	Rev
D	QN6139A_MB_V1	V1

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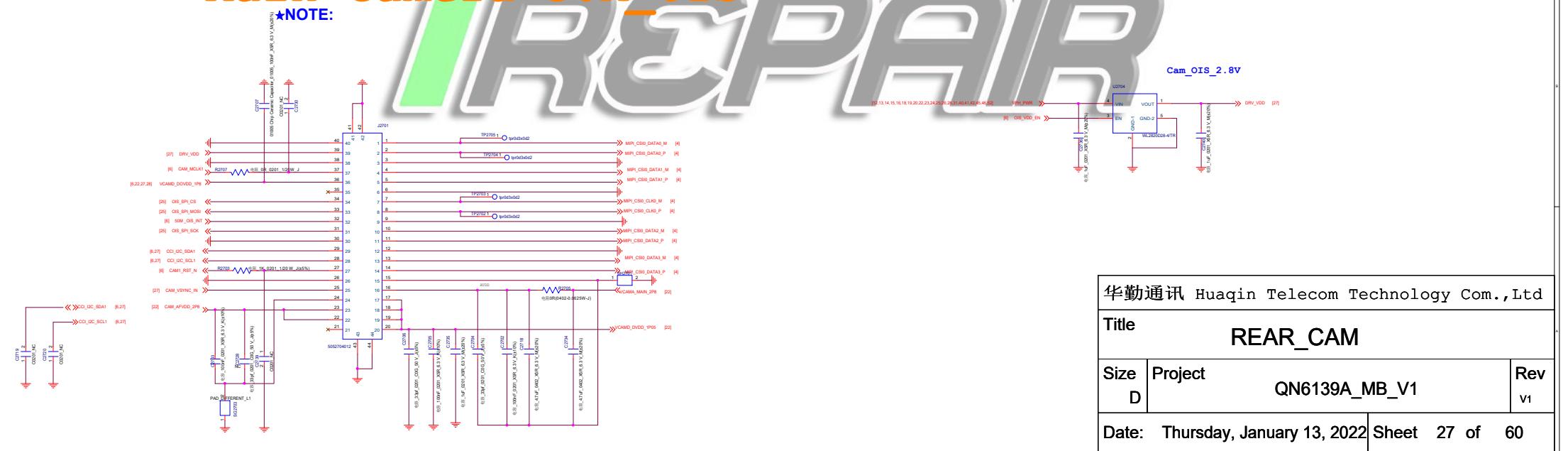
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## Main Camera 50M OIS



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Title REAR\_CAM

Size D Project QN6139A\_MB\_V1 Rev V1

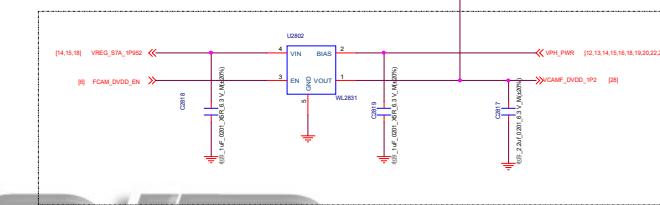
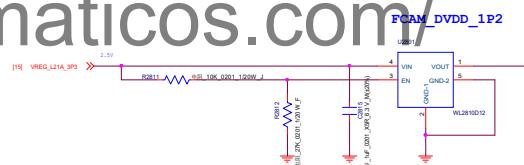
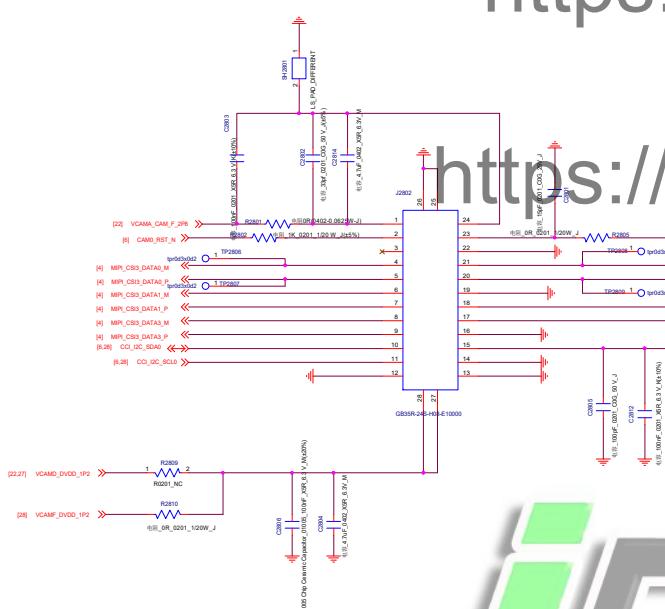
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**Front CAM 16M** ANIBAL GARCIA VENEZUELA

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Title	FRONT_CAM
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Size D	Project QN6139A_MB_V1	Rev V1
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Date: Thursday, January 13, 2022	Sheet 28 of 60
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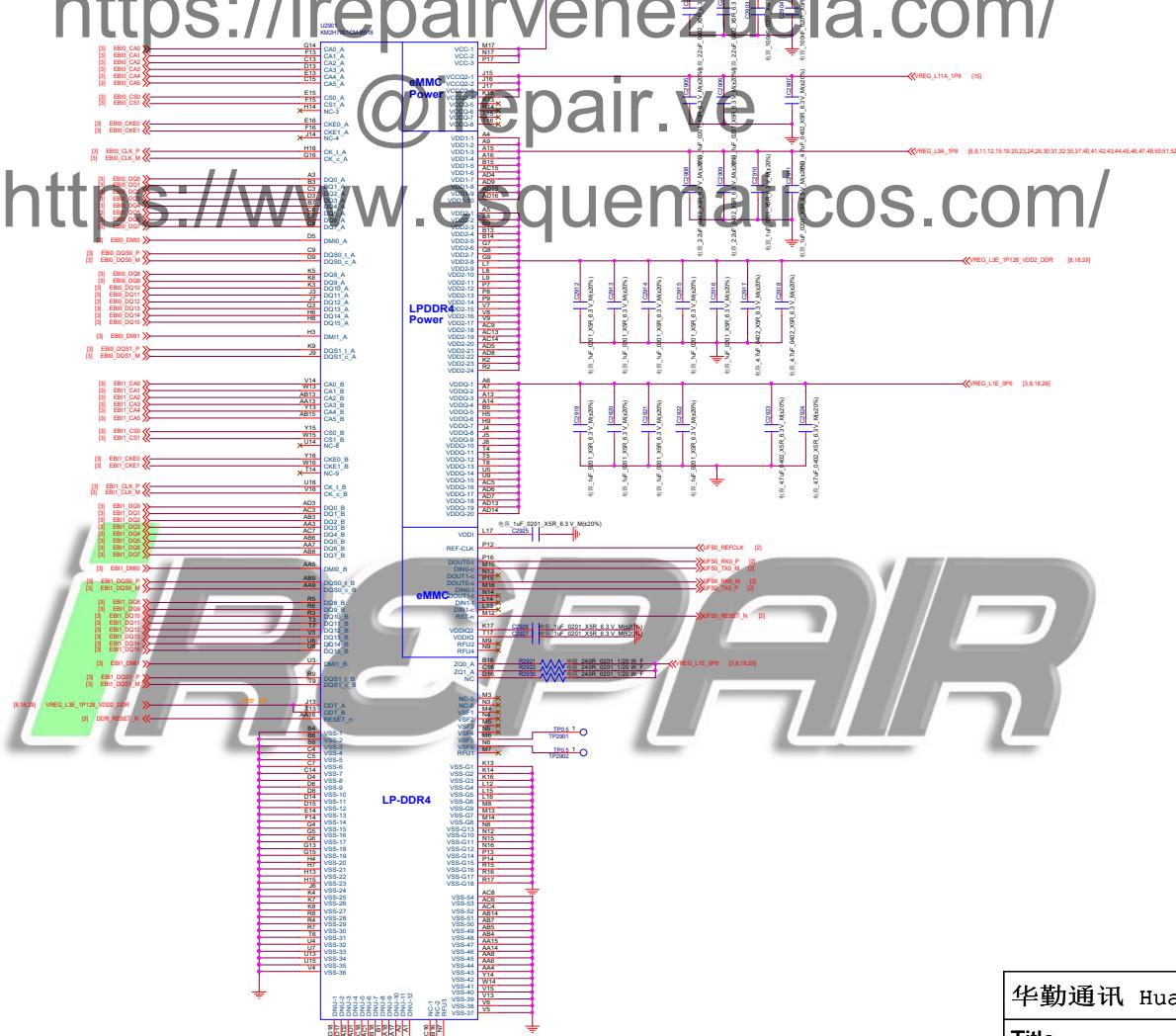
**UFS+LPDDR4x**

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Title

UFS+LPDDR4x

Size  
D

Project  
QN6139A\_MB\_V1

Rev  
V1

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# 3IN2 SIM1 & SIM2 & ASD

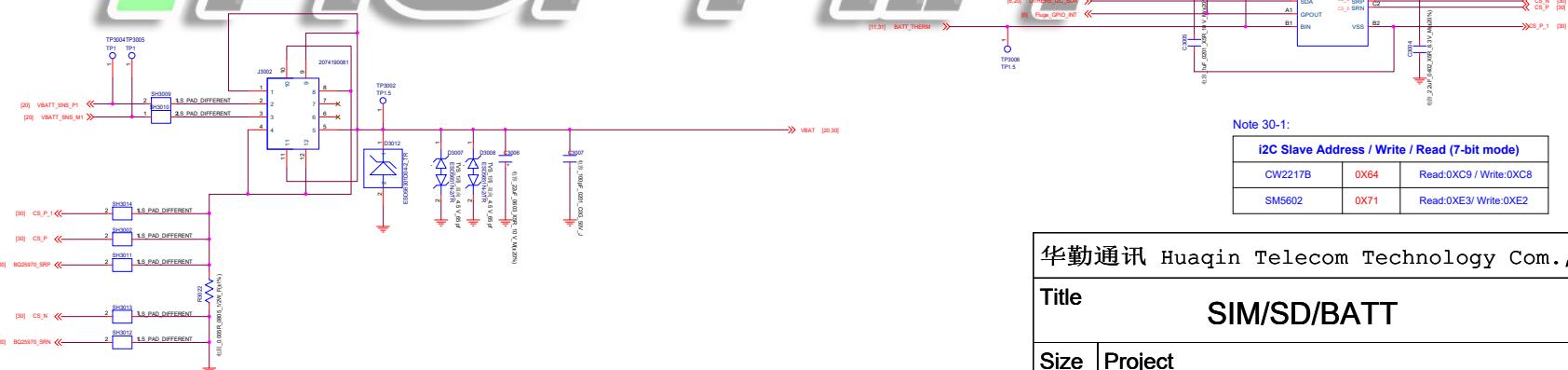
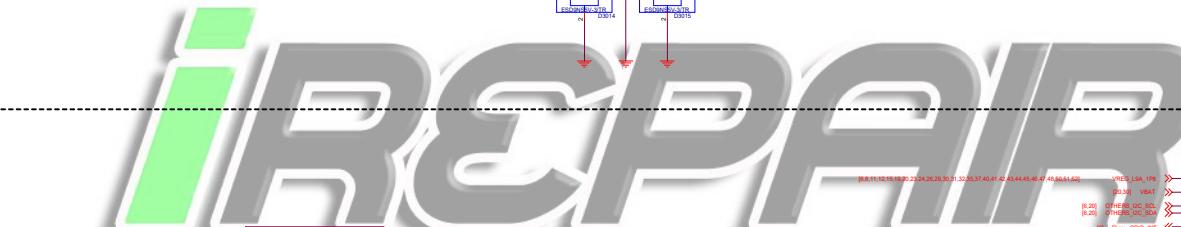
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BATT CON



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Title  
SIM/SD/BATT

Size D Project QN6139A\_MB\_V1 Rev V1

Date: Thursday, January 13, 2022 Sheet 30 of 60

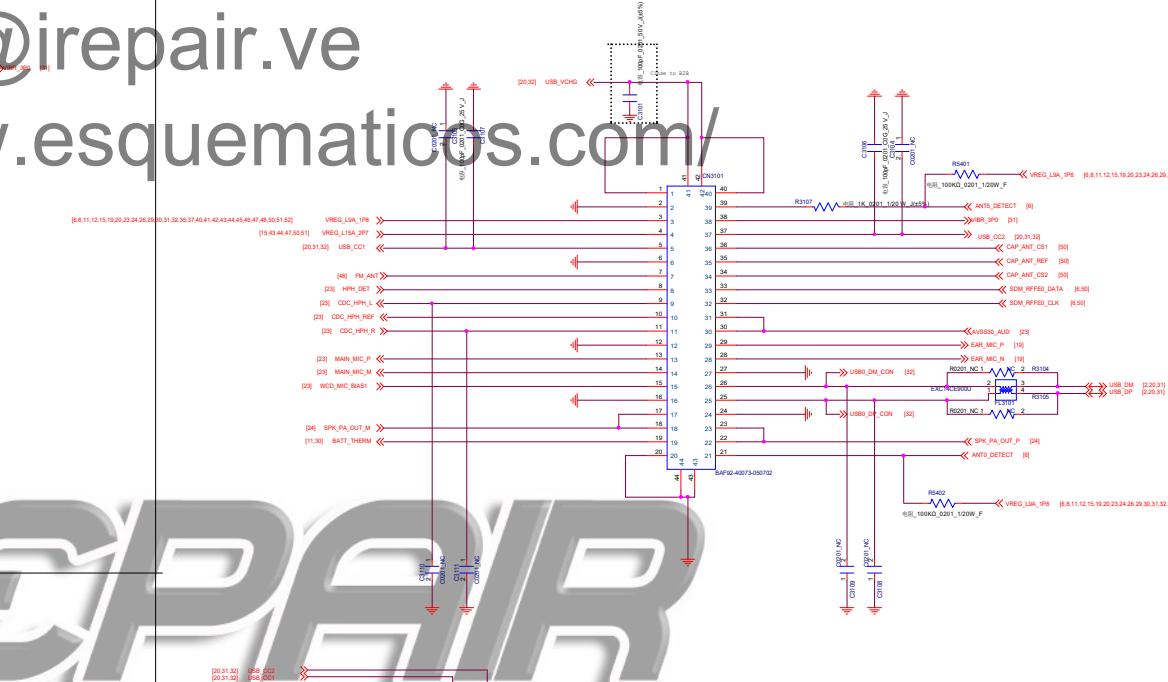
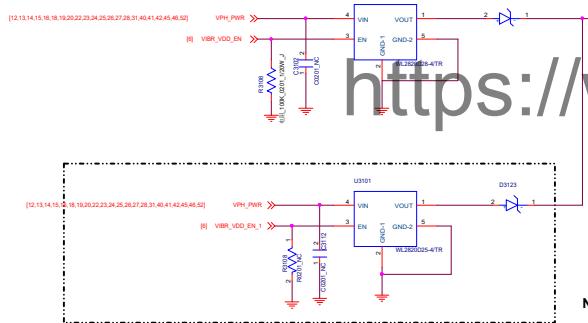
VIB DIRVE

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USB&UART



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Title			LINK/MOTO
Size D	Project	QN6139A_MB_V1	Rev V1
Date: Thursday, January 13, 2022		Sheet	31 of 60

**KEYPAD**

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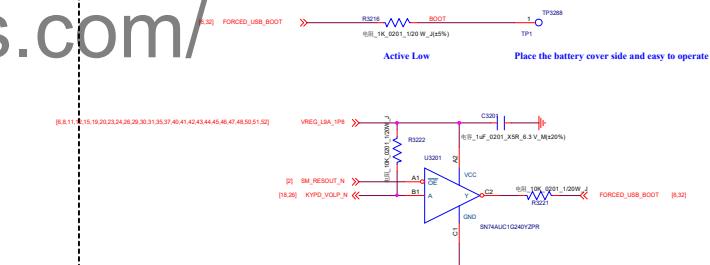
VCHG

<https://irepairvenezuela.com/>

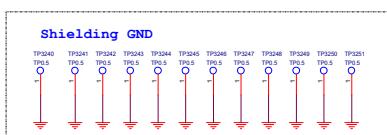
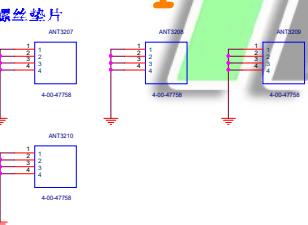
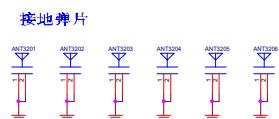
**FORCE USB BOOT**



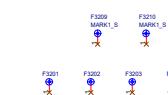
**UART**



*Grounding shrapnel*



*HOLE and MARK*



**JTAG**



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Title TEST\_POINT/SIELD

Size D Project QN6139A\_MB\_V1 Rev V1

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File	>Title>
Document Number	D CNE150A_ML_V1
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Title  
Reserve\_NC\_BB

Size	Project	Rev
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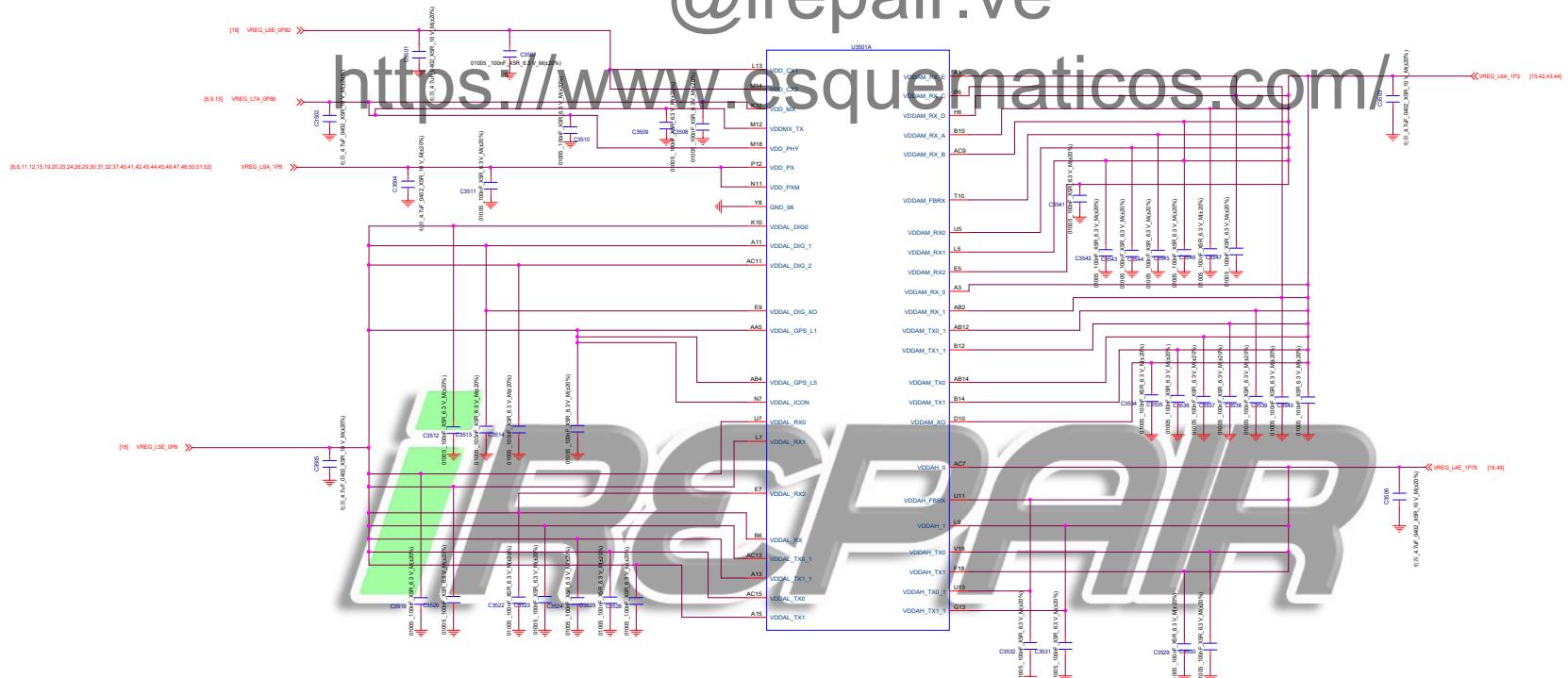
Date: Thursday, January 13, 2022 Sheet 34 of 60

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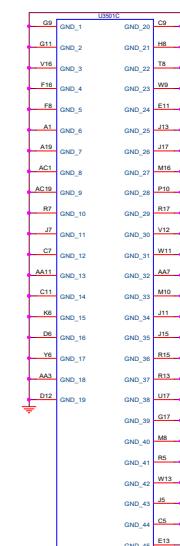
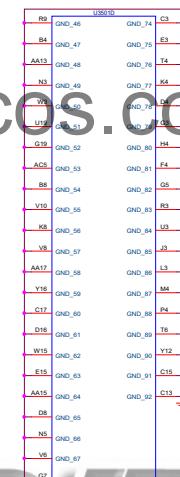
华勤通讯 Huaqin Telecom Technology Com., Ltd	
Title	SDR735_PWR
Size	Project
D	QN6139A_MB_V1
Date:	Thursday, January 13, 2022
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Title	SDR735_WMSS		
Size D	Project QN6139A_MB_V1	Rev V1	
Date: Thursday, January 13, 2022			Sheet 36 of 54

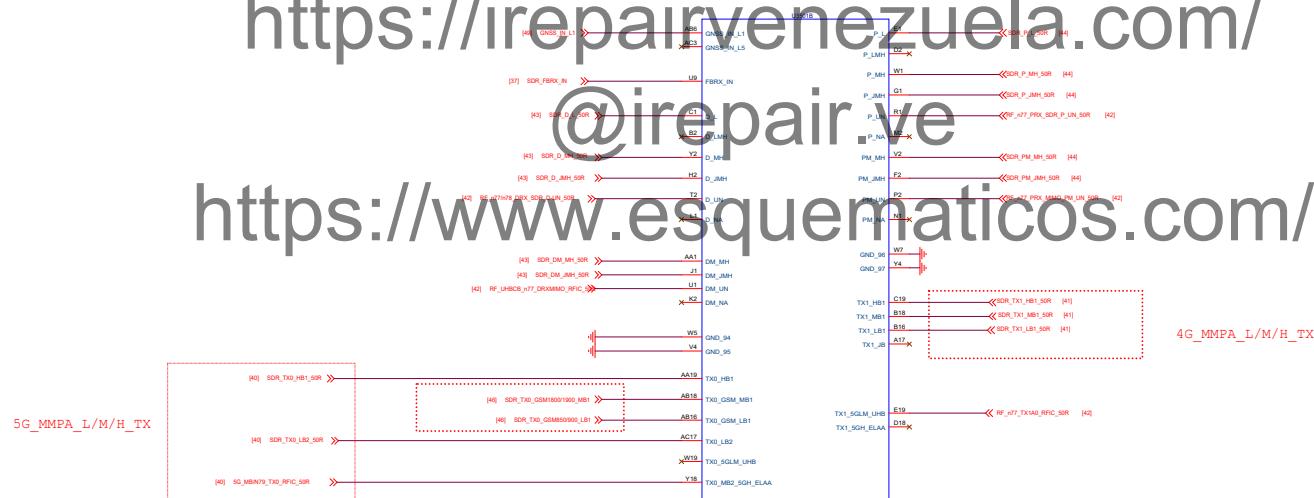
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SDR735 RF Port

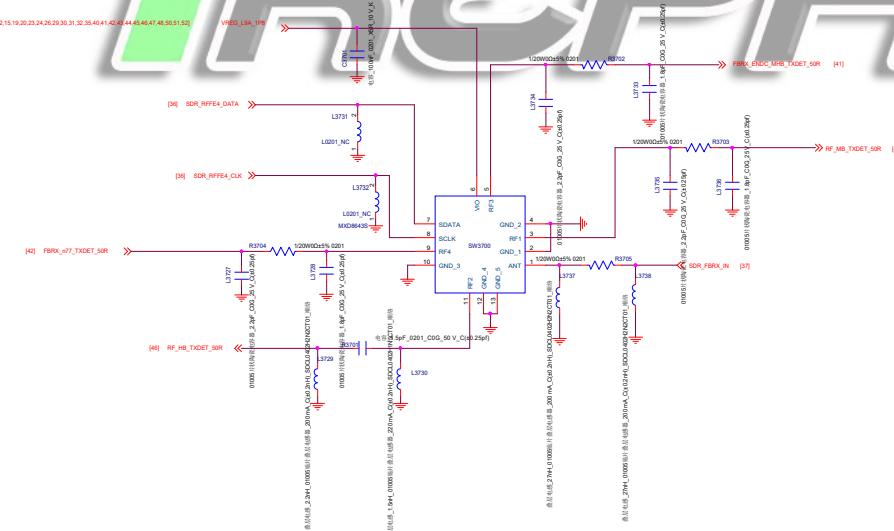
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FBRX CPL SP4T



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Title SDR735_TX_RX		
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Size D	Project QN6139A_MB_V1	Rev V1
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Title  
Reserve\_NC\_RF

Size	Project	Rev
D	QN6139A_MB_V1	v1

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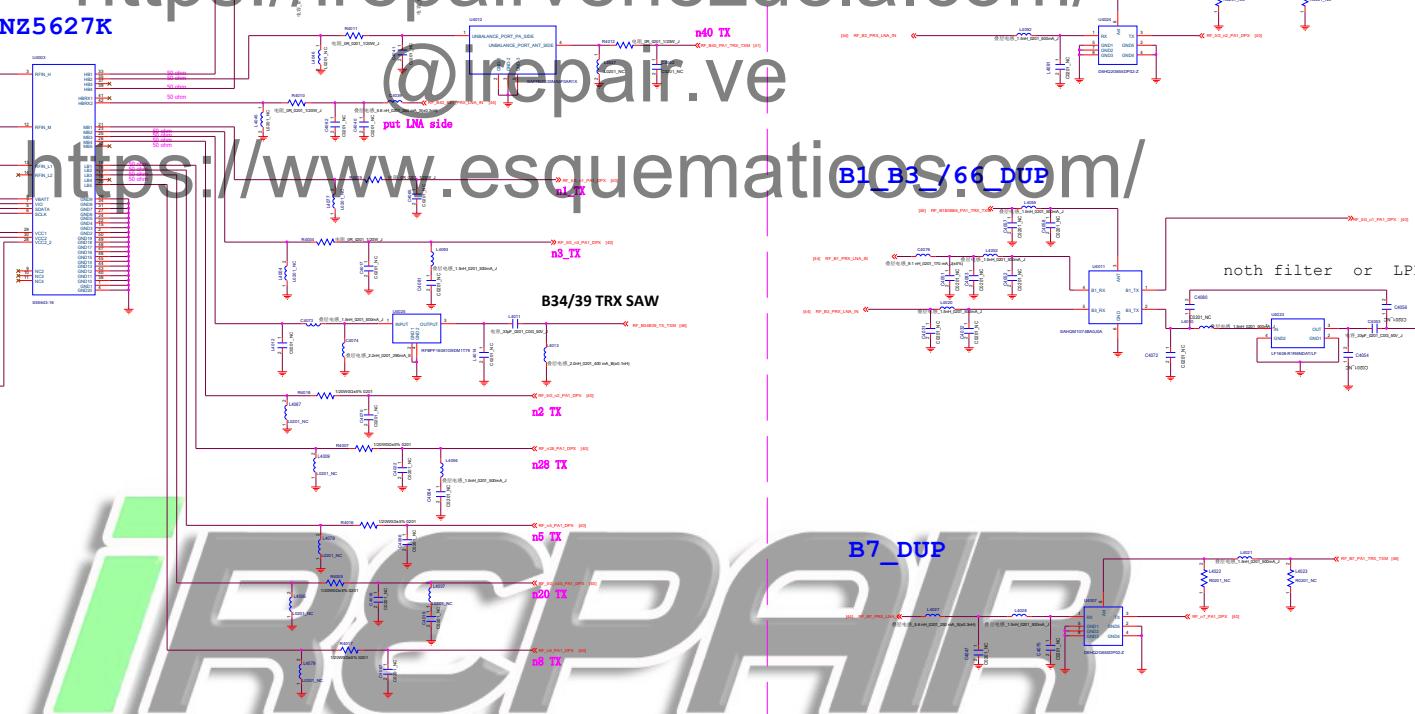


华勤通讯 Huaqin Telecom Technology Com., Ltd

Title

Size D	Project QN6139A_MB_V1	Rev v1
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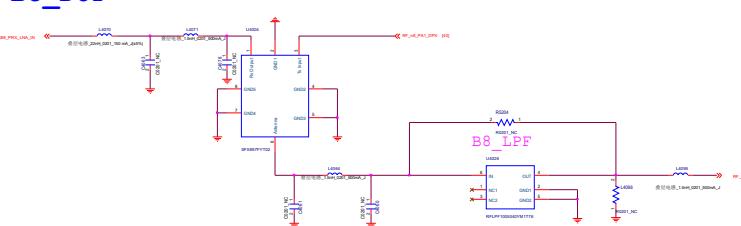


## LB TRX

B5\_B26\_DUP



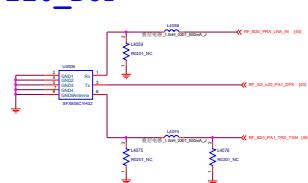
B8\_DUP



B28\_DUP



B20\_DUP

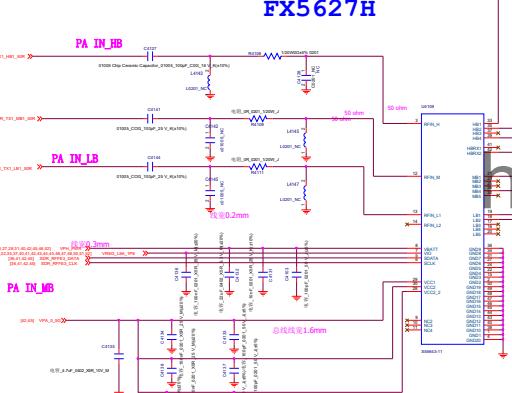


华勤通讯 Huaqin Telecom Technology Com., Ltd		
Title 5G_PA0_NZ5627K		
Size E	Project QN6139A_MB_V1	Rev v1
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4G 5G PA

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FX5627H



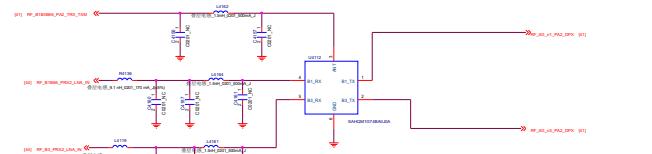
PRX\_MIMO\_SP4T\_MHB

MB+HB\_PA2\_OUT\_Diplexer

B13\_DUP



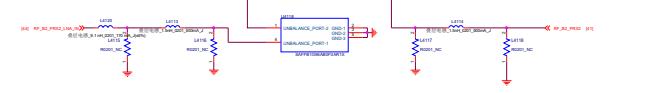
B1\_B3\_66\_QUAD



B12\_B17\_DUP

B7\_DUP

B2\_SAW



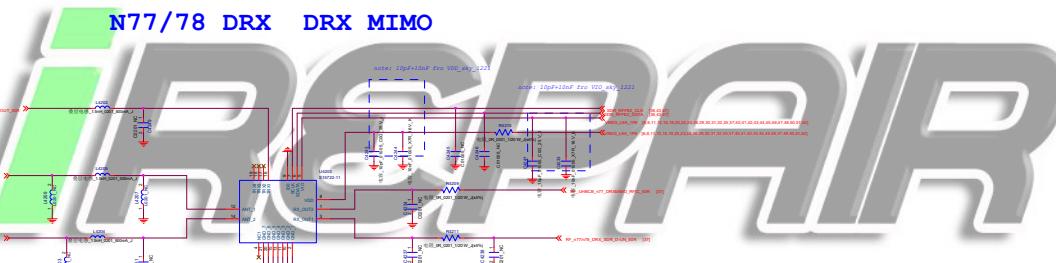
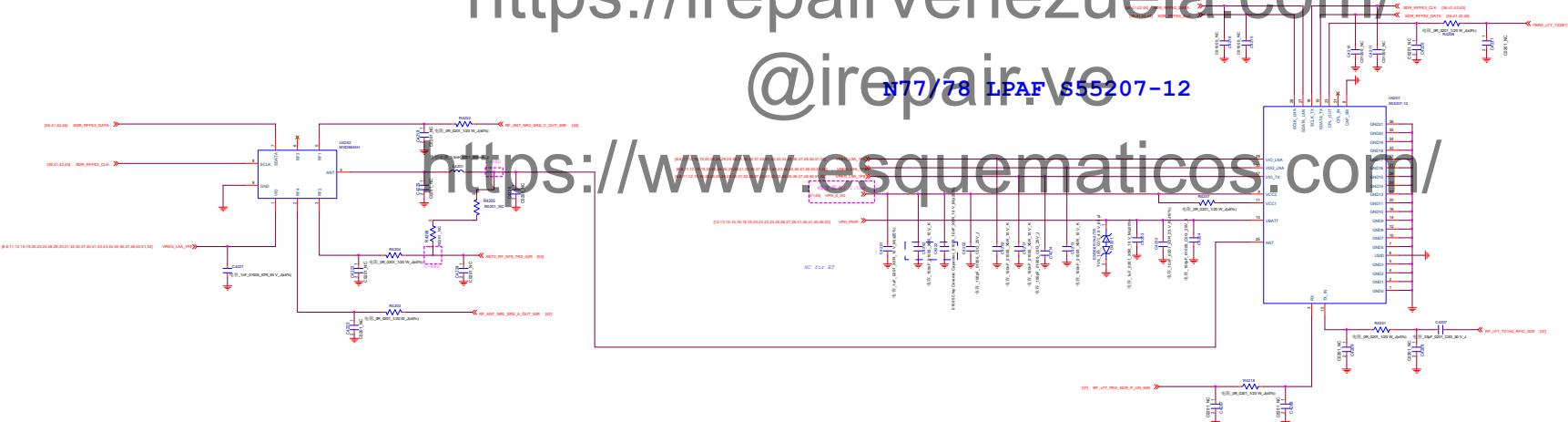
华勤通讯 Huaqin Telecom Technology Com., Ltd		
Title 5G_PA2_FX5627H		
Size E	Project QN6139A_MB_V1	Rev v1
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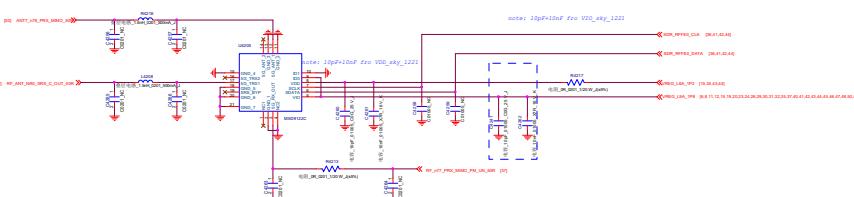
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N77/78 LPAF S55207-12



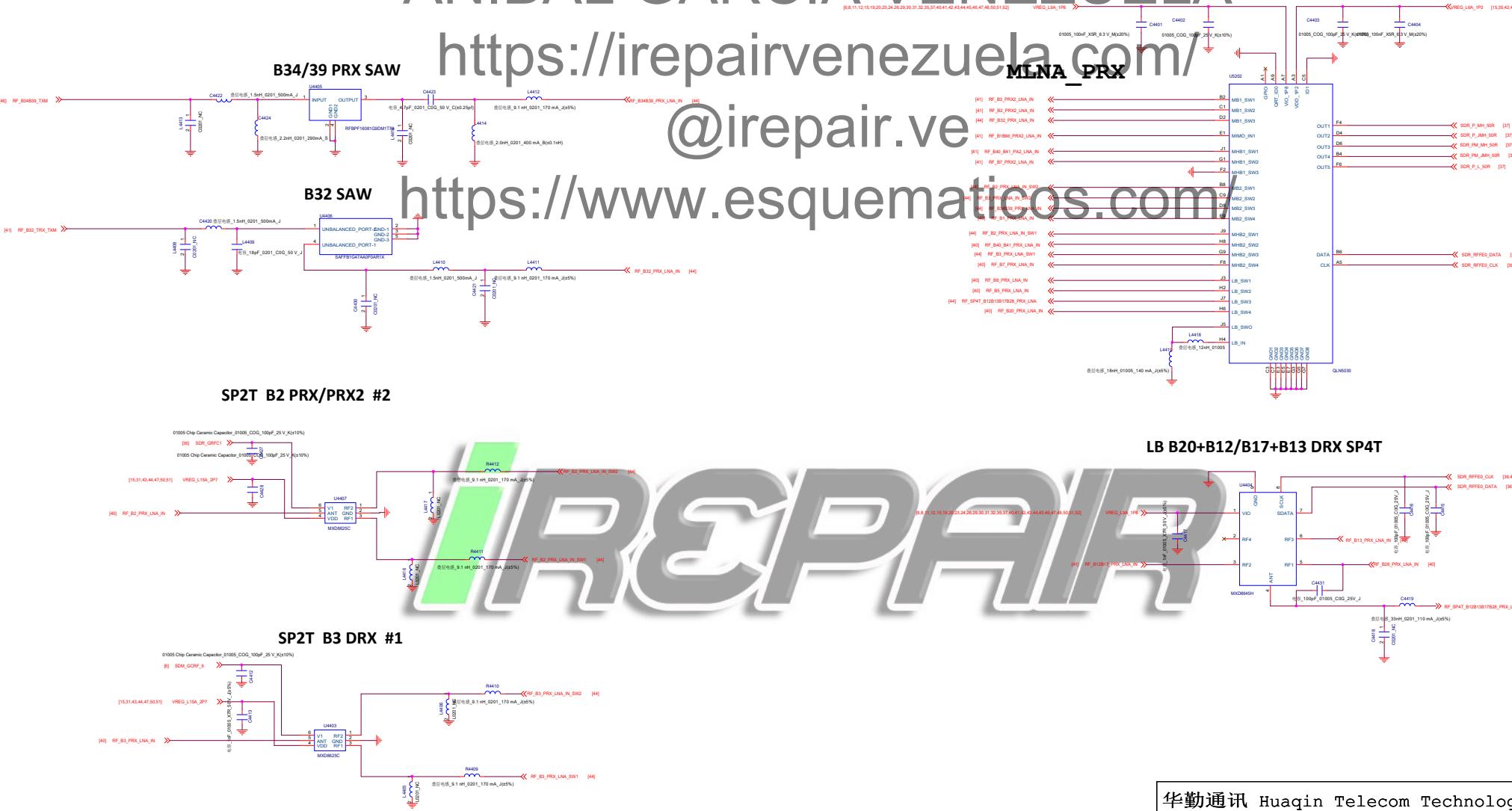
RF\_N77/78 PRX\_MIMO



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Title N77/N78/N79/B42_LPAF_FX6877	
Size E	Project QN6139A_MB_V1
Rev v1	

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Title PRx_PRX MIMO		
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Size D	Project QN6139A_MB_V1	Rev V1
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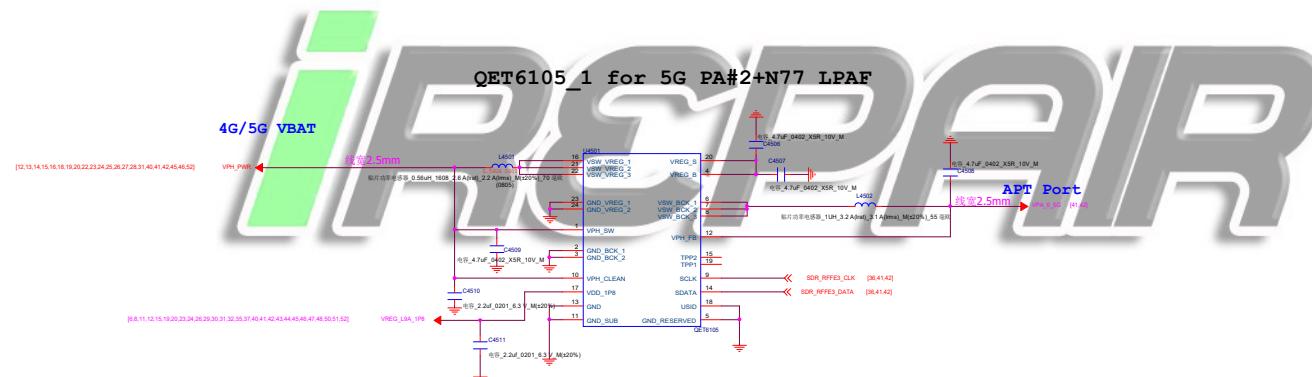
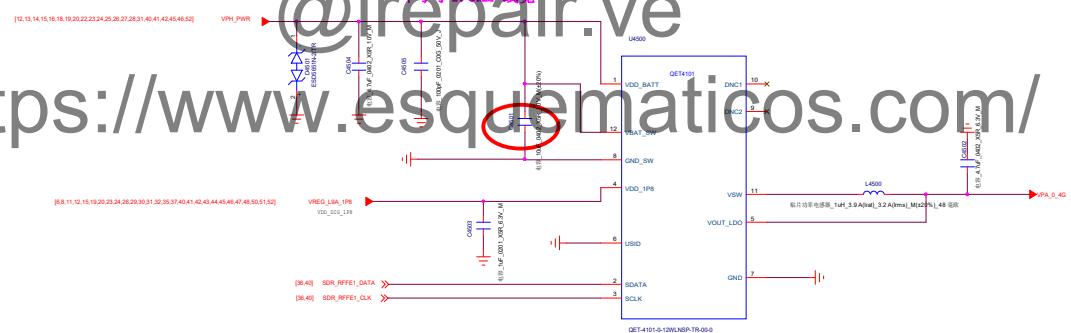
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QET4101&QET6105\_1 for 3/4/5G PA#1

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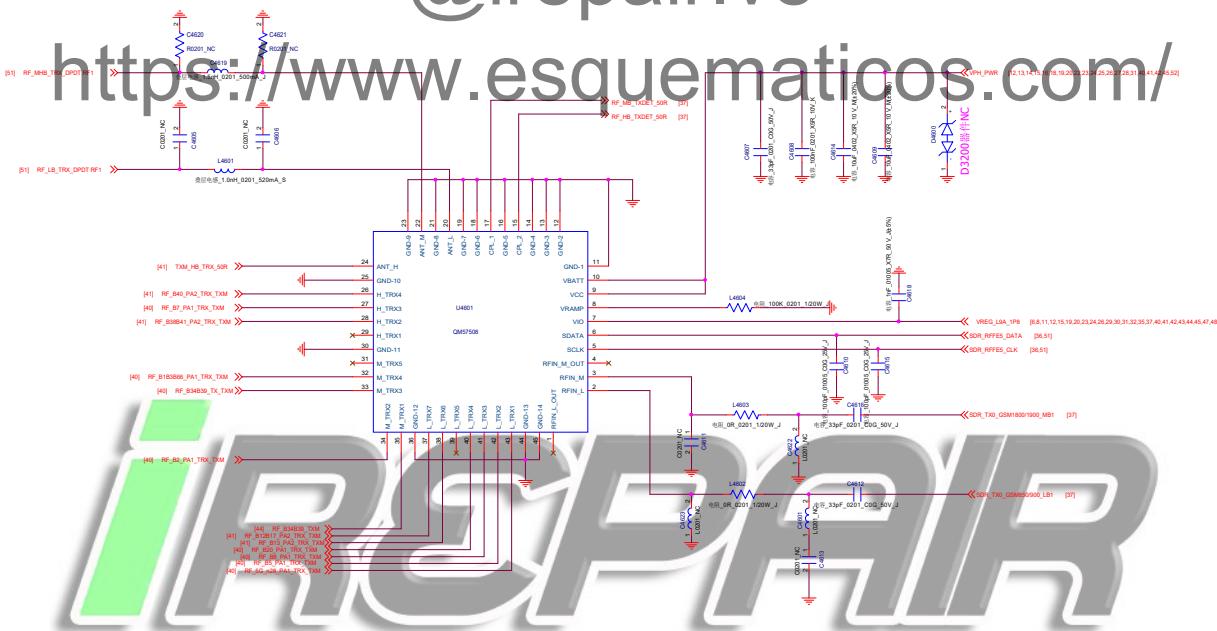
华勤通讯 Huaqin Telecom Technology Com.,Ltd			
Title	QFE6105_QET4101		
Size D	Project QN6139A_MB_V1	Rev V1	
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Title RF_TXM		
Size D	Project QN6139A_MB_V1	Rev v1
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MB+HB DRX 2

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SP4T DRX2 (DIP)

B7 DRX 2

B41 DRX 2

B40 DRX 2

B1/66\_B3 DRX2

B2 DRX 2

SP2T B1B3B66+B2 DRX2 (DIP) #7

SP2T B1/66+B2 DRX2 (LNA BANK) #6



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Title  
DRX MIMO

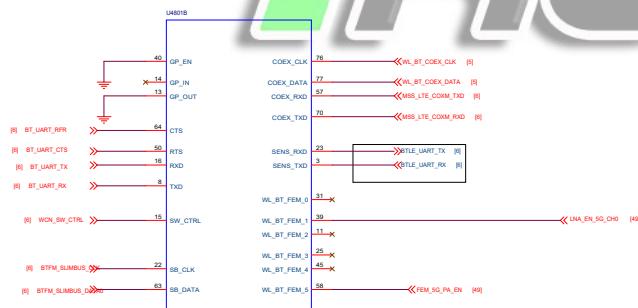
Size D Project QN6139A\_MB\_V1 Rev V1

Date: Thursday, January 13, 2022 Sheet 47 of 54

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<https://irepairvenezuela.com/>  
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<https://www.esquematicos.com/>



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Title  
**WCN3998**

Size D Project QN6139A\_MB\_V1 Rev V1

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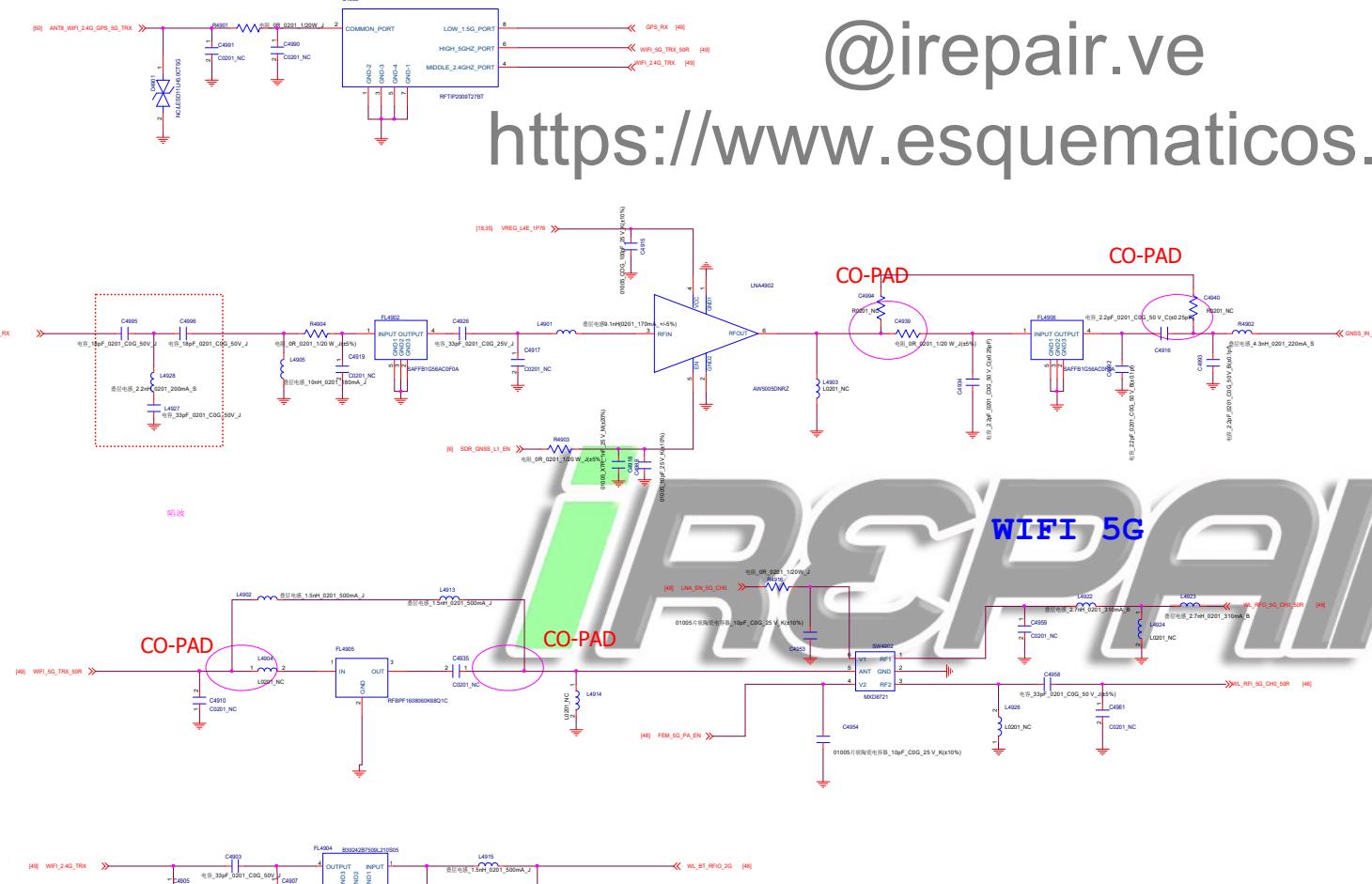
ANT6:WIFI CHO 5G&2.4G\_GPS\_B32\_PRX

# ANIBAL GARCIA VENEZUELA

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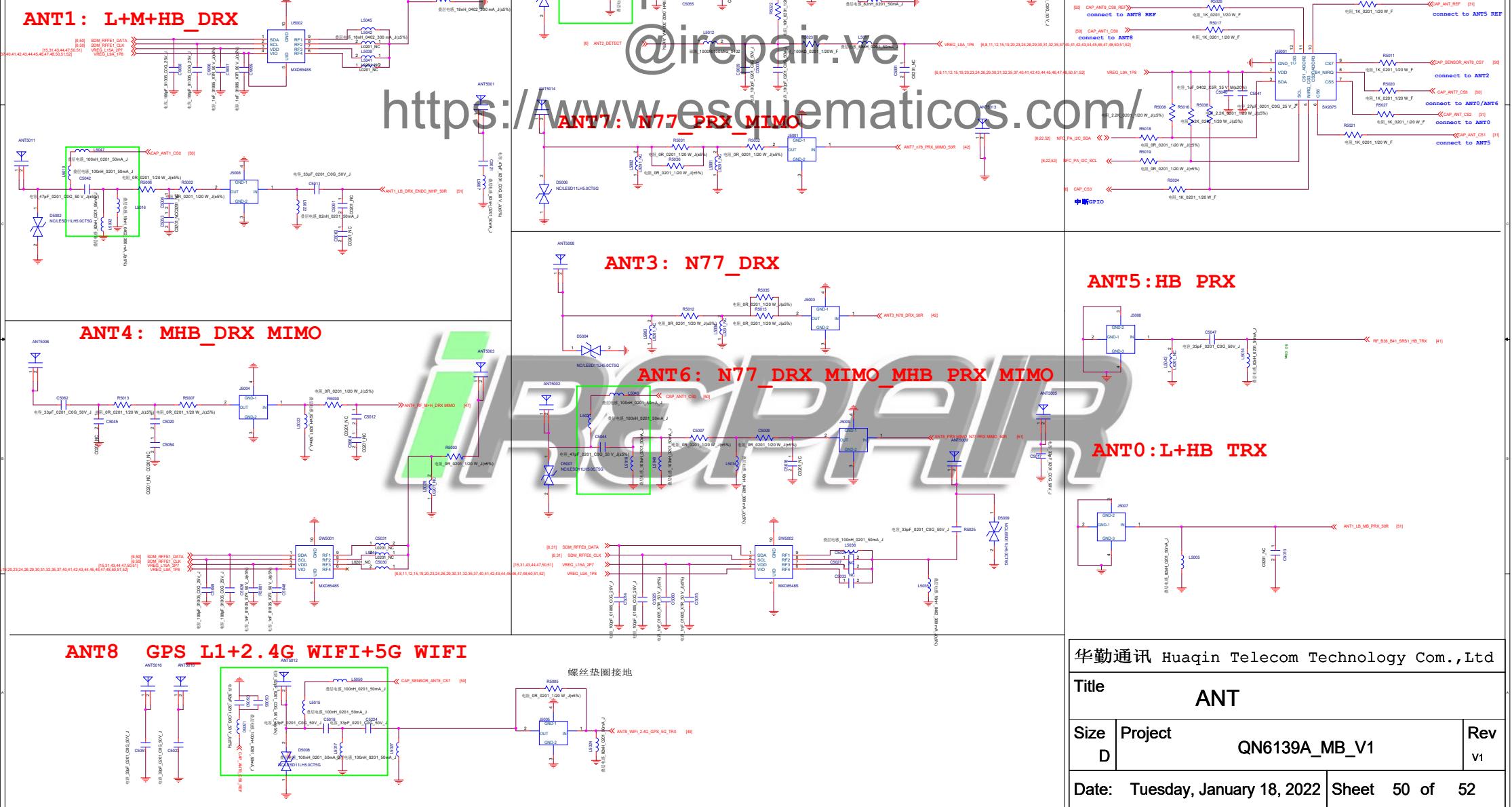
<https://www.esquematicos.com/>



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Title	49.GPS/WIFI_2.4G_5G/BT		
Size D	Project QN5965	Rev V1	
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**ANT2 N77 TRX**

<https://www.esquematicos.com>



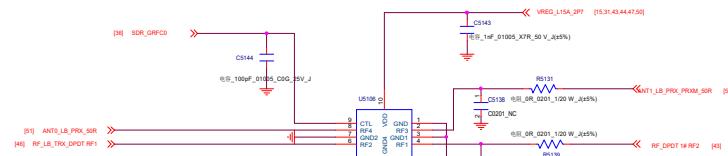
# ANIBAL GARCIA VENEZUELA

<https://irepairvenezuela.com/>

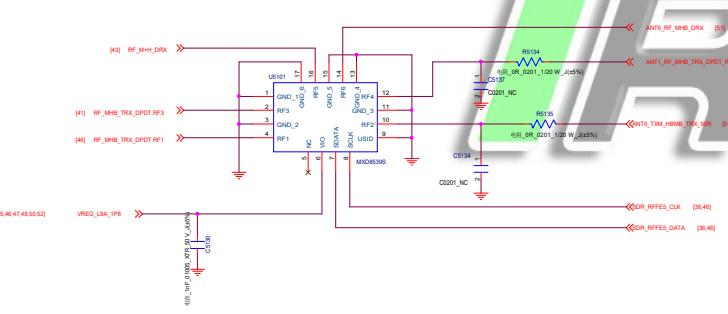
@irepair.ve

<https://www.esquematicos.com/>

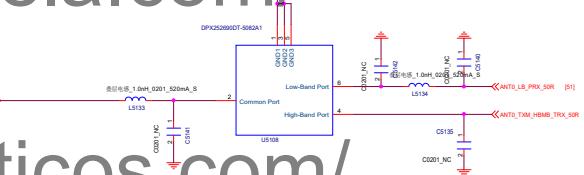
DPDT #1



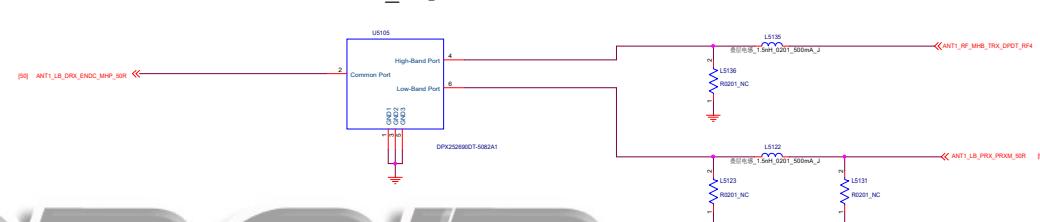
3P3T #1



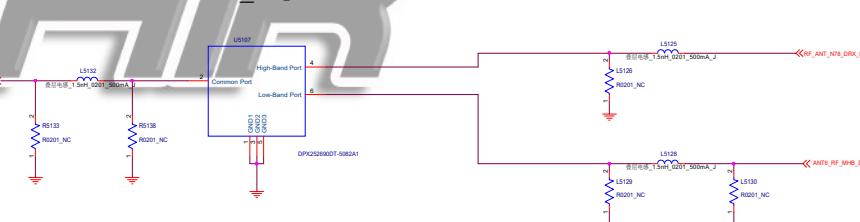
L+MHDIP ANT0



LB+MHB\_Diplexer ANT1



LMB+UHB\_Diplexer ANT6



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Title  
ANT\_SW

Size D	Project QN6139A_MB_V1	Rev V1
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For using 5V DCDC or not

	Use DCDC	No DCDC
R5001	0ohm	NFC NC
R5019	NC	0ohm
R5018	确认 Follow 0ohm	系统关机以后还有供电 NC

# ANIBAL GARCIA VENEZUELA

Refer 80-056-41 and Rhode 2026.3  
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PN553 I2C Adress:0x28 ( Write:0x50 Read:0x51 )

REF 3201-3299

27.12MHz Crystal

PVDD, 开机常开, 关机0V

(8,11,12,15,19,20,22,24,26,29,30,31,32,35,37,40,41,42,43,44,45,46,47,48,50,51,52)

(8) NFC\_XTAL2

(8) NFC\_PD

(8) NFC\_DVDD

(8) NFC\_AVDD

(11) PMC\_LNBCLK2\_NFC

电容\_0R\_0201\_120W\_J

(8) NFC\_XTAL1

(12,13,14,15,16,18,19,20,22,24,26,27,28,31,40,41,42,45,46)

VPH\_PWR

SHD01 SHD01

(8) NFC\_RXN1

(8) NFC\_RXN2

(8) NFC\_RXN3

(8) NFC\_RXN4

(8) NFC\_RXN5

(8) NFC\_RXN6

(8) NFC\_RXN7

(8) NFC\_RXN8

(8) NFC\_RXN9

(8) NFC\_RXN10

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(8) NFC\_RXN224

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Title

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v2.0

2021/11/09: U4301 change to QLN5030, add L4359/L4360;

2021/11/09: U5202 change to QLN5030, add L4418/L4419;

11/08 1. DEL L5137, 2. 更换 LNA4902 GES LNA 为JW5005DNRZ

11/09 1. 修改L5201/L5205封装 0603.

2021/11/09: U4304/U4702/U4116 封装由TDK 变更为村田。

2021/11/12: 删除电感L4342, 增加电阻R4320

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QLN5030 v3.0 test

2021/11/20: 删除L4407

2021/11/20: 删除L4731,C4706,C4705

2021/11/24: U4304/U4702/U4116 封装更改2520;

2021/11/24: C4320/C4321/C4304/C4306/C4315/C4363/C4311

NFC V3.0

2021/11/30: 删除C5222/C5224/C5225/L5206/L5203

2021/11/30: C5217/C5204/C5205 封装0402

2021/12/8: RF\_B3\_PRX2\_LNA\_IN和RF\_B1B66\_PRX2\_LNA\_IN port 交换;

2021/12/8: add C4431, PRC co-pad兼容设计;

V4.0

2021/12/8: add R5401,R5402;

V5.0

2021/01/18: L5036 0402变为0201



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Title  
RF\_Changelist

Size	Project	Rev
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# ANIBAL GARCIA VENEZUELA

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10/2 R692 10K改为NC  
10/8 修改电池连接器PIN定义  
增加FM弹片  
10/9 删除内置FM功能  
10/10 删除JTAG测试座, 改为测试点  
10/13 exchange Speaker PA output  
10/16 VREG\_L21A\_3P3接到W8M DVDD 1.2V, 增加R2610  
10/16 换A+G封装  
10/18 删除TP2406 TP2407  
10/19 add R2033 R2034 D2004  
10/19 change GPIO\_6(GPIO\_8)/GPIO\_38  
10/20 change GPIO\_49(GPIO\_134)/GPIO\_135  
10/21 change GPIO\_45(GPIO\_46)/GPIO\_56(GPIO\_57)/GPIO\_84

## EVT

10/27 R2384:0201 OR--->NC  
10/28 增加TP3004 TP3005 C+ C-测试点  
删除R1614 R1615  
10/28 R2009:0201 10K-->100K  
11/1 增加R2035 0201 OR;R2036 0201 20R  
删除TP1602,UL601.E3接地  
LINK 连接器CN3101换库  
C1612 C1613:0603 10uf 25V-->0402 10uf 6.3V  
C3110 C3111:0201 1uf-->NC  
11/2 增加R2037 0201 47R  
增加IC2038 0201 51pf  
11/3 C3002:0402 4.7uf-->0201 1uf  
11/8 删除R1904 R1905 R1903 R2410 R2412 R2422  
GPIO\_45 Charge\_EN  
GPIO\_58 CC\_EN  
删除SG3101 SG3102  
增加R1613 0201 100K  
B2001 B2002:0201 100r@100M-->0402 1000R@100M  
C2030:0201 1nf-->0201 100nf  
C3004:0201 2.2uf-->0402 2.2uf  
11/9 删除R2033  
add C3004:0402 10R  
add D2005  
add R2038:0201 100K  
add R2040:0201 NC  
add R2041:0201 10K  
del:D2611 D2612 D2613 D2614  
add D2619 D2620 D2621 D2622 D2623 D2624 D2625 D2626  
add C2039:0201 NC  
add R2042:0201 OR  
change D2005 package  
add R1614 R1615:0201 NC  
11/13 七路LDO增加R2203兼容

DVT  
11/26 删除TR1104  
PM6375.GPIO\_04:RFC\_CAM\_THERMAL-->CAM\_FLASH\_THERM  
PM6375.GPIO\_06:CAM\_FLASH\_THERM-->USB\_VBUS\_IN  
增加R2181 0201 10K  
增加R2197 0201 20K  
增加SH3014, 电量计VSS接采样电阻上端

12/1 增加R3108:0201 NC  
增加C3112:0201 NC  
增加D3109  
删除D2608  
GPIO\_102:FP\_MOS\_N\_ON-->VIBR\_VDD\_EN\_1  
12/1 增加D3122 D3123

DVT2  
12/29 前摄DVDD供电修改: 增加U2801,C2815,C1816,R2811,R2812  
增加R2664 R2665:0201 1K  
C3005下地改为连到电量计VSS  
PM6375.GPIO\_03:USB\_CONN\_THERM1删除  
BATT thermal兼容设计修改到KB板上  
1/1 前摄DVDD供电: 增加U2802,C2817,C2818,C2819  
PVT  
1/12 删除测试点TP3003,增加TP3006



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Title  
**BB\_Changelist**

Size	Project	Rev
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