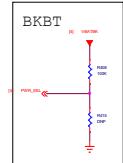
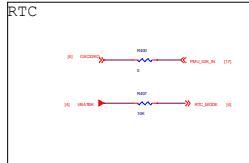
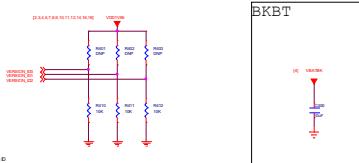
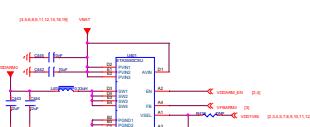
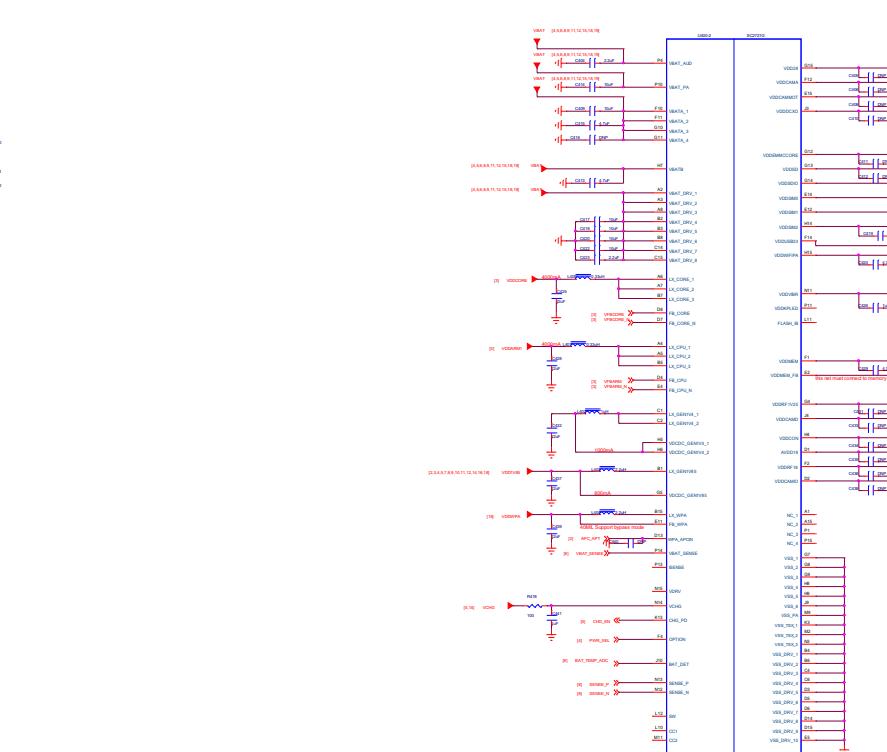
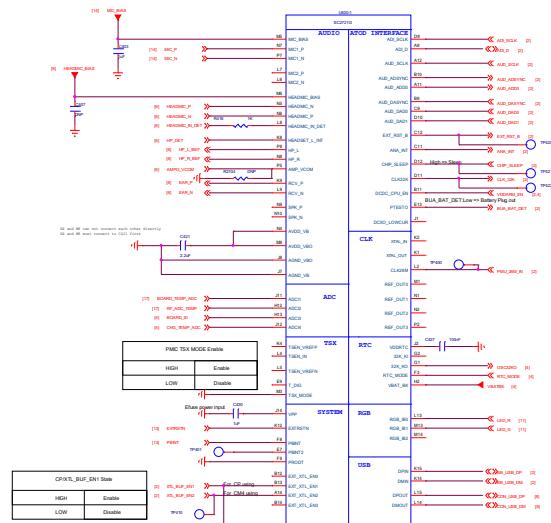


## BOARD ID

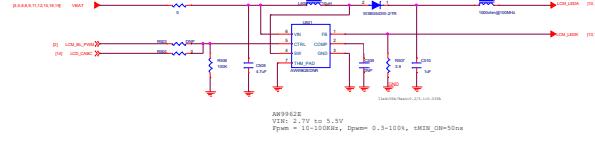


SC2711 DCDC	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
VBAT	DCDCCPU	0.6±1.3	4000mA	0.6V	ON
VBAT	DCDCM01T	0.6±1.3	4000mA	0.6V	ON
VBAT	DCDCM01M	0.6±1.3	1000mA	1.2V	ON
VBAT	DCDCM01G	0.6±1.3	2000mA	1.6V	ON
VBAT	DCDCM01PA	0.6±1.3	1000mA	0.6V	OFF

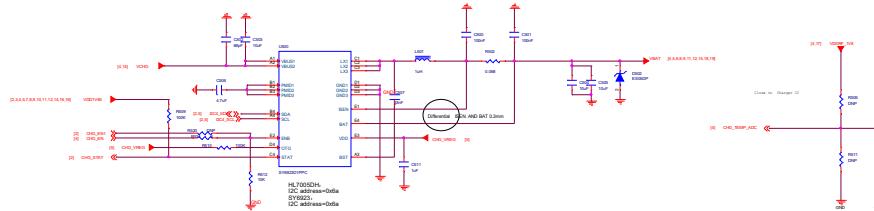
SC2711 LDO	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
LOD FOR VBAT	VDC00A	1.8±0.1	150mA	2.6V	ON
LOD FOR VBAT	VDC00M01T	1.8±0.1	150mA	2.6V	ON
LOD FOR VBAT	VDC00M01M	1.8±0.1	50mA	1.6V	ON
LOD FOR VBAT	VDC00M01	1.8±0.1	50mA	1.6V	ON
LOD FOR VBAT	VDC00M02	1.8±0.1	200mA	1.6V	ON
LOD FOR VBAT	VDC00R	1.8±0.1	200mA	2.6V	ON
LOD FOR VBAT	VDC00MMCORE	1.8±0.1	400mA	3.0V	ON
LOD FOR VBAT	VDC00S	1.8±0.1	400mA	3.0V	ON
LOD FOR VBAT	VDC00SOO	1.8±0.1	100mA	3.0V	ON
LOD FOR VBAT	VDC01B	2.0±0.2	2mA	3.0V	ON
LOD FOR VBAT	VDC01E	1.8±0.1	100mA	3.0V	OFF
LOD FOR VBAT	VDC01LED	1.8±0.1	400mA	3.0V/3.2V/3.6V/3.9V/4.2V	OFF
LOD FOR VBAT	VDC01PA	1.8±0.1	400mA	3.0V	OFF
LOD FOR VBAT	VDC01Q	1.8±0.1	100mA	1.6V	ON
LOD FOR VBAT	VDC01S33	1.8±0.1	650mA	3.0V	ON
LOD FOR VBAT	VDC01M	1.5±0.1	400mA	3.0V	OFF
LOD FOR VBAT	VDC01PVS	1.1±0.1	200mA	3.0V	OFF
LOD FOR VBAT	VDC01MEM	1.1±0.1	800mA	1.2V	OFF
LOD FOR VBAT	VDC01CAM	1.1±0.1	200mA	3.0V	OFF
LOD FOR VBAT	VDC01D	1.1±0.1	200mA	1.6V	ON
LOD FOR VBAT	VDC01D10	1.1±0.1	200mA	1.6V	ON
LOD FOR VBAT	VDC01F18	1.1±0.1	200mA	1.6V	ON
LOD FOR VBAT	VDC01F15	1.1±0.1	400mA	3.0V	OFF



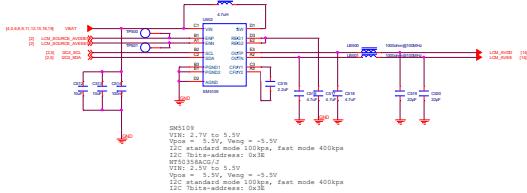
## LCM Backlight LED driver



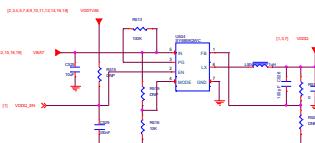
## Charger



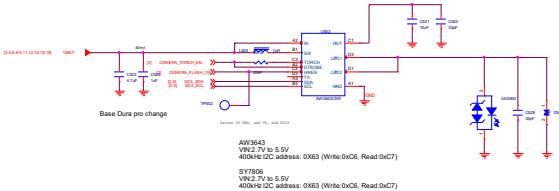
## LCD Bias



## Buck default for VDDQ of LPDDR4X



## Flash LED 5V Boost

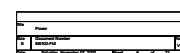


Schematic design notice of "27\_POWER\_SubPMIC-HV powers" page:

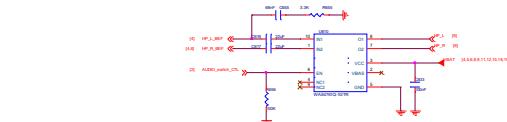
Note 27-1: To minimize RF de-sense, it is recommended to reserve 0-ohm and 0402 cap for BOM fine tuning.

Note 27-2: To minimize RF de-sense, it is recommended to reserve 0-ohm and 0201 cap. for BOM fine tuning.

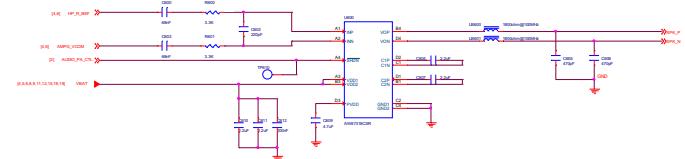
Note 27-3: C2705 could be replaced with C / 1 / uF / 50V + C / 1 / uF / 50V



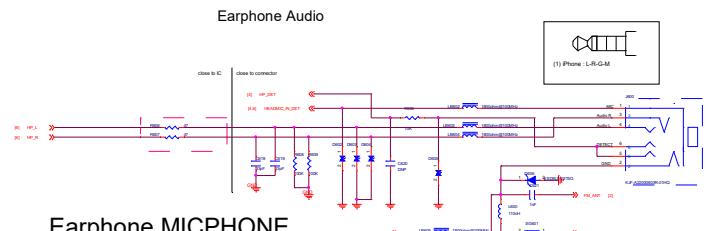
### Handset 2nd Microphone



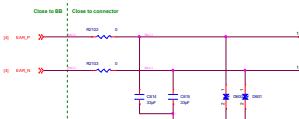
### SPEAKER



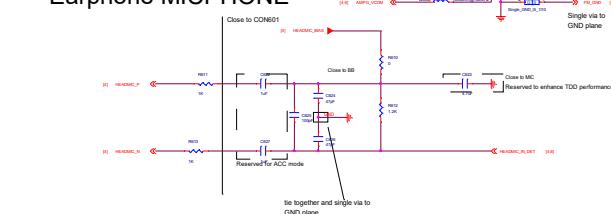
### Earphone Audio



### EAR PIECE

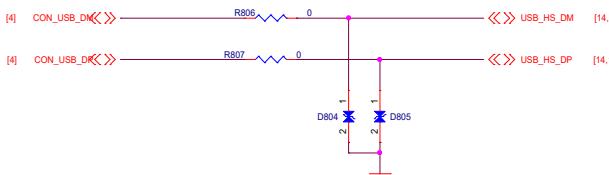
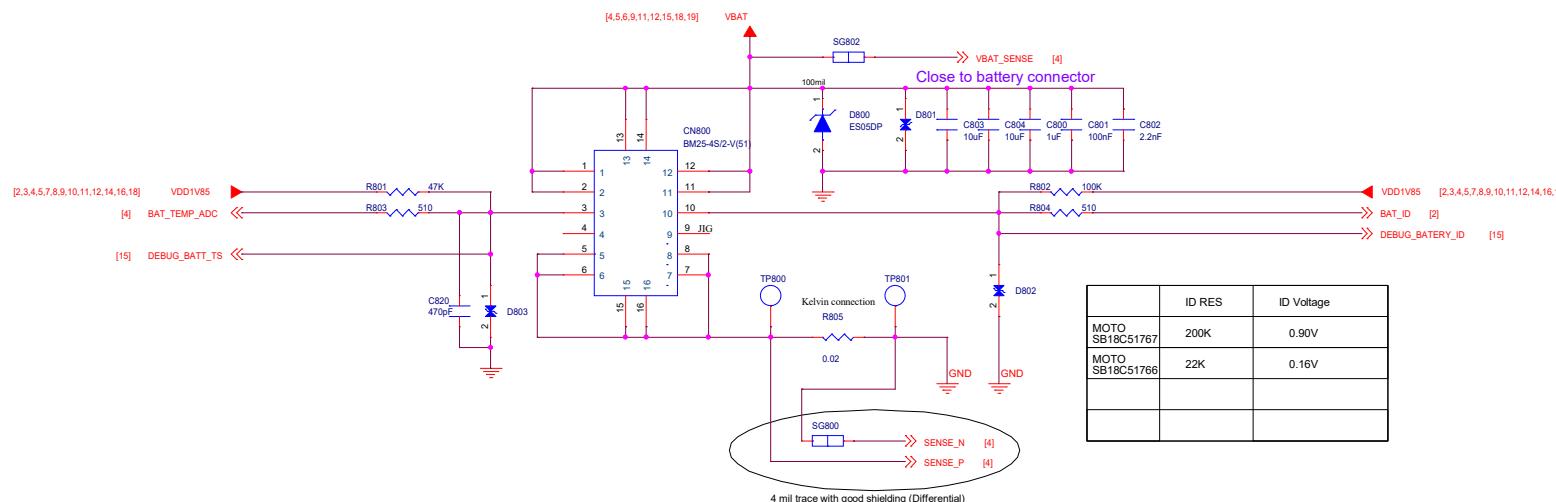


### Earphone MICPHONE





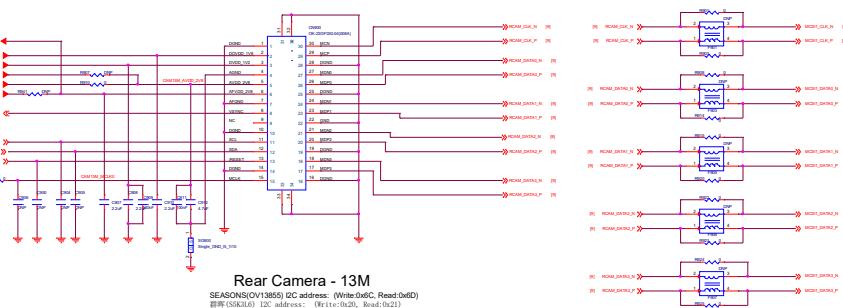
# BATTERY CONNECTOR



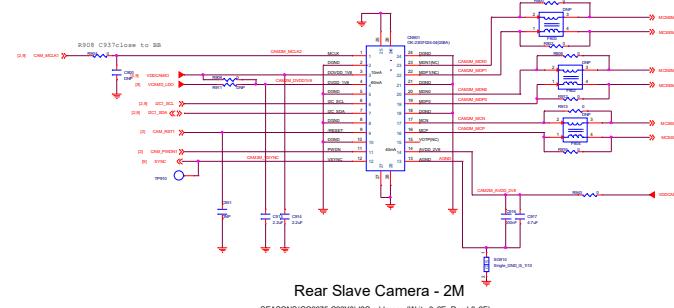
Title		
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Page09: Rear-CAM-13M, 2M  
Front-CAM-5M

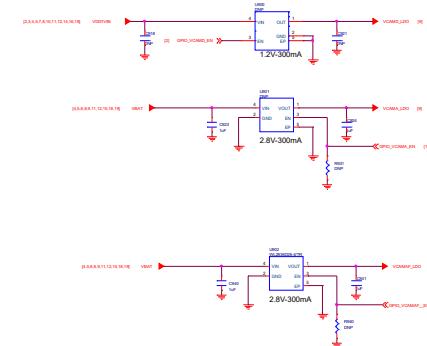
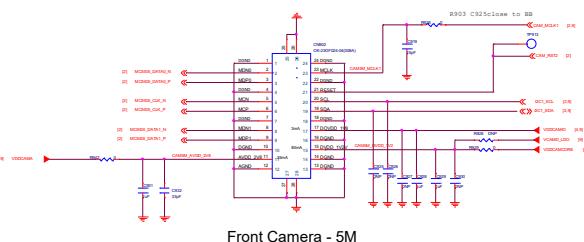
Rear-Main Camera(AF-13M)

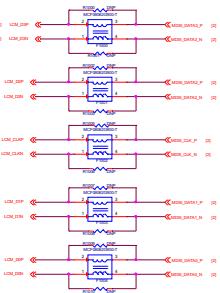
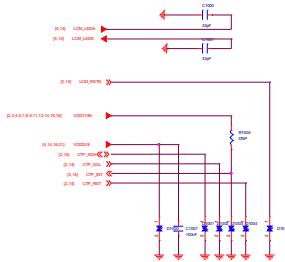


Rear-Slave Camera(FF-2M)

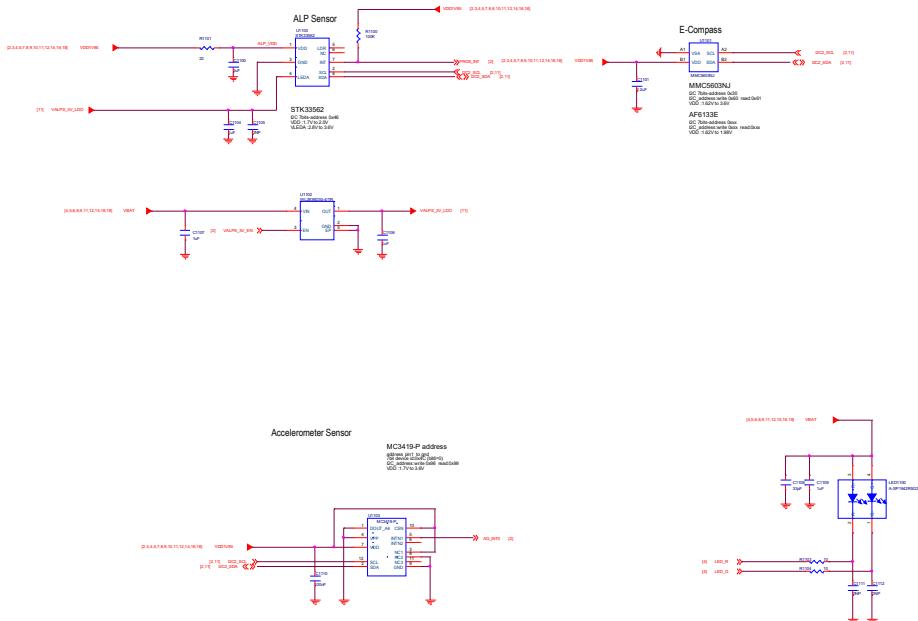


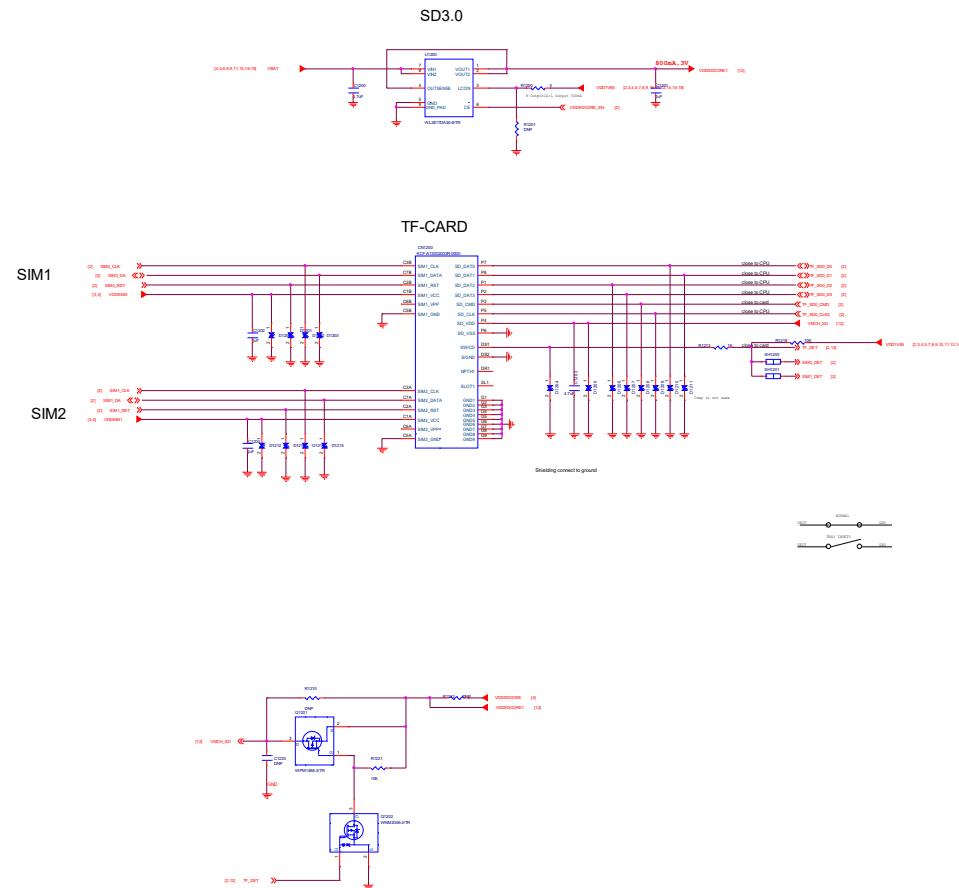
Front-FF-5M Camera Connector





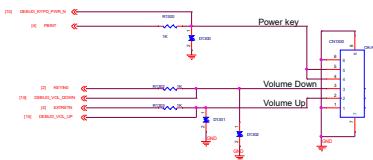
GT1151 I2C address: 0X5D (Write:0xBA, Read:0xBB)  
or 0x14 (Write:0x28, Read:0x29)



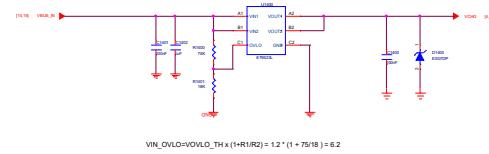


## KEY

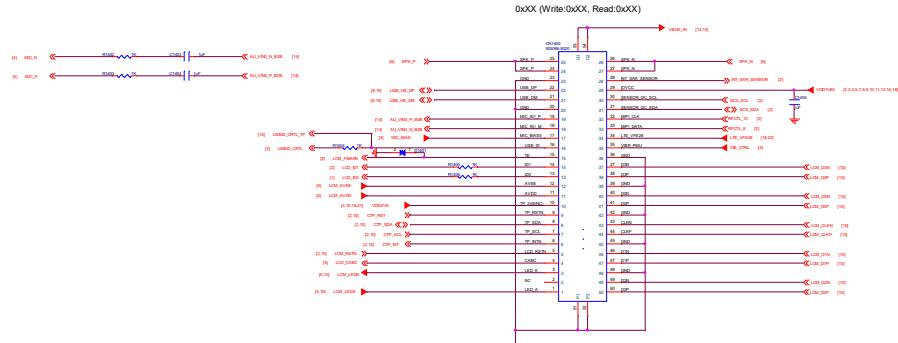
Volume Up : HOME Key / GND  
Volume Down : KPCOL1/GND  
DO NOT put pull-up resistor on PWRKEY



OVP

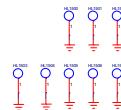
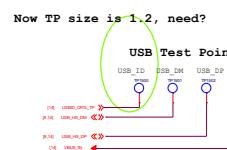


## Sub Connector



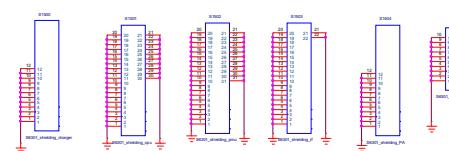
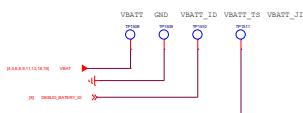
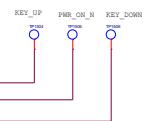
### Download mode TP

### USB Test point



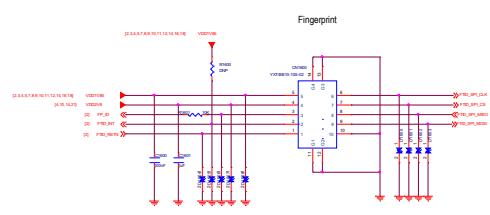
### KEY Test point

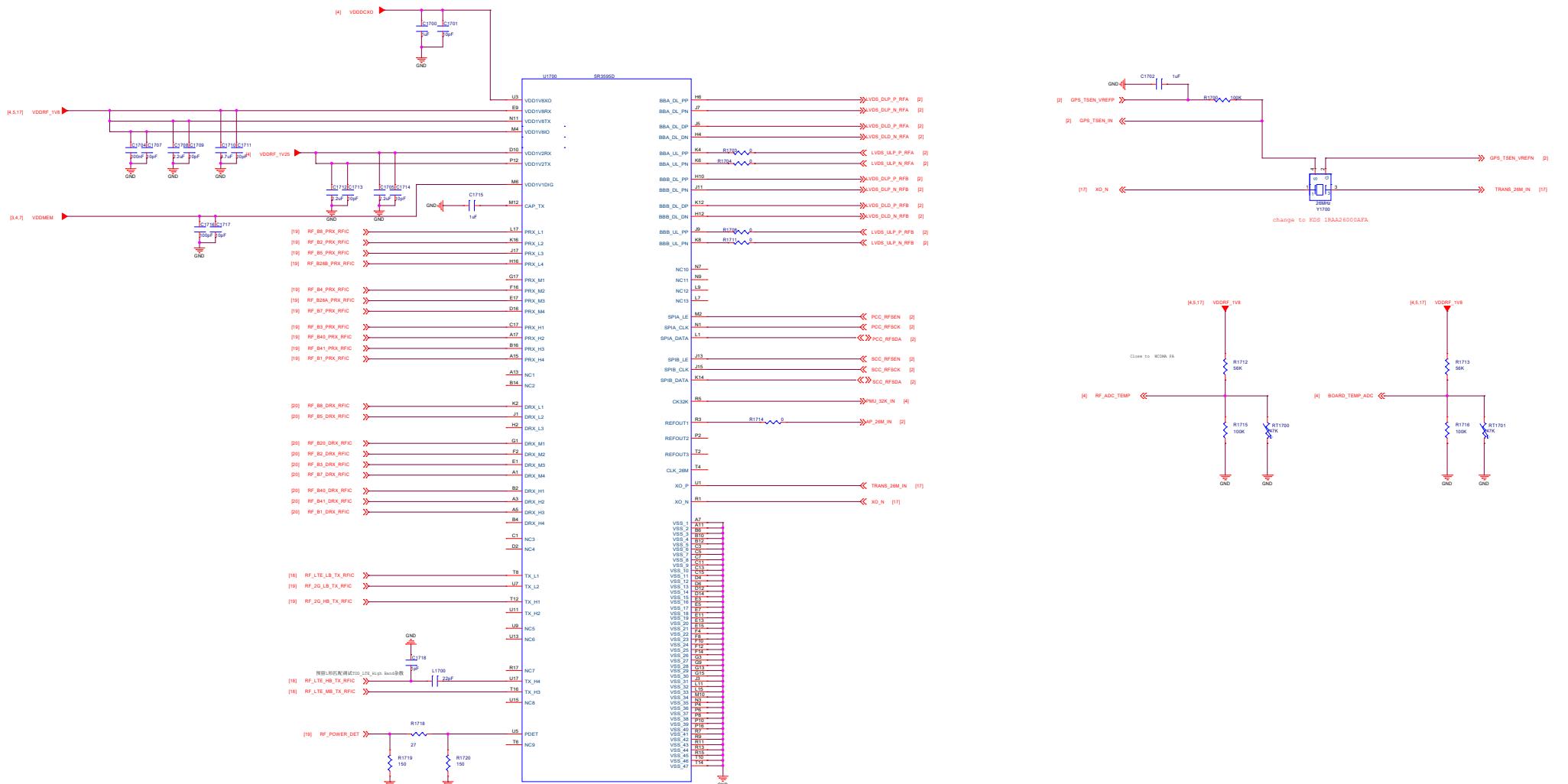
### BATTERY Test point

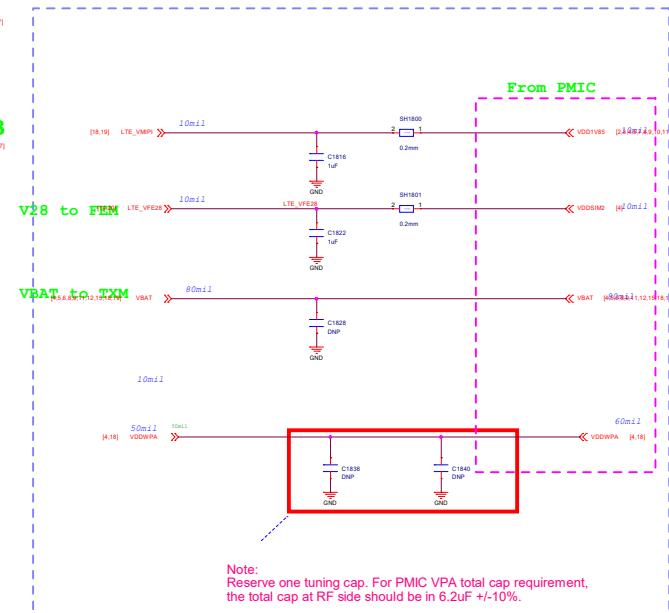
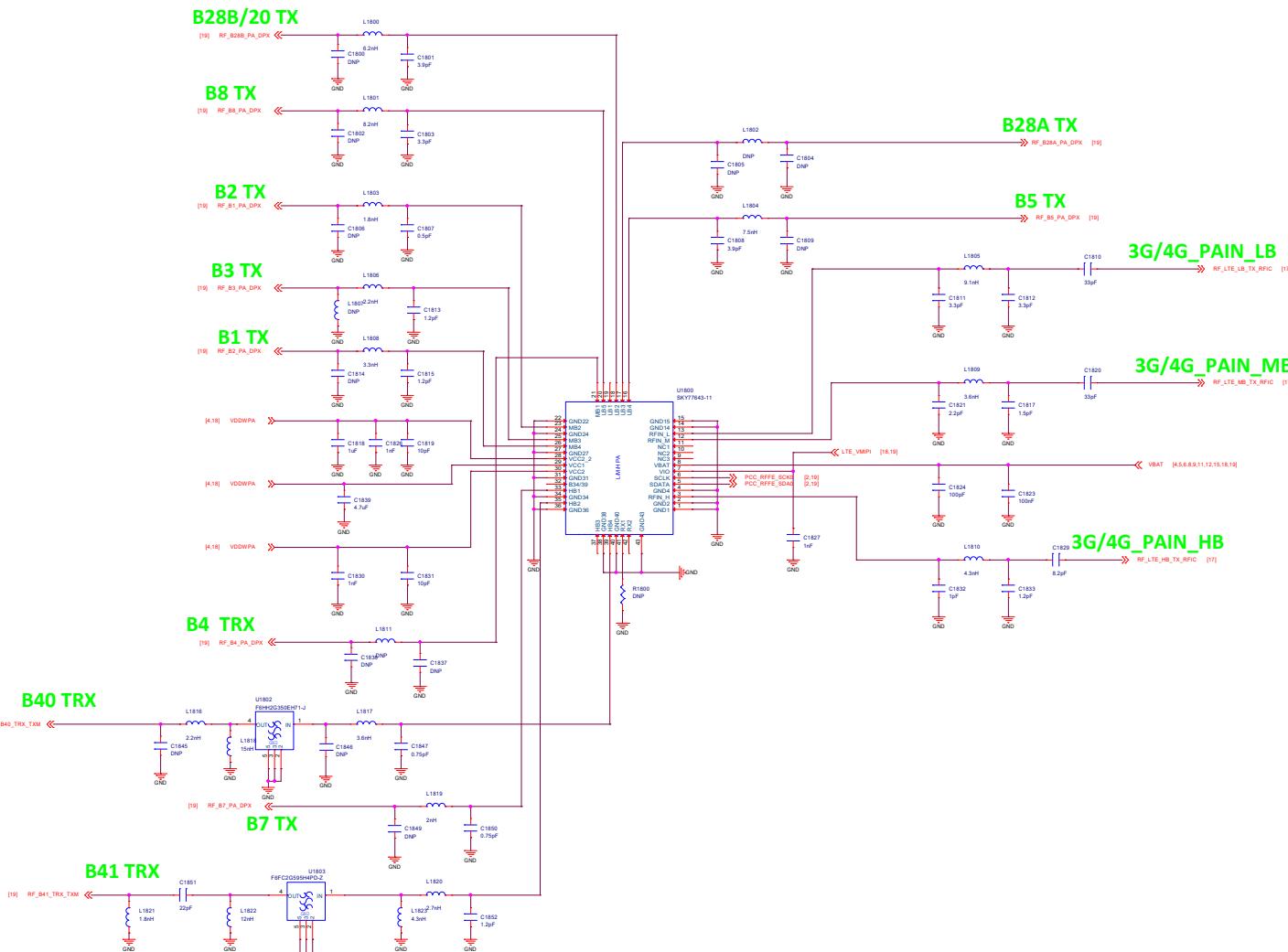


### UART



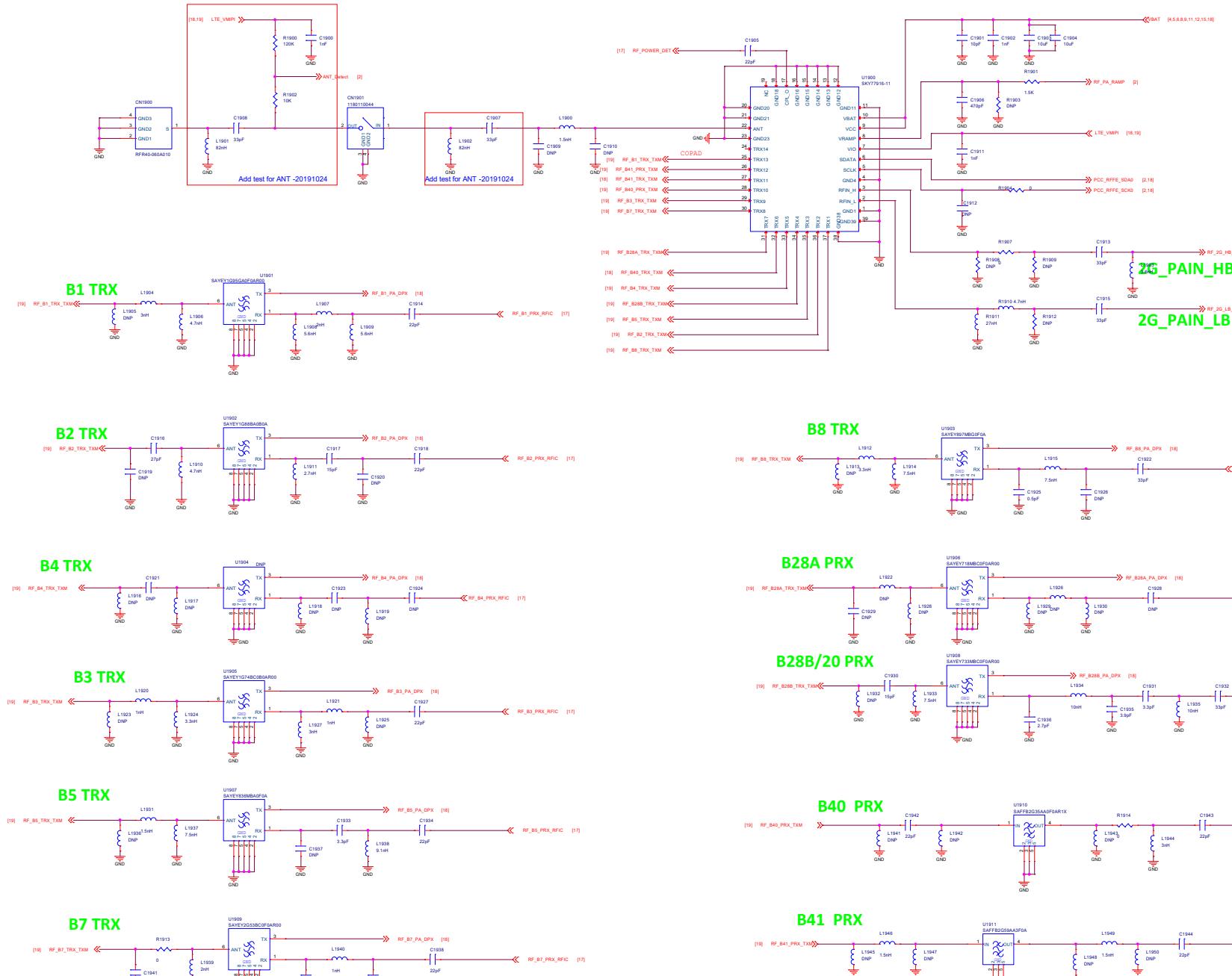




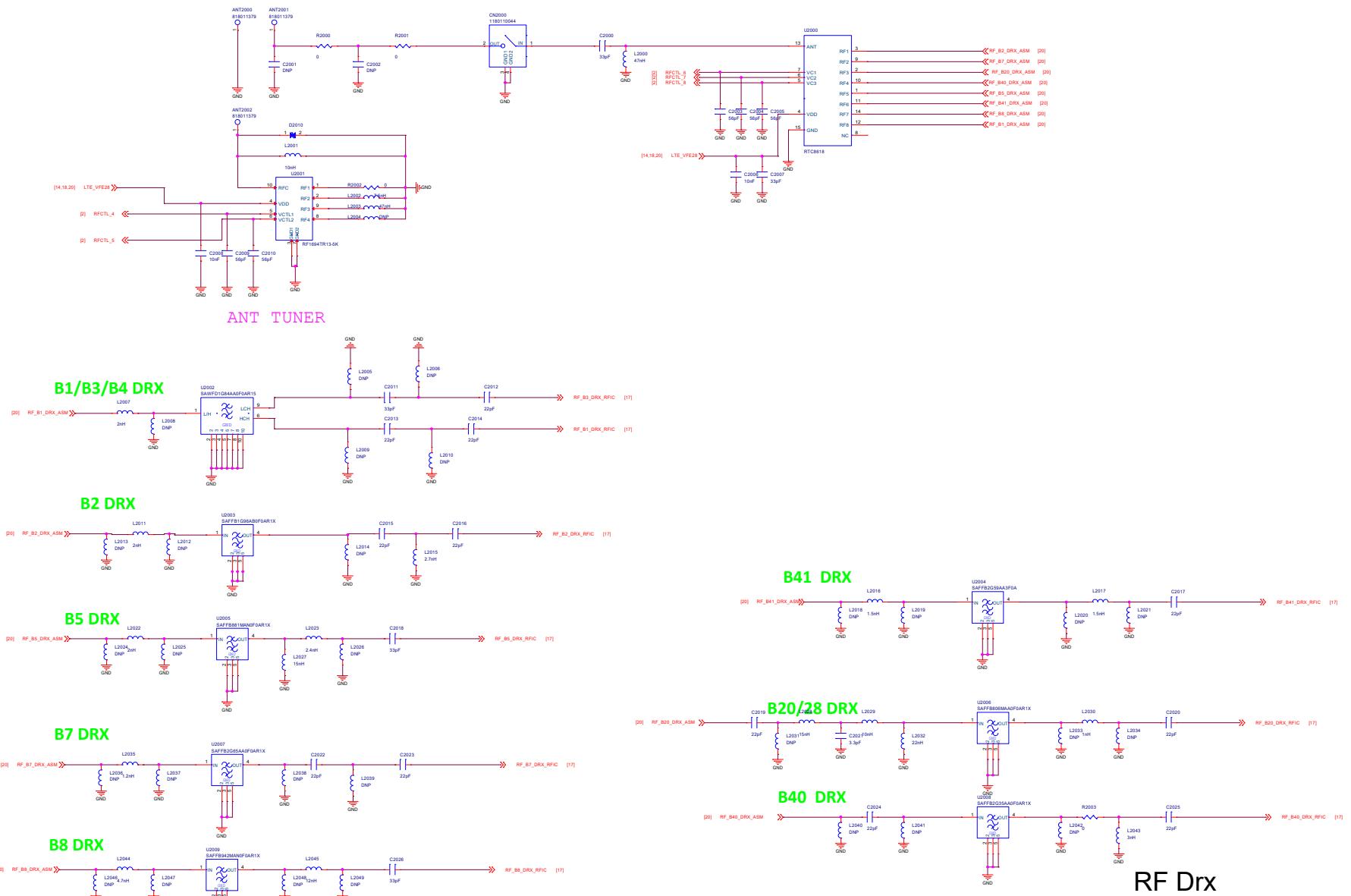


Note:  
Reserve one tuning cap. For PMIC VPA total cap requirement, the total cap at RF side should be in 6.2 $\mu$ F +/-10%.

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Date Saturday, November 07, 2020	Cheat	19 of 21



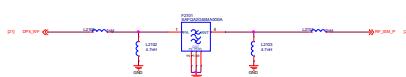
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Size	47.76 MB
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### WIFI&BT&GPS ANT



### WIFI/BT SAW



### GPS SAW LNA

