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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

REAR CAMERA #0

REAR CAMERA #2 (MACRO)

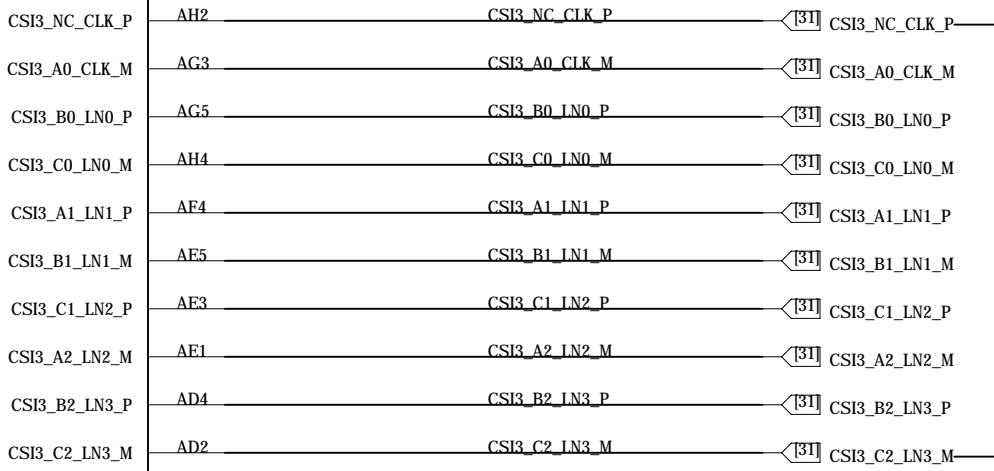
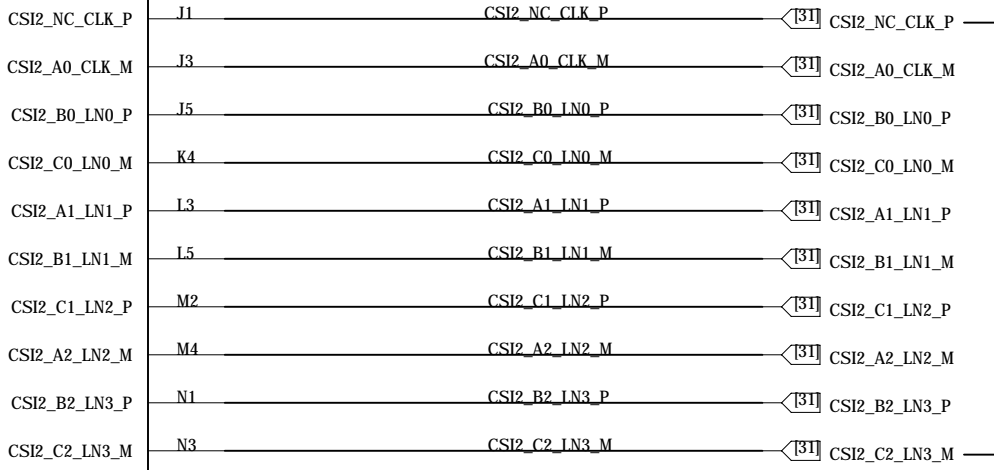
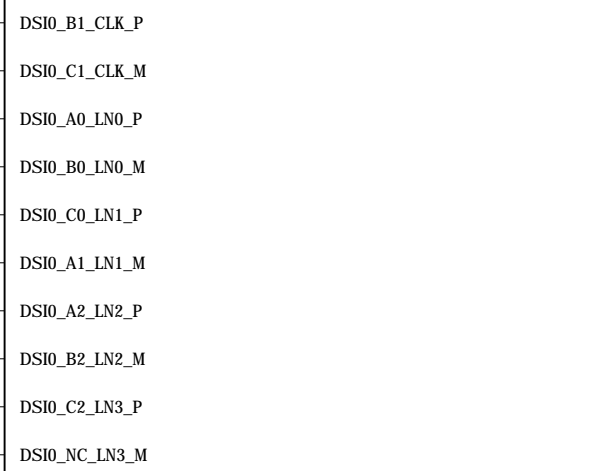
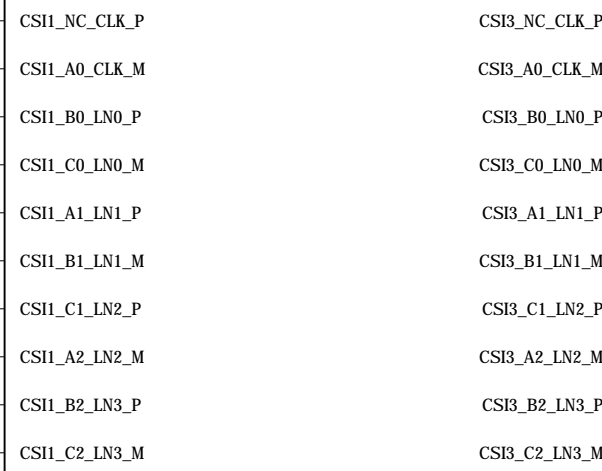
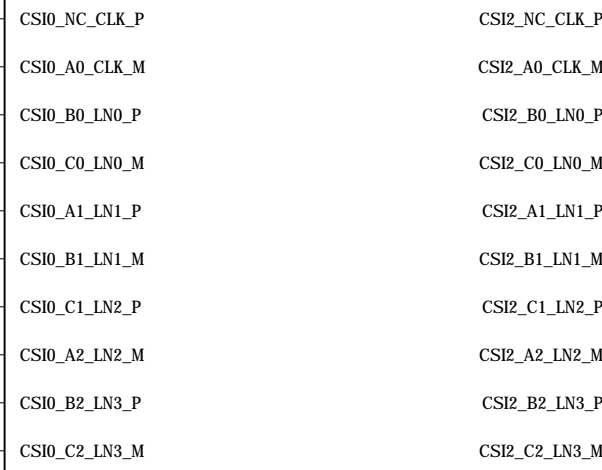
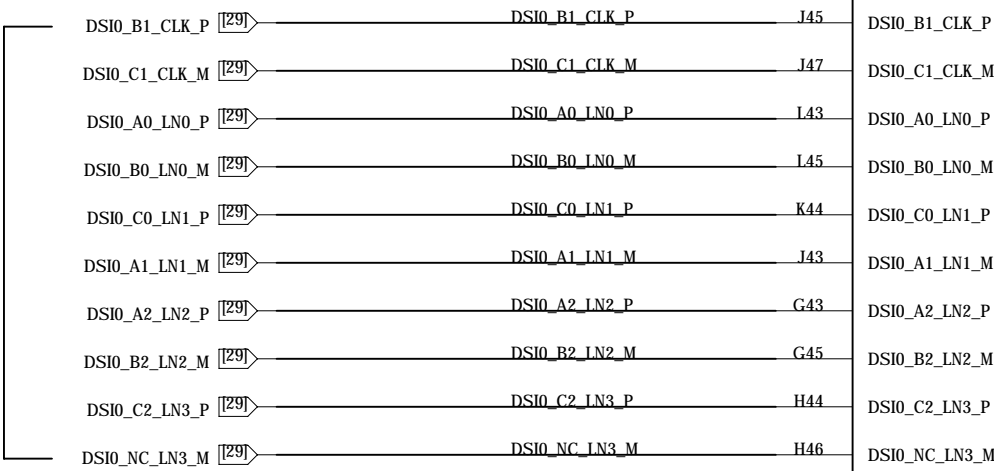
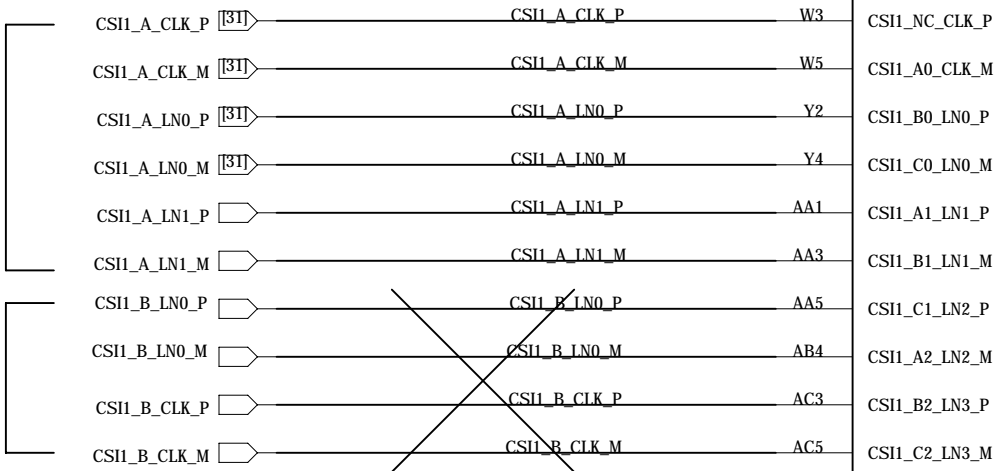
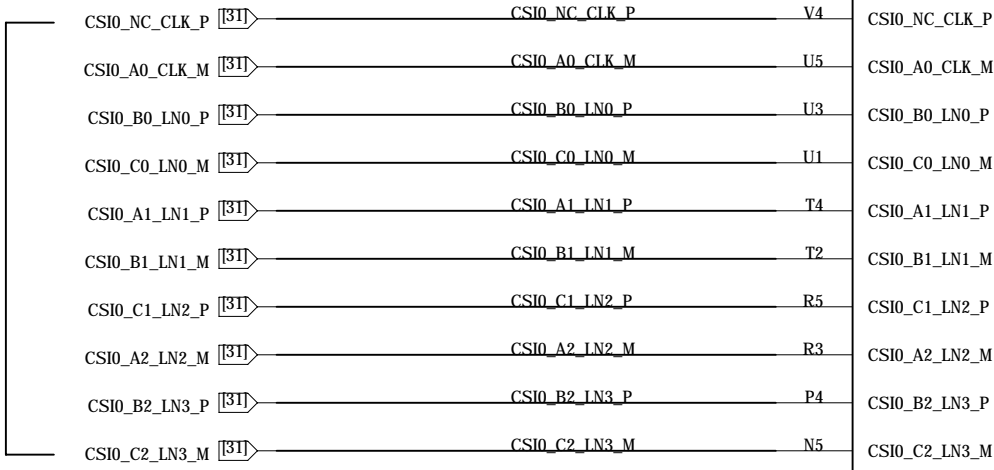
REAR CAMERA #1 (DEPTH)

DSI #0

DCLK => DCLK\_A  
DLN0 => DLN0\_A  
DLN1 => DLN1\_A  
DLN2 => DLN0\_B  
DLN3 => DCLK\_B

SM-6375-1-PSF937-TR-00-0-AB

U1000-H



REAR CAMERA #3 (U.WIDE)

FRONT CAMERA

D

C

C

B

B

A

A

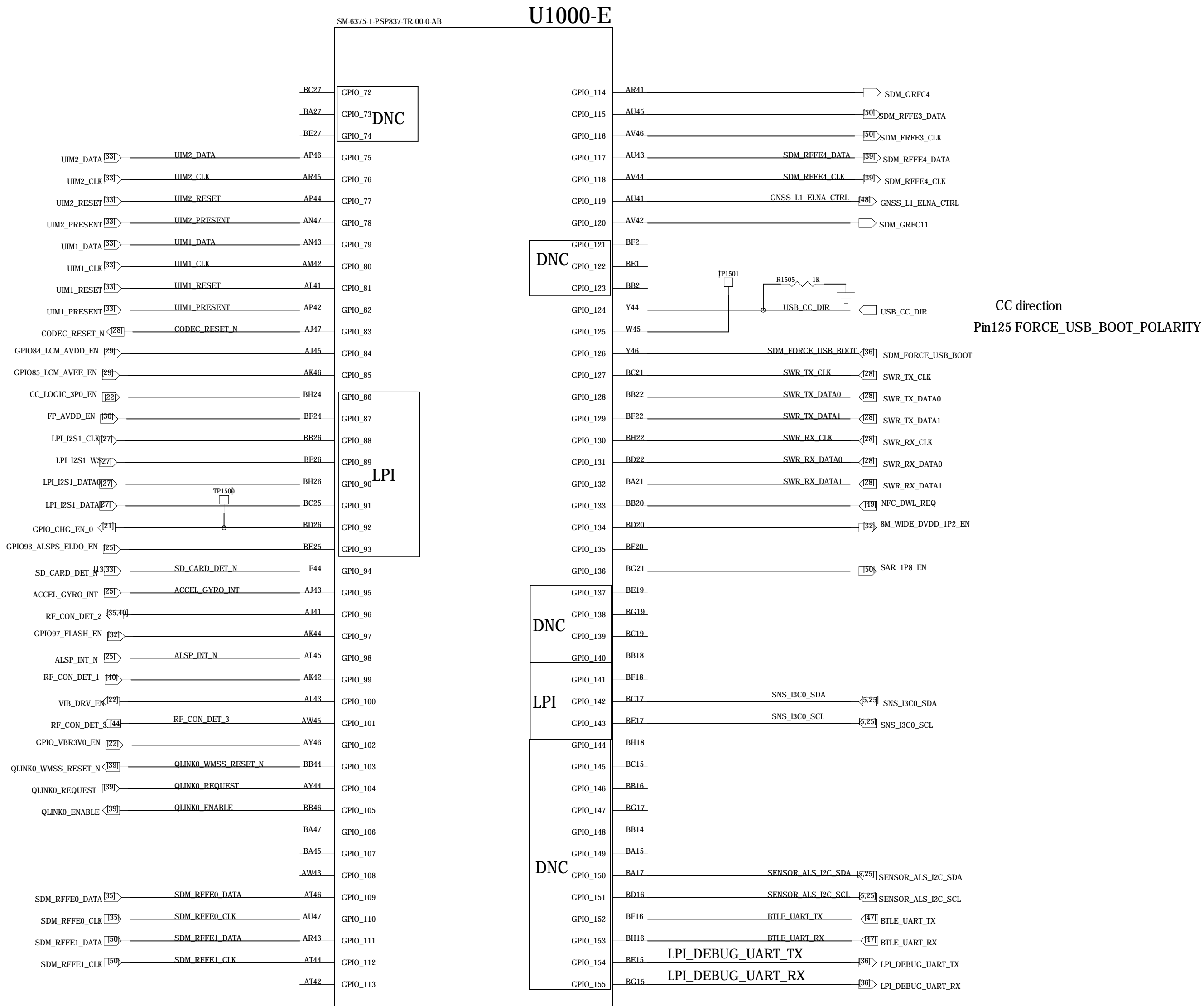
DRAWN: MichaelLiang		DATED: 20211025		COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
CHECKED: <Checked By>		DATED: <Checked Date>		TITLE: P721CE Main Board			
QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
RELEASED: <Released By>		DATED: <Release Date>		SCALE: <Scale>		SHEET 3f 51	





REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

For SMxxxx only When the design of WSA8815 is used



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 6f 51	

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

6

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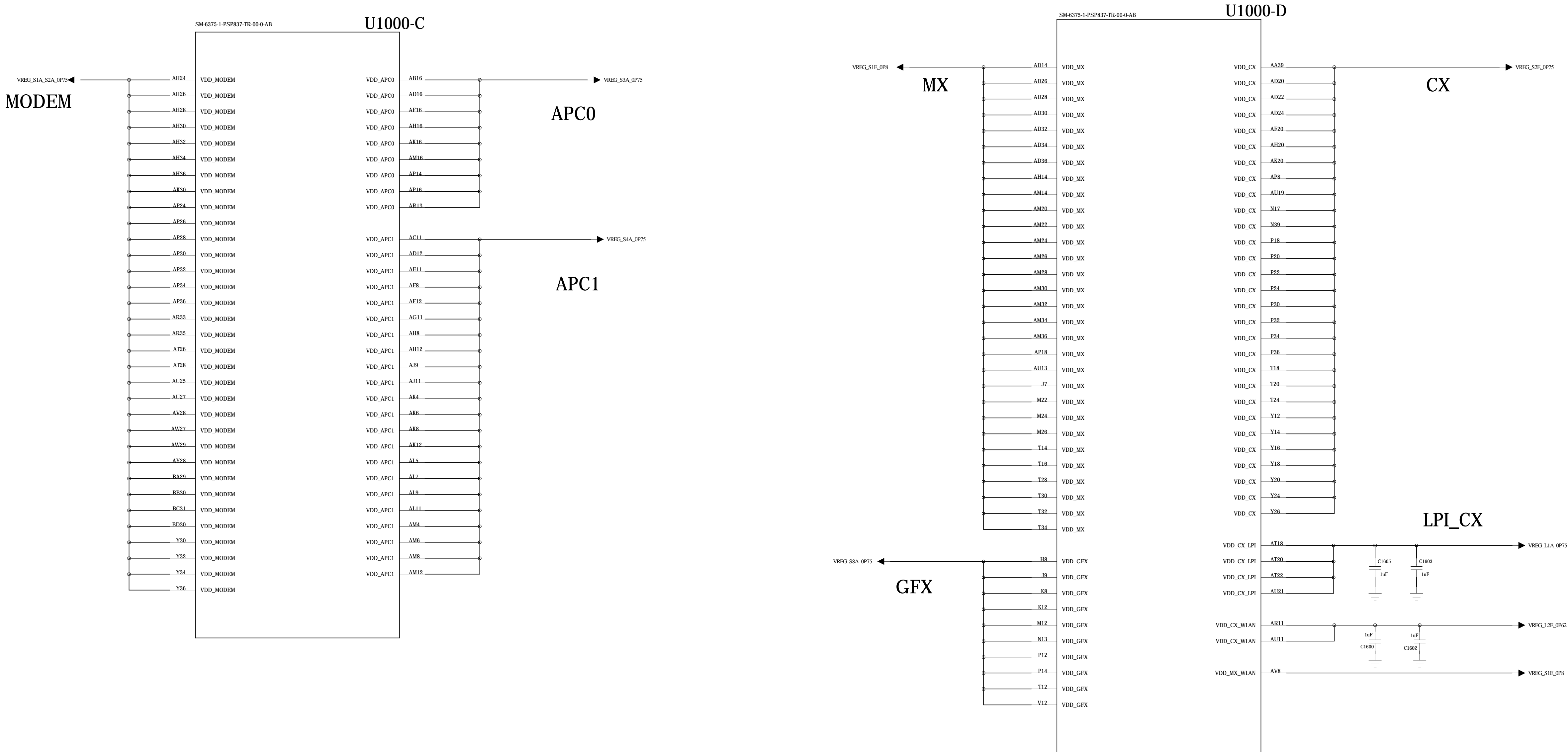
4

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REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
SCALE: <Scale>		SHEET: 51	

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>







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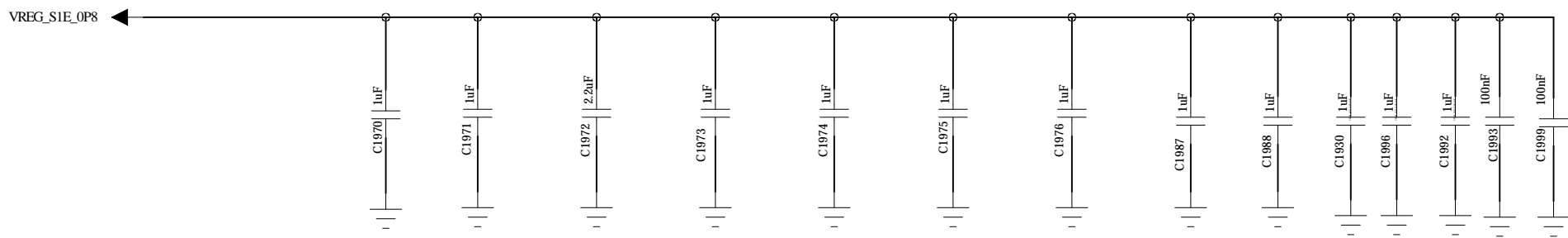
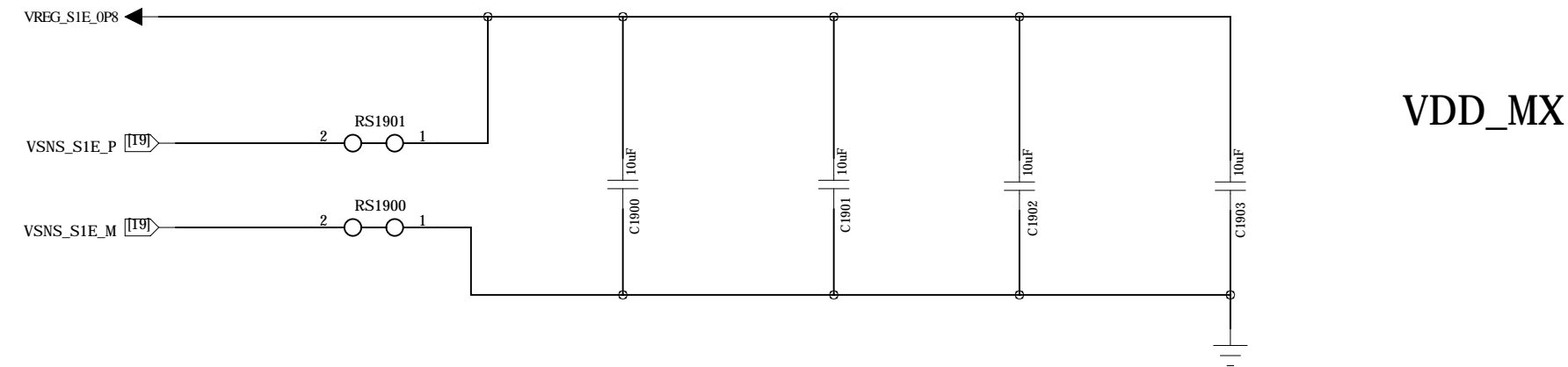
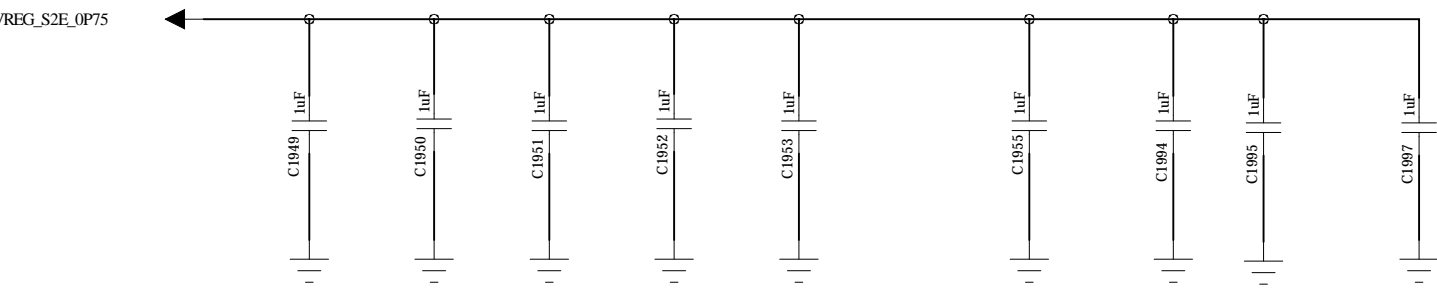
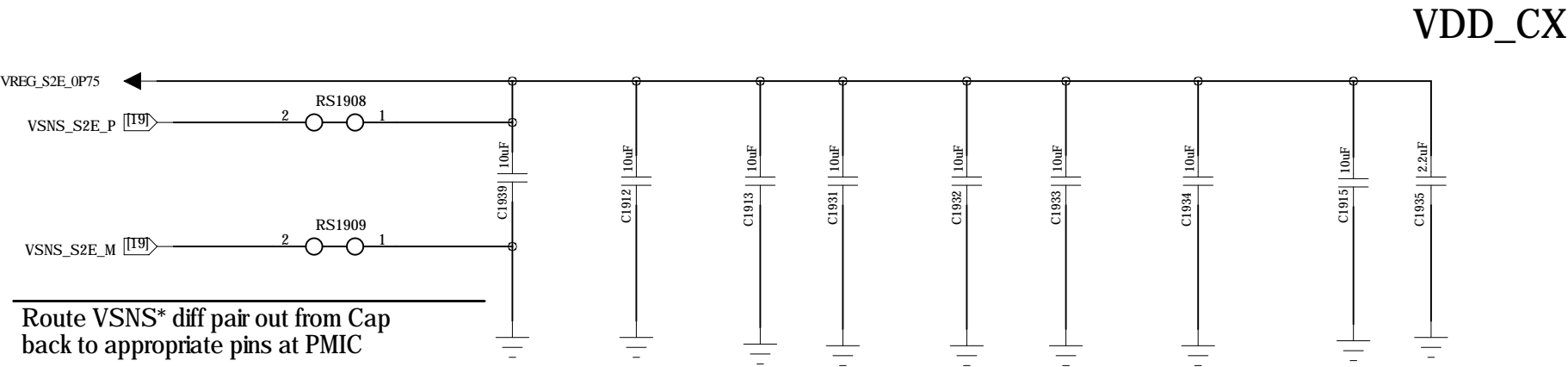
3

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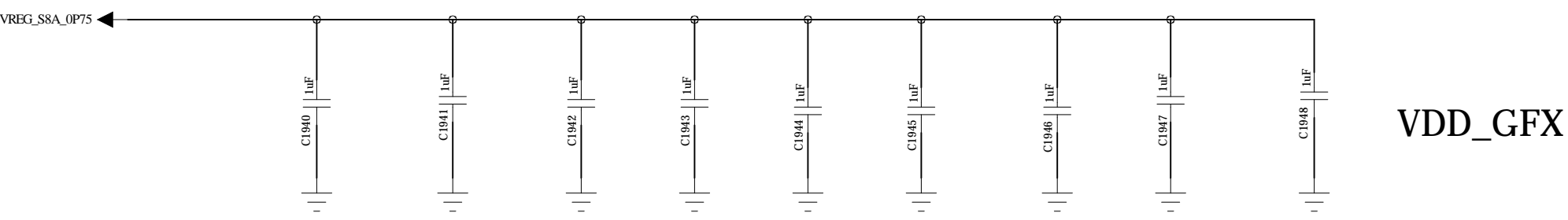
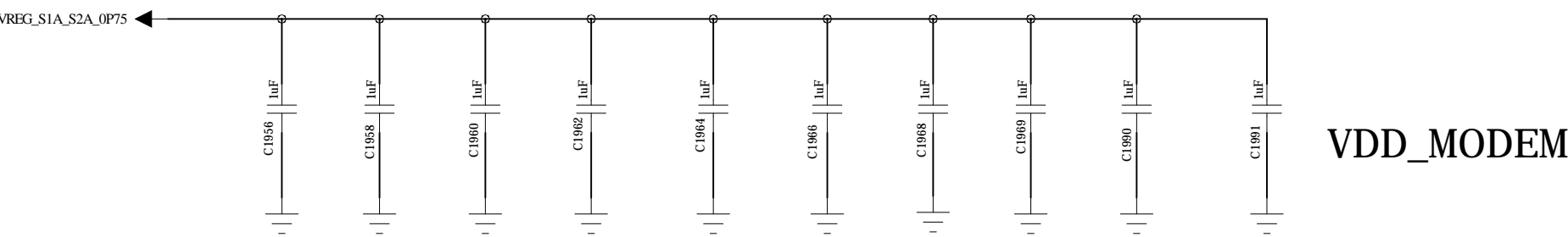
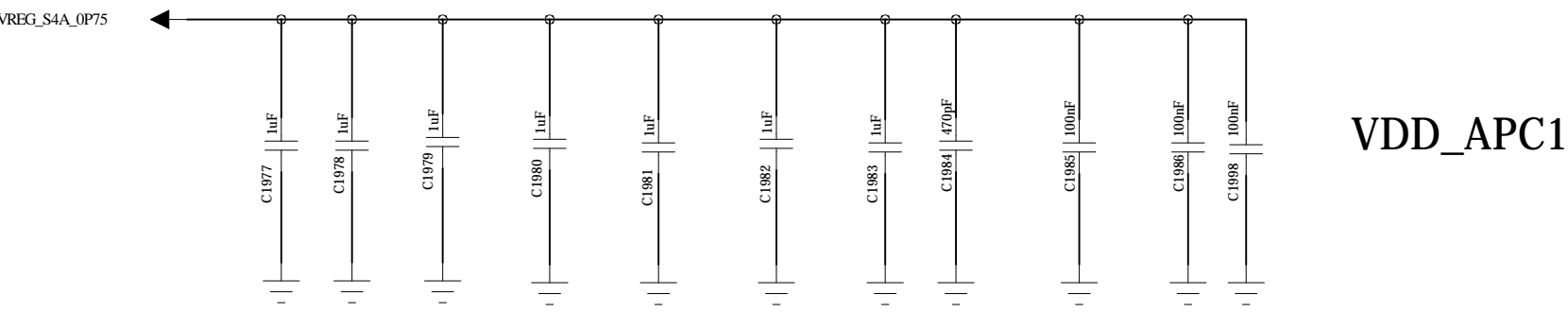
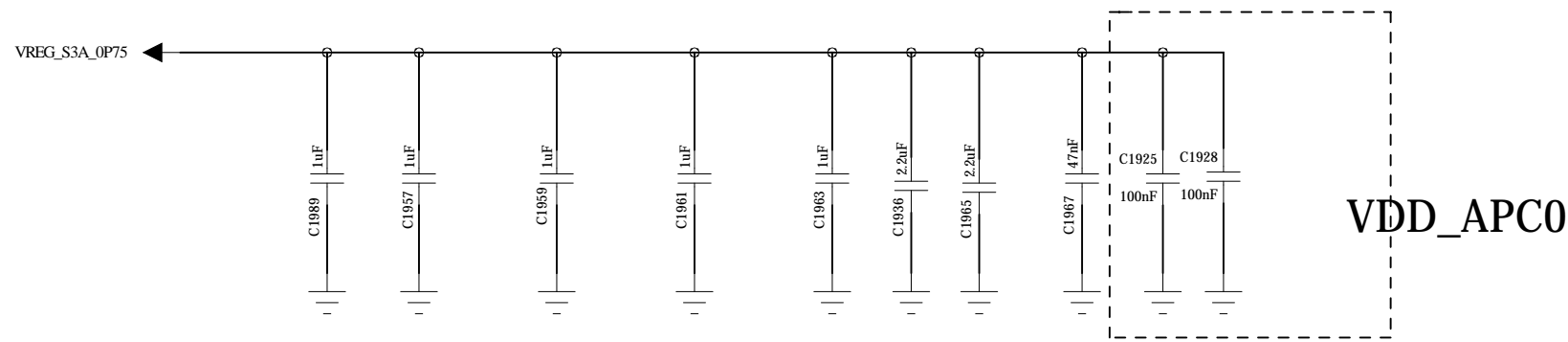
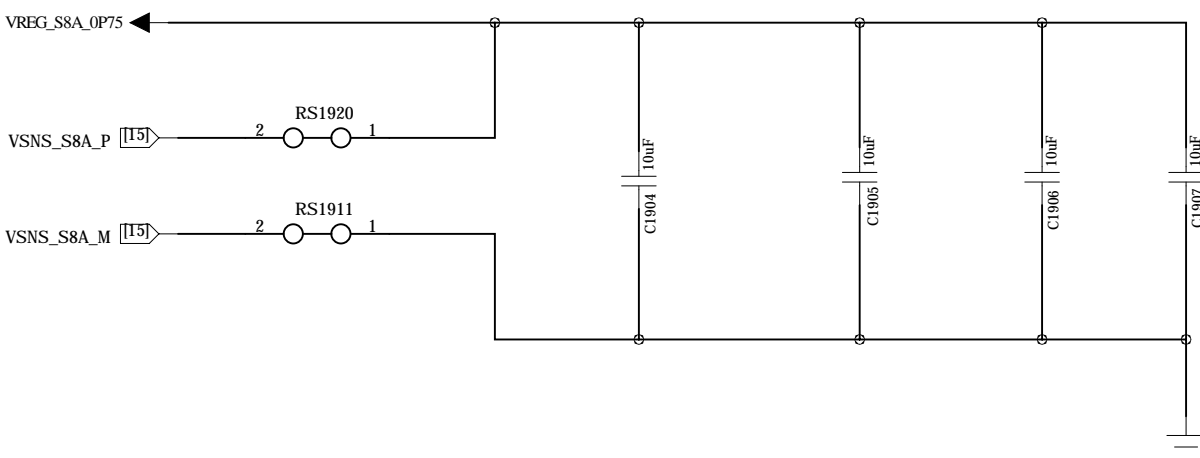
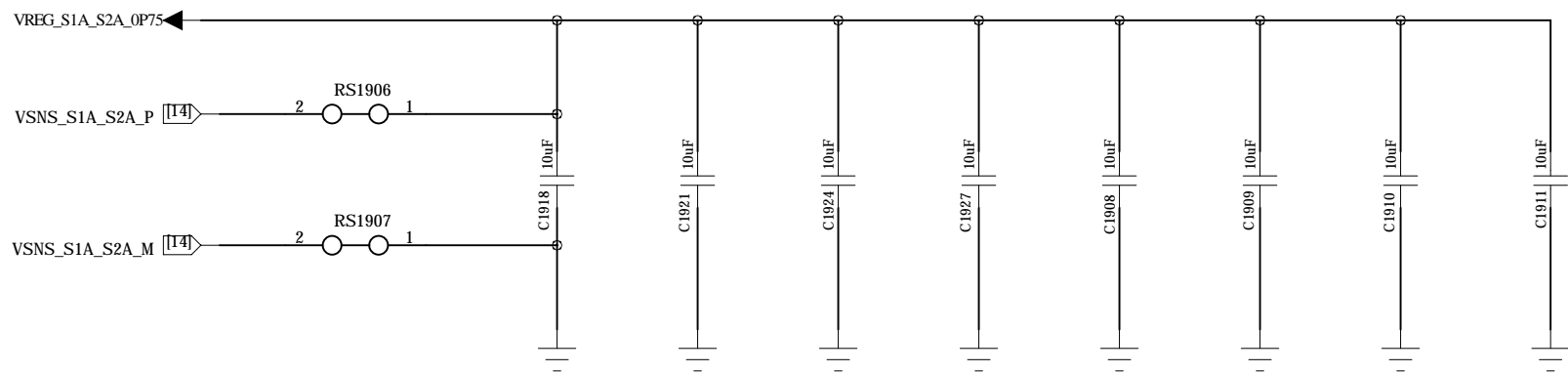
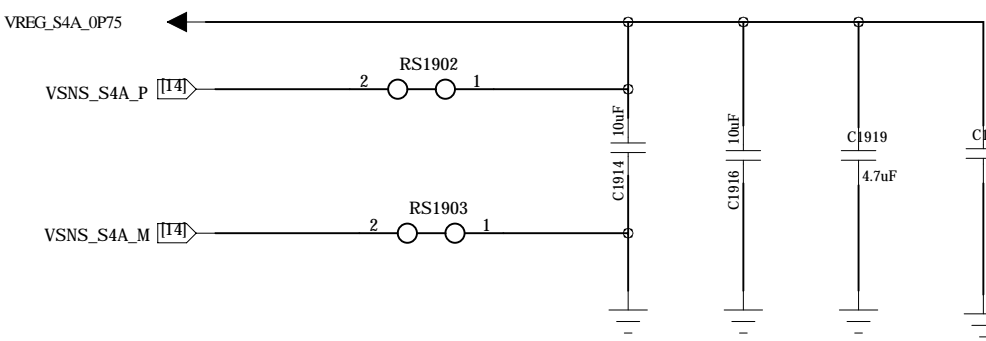
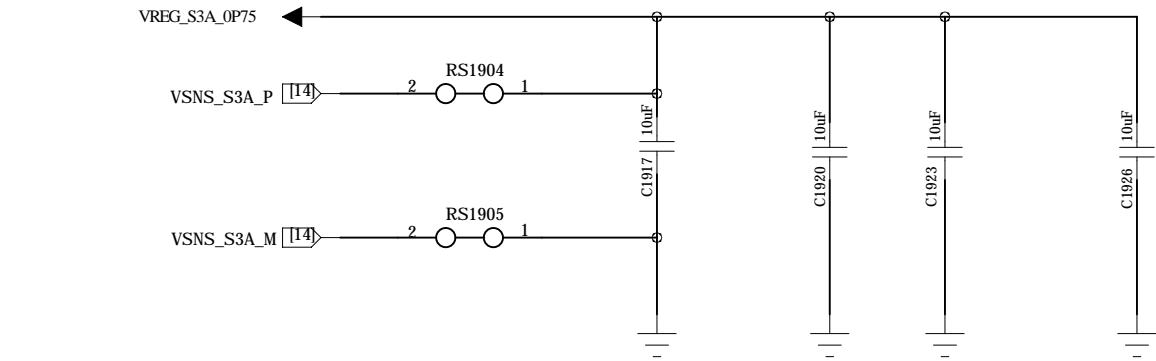
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REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

PMR735A FTS Bucks



PM6375 FTS Bucks



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
SCALE: <Scale>		SHEET: 40 51	

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

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REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

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D

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C

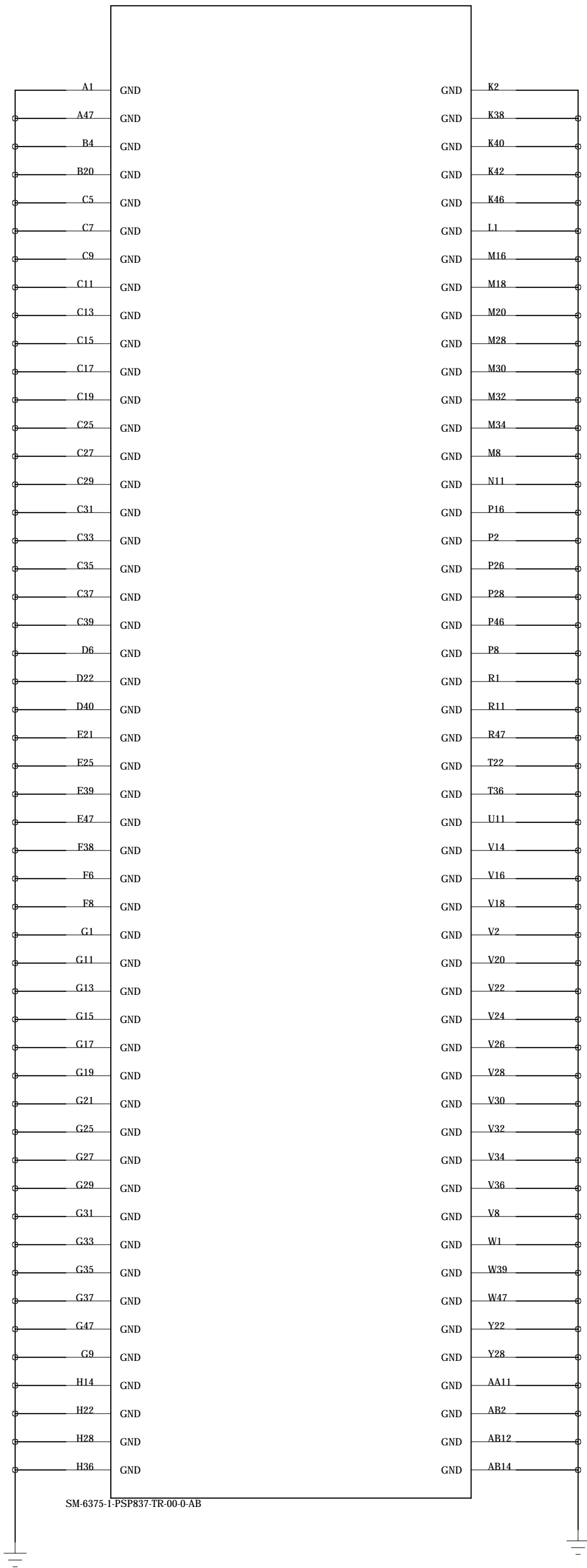
B

B

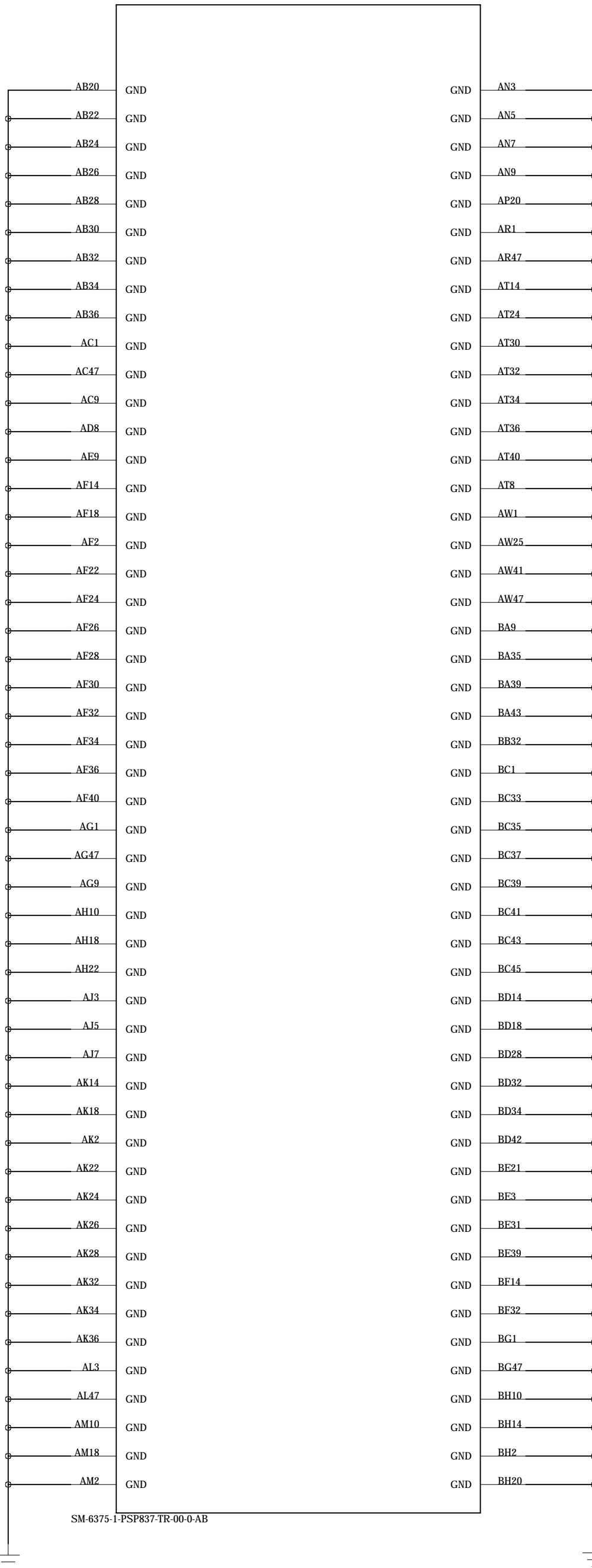
A

A

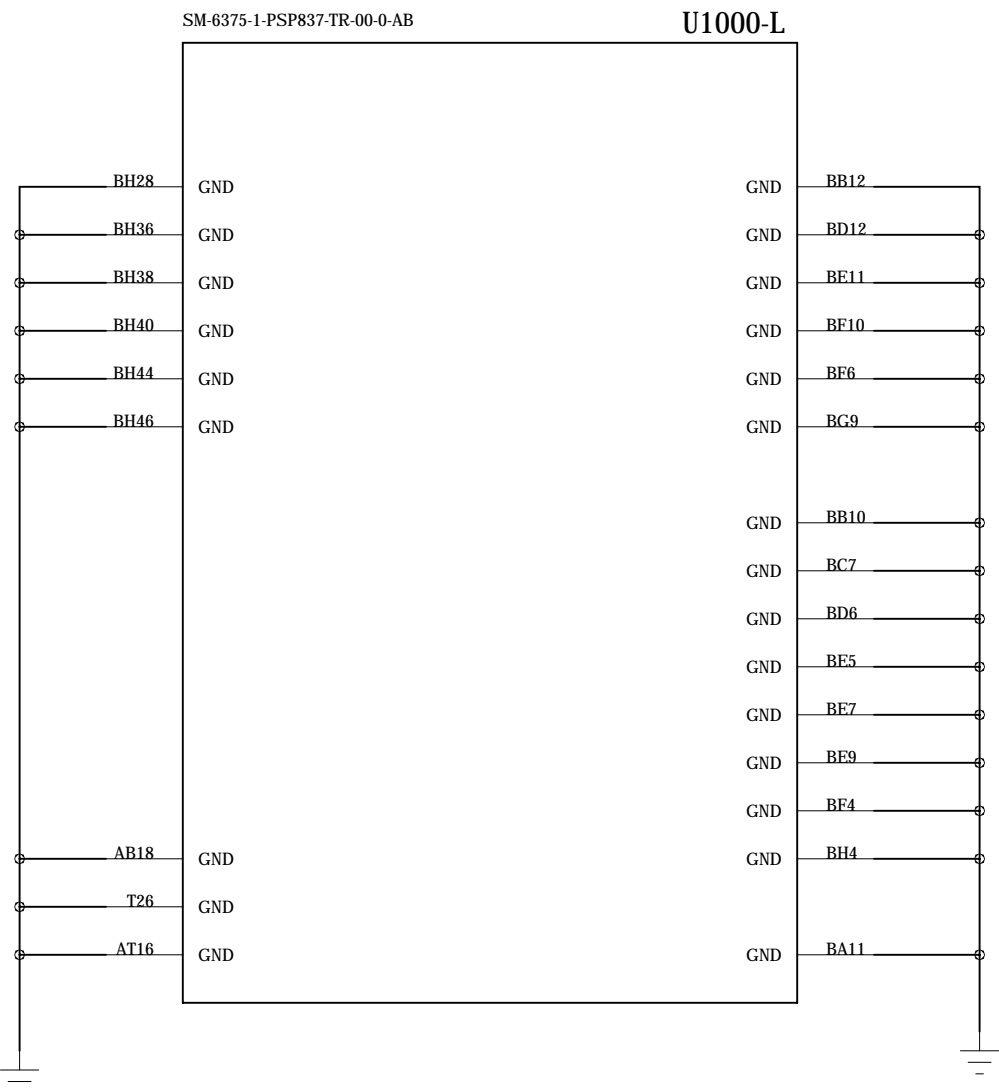
U1000-N



U1000-M



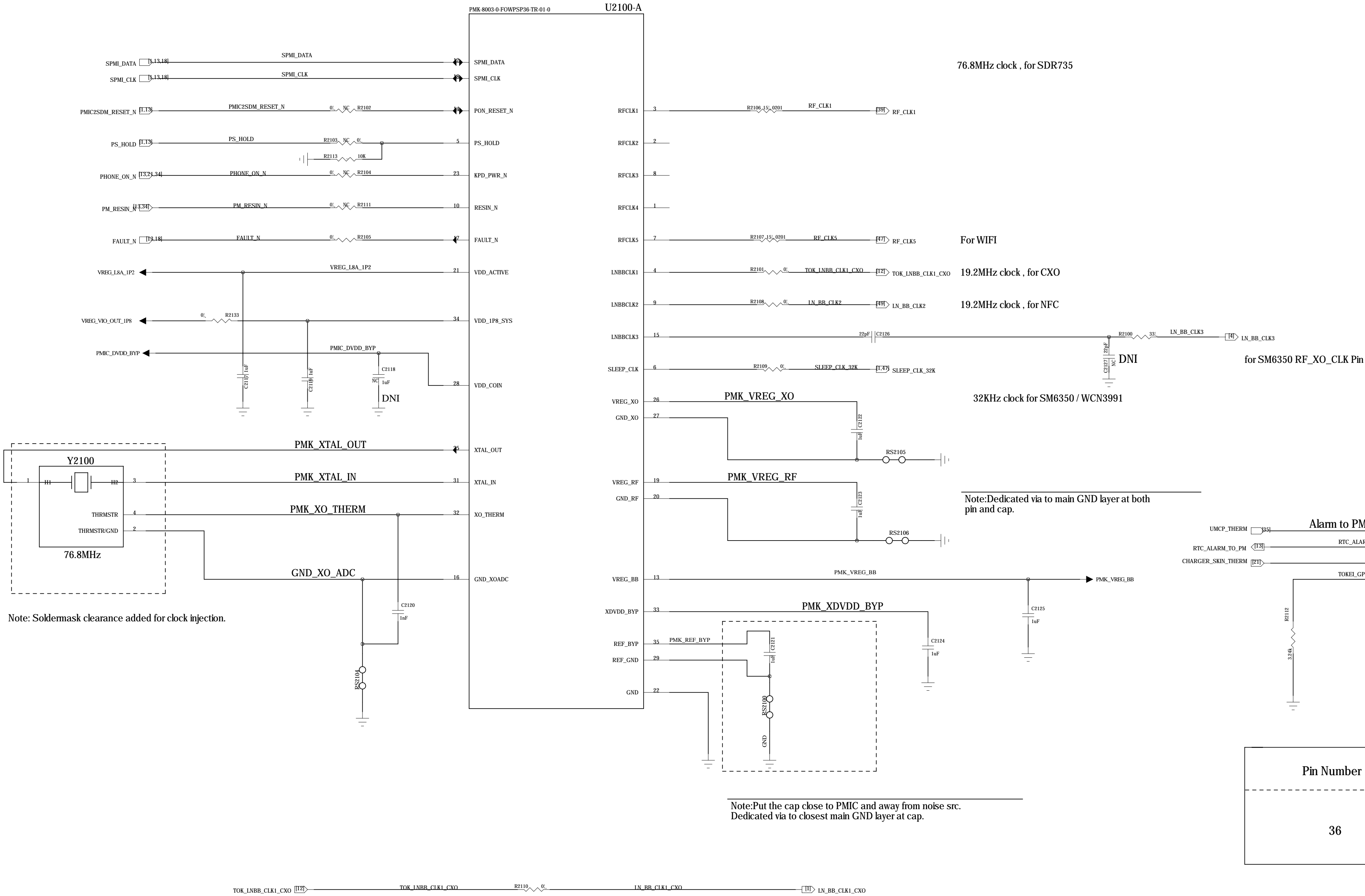
U1000-L



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
DRAWN: MichaelLiang	DATED: 20211025	CODE: P721CE	SIZE: D
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	
RELEASED: <Released By>	DATED: <Release Date>	SHEET: 51	

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

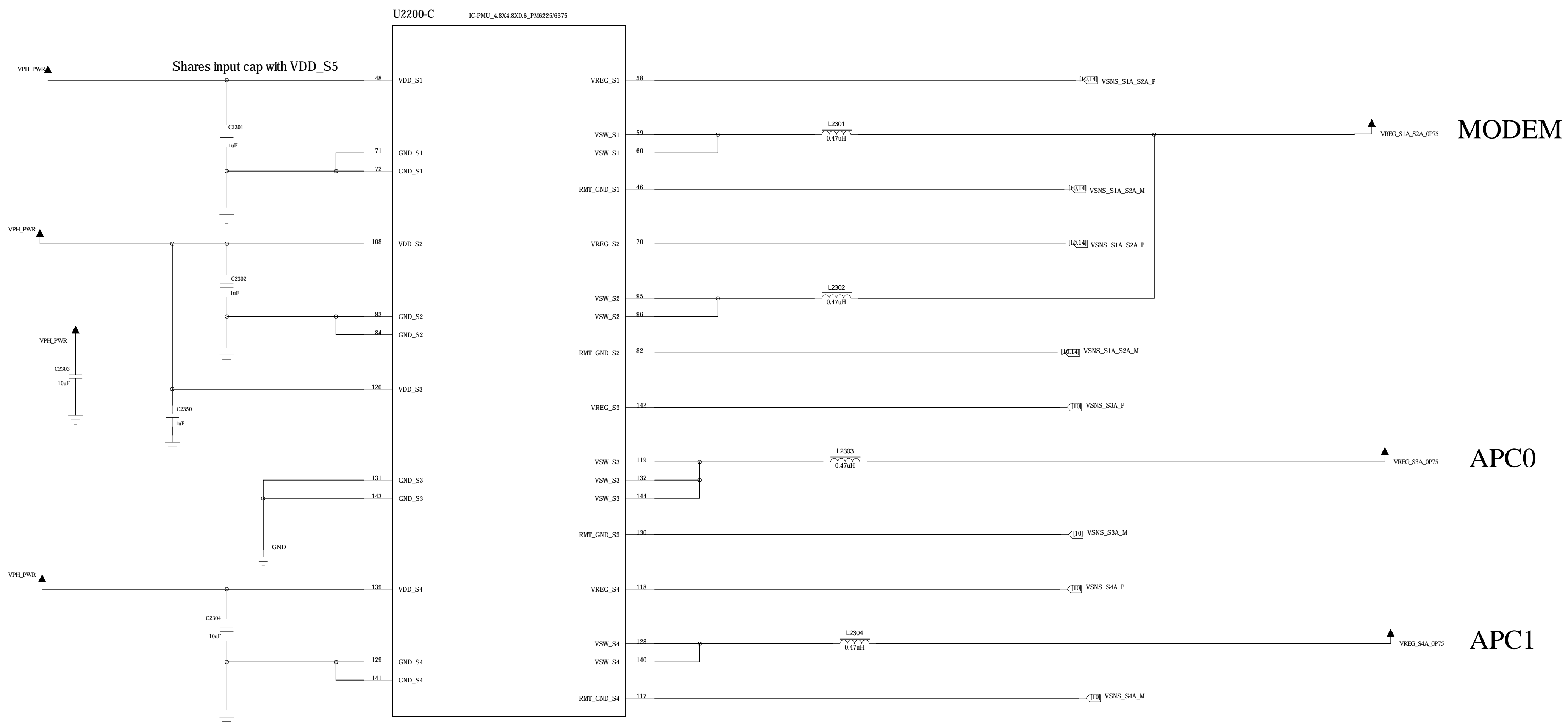
Note:SPML\_CLK & SPML\_DATA should be routed away from noisy traces  
Route signals as a diff pair as much as possible



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
DRAWN: MichaelLiang	DATED: 20211025	CODE: P721CE	SIZE: D
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	
RELEASED: <Released By>	DATED: <Release Date>	SHEET: 42 51	



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



OPTION	BUCK	RAIL
STRAIT	S4A	APC1
S3A	APC0	
STRAIT FUTURE:	S3A_4A	APC1
S1I	APC0	

DRAWN: MichaelLiang		DATED: 20211025		P721CE Main Board			
CHECKED: <Checked By>		DATED: <Checked Date>		CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>		DATED: <QC Date>					
RELEASED: <Released By>		DATED: <Release Date>					
SCALE: <Scale>				SHEET: 4 of 51			

COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD

TITLE:	P721CE Main Board
--------	-------------------

CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31

SCALE: <Scale> SHEET: 14 51

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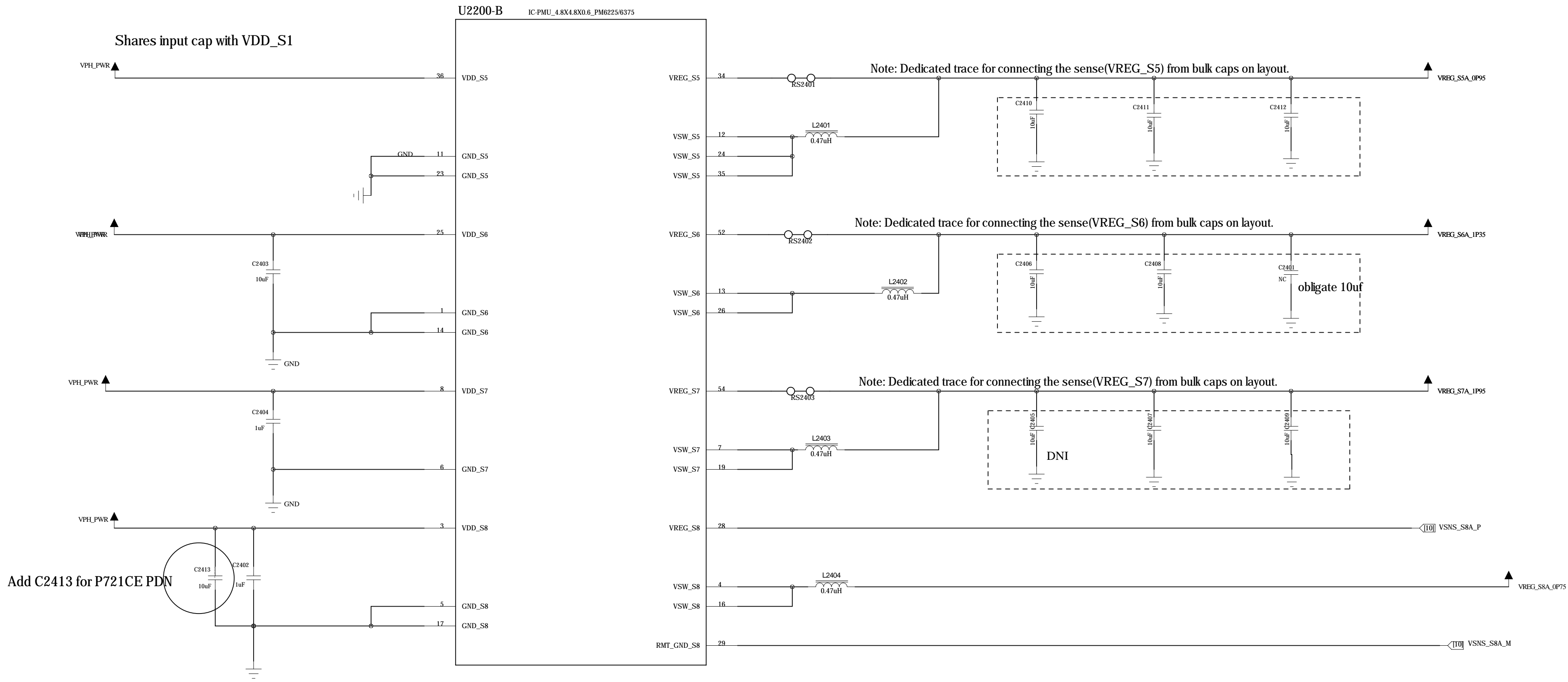
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REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



GFX

COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD

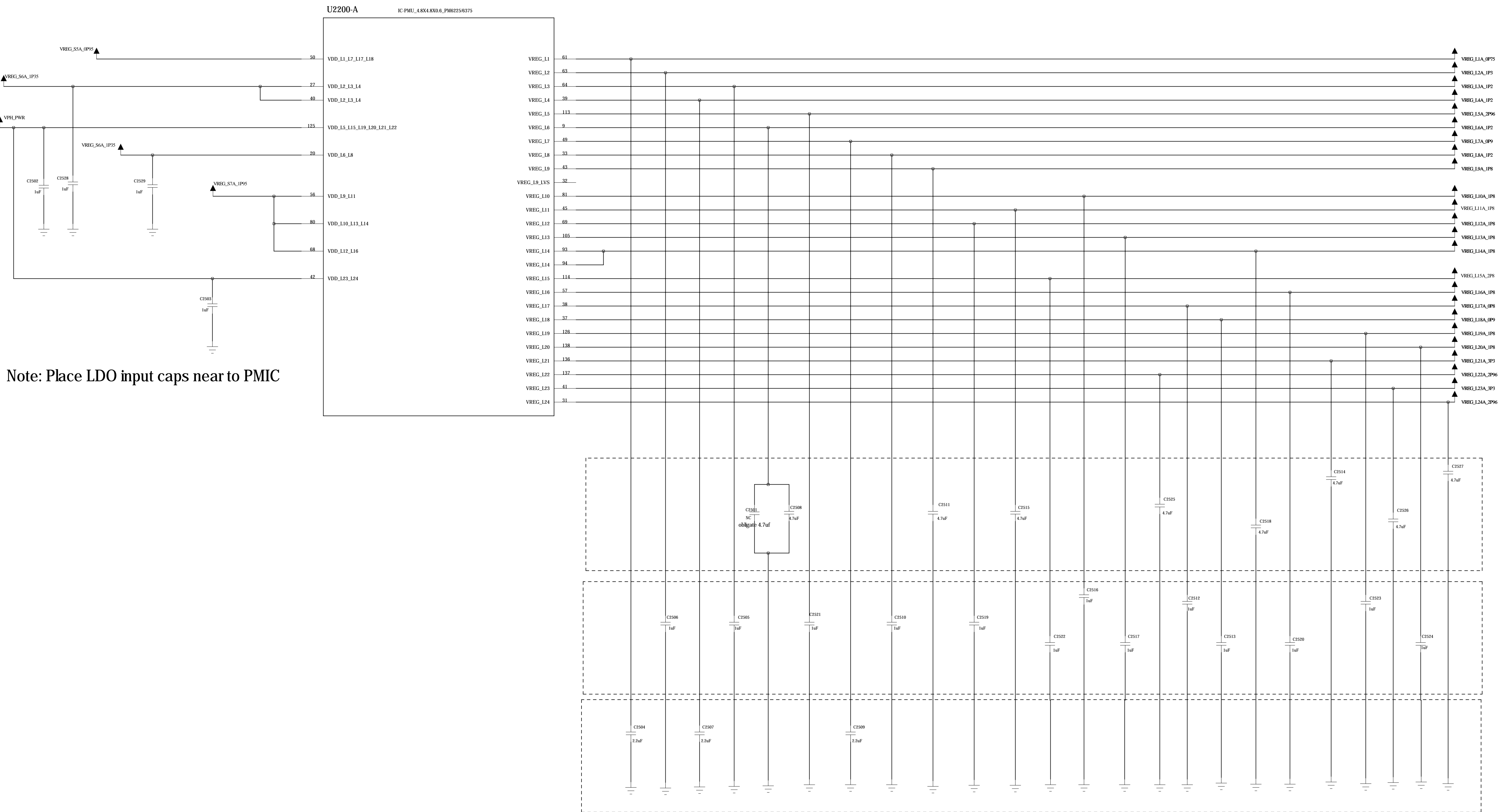
TITLE: P721CE Main Board

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 45 51	

# 1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Note: Place LDO input caps near to PMIC

VDD\_LPI\_CX  
VDD\_A\_WLAN\_ADC / WCN  
NOT USED  
VDD\_A\_CSI / DSI / QLINK  
PX2  
SDR735\_VDDAM  
VDDA\_0P9/SDR735\_VDDMX  
PX11  
VDD1 / PX3 / PX7

VDD\_A\_USBHS/CXO\_1P8  
VCCQ2  
SENSOR  
LCD\_VDDIO  
WCD9375\_VDD\_BUCK

```
RF switch
WCN3998-1
VDD_LPI_MX
VDD_A_USBSSDP
UIM_0/SN110U/VDD_PX5
UIM_1/SN110U/VDD_PX6
WCN3998-1_3P3_CH1 NOT USED
SDCARD
WCN3998-1_3P3_CH0
UFS_2L_VCC
```

4.7uF

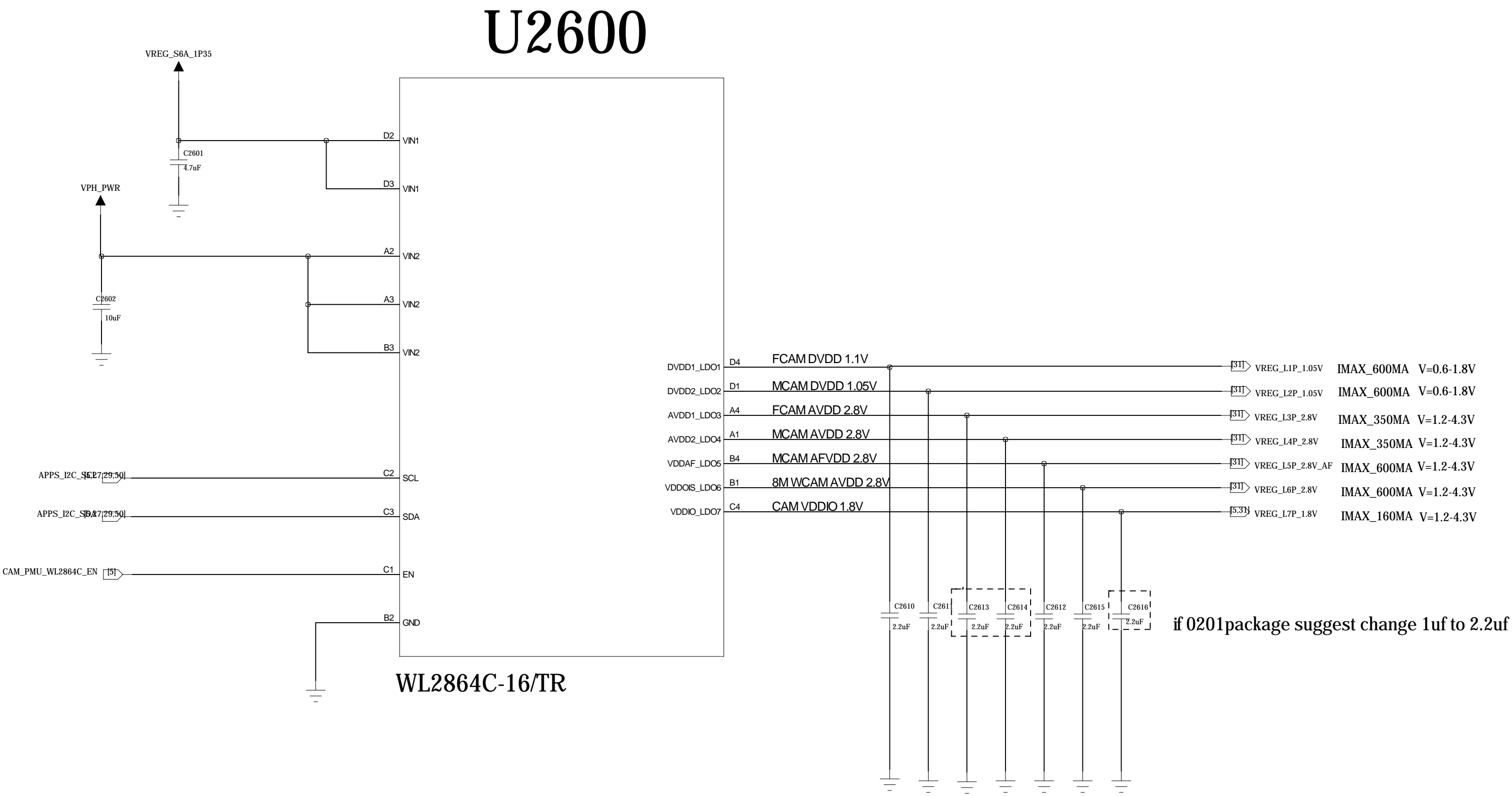
1.0uF

2.2uF

		COMPANY: <b>TINNO MOBILE TECHNOLOGY CO.,LTD</b>			
		TITLE: <b>P721CE Main Board</b>			
DRAWN: <b>MichaelLiang</b>	DATED: <b>20211025</b>				
CHECKED: <b>&lt;Checked By&gt;</b>	DATED: <b>&lt;Checked Date&gt;</b>	CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL: <b>&lt;QC By&gt;</b>	DATED: <b>&lt;QC Date&gt;</b>	<b>P721CE</b>	<b>D</b>	<b>P721CE</b>	<b>V0.3</b>
RELEASED: <b>&lt;Released By&gt;</b>	DATED: <b>&lt;Release Date&gt;</b>	SCALE: <b>&lt;Scale&gt;</b>			SHEET: <b>46</b> <b>51</b>



REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

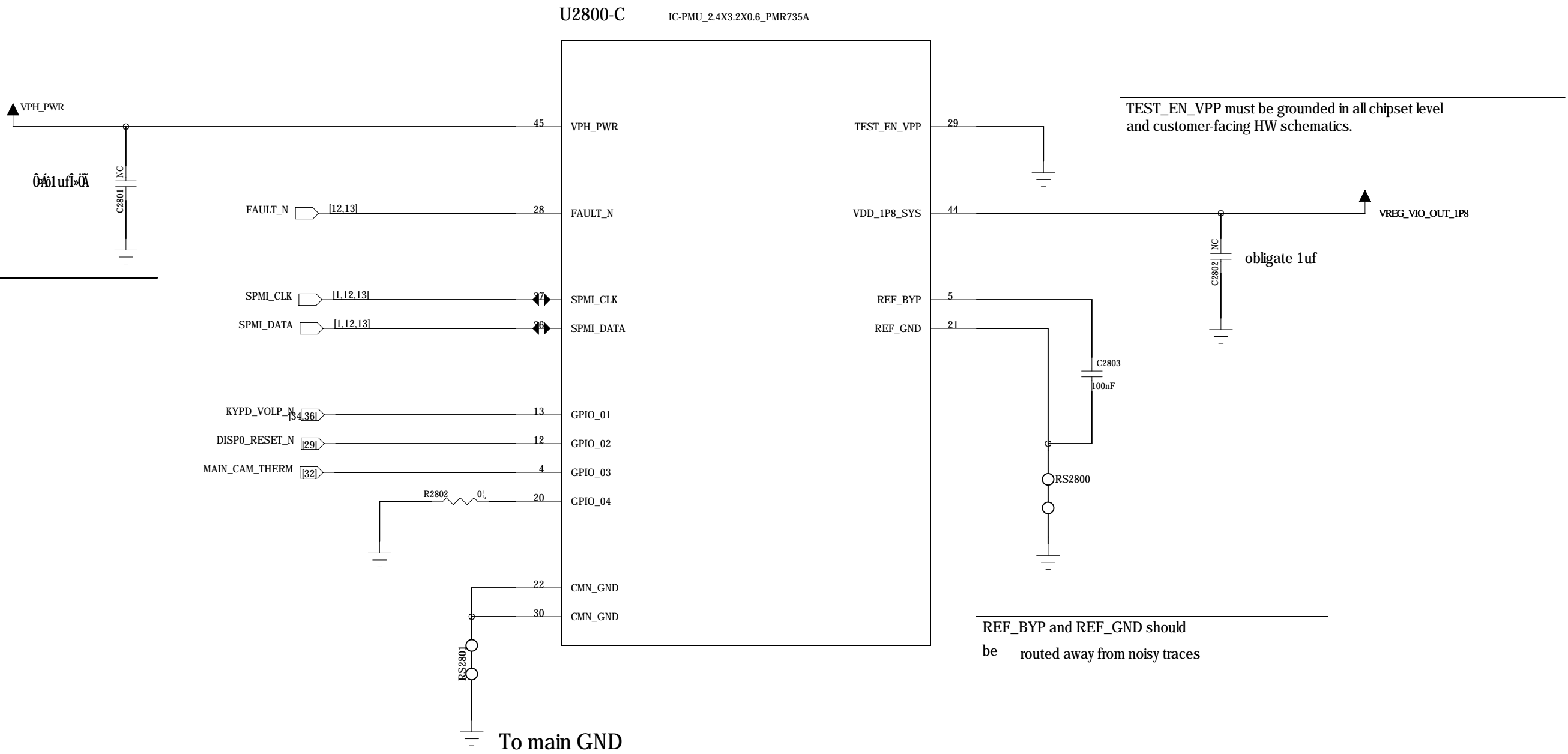


COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 47 51	

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

SPMI\_CLK & SPMI\_DATA should be routed away from noisy traces  
Route signals as a diff pair as much as possible

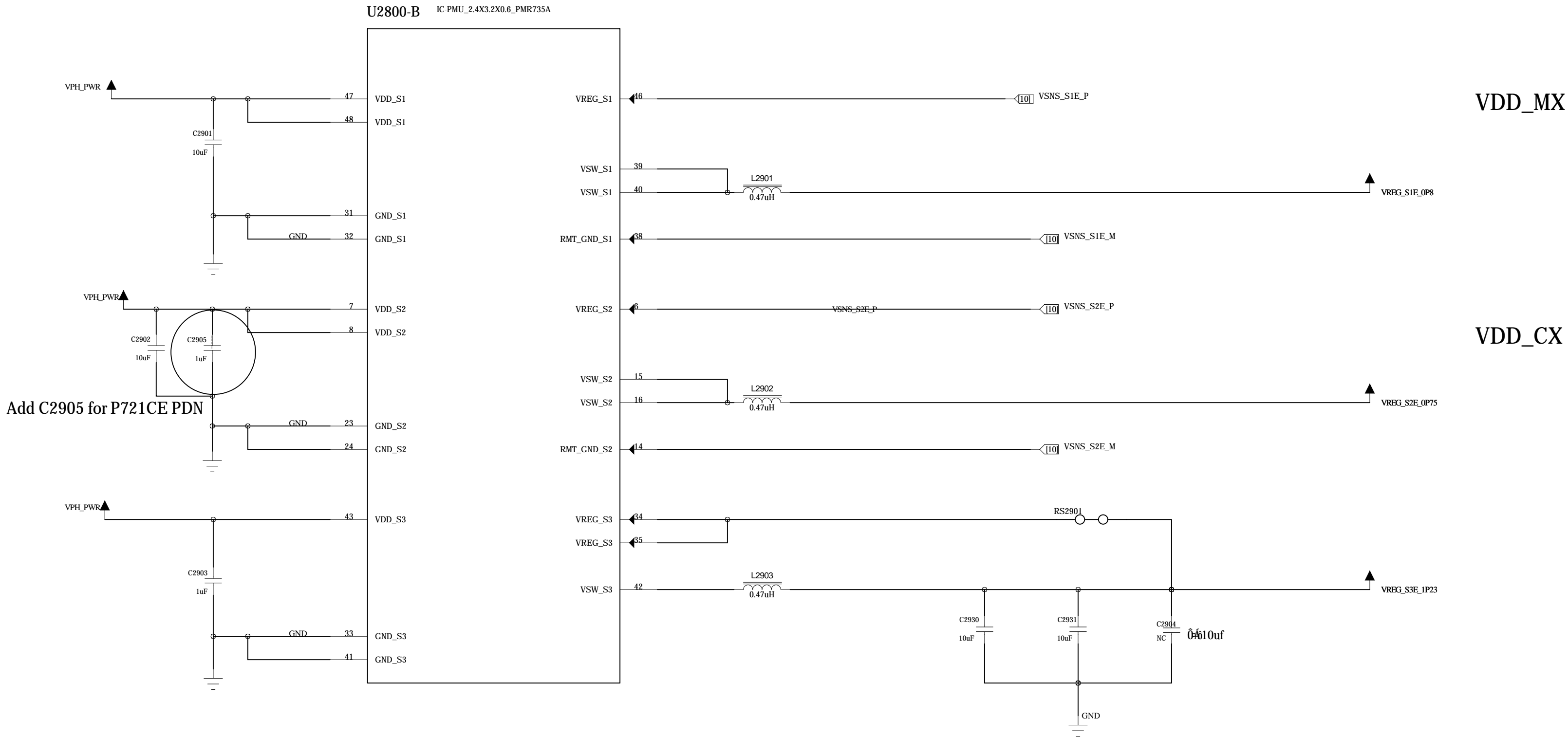


Dedicated trace from REF\_BYP to cap.  
Dedicated trace from REF\_GND to cap.  
Dedicated via to main ground plane right under PMIC pin (REF\_GND)  
or as close to the pin as possible.  
Do NOT add via at cap.

COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 48 51	

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



Place Cin as close to PMIC as possible to achieve this requirement.

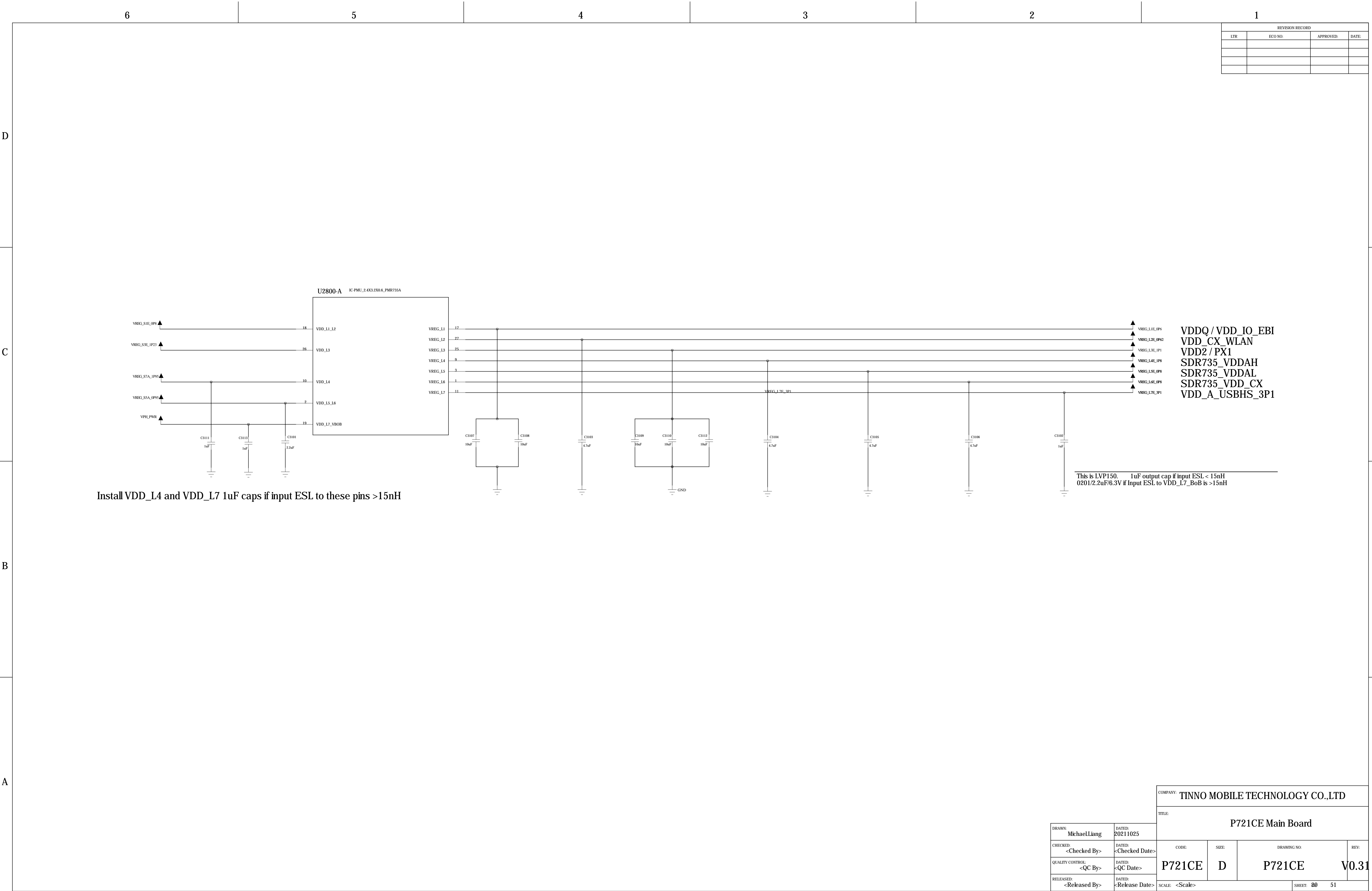
Loop inductance from (VIN\_Sx to Cin + Cin to GND\_Sx) must be < 3nH

COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD

TITLE: P721CE Main Board

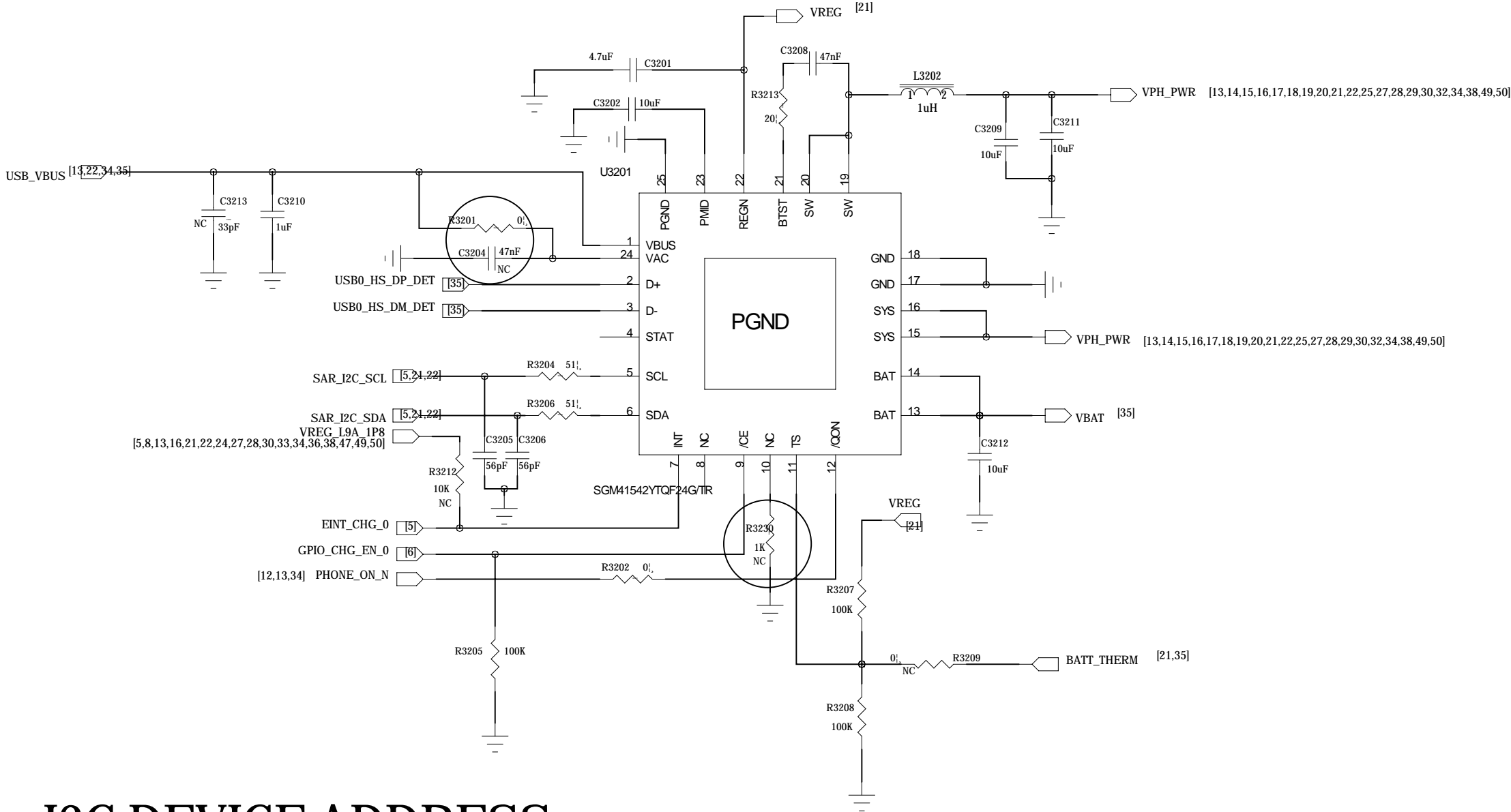
DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

CODE	SIZE	DRAWING NO.	REV.
P721CE	D	P721CE	V0.31
SCALE: <Scale>			SHEET 00 51



REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

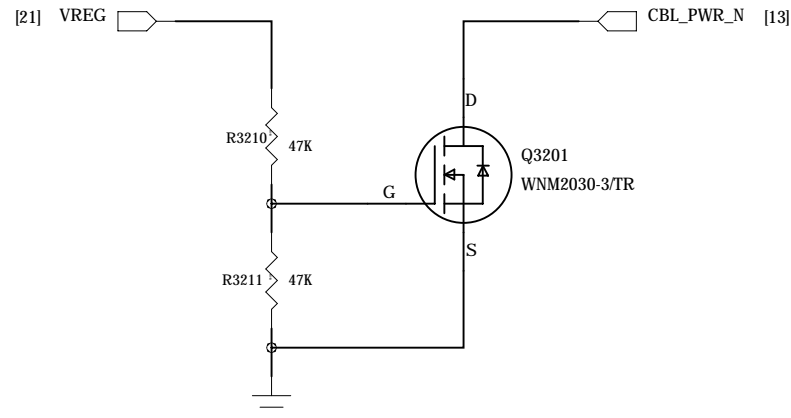
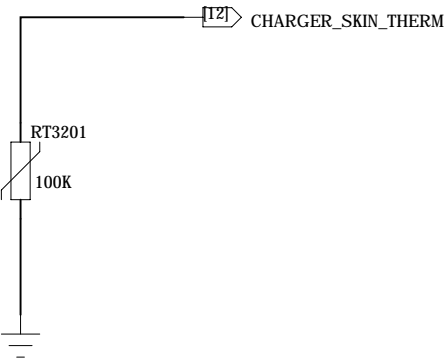
	R3201	C3204	R3230
BQ25601D//SGM41512//SGM41542	0Ω	NC	NC
BQ25890H	NC	47nF	1K



I2C DEVICE ADDRESS:

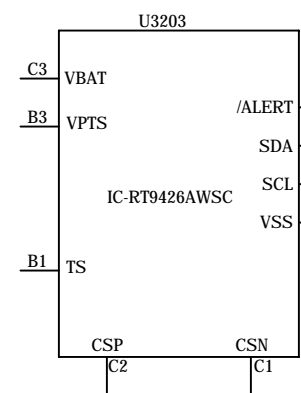
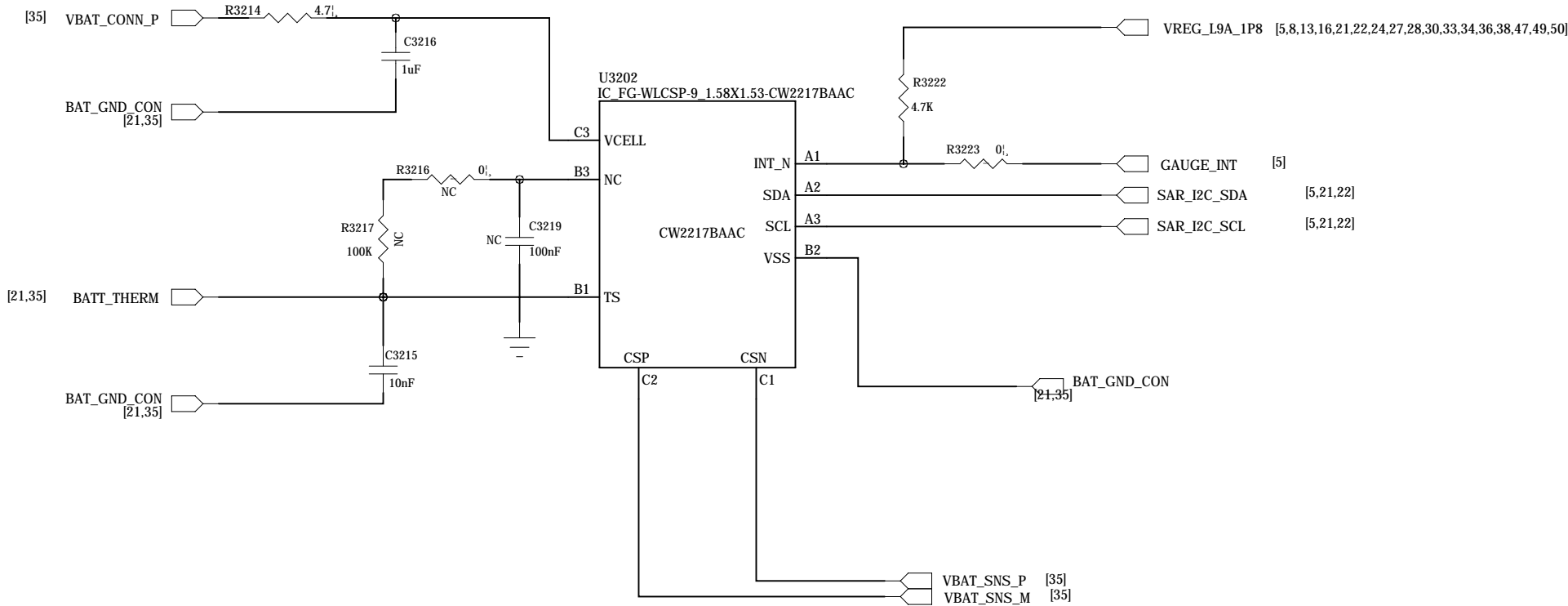
SGM41512(0x6B)

bq25601(6BH)



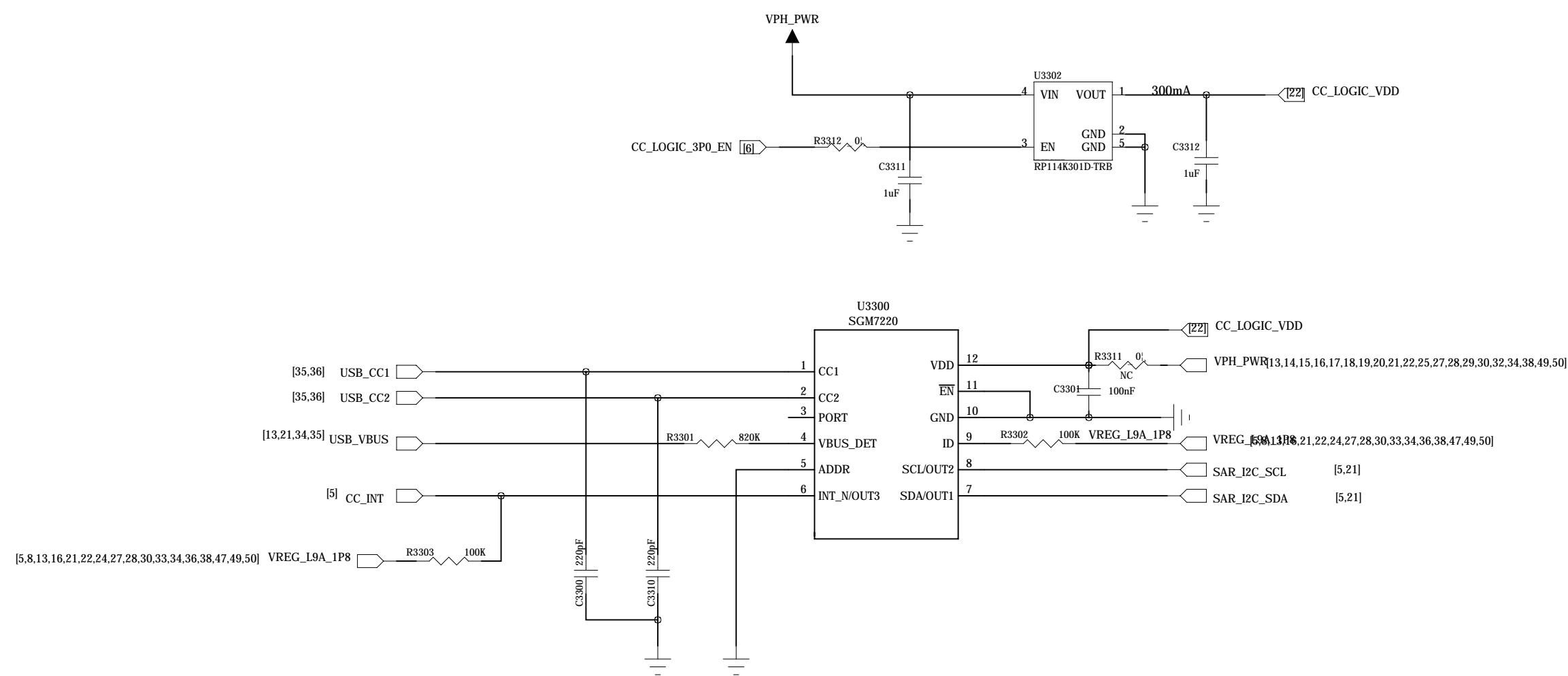
	R3216	R3217	C3219
CW2217	NC	NC	NC
RT9426A	0Ω	100K	100nF

CW2177 I2C slave address = 7'b1100100  
RT9426A I2C slave address = 7'b1010101



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
DRAWN: MichaelLiang	DATED: 20211025	CODE: P721CE	SIZE: D
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	
RELEASED: <Released By>	DATED: <Release Date>	SHEET: 21 51	

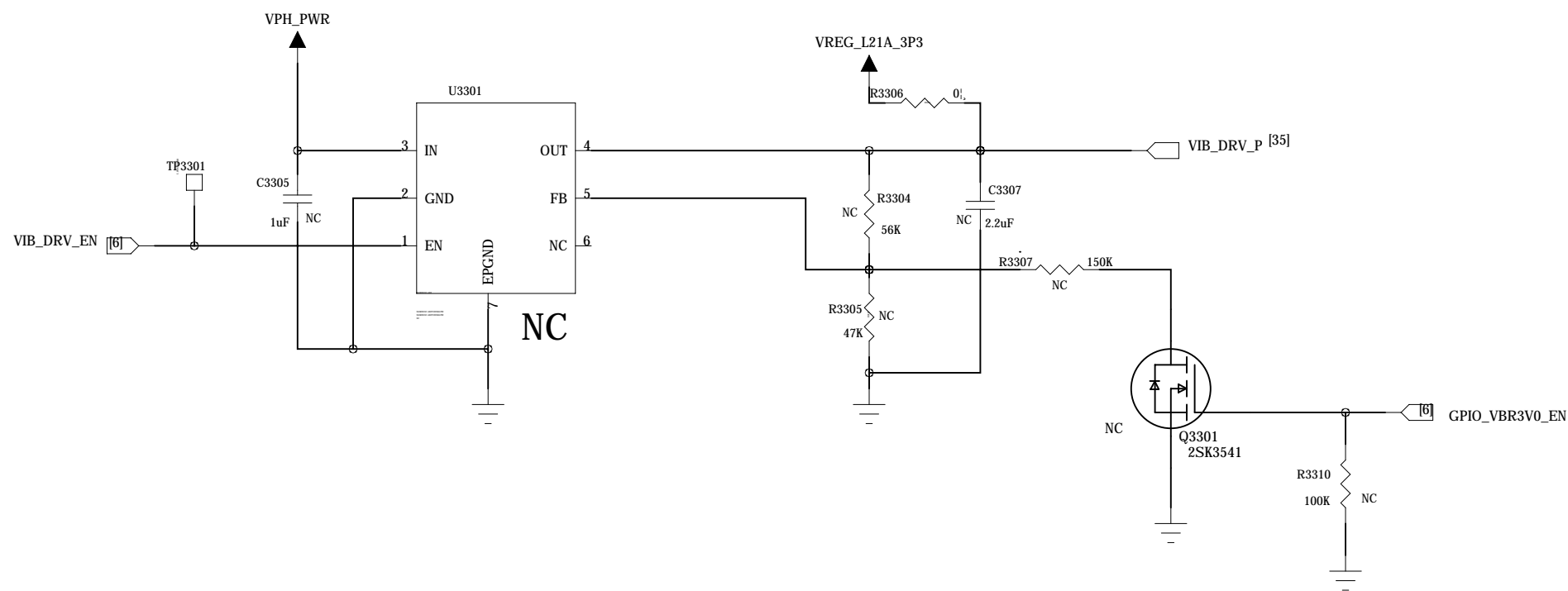
REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



SGM7220 I <sup>2</sup> C Target Address								
ADDR Pin	Bit 7 (MSB)	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0 (W/R)
H	1	1	0	0	1	1	1	0/1
L	1	0	0	0	1	1	1	0/1

WHEN GPIO\_VBR3V0\_EN IS LOW  $V_{IB\_DRV\_P} = 1.206V * (R3304/R3305 + 1) = 2.643V$

WHEN GPIO\_VBR3V0\_EN IS HIGH  $VIB\_DRV\_P = 1.206V * [R3304 * (R3305 + R3307) / (R3305 * R3307) + 1] = 3.093V$



		COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
		TITLE: P721CE Main Board			
DRAWN: MichaelLiang	DATED: 20211025				
CHECKED: <Checked By>	DATED: <Checked Date>	CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>	DATED: <QC Date>				
RELEASED: <Released By>	DATED: <Release Date>				
SCALE: <Scale>				SHEET: 02	51

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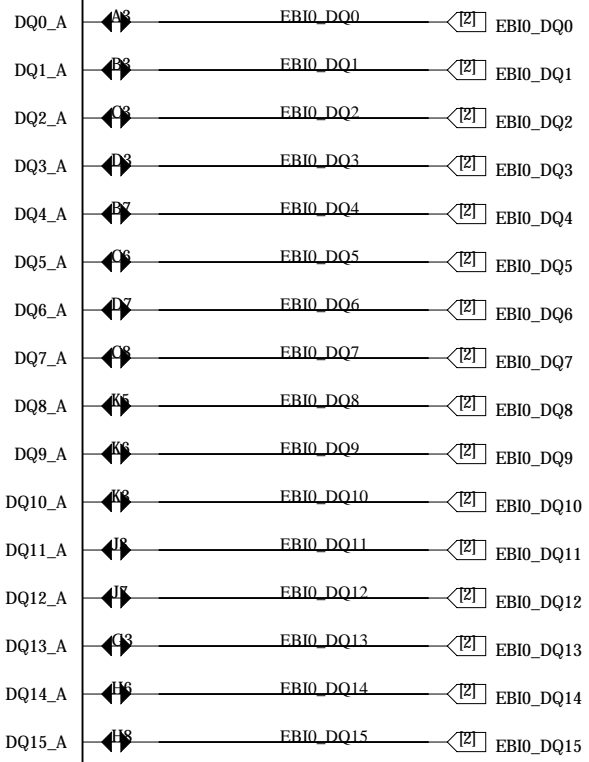
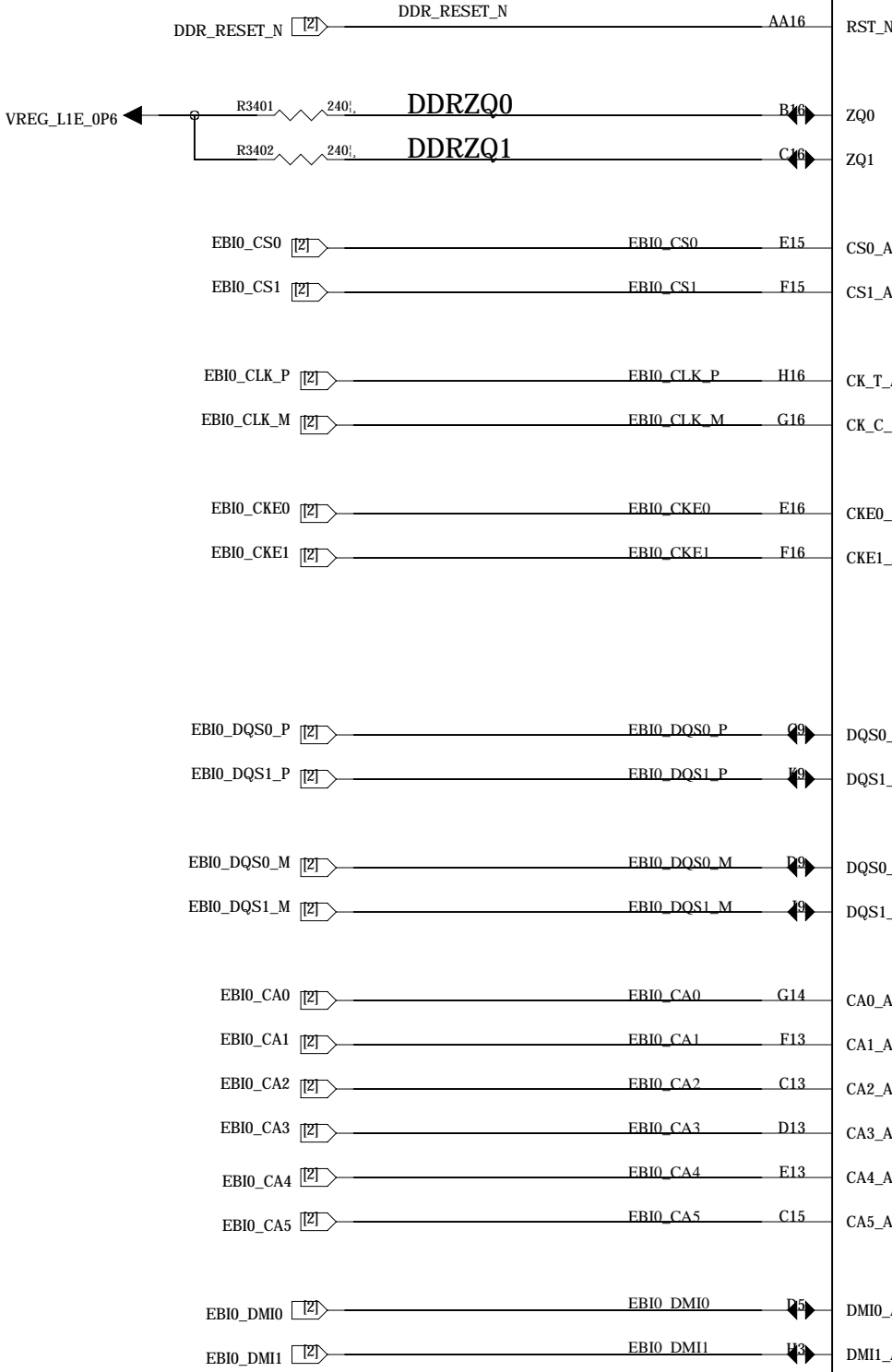
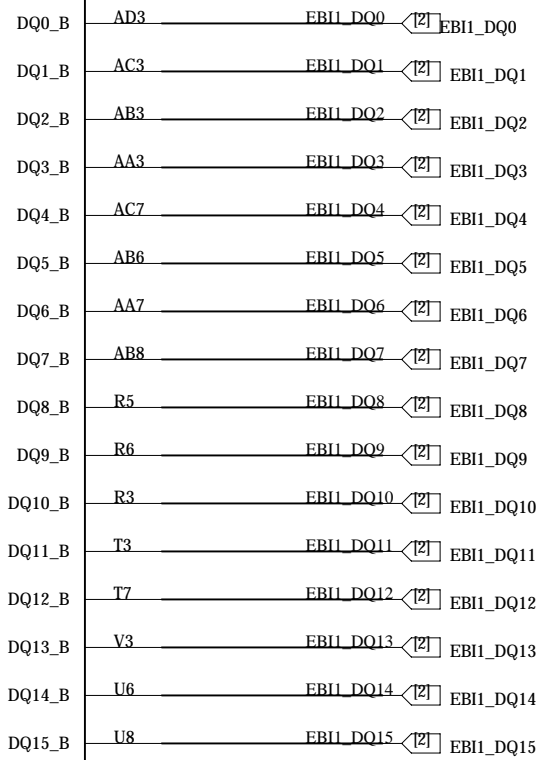
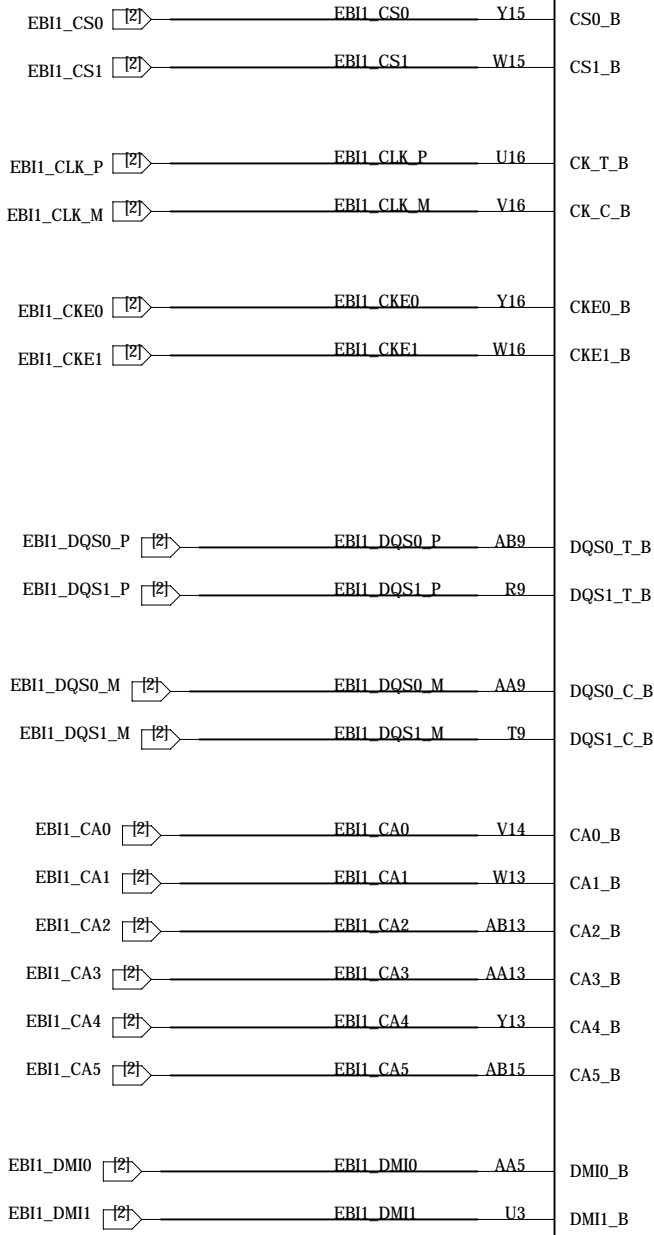
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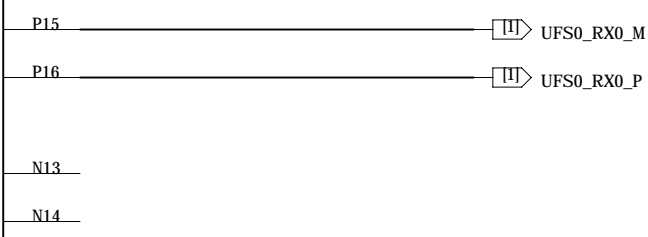
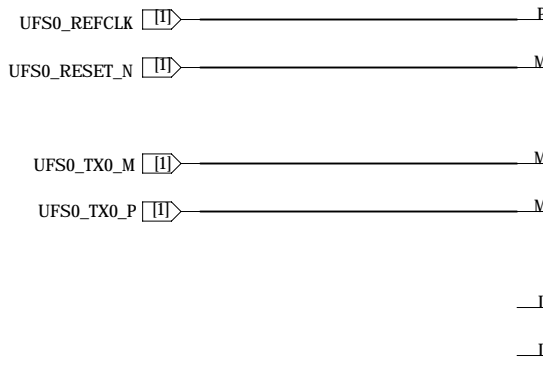
REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

SS uMCP MCN 271-93557-0001  
SKHynix uMCP MCN  
271-94865-0000(default)  
Micron eMCP MCN 271-94866-0000

U3400-E

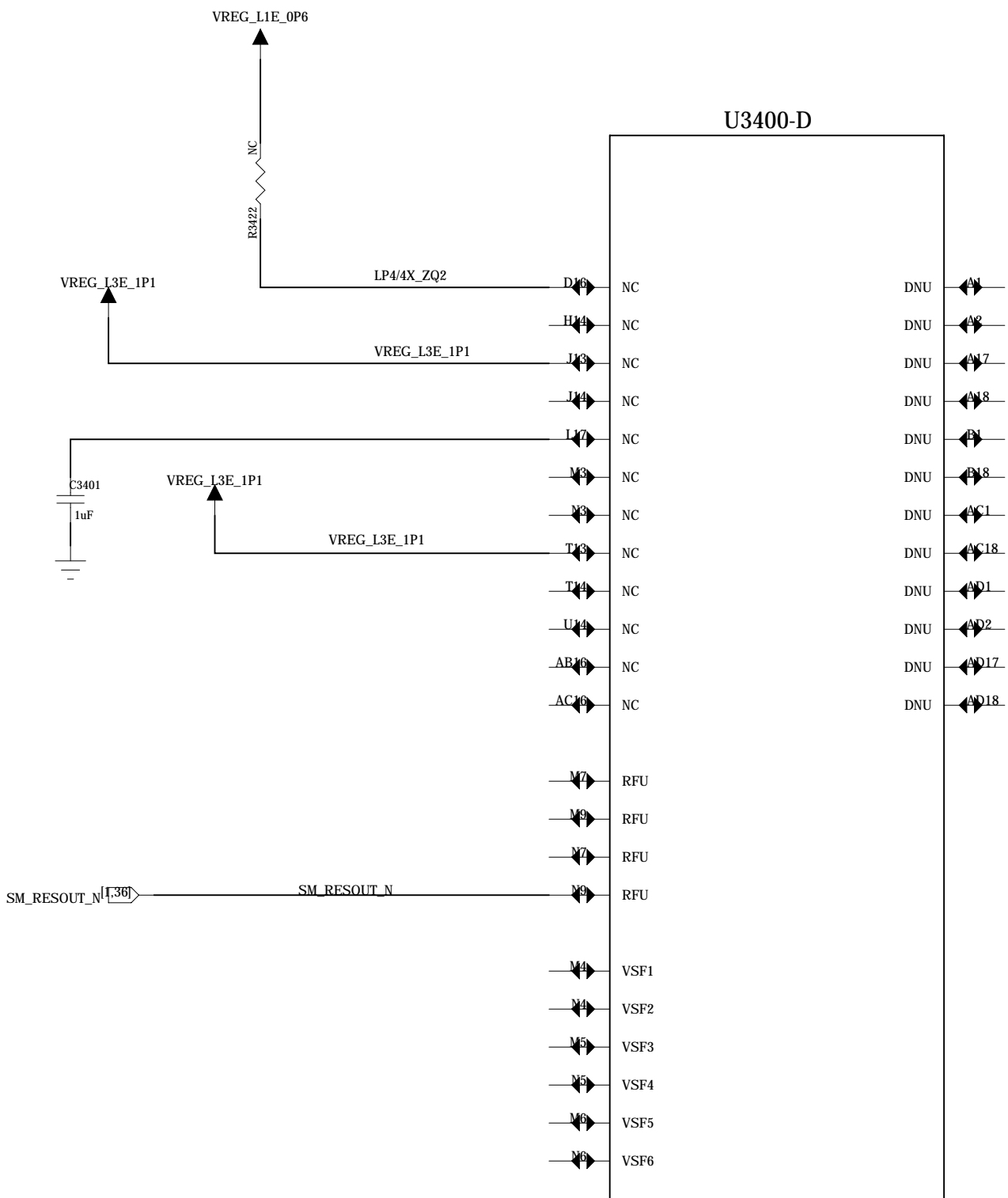


U3400-C



DNI C3401 for SKHynix UMCP  
Mount for SS uMCP and EMCP

U3400-D



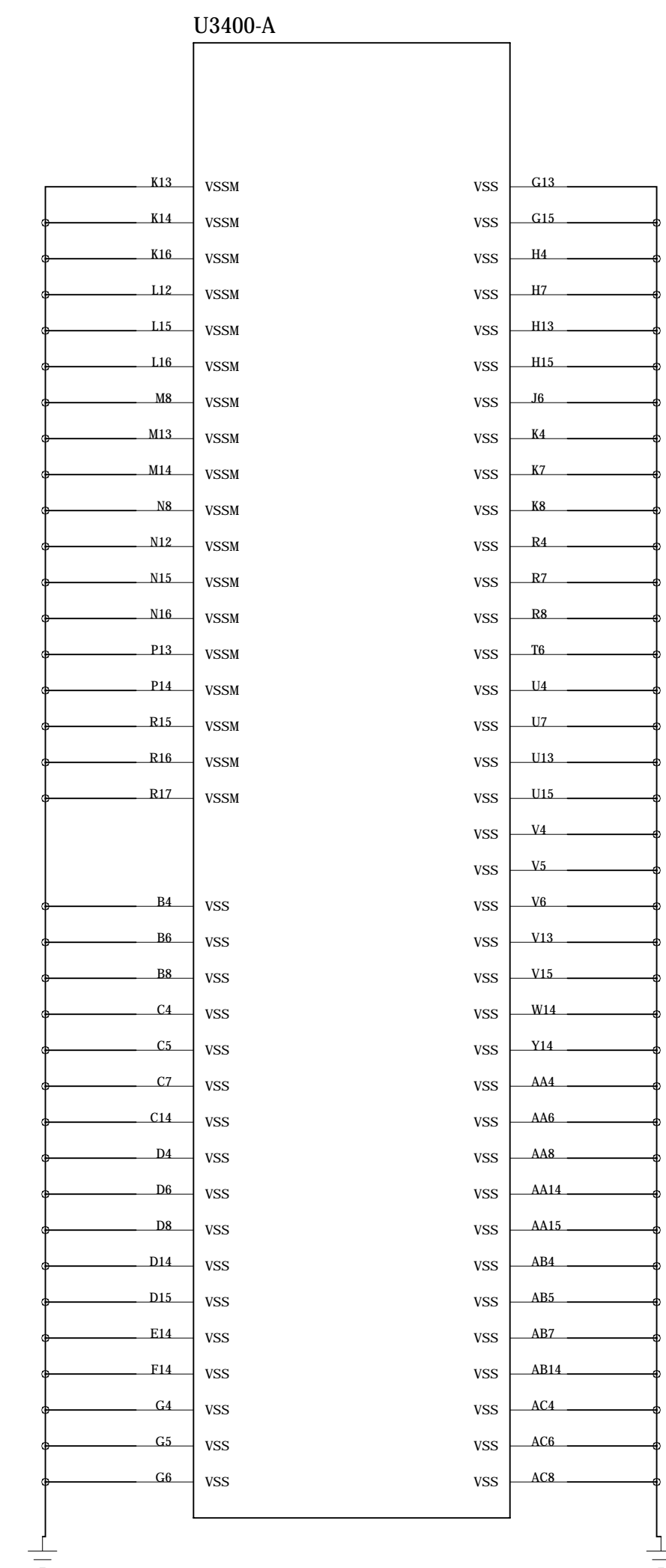
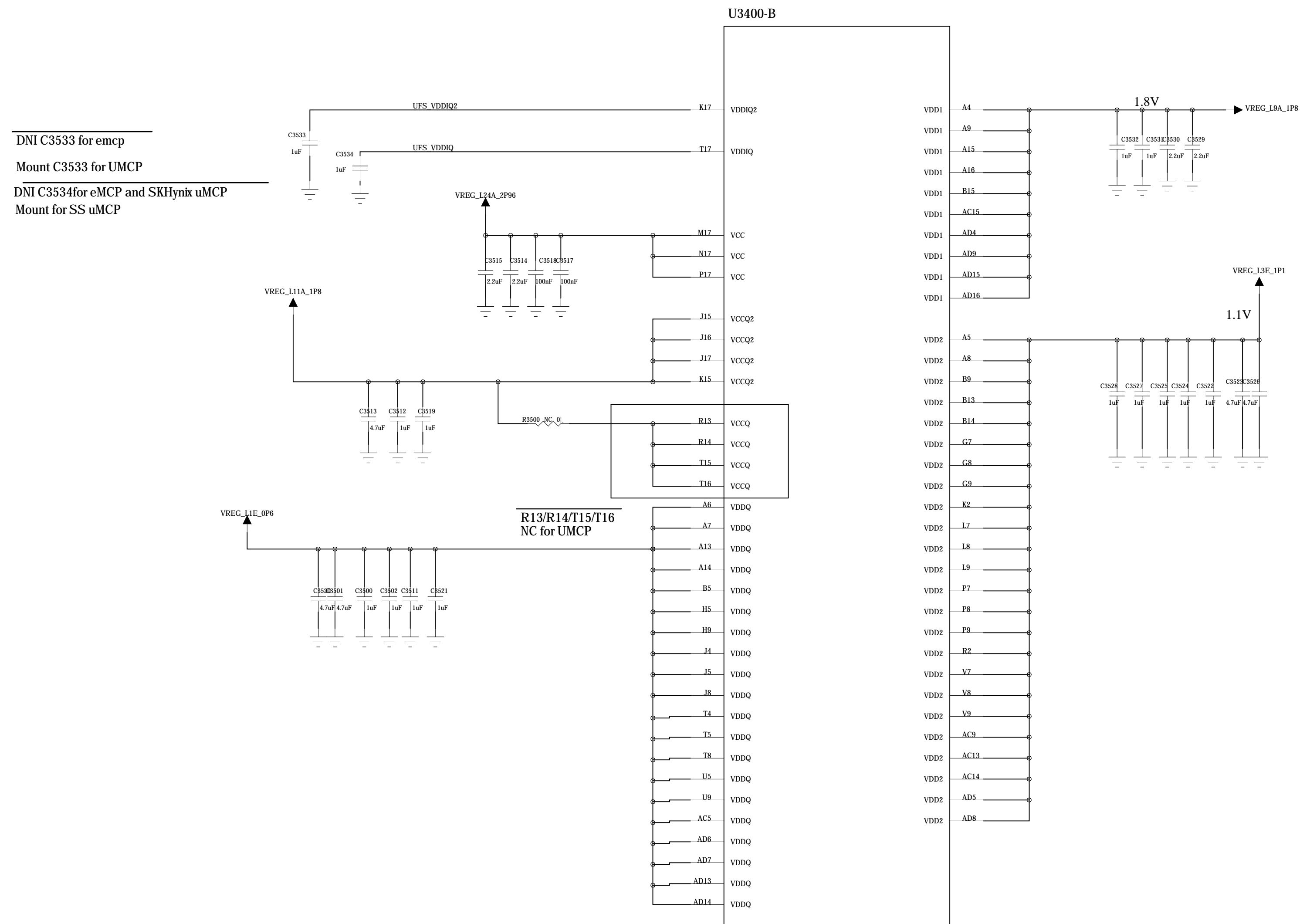
COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD

TITLE: P721CE Main Board

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

CODE	SIZE	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET 88	51

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



		COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
		TITLE: P721CE Main Board			
DRAWN: Michael.Liang	DATE: 20211025				
CHECKED: <Checked By>	DATE: <Checked Date>	CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>	DATE: <QC Date>				
RELEASED: <Released By>	DATE: <Release Date>	SCALE: <Scale>		SHEET: 24	51

DRAWN: MichaelLiang		DATED: 20211025		P721CE Main Board			
CHECKED: <Checked By>		DATED: <Checked Date>		CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>		DATED: <QC Date>					
RELEASED: <Released By>		DATED: <Release Date>					
				SCALE: <Scale>		SHEET: 24 51	



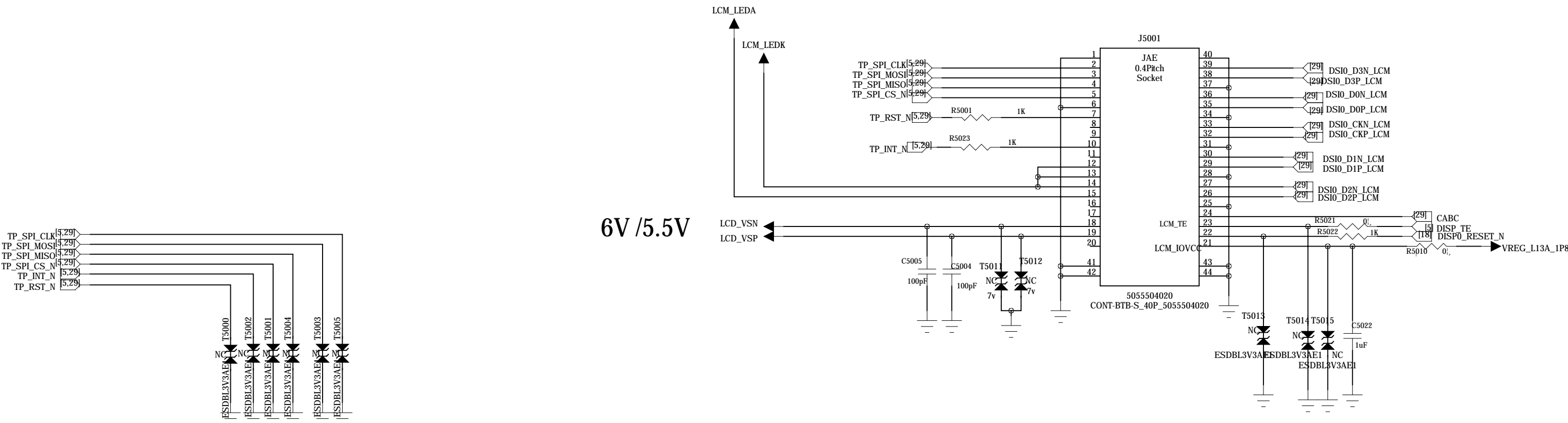








REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

C

B

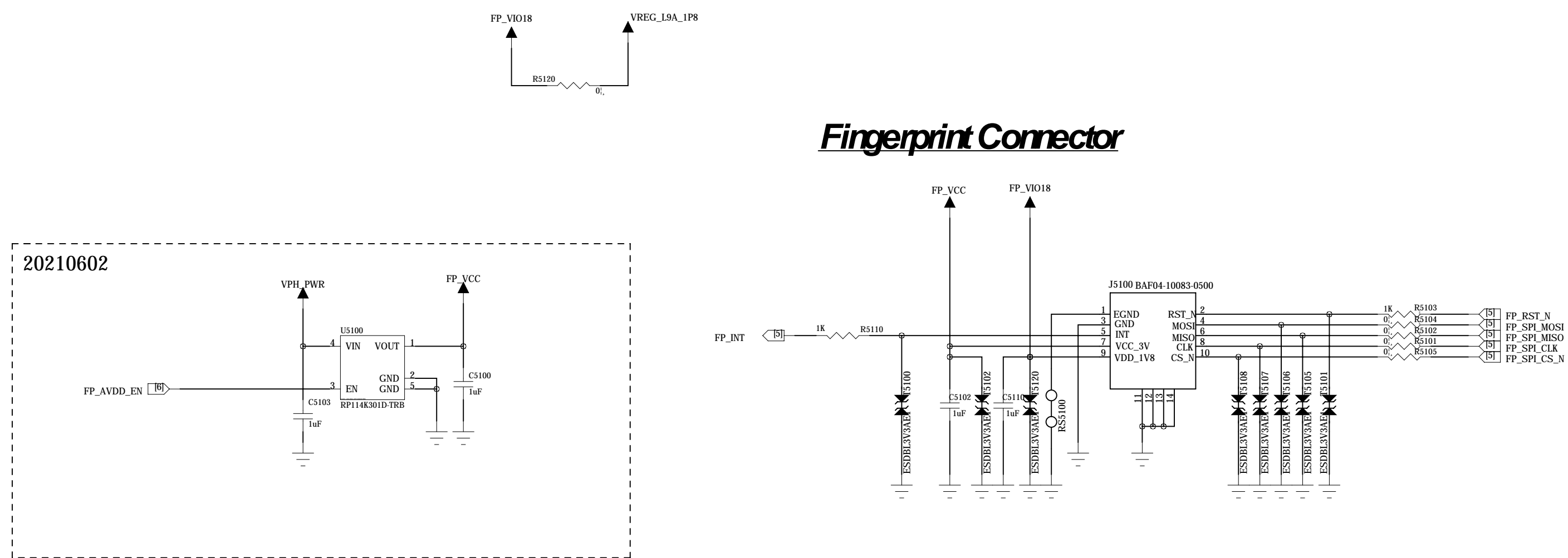
A

D

C

B

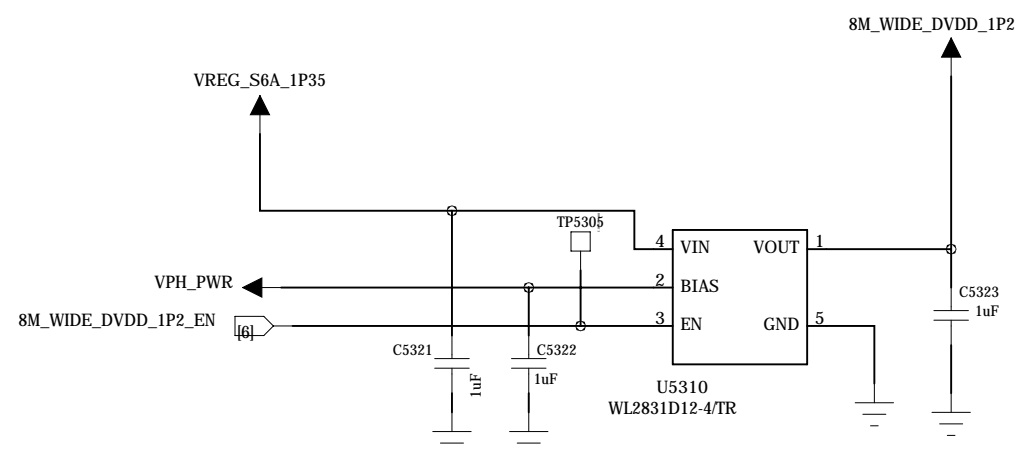
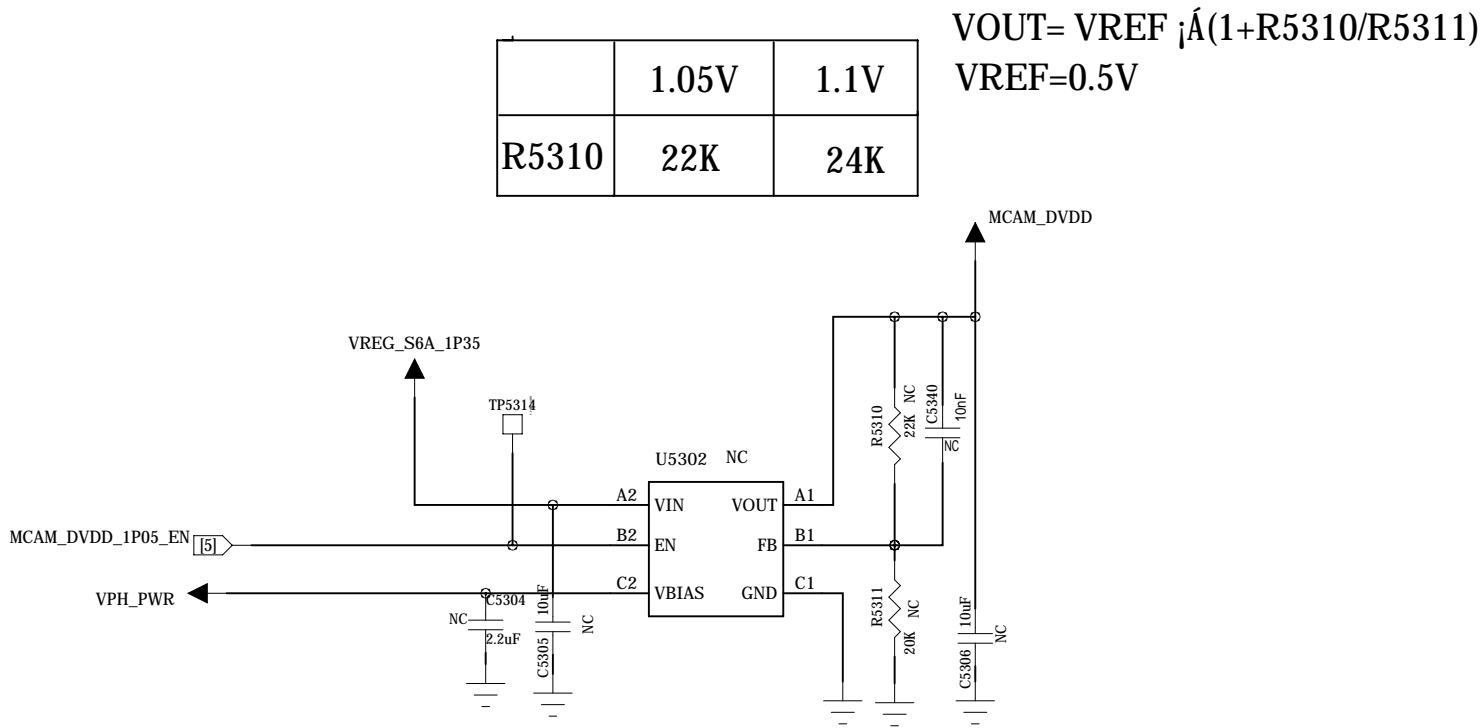
A



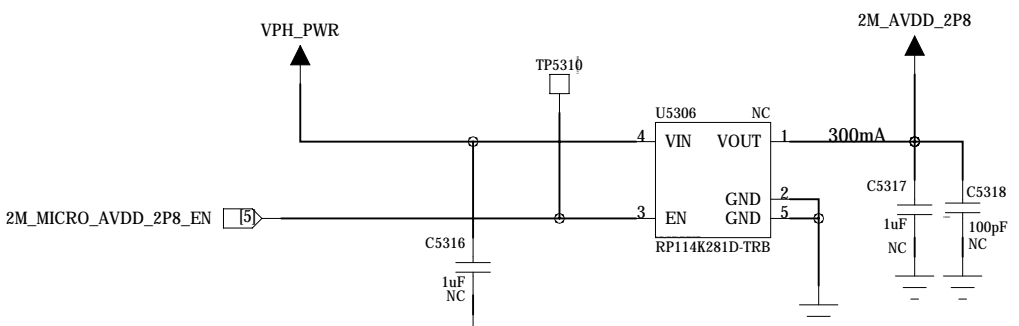
		COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
		TITLE: P721CE Main Board			
DRAWN: MichaelLiang	DATED: 20211025				
CHECKED: <Checked By>	DATED: <Checked Date>	CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>	DATED: <QC Date>				
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>		SHEET: 30	51



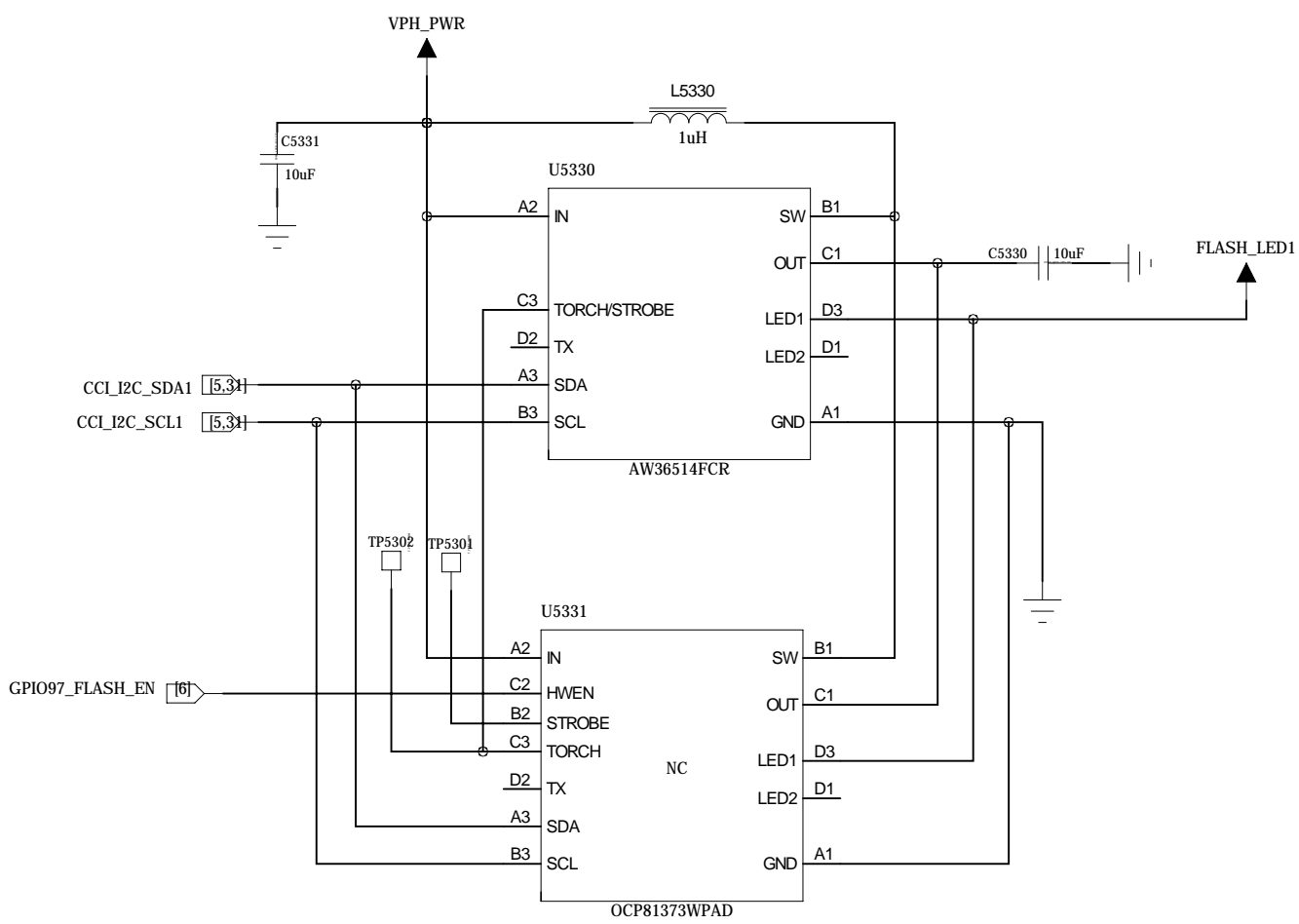
REVISION RECORD			
UTR	ECO NO:	APPROVED:	DATE:



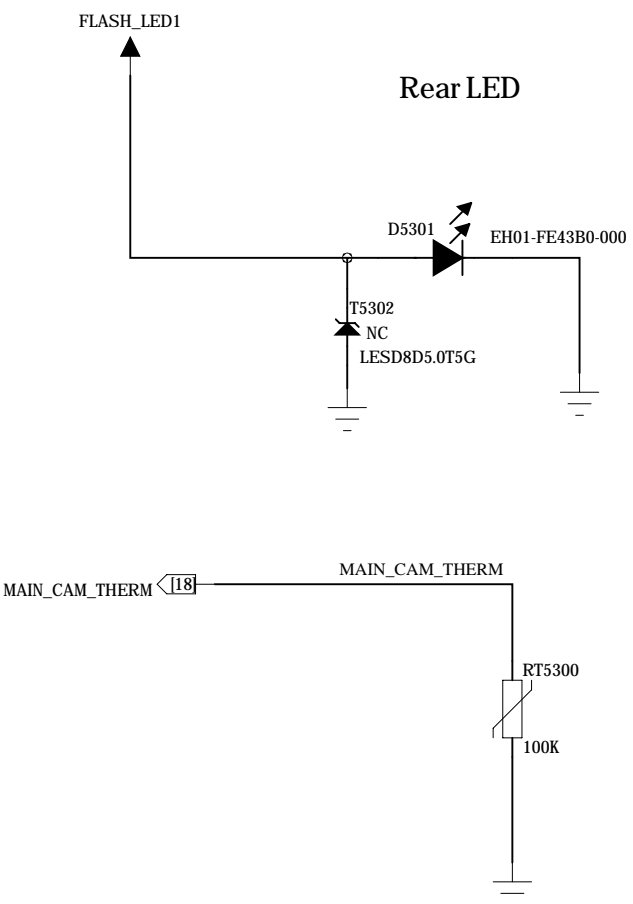
8M Wide DVDD 1.2v



2M CAM AVDD 2.8v



NOTE:  
common pads



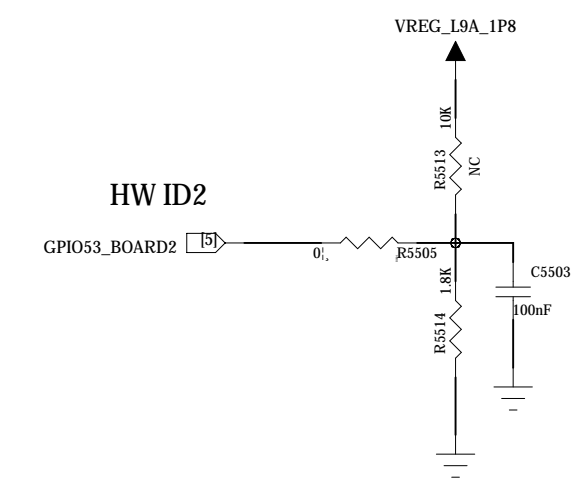
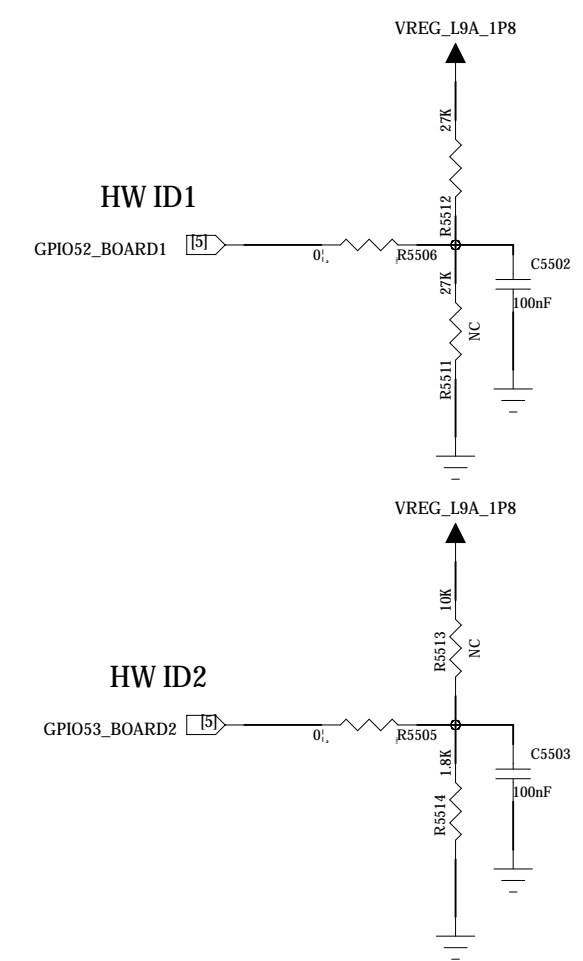
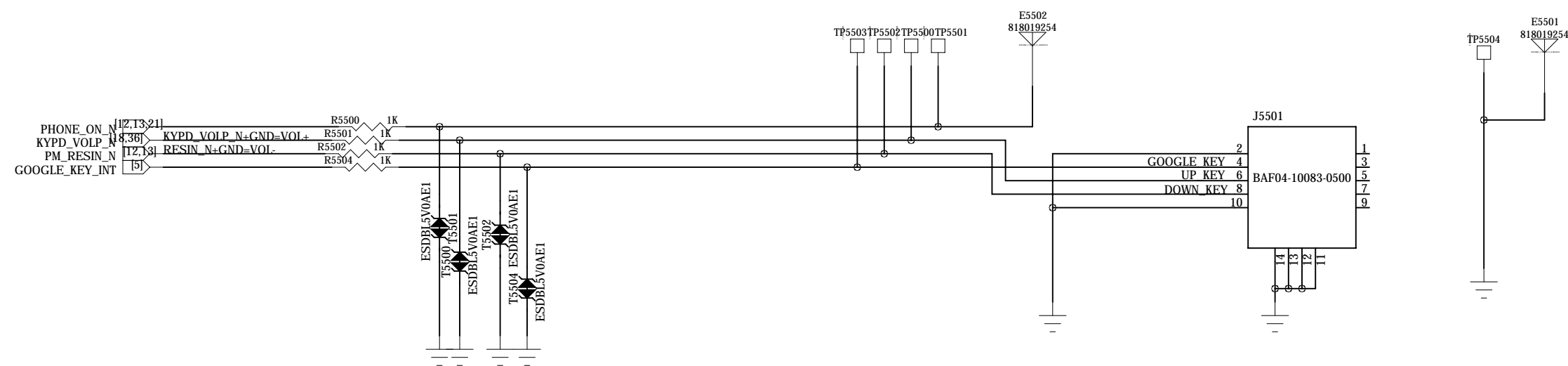
COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
DRAWN: Michael.Liang	DATED: 20211025	CODE:	SIZE:
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO:	REV:
QUALITY CONTROL: <QC By>	DATED: <QC Date>	P721CE	D
RELEASED: <Released By>	DATED: <Release Date>	P721CE	V0.31
SCALE: <Scale>		SHEET: 82 51	



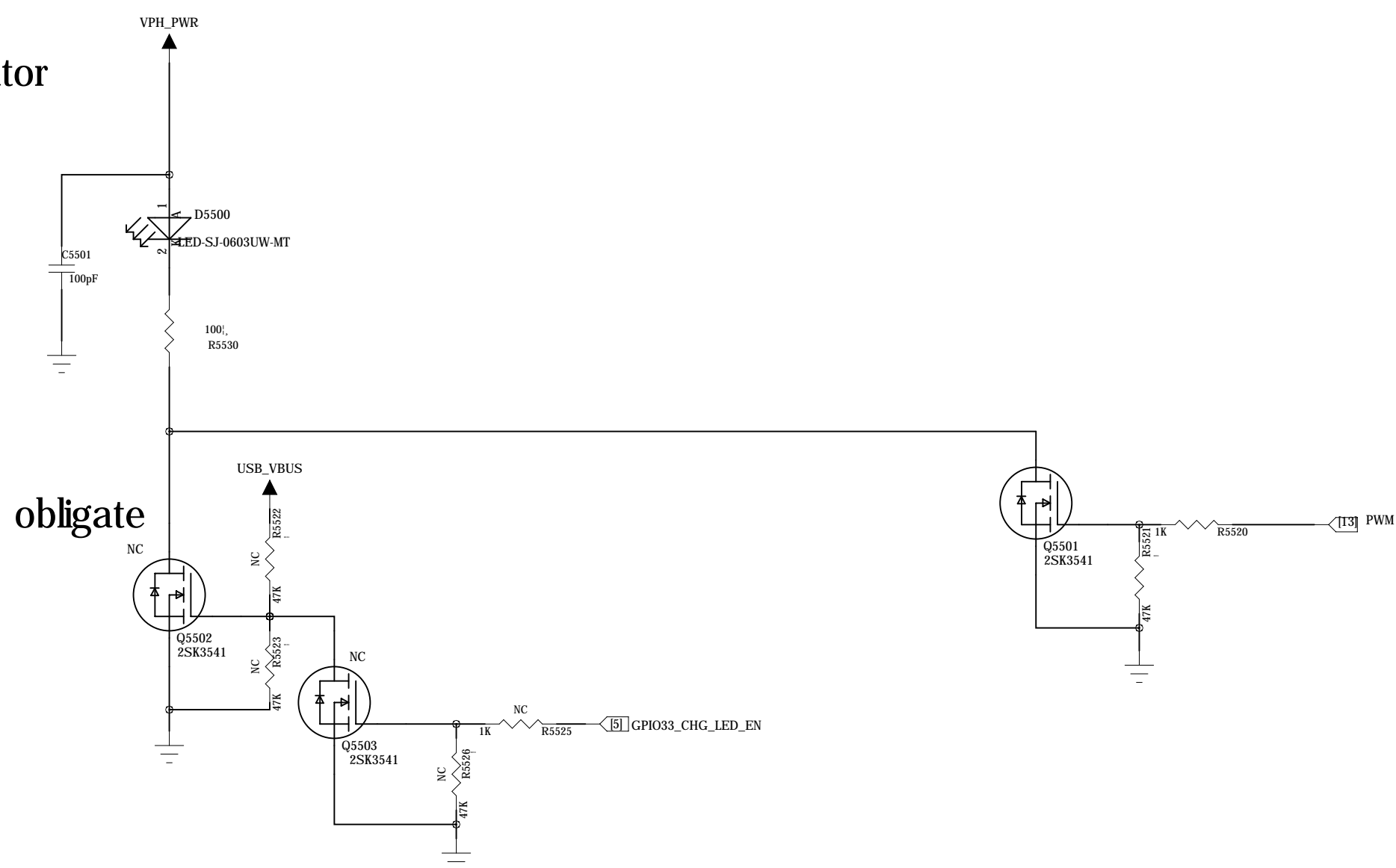


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Side Key



## Charge Indicator



obligate

DRAWN: <b>Michael Liang</b>	DATED: <b>20211025</b>
CHECKED: <b>&lt;Checked By&gt;</b>	DATED: <b>&lt;Checked Date&gt;</b>
QUALITY CONTROL: <b>&lt;QC By&gt;</b>	DATED: <b>&lt;QC Date&gt;</b>
RELEASED: <b>&lt;Released By&gt;</b>	DATED: <b>&lt;Release Date&gt;</b>

DATED:	20211025
DATED:	<Checked Date>
DATED:	<QC Date>
DATED:	<Release Date>

CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

DATED: <Checked Date>	
DATED: <QC Date>	
DATED: <Release Date>	

QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

DATED: <QC Date>	
DATED: <Release Date>	

RELEASED: <Released By>	DATED: <Release Date>
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DATED:  
<Release Date>

COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE:  P721CE Main Board			
CODE:  P721CE	SIZE:  D	DRAWING NO:  P721CE	REV:  V0.31
SCALE: <Scale>		SHEET: 3d	51

TITLE: P721CE Main Board			
CODE: <b>P721CE</b>	SIZE: <b>D</b>	DRAWING NO.: <b>P721CE</b>	REV.: <b>V0.31</b>
SCALE: <Scale>		SHEET: <b>34</b> 51	

CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 3d 51	

CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 3d 51	

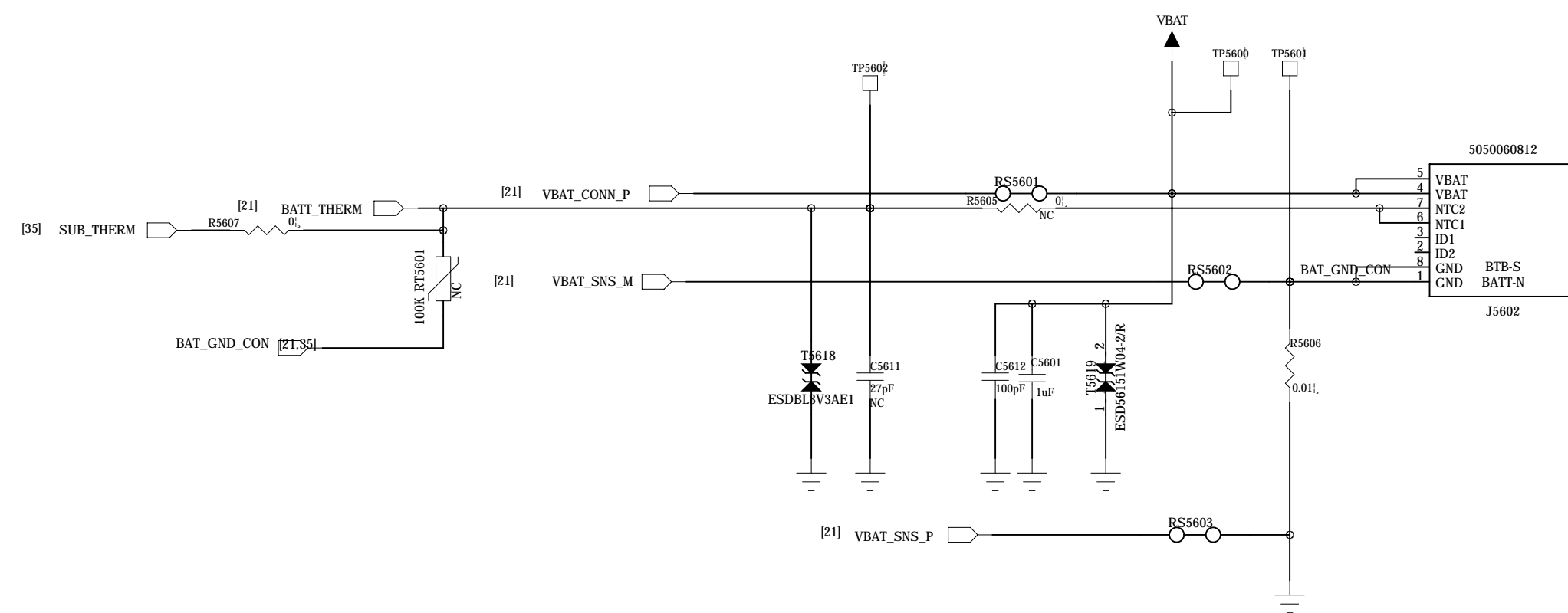
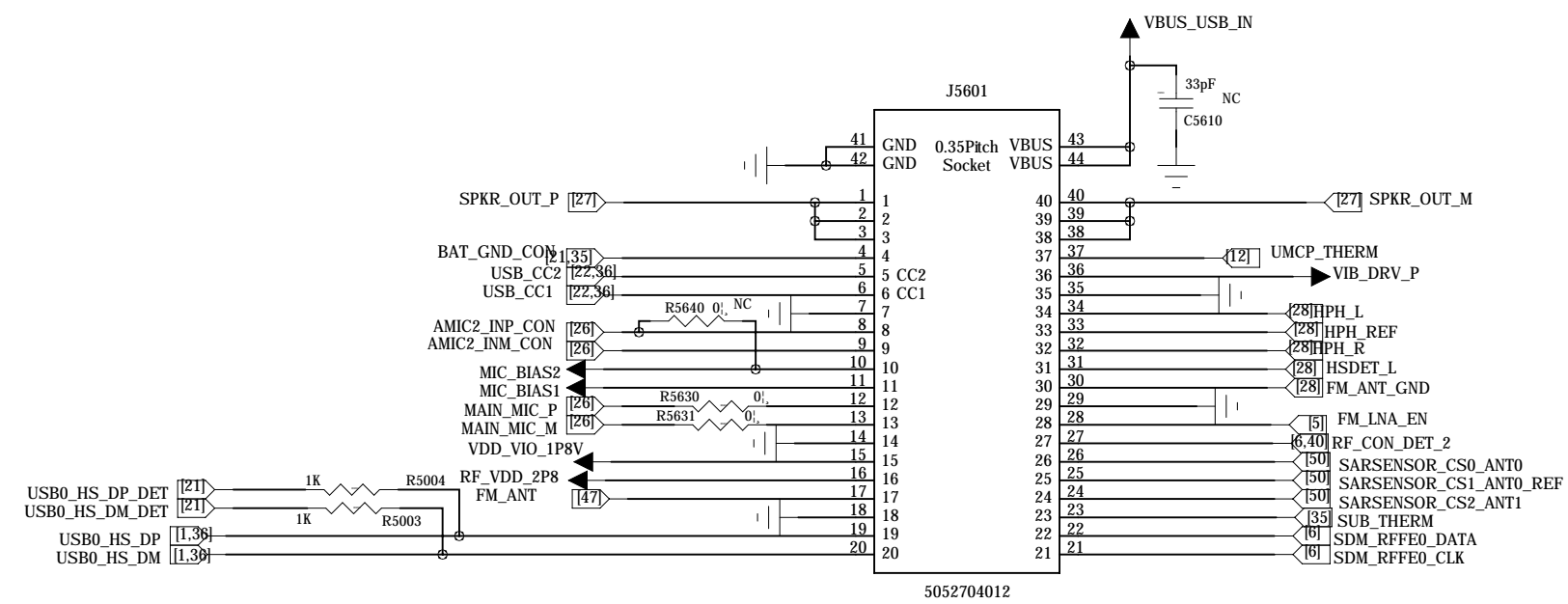
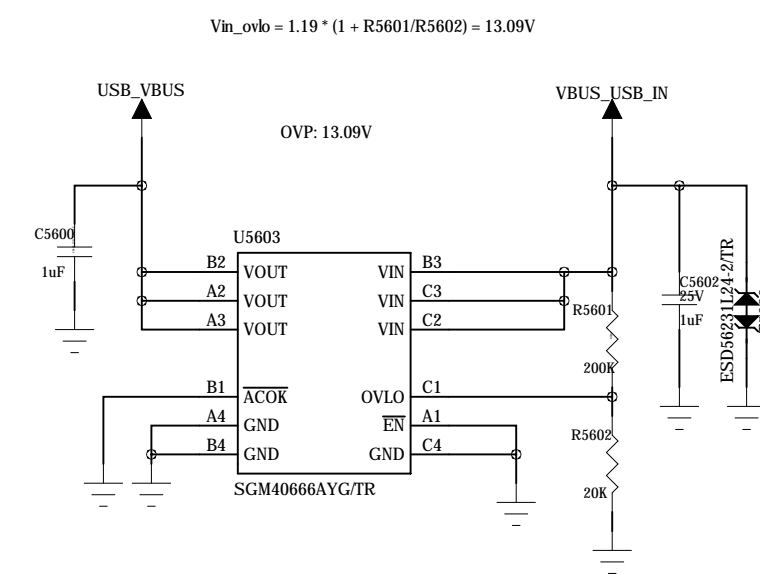
CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 3d 51	

CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 3d 51	

SCALE: <Scale>	SHEET: 34 51
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SCALE: <Scale>	SHEET: 34 51
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

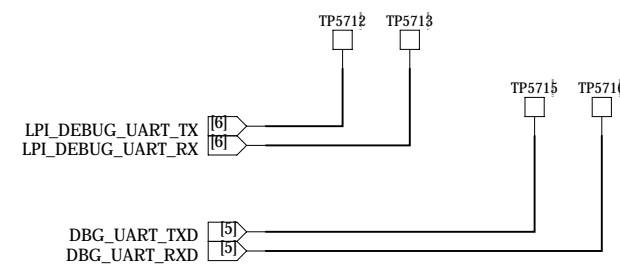


		COMPANY: <b>TINNO MOBILE TECHNOLOGY CO.,LTD</b>			
		TITLE:  <b>P721CE Main Board</b>			
DRAWN: <b>MichaelLiang</b>	DATED: <b>20211025</b>				
CHECKED: <b>&lt;Checked By&gt;</b>	DATED: <b>&lt;Checked Date&gt;</b>	CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL: <b>&lt;QC By&gt;</b>	DATED: <b>&lt;QC Date&gt;</b>	<b>P721CE</b>	<b>D</b>	<b>P721CE</b>	<b>V0.31</b>
RELEASED: <b>&lt;Released By&gt;</b>	DATED: <b>&lt;Release Date&gt;</b>	SCALE: <b>&lt;Scale&gt;</b>			SHEET: <b>86</b> <b>51</b>

## Debug Test Point

RAM dump

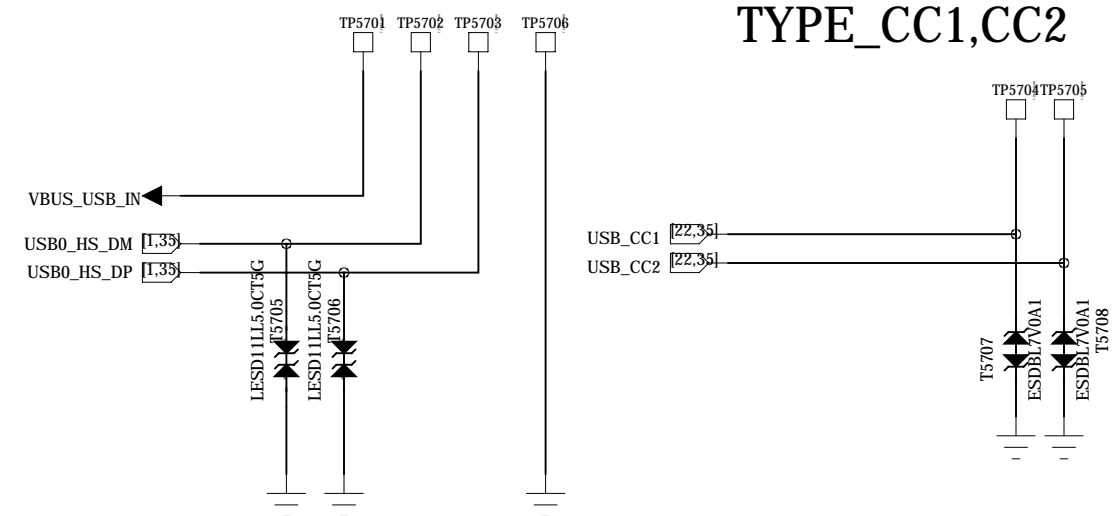
UART



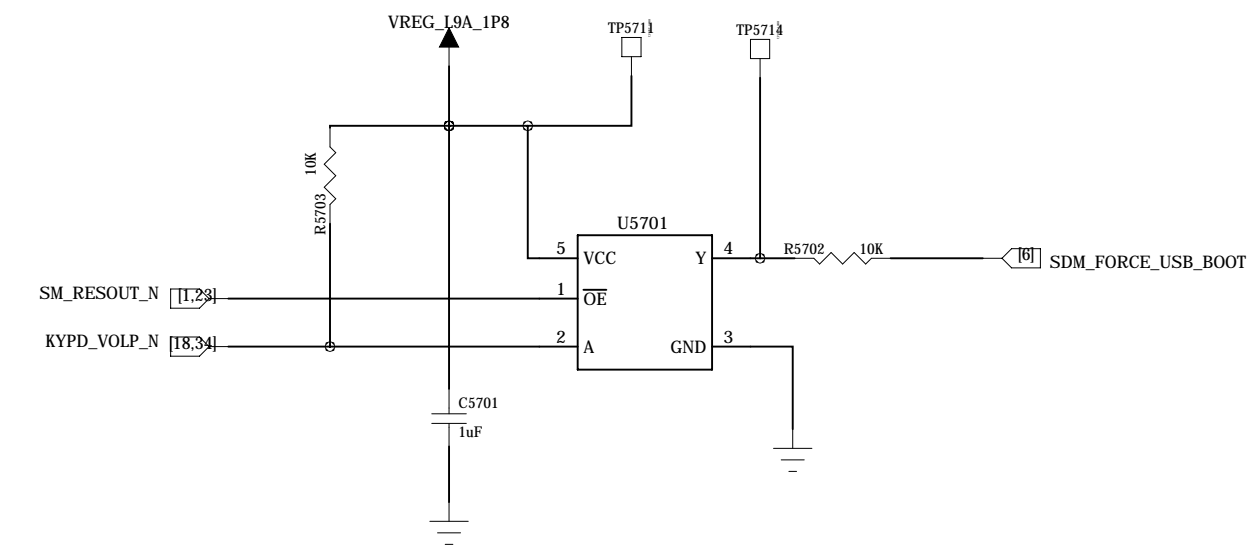
## Fixture Test Point

USB

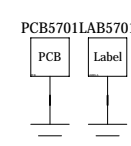
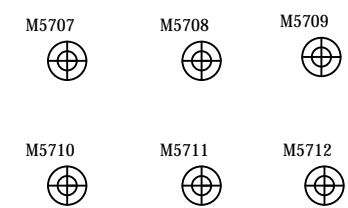
TYPE\_CC1,CC2



FORCED\_USB\_BOOT



**Mark**



**shielding can**

BB

4G PA

5G PA

UHB PA

WCN

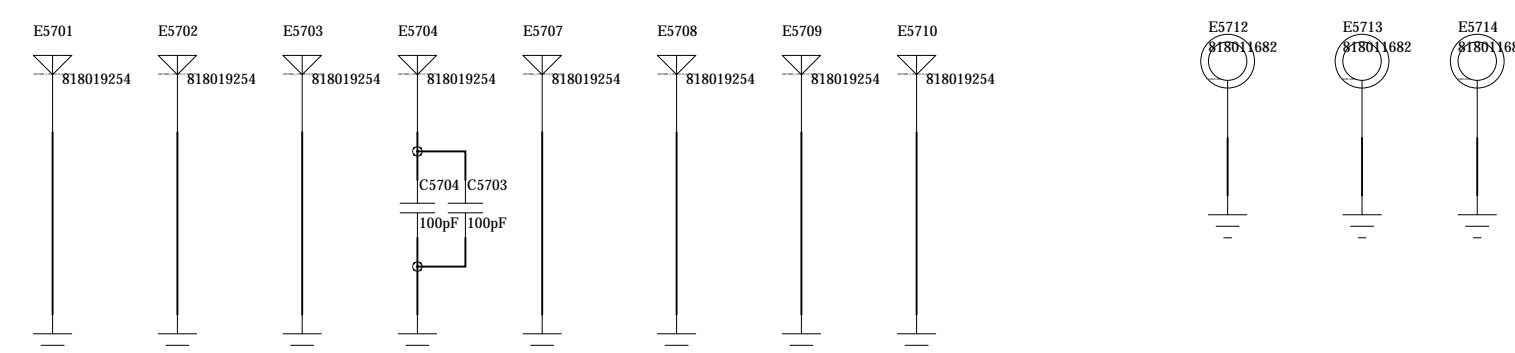
**sensor**

RF TXM

RF DRX

PDN

## **Metal Grounding**



**Bottom**

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

		COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
		TITLE: P721CE Main Board			
DRAWN: Michael.Liang	DATED: 20211025				
CHECKED: <Checked By>	DATED: <Checked Date>	CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
QUALITY CONTROL: <QC By>	DATED: <QC Date>				
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>		SHEET: 36	51

6

5

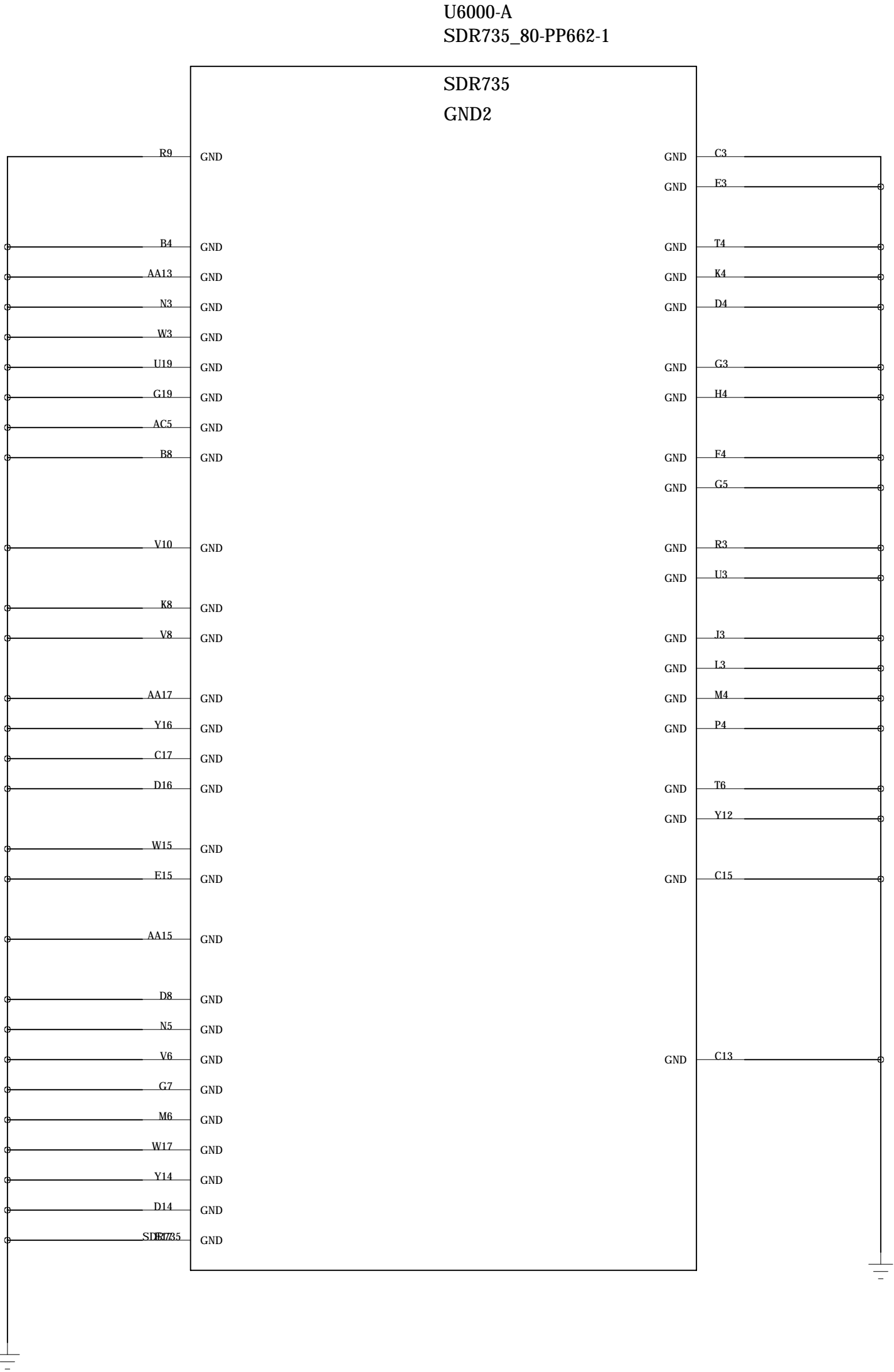
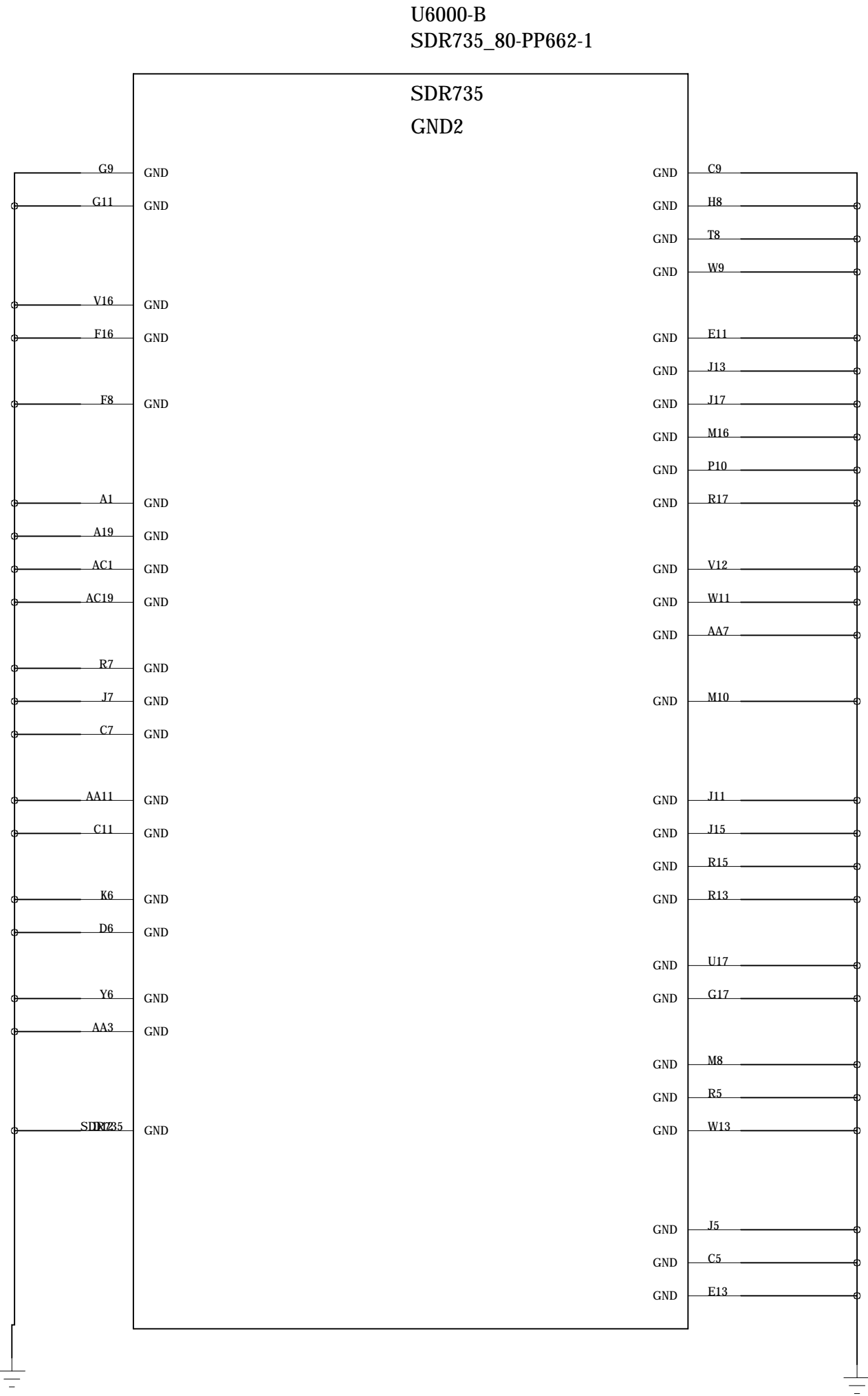
4

3

2

1

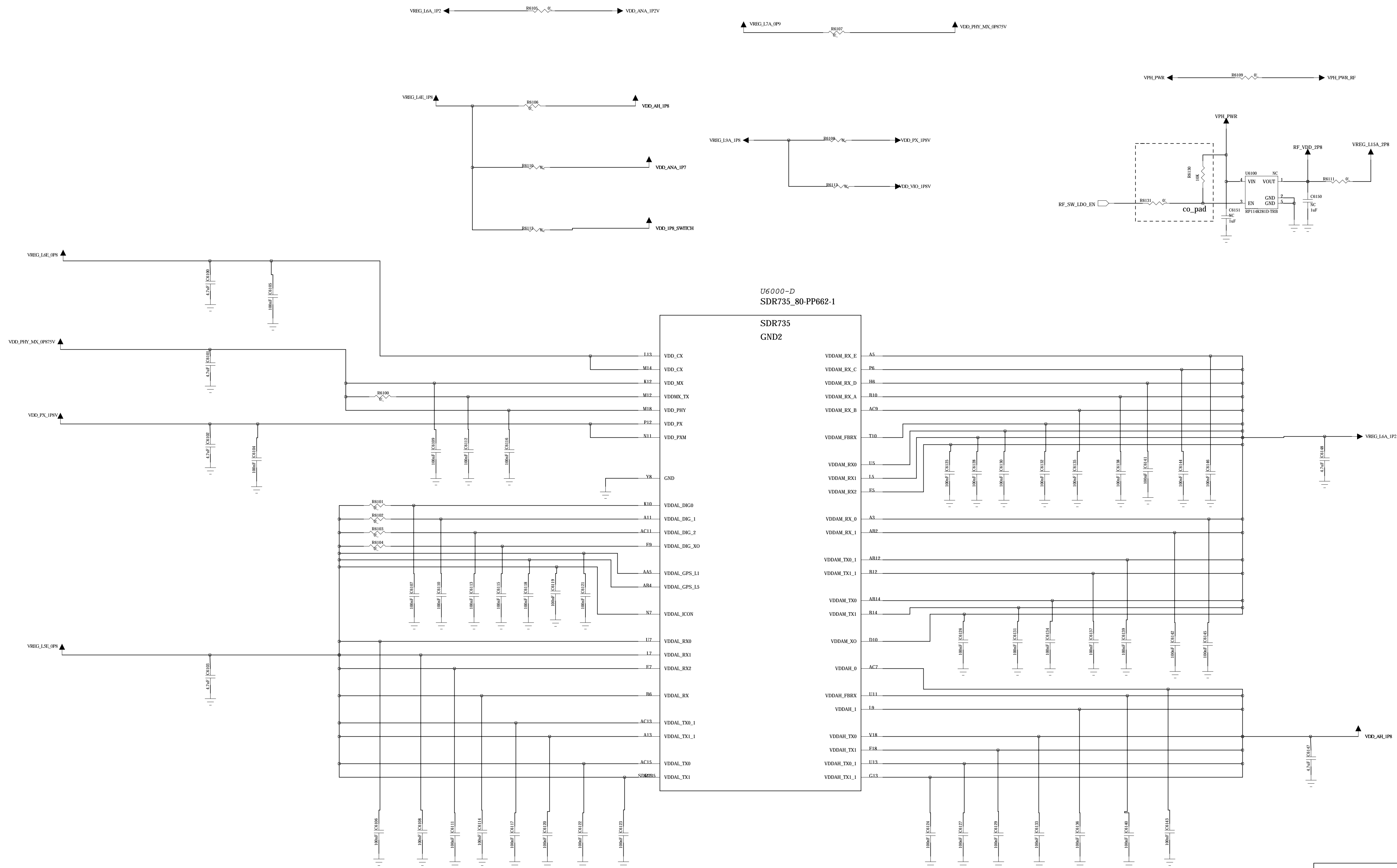
REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 37 51	

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: MichaelLiang		DATED: 20211025		P721CE Main Board			
CHECKED: -Checked By-		-Checked Date-		CODE:  P721CE	SIZE:  D	DRAWING NO:  P721CE	REV:  V0.31
QUALITY CONTROL: -QC By-		-QC Date-					
RELEASED: -Released By-		-Release Date-		SCALE: <Scale>			SHEET: 38 51

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

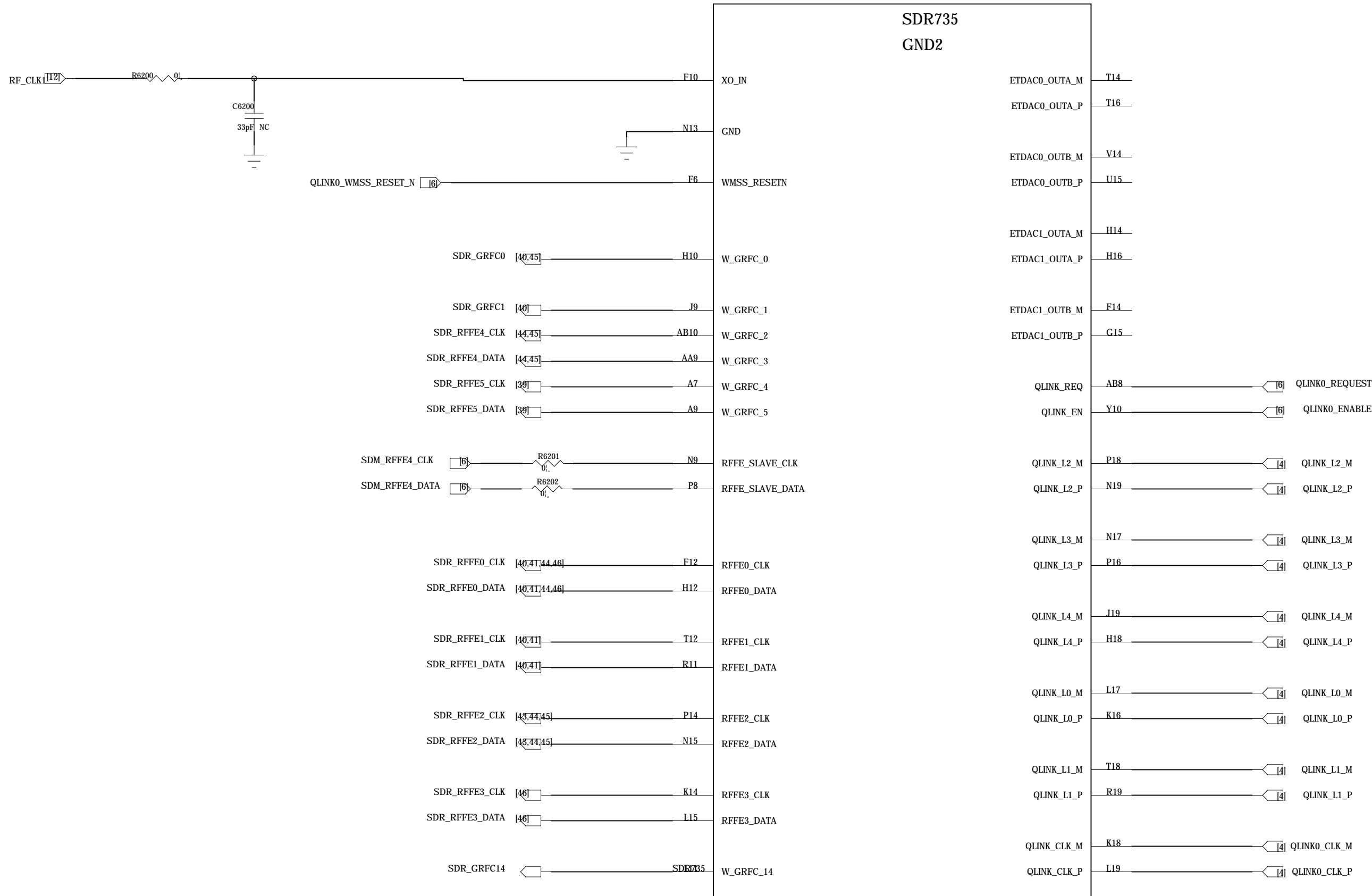
D

C

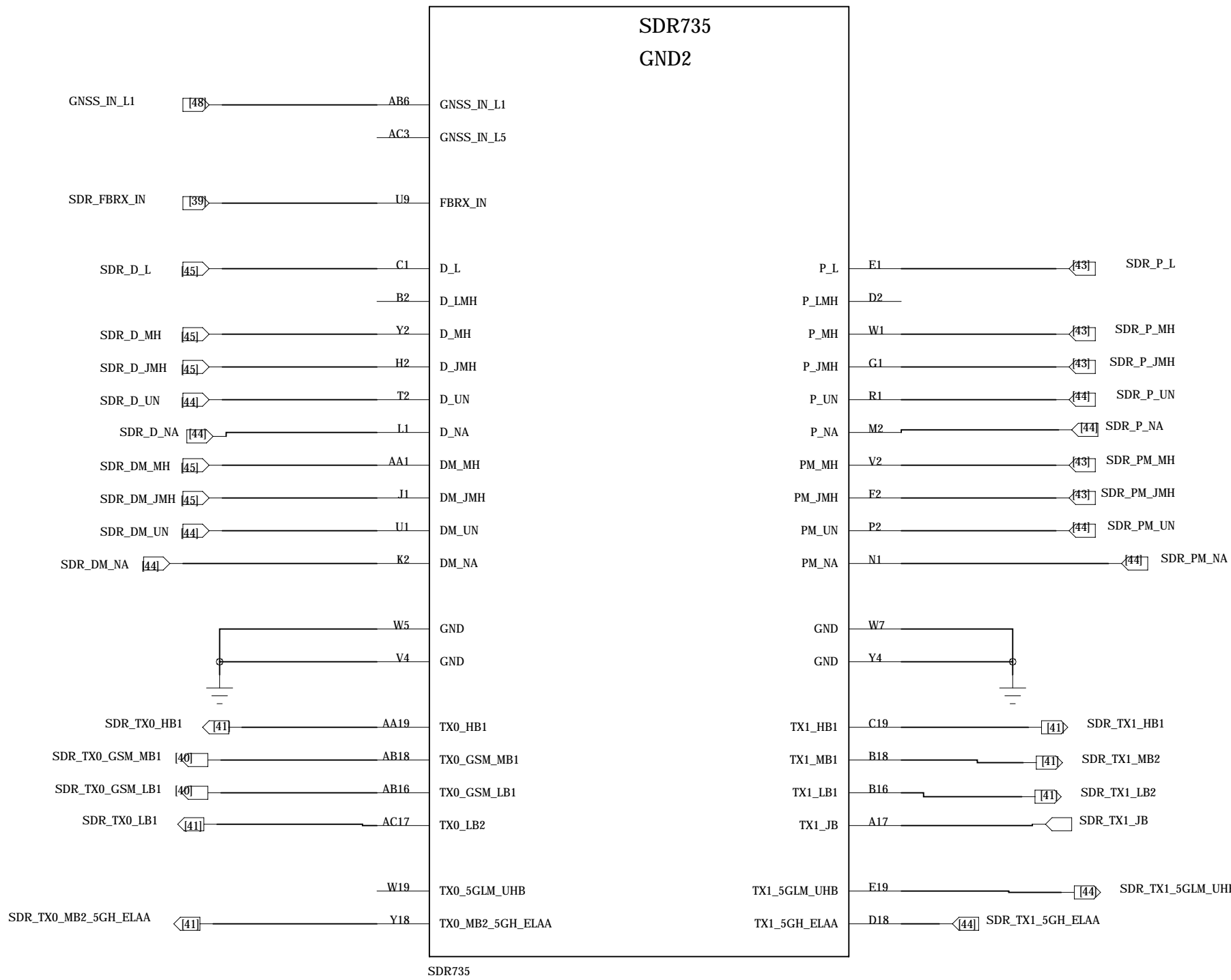
B

A

U6000-E  
SDR735\_80-PP662-1



U6000-C  
SDR735\_80-PP662-1



D

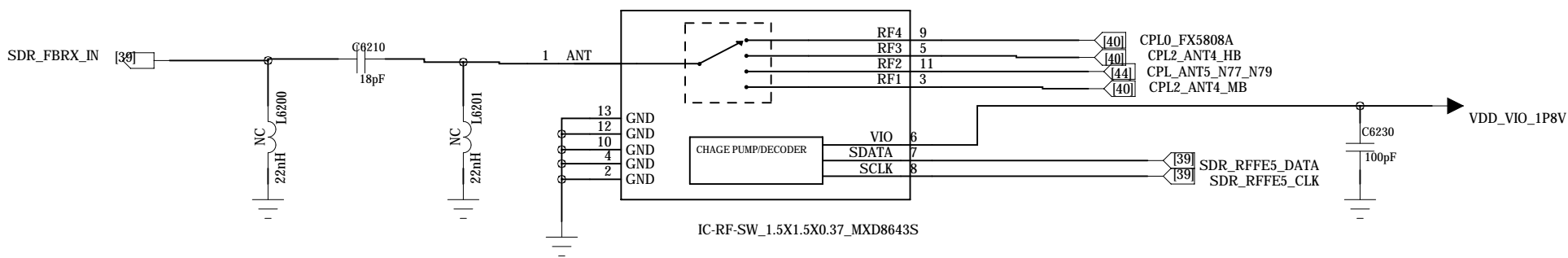
C

B

A

FBRX

U6203



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD

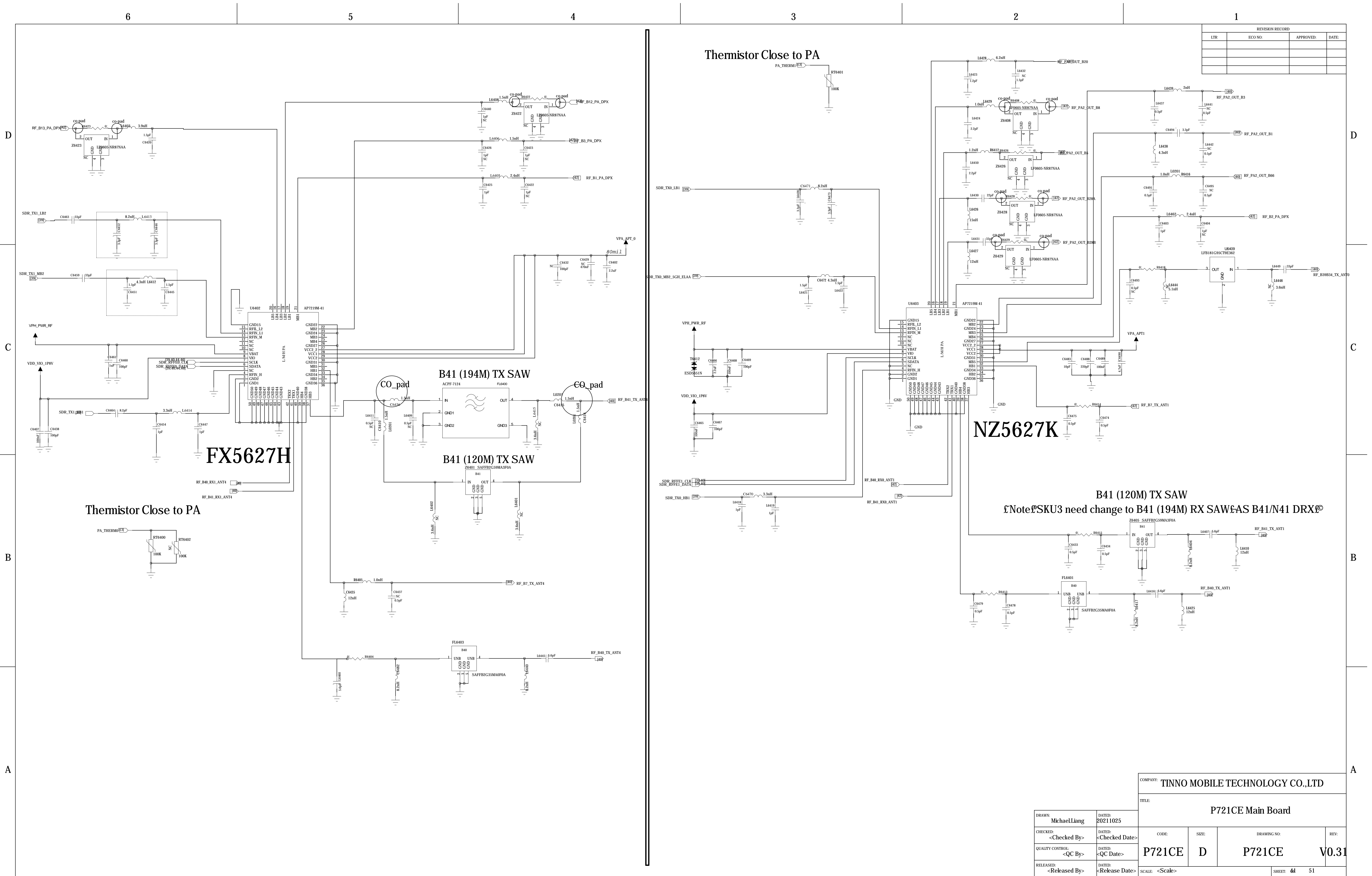
TITLE: P721CE Main Board

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

CODE: P721CE	SIZE: D	DRAWING NO: P721CE	REV: V0.31
SCALE: <Scale>		SHEET: 30 51	









REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

D

C

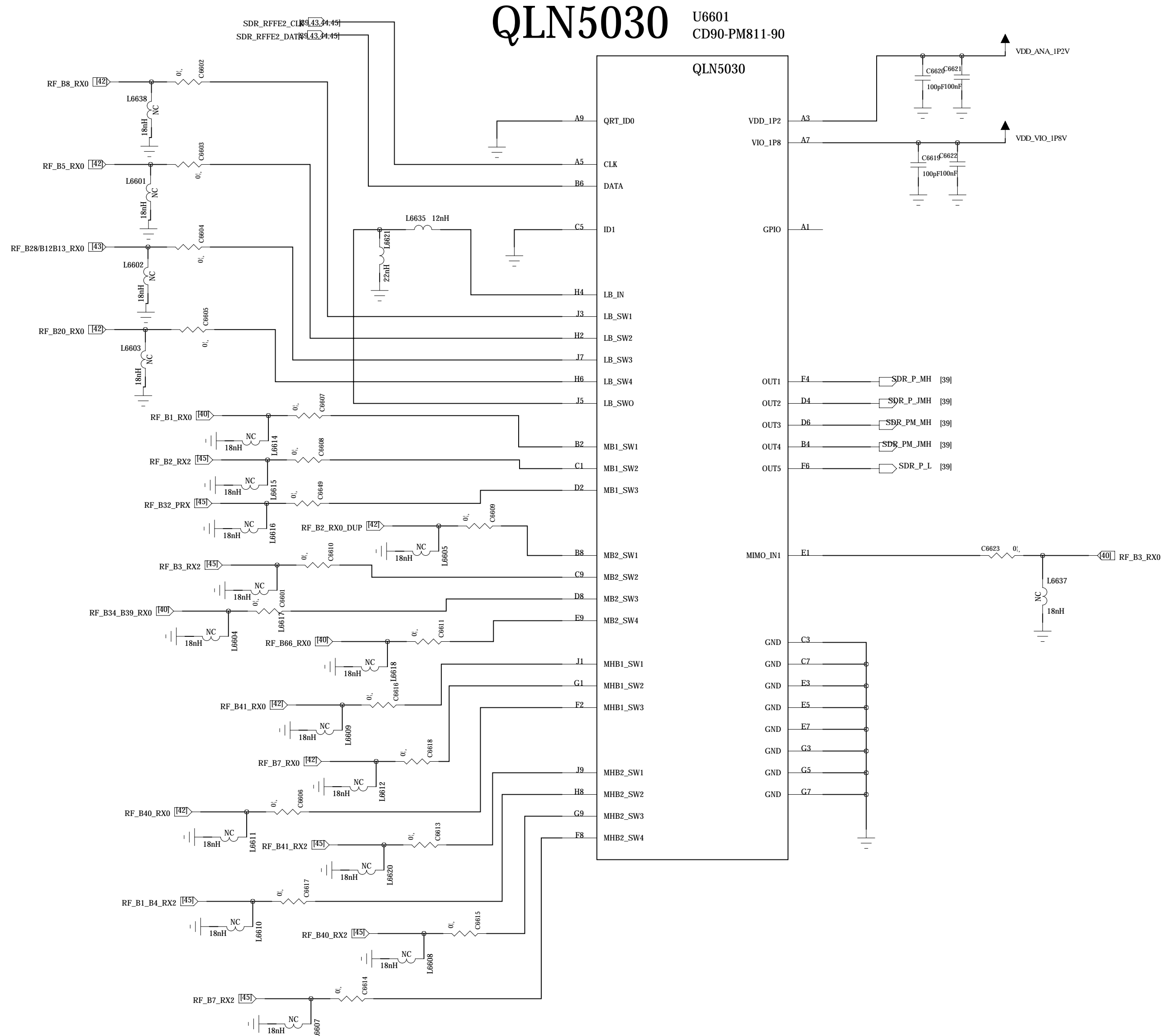
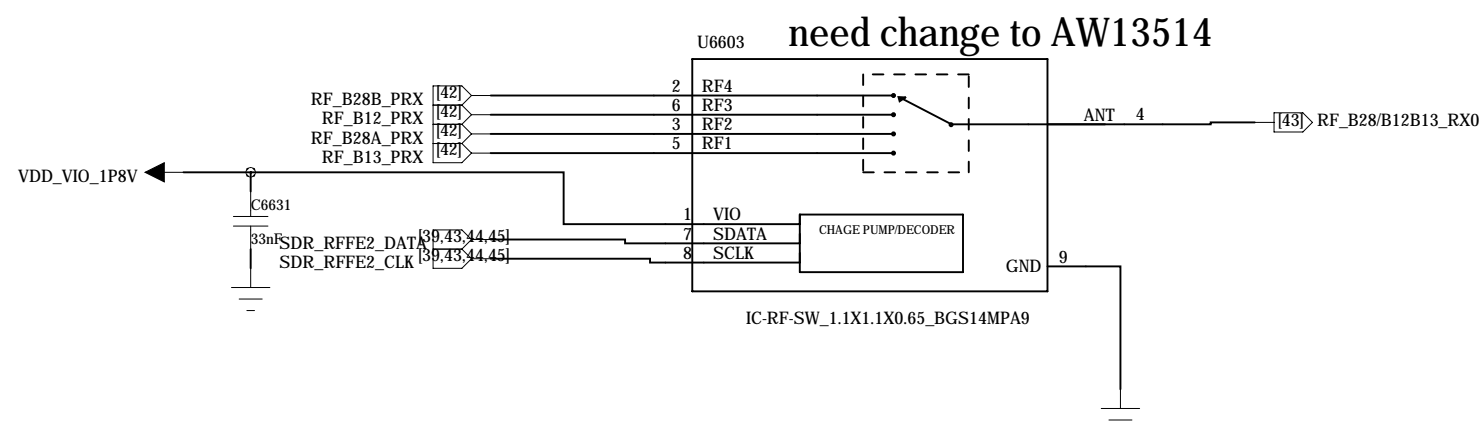
C

B

B

A

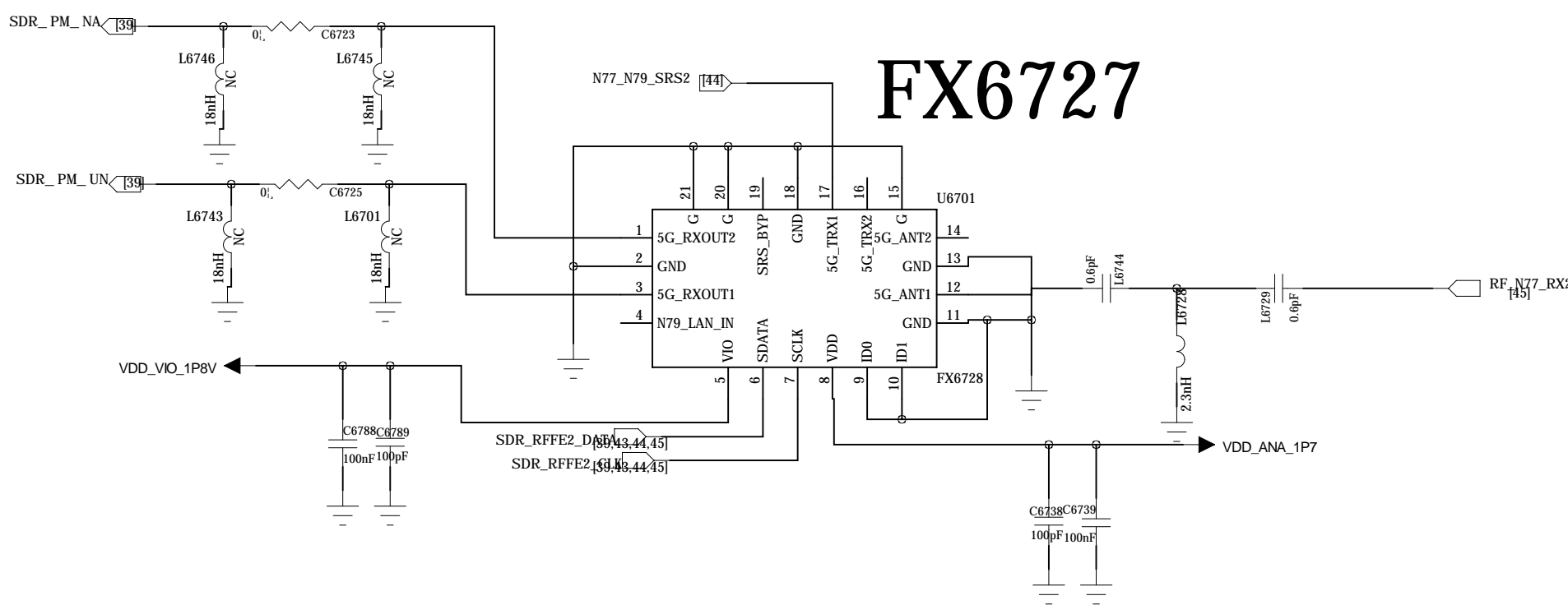
A



DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

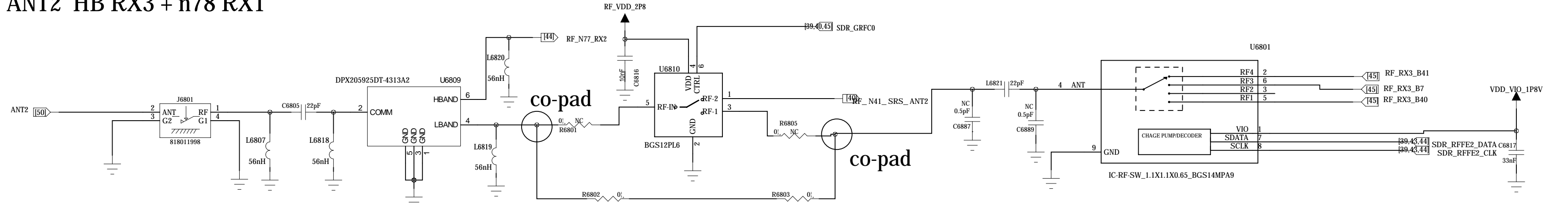
COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE	SIZE	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 48	51

COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE:  P721CE Main Board			
CODE:  P721CE	SIZE:  D	DRAWING NO:  P721CE	REV:  V0.31
SCALE: <Scale>		SHEET: 44	51

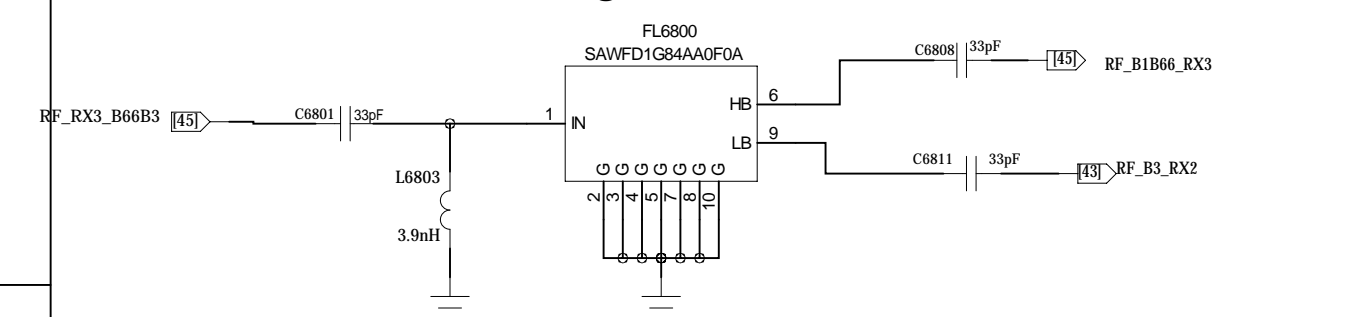


FX6727

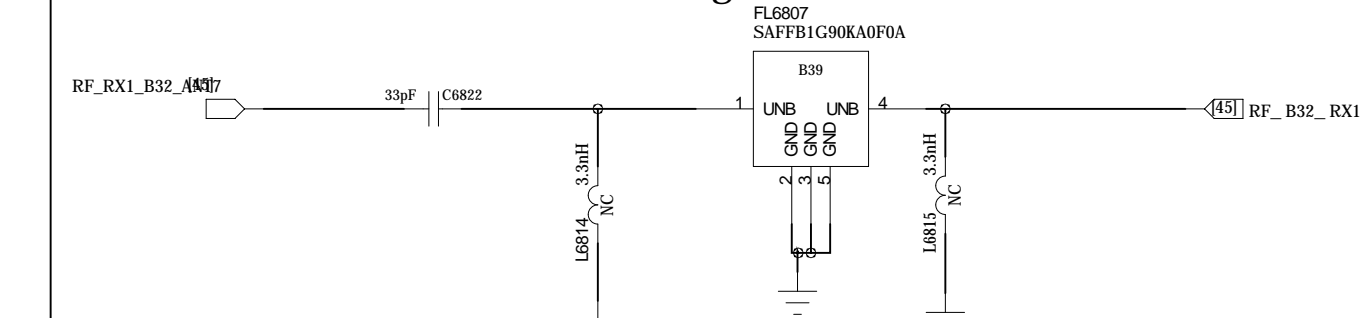
ANT2 HB RX3 + n78 RX1



need change to B66/B3 dual saw

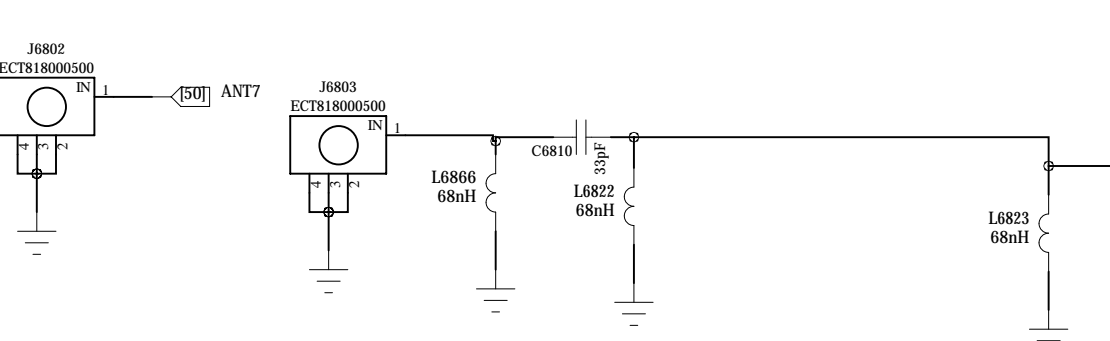


need change to B32 SAW



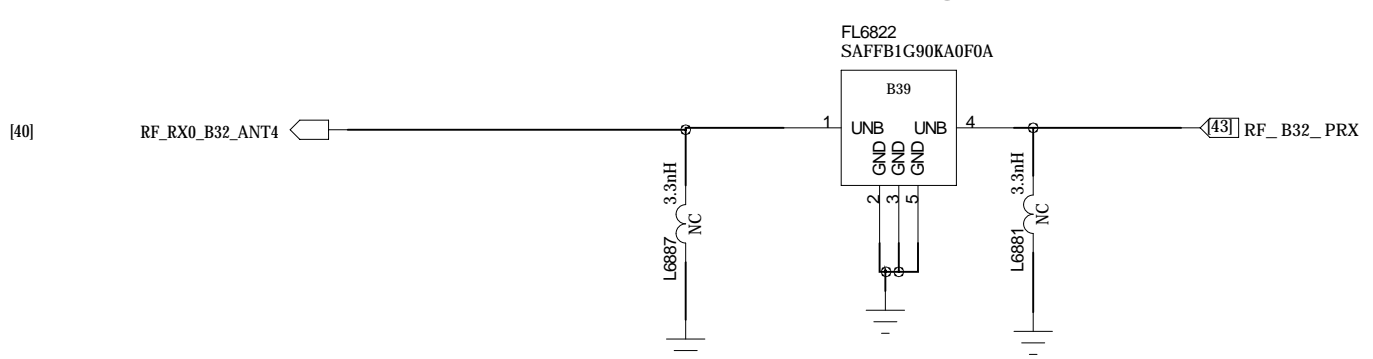
B

ANT 7 : MHB RX3

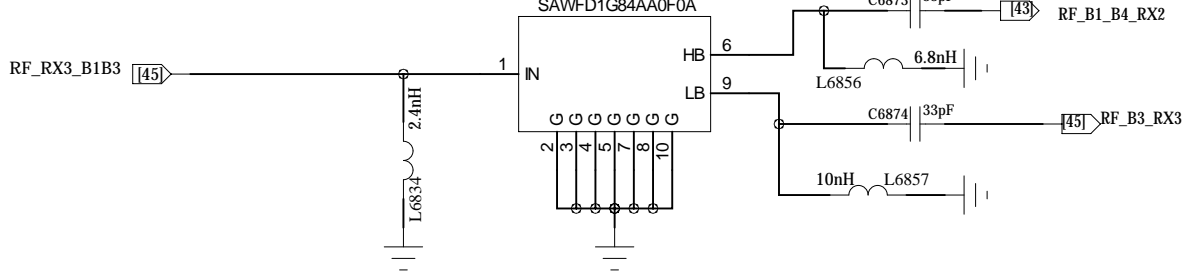


A

need change to B32 saw

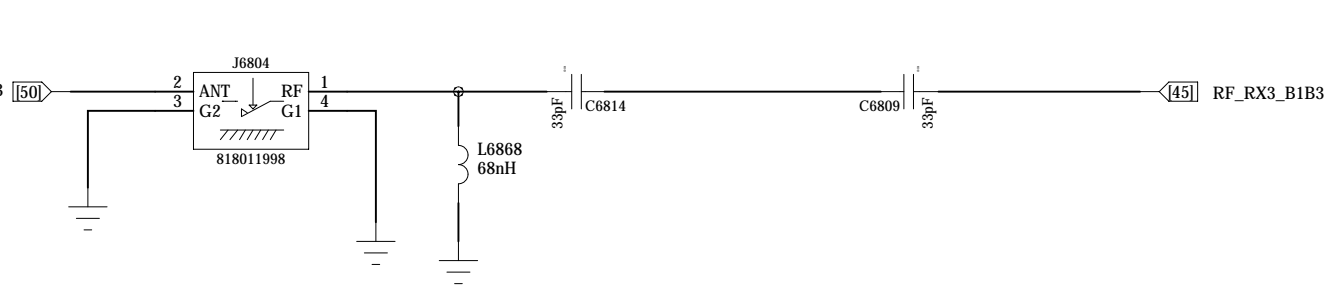


need change to B66/B3 dual saw



MXD86C2F

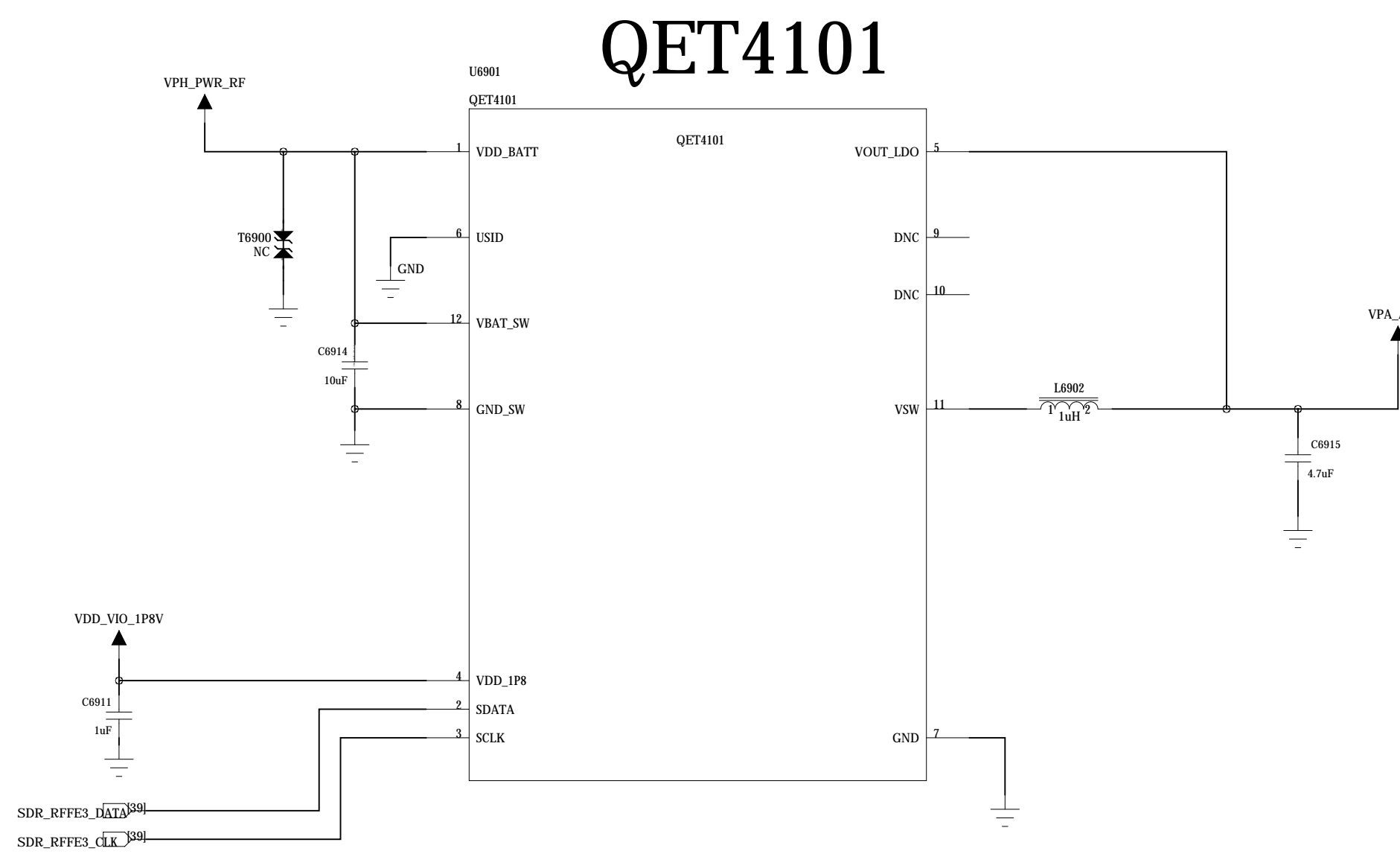
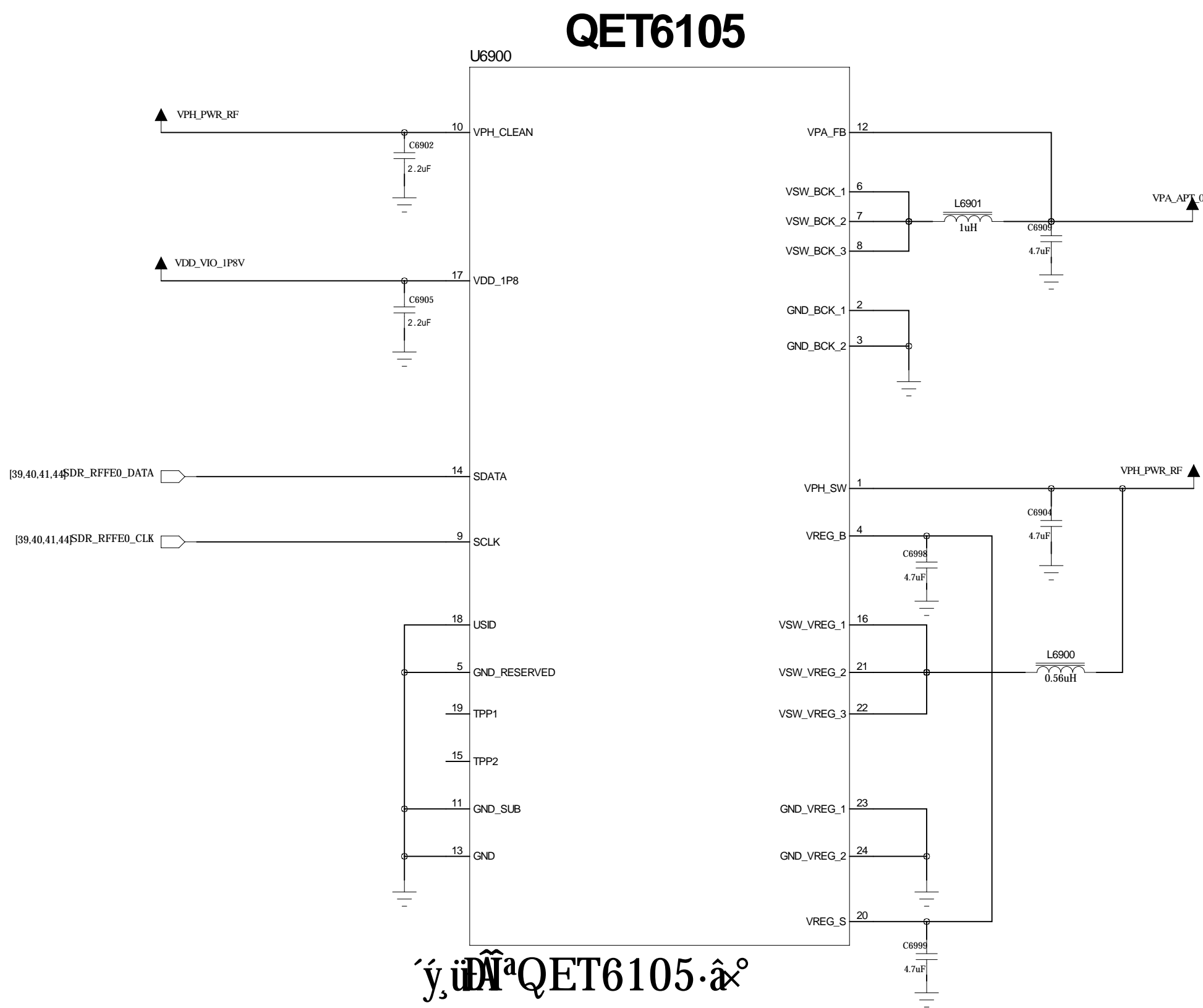
ANT 3: MB RX



DRAWN:	MichaelLiang	DATED:	20211025
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 46 51	

REVISION RECORD			
LTR	ECO NO.	APPROVER	DATE



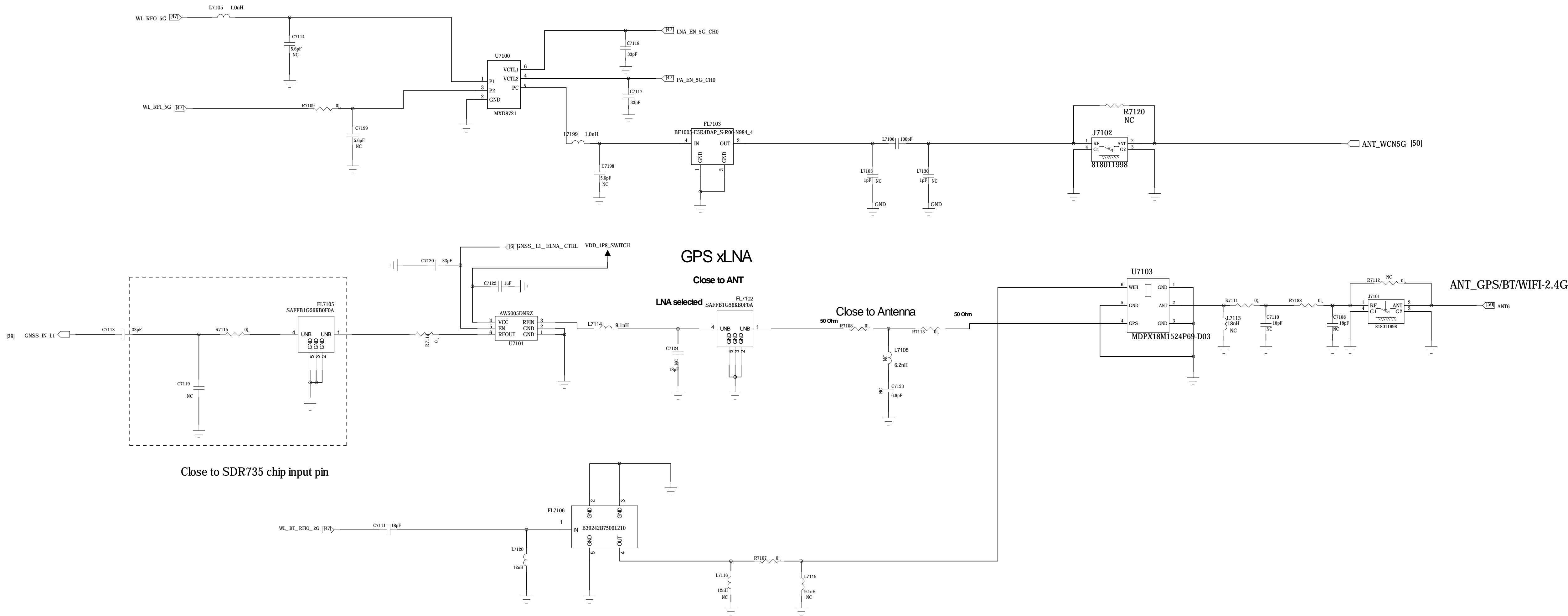
COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD			
TITLE: P721CE Main Board			
CODE:	SIZE:	DRAWING NO:	REV:
P721CE	D	P721CE	V0.31
SCALE: <Scale>		SHEET: 46 51	

DRAWN: MichaelLiang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>



REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

# WiFi BT GNSS L1 RF Circuit



COMPANY: TINNO MOBILE TECHNOLOGY CO.,LTD

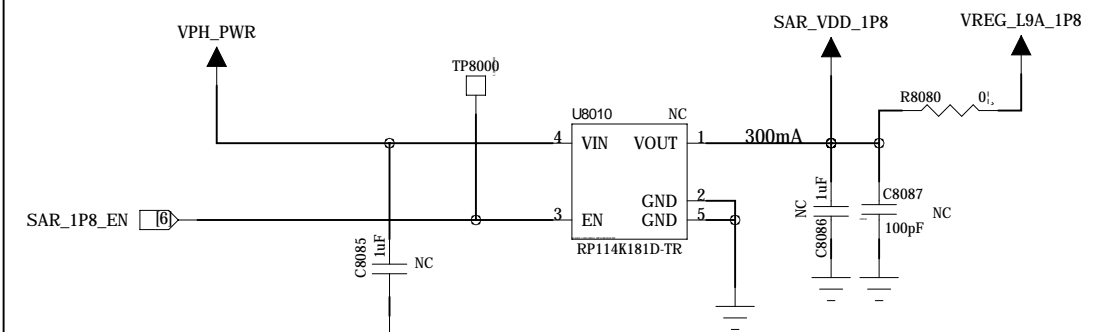
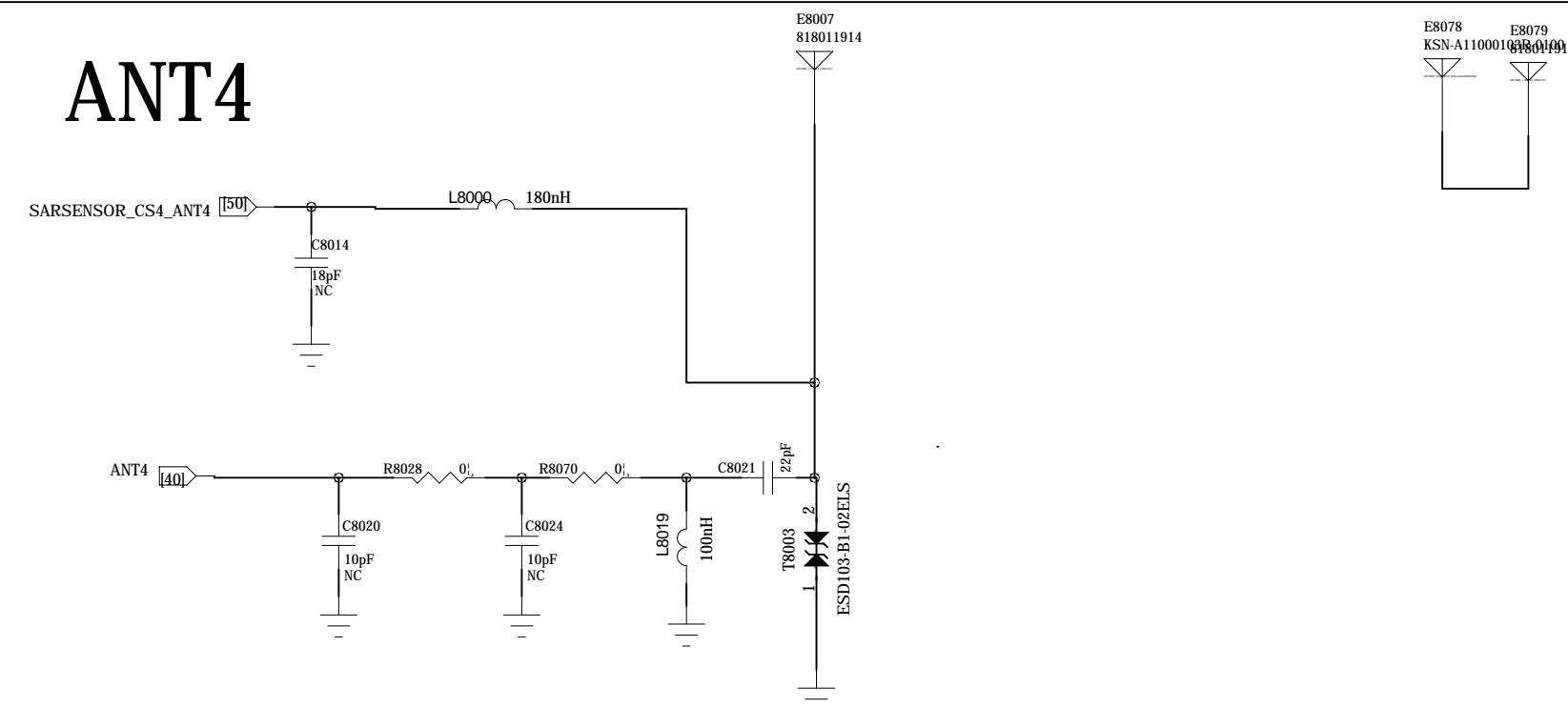
TITLE: P721CE Main Board

DRAWN: MichaelLiang		DATED: 20211025		P721CE Main Board			
CHECKED: <Checked By>	DATED: <Checked Date>		CODE:  P721CE	SIZE:  D	DRAWING NO:  P721CE		REV:  V0.31
QUALITY CONTROL: <QC By>		DATED: <QC Date>					
RELEASED: <Released By>		DATED: <Release Date>		SCALE: <Scale>		SHEET: 46 51	

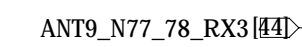
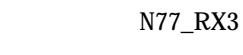




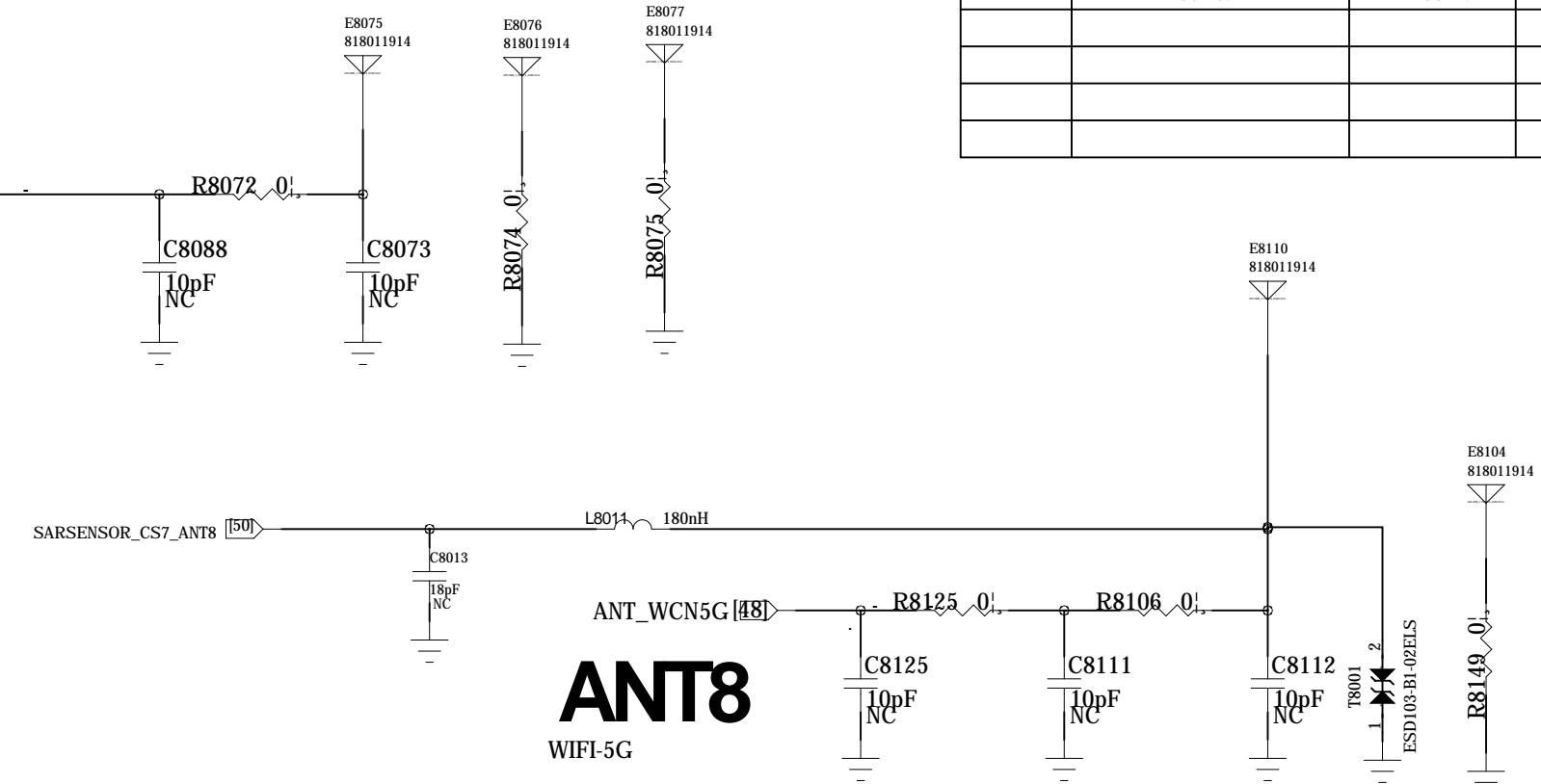
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



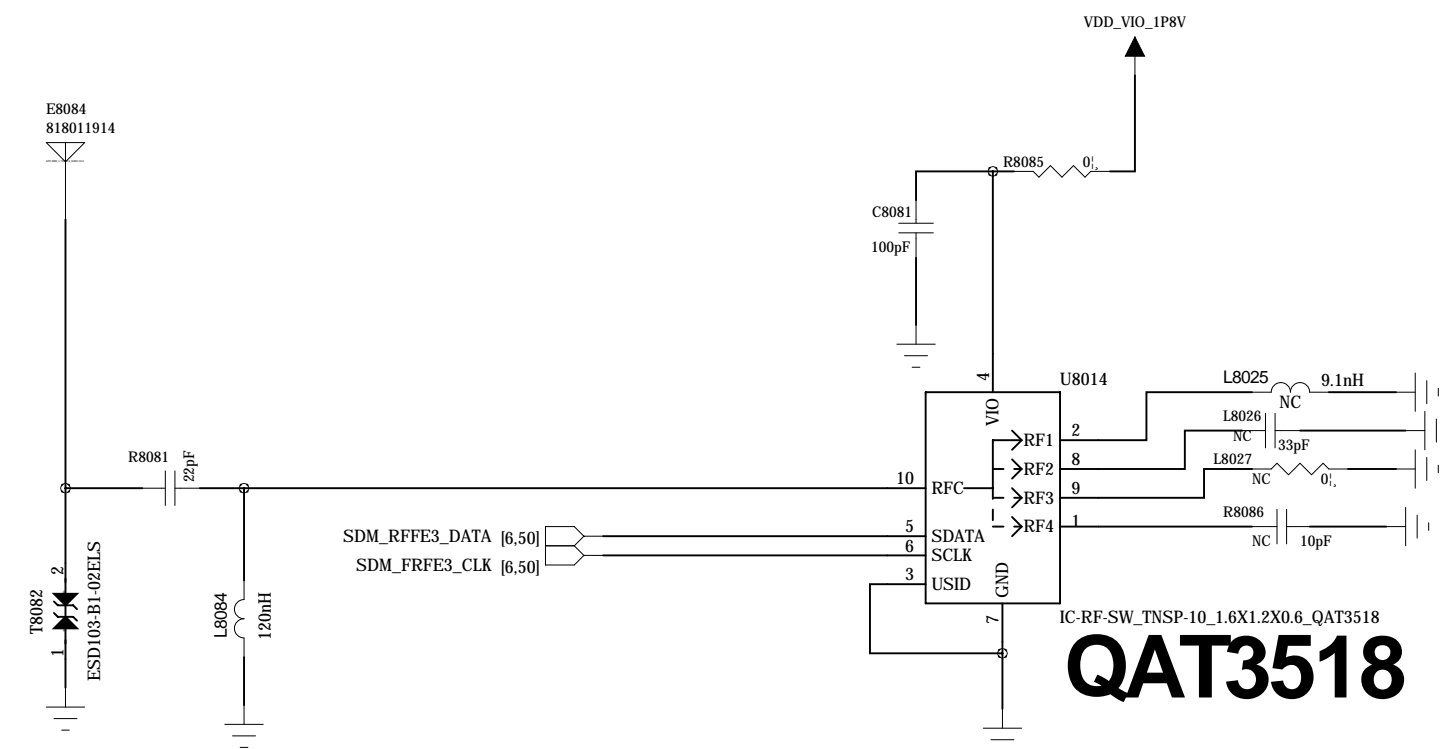
SAR sensor 1.8V



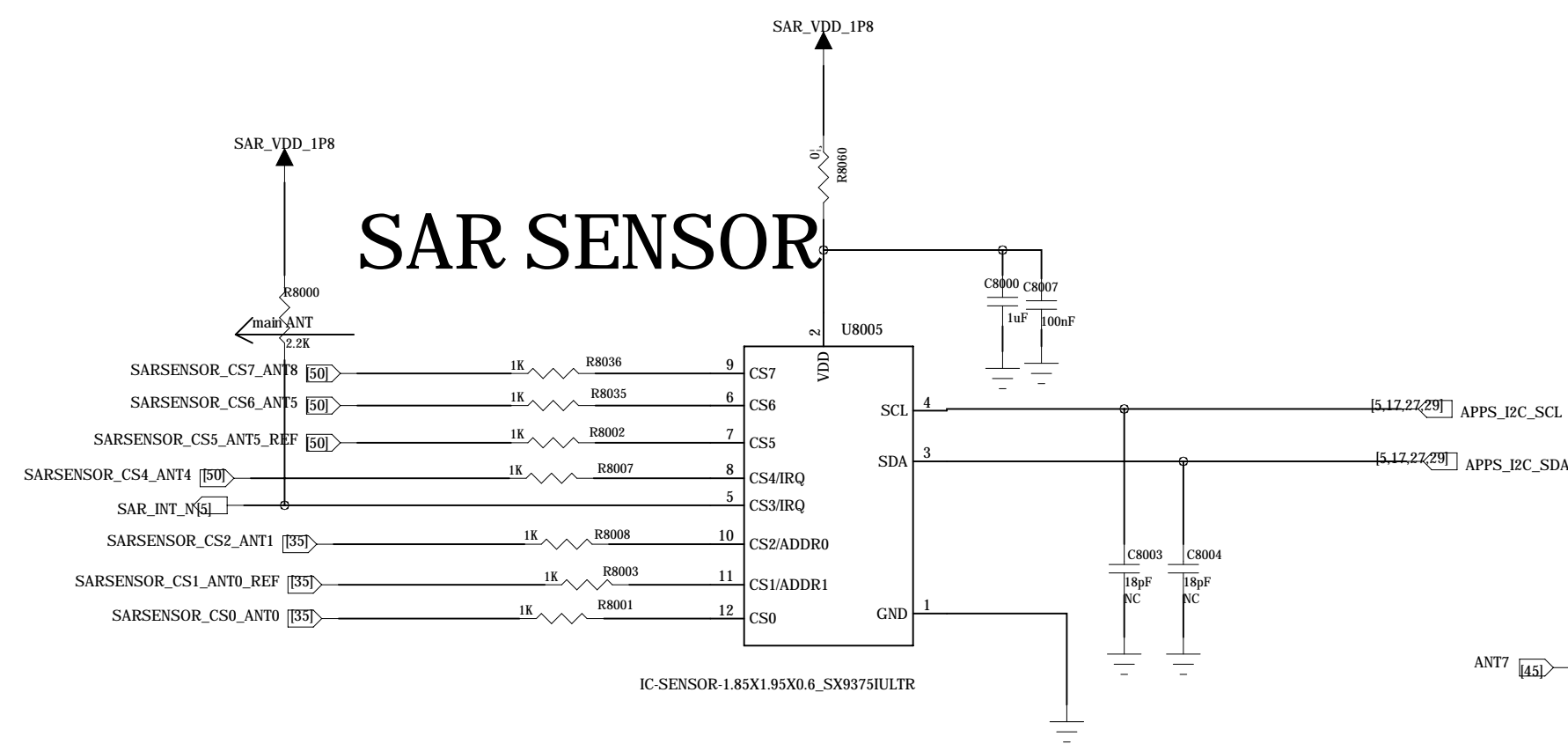
# ANT9



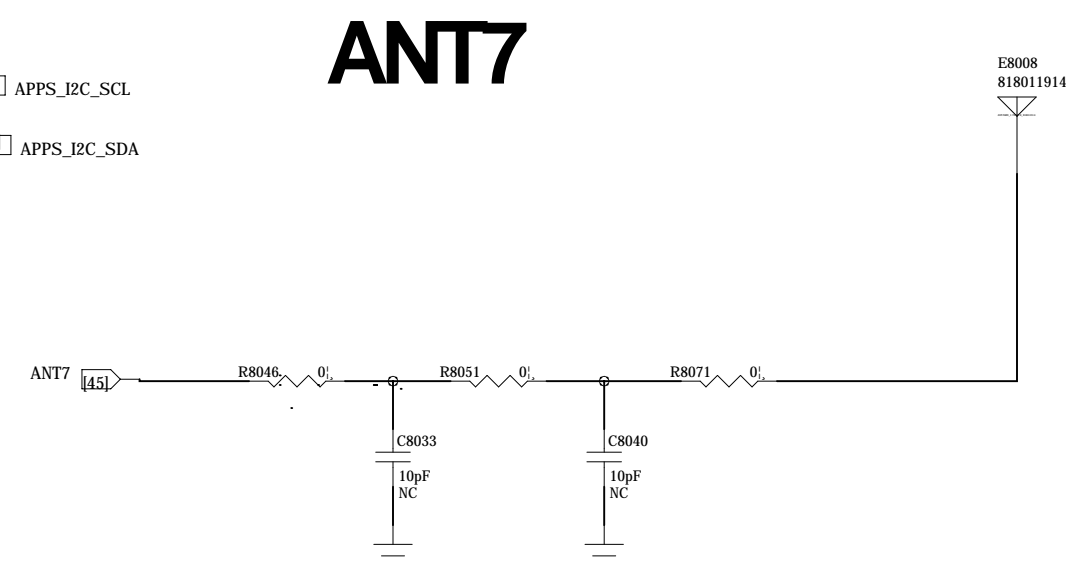
# ANT8



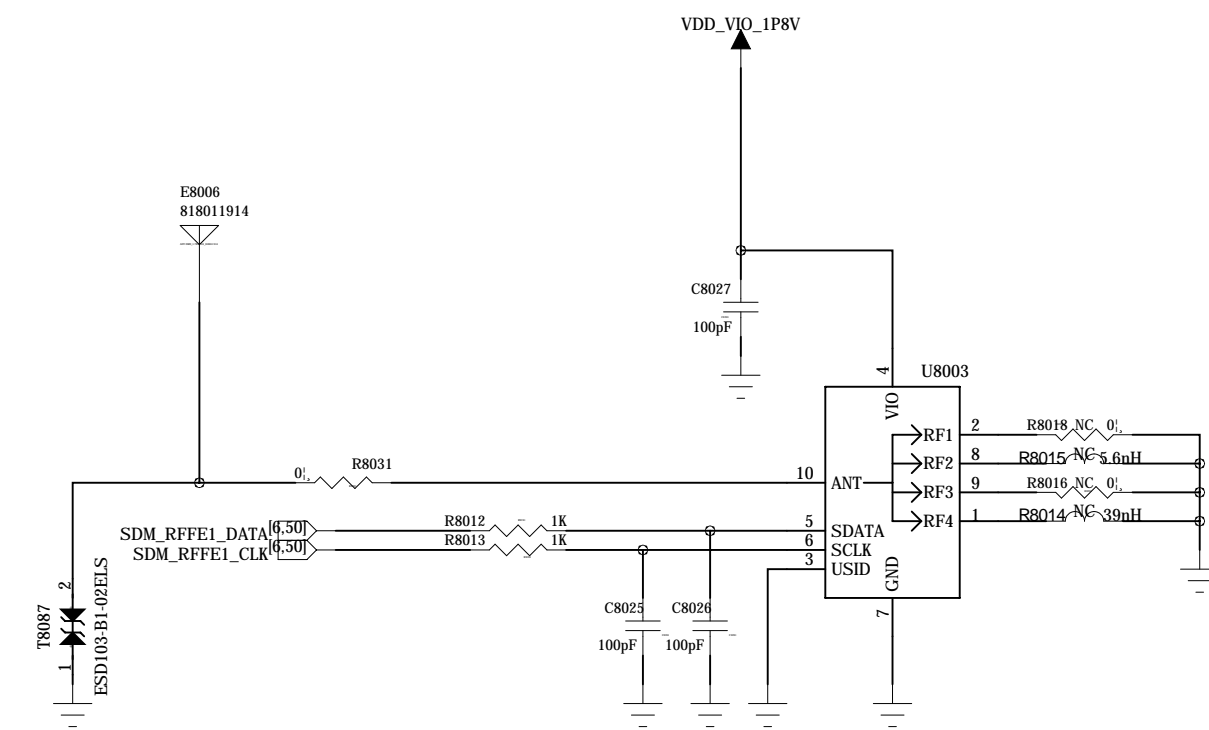
**QAT3518**



# SAR SENSOR

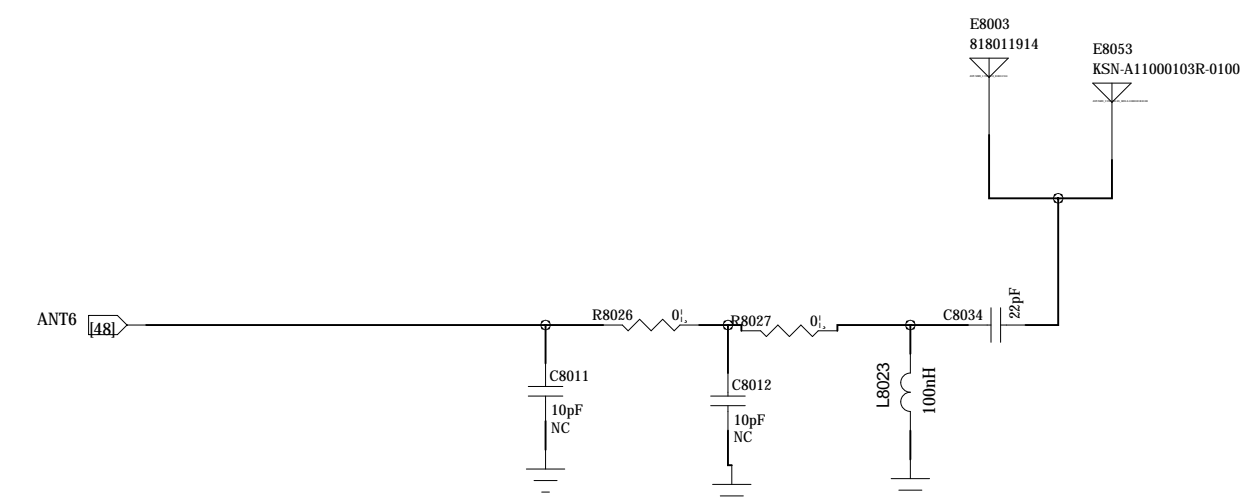
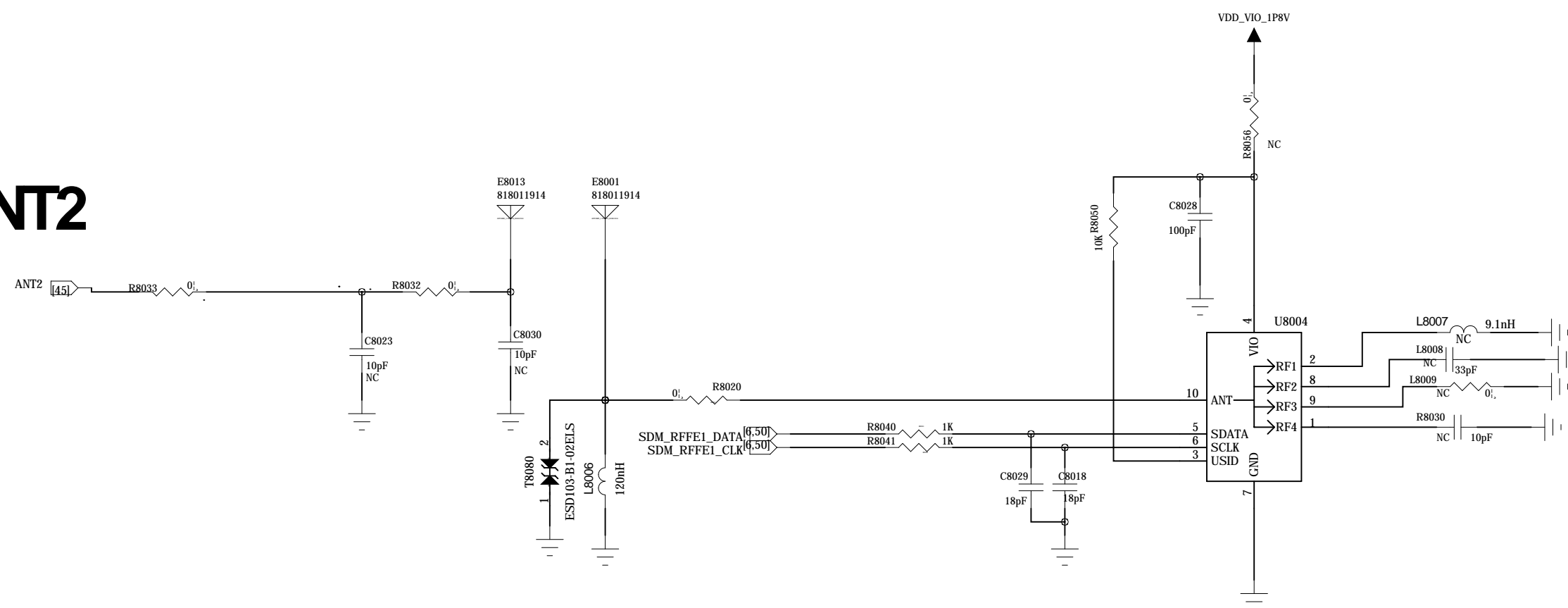


# ANT7



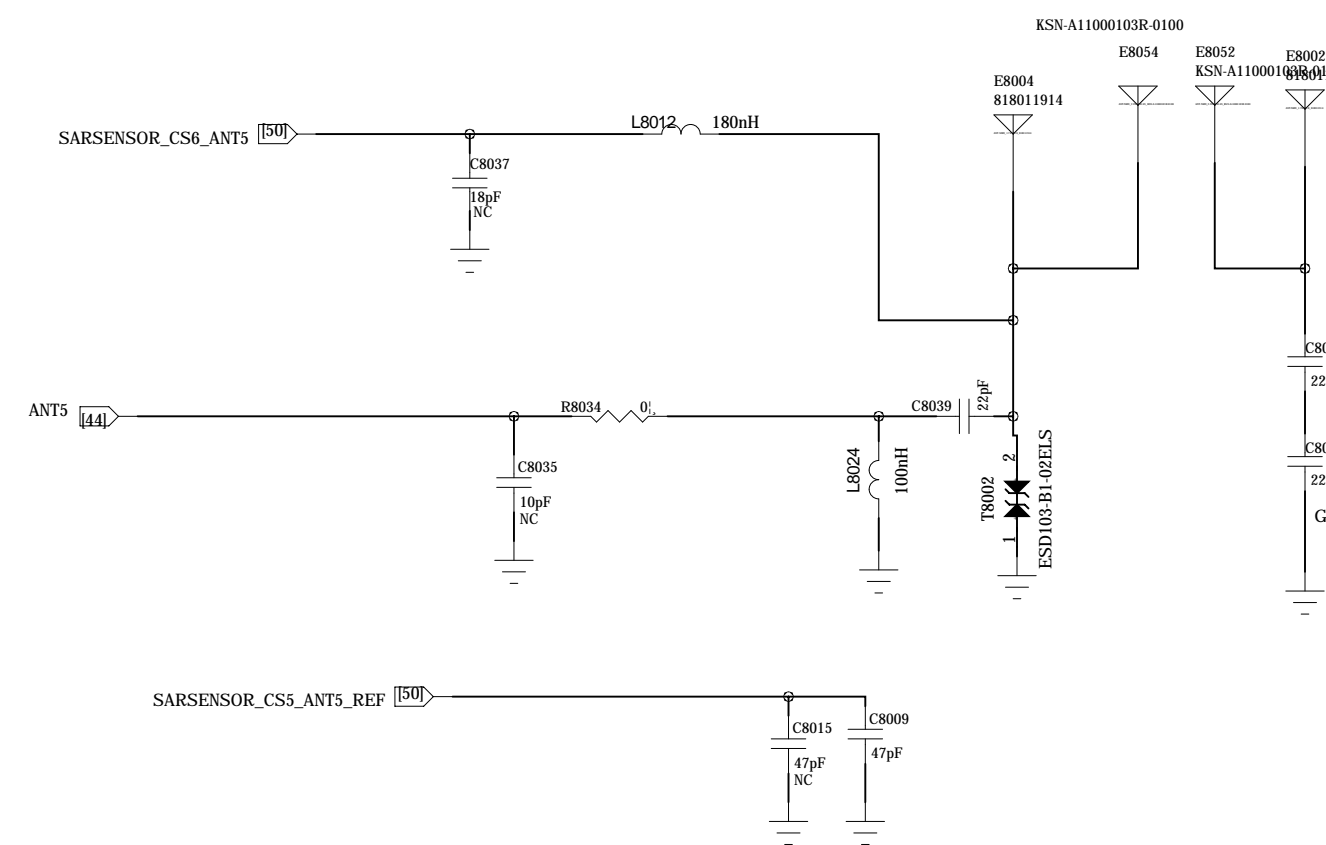
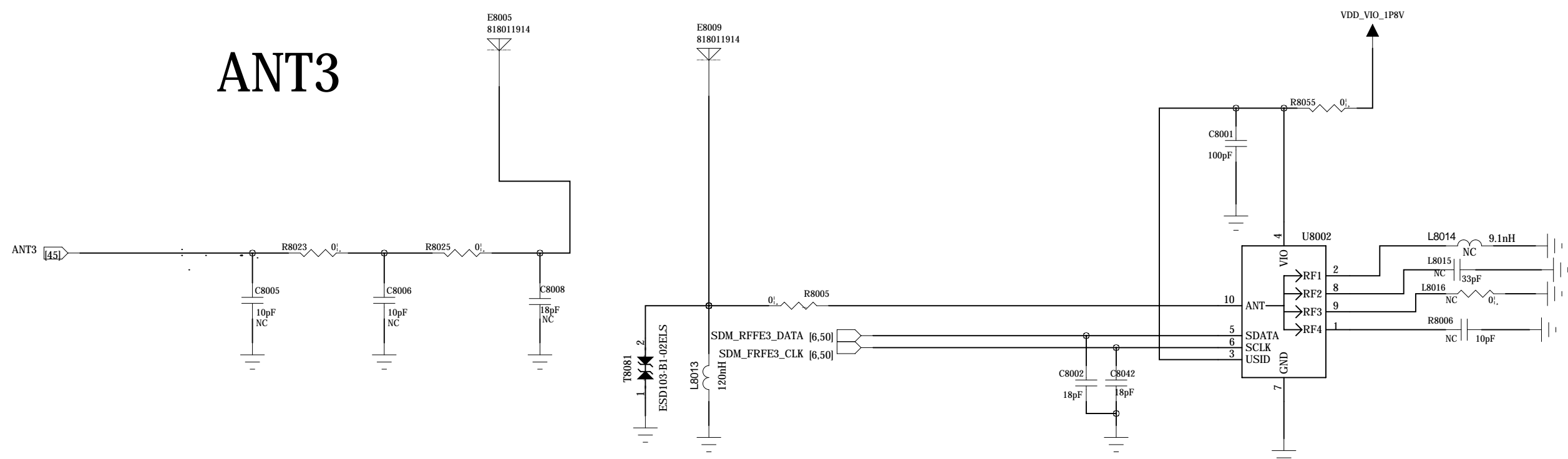
ANT6:WIFI-2.4G/BT/GPS

# ANT2



## ANT5

# ANT3



DRAWN: Michael Liang	DATED: 20211025
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

COMPANY: <b>TINNO MOBILE TECHNOLOGY CO.,LTD</b>			
TITLE:  <b>P721CE Main Board</b>			
V	CODE:  <b>P721CE</b>	SIZE:  <b>D</b>	DRAWING NO:  <b>P721CE</b>
			REV:  <b>V0.31</b>
SCALE: <Scale>			SHEET: <b>60</b> <b>51</b>