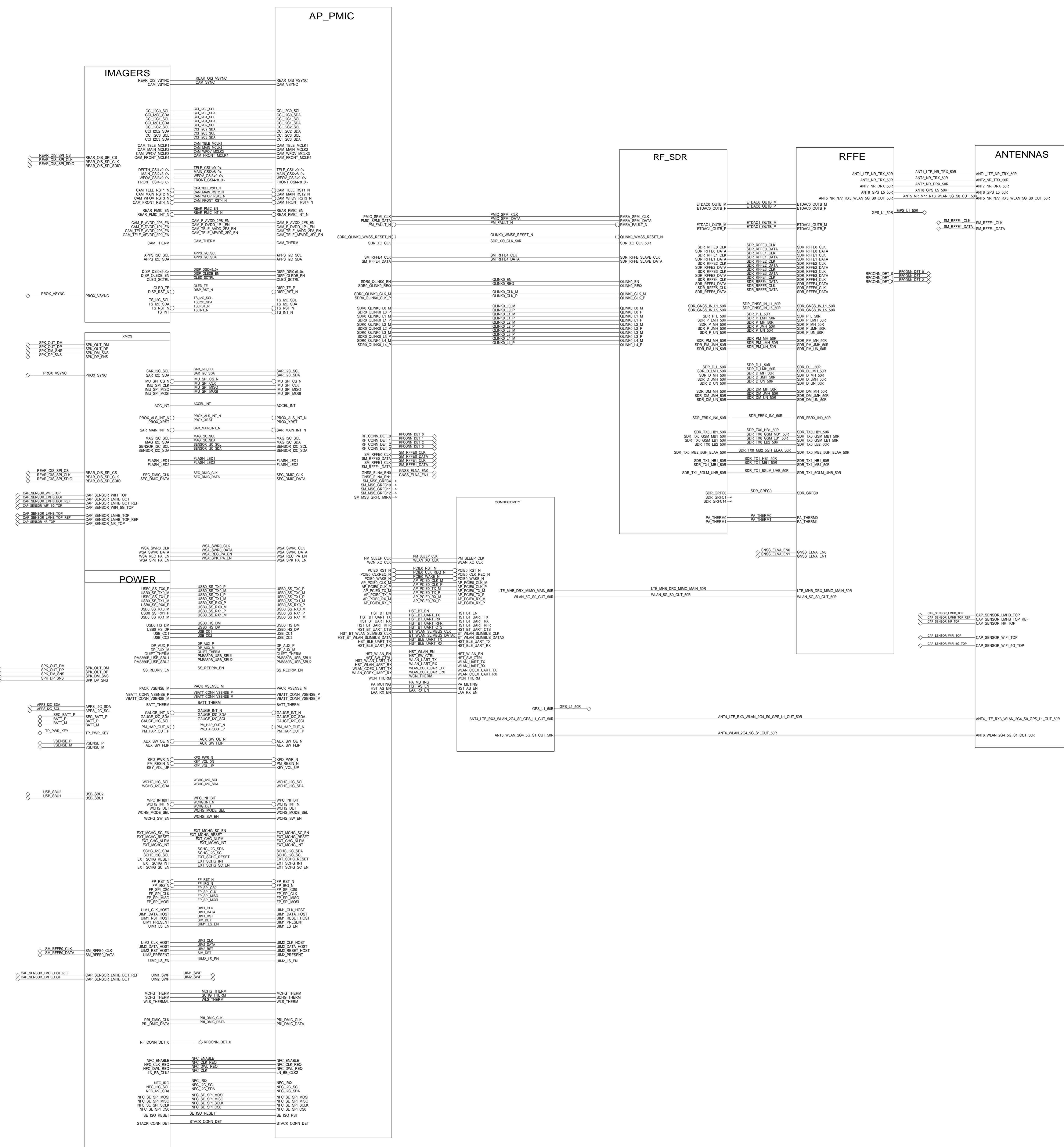
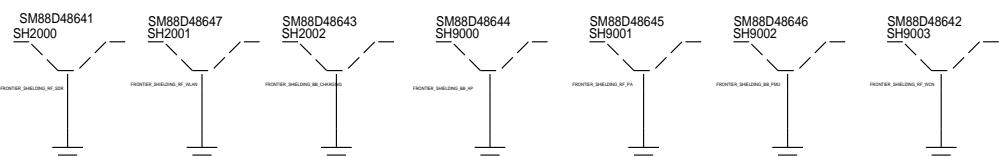


FRONTIER_SM8475

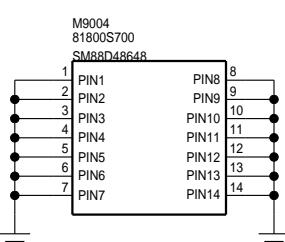


APPROVALS		 MOTOROLA MOBILE DEVICES		
ENGINEER: MOTOROLA		BLOCK TITLE: frontier_sm8475		
LAST MODIFIED BY: MOTOROLA Fri May 06 15:19:32 2022				
MOTOROLA CONFIDENTIAL RESTRICTED <hr/> THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCUSSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA. THIS DOES NOT IMPLY PUBLICATION		E	PROJECT NAME frontier_sm8475.cpm	REV 01
		CAD FILE NAME:		PAGE 1 OF 77

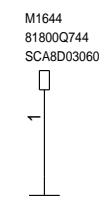
PCB SHIELD



GROUND CONTACTS

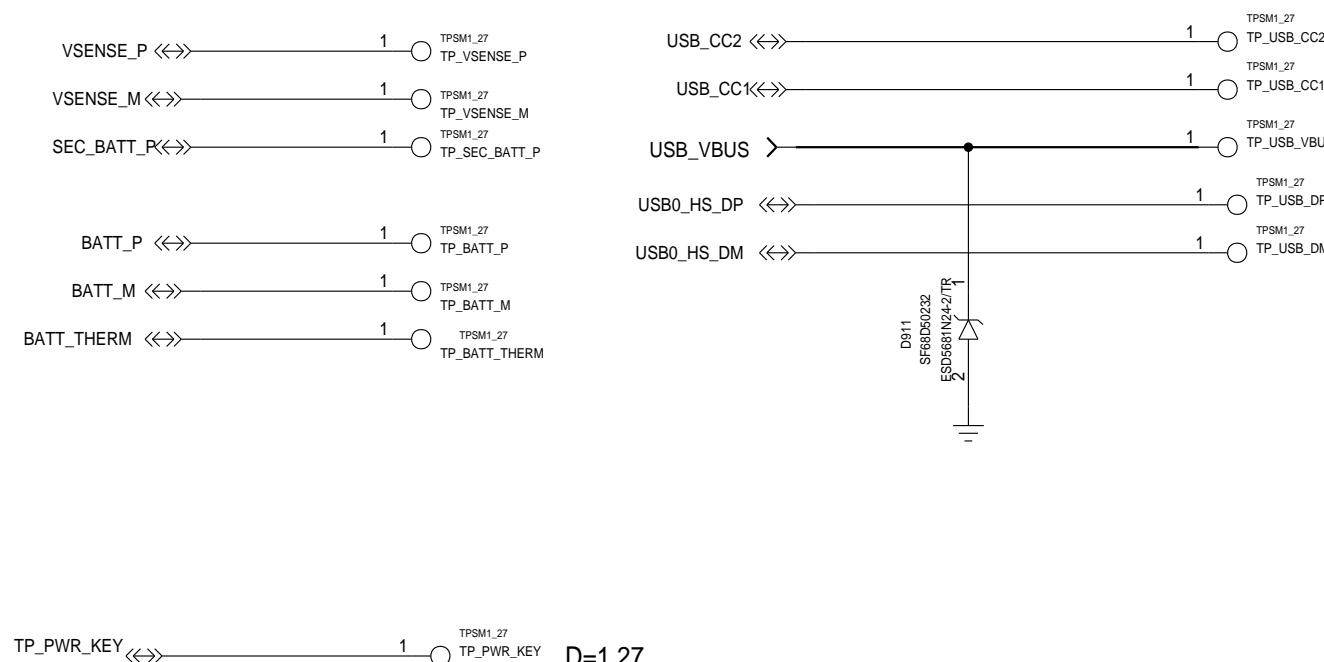


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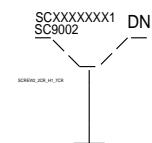


2022/1/4

TEST POINT FOR FACTORY



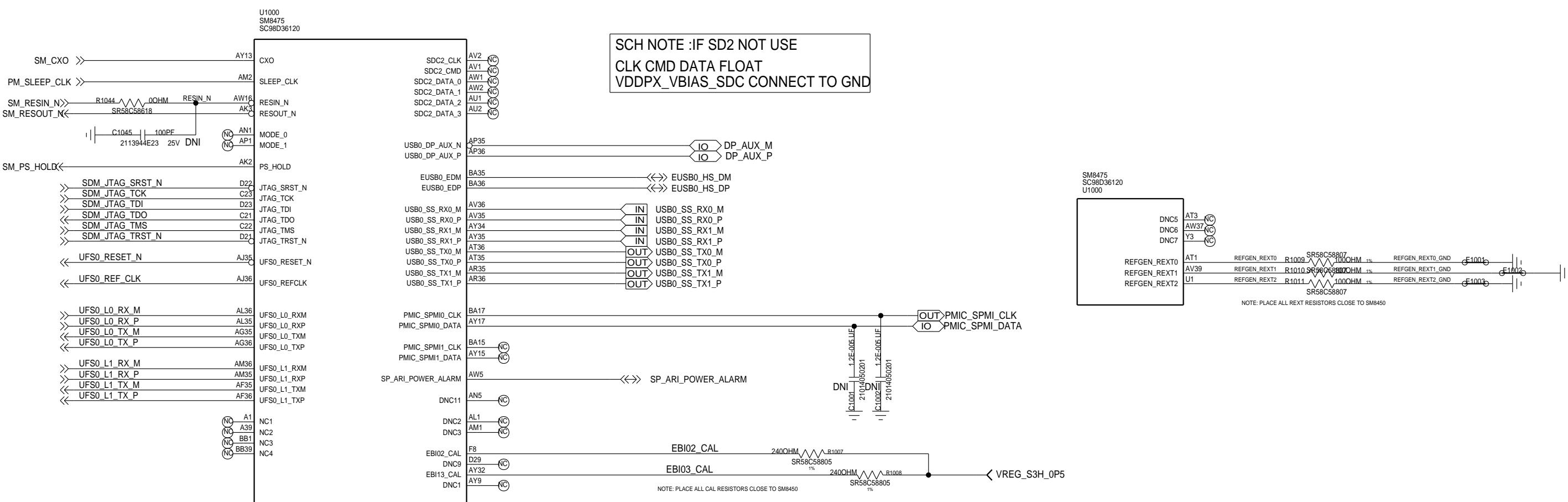
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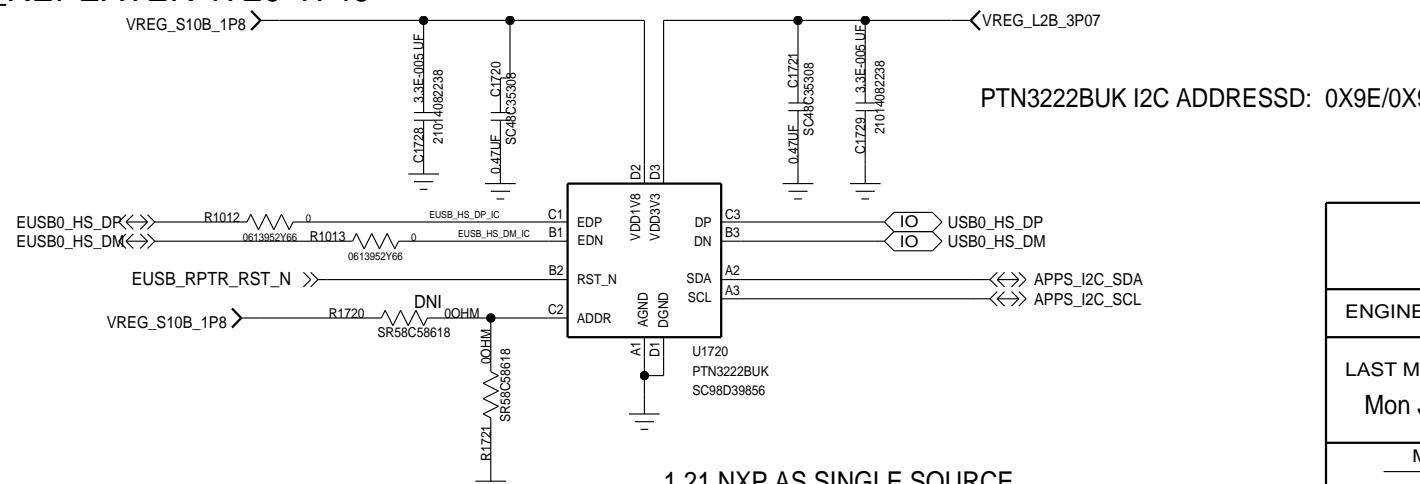
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ENGINEER: MOTOROLA	BLOCK TITLE: frontier_sm8475	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:48 2022		
MOTOROLA CONFIDENTIAL RESTRICTED	PROJECT NAME	REV
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SM8475: CONTROL

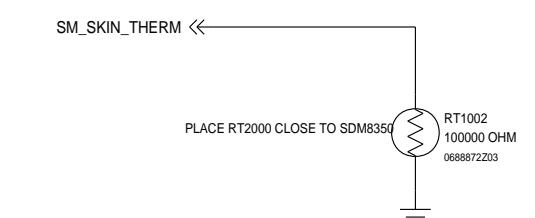
REFDES: 1000-1099



USB REPEATER 1720-1749



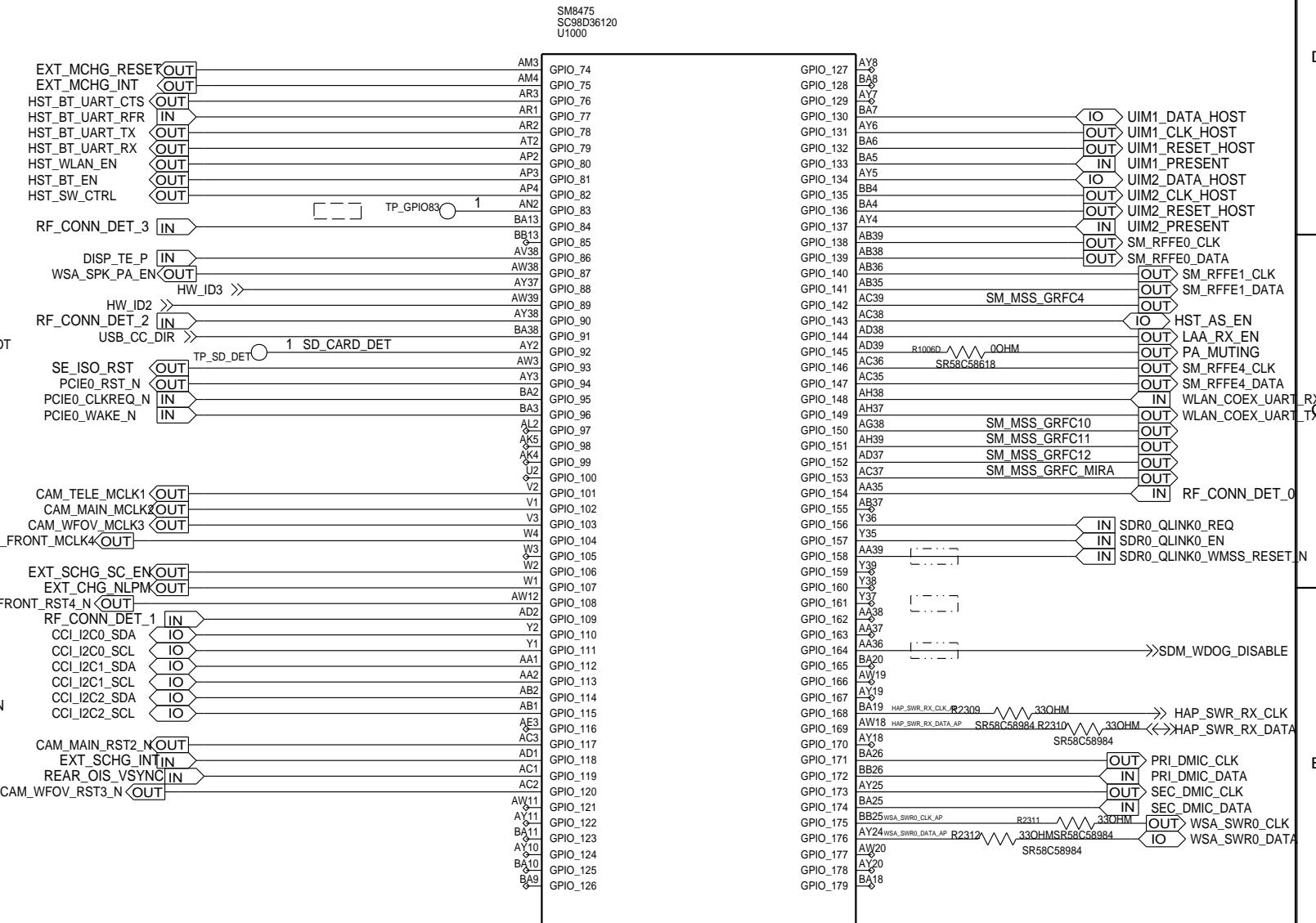
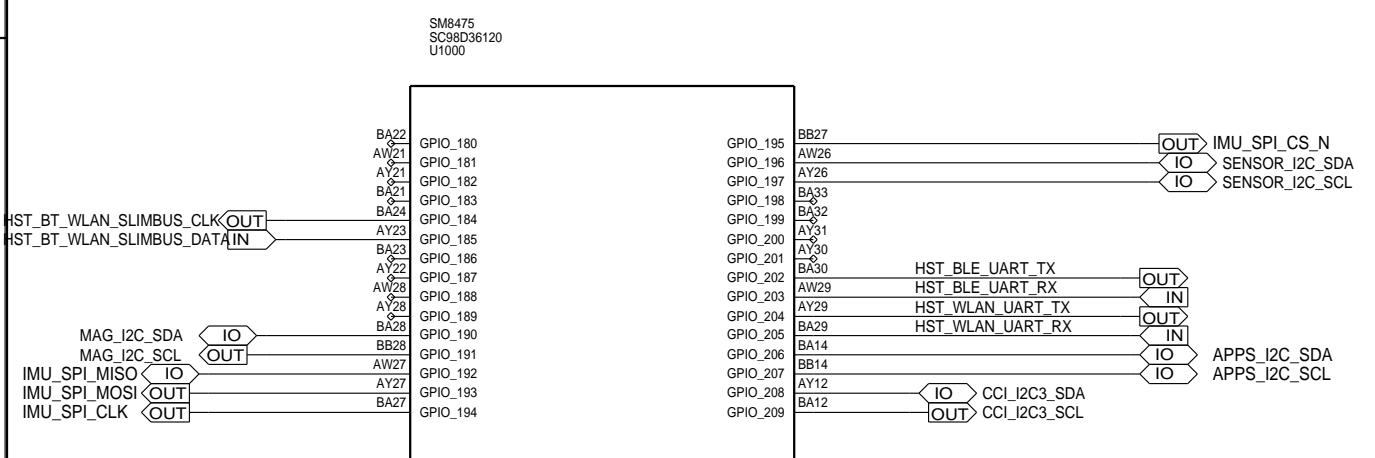
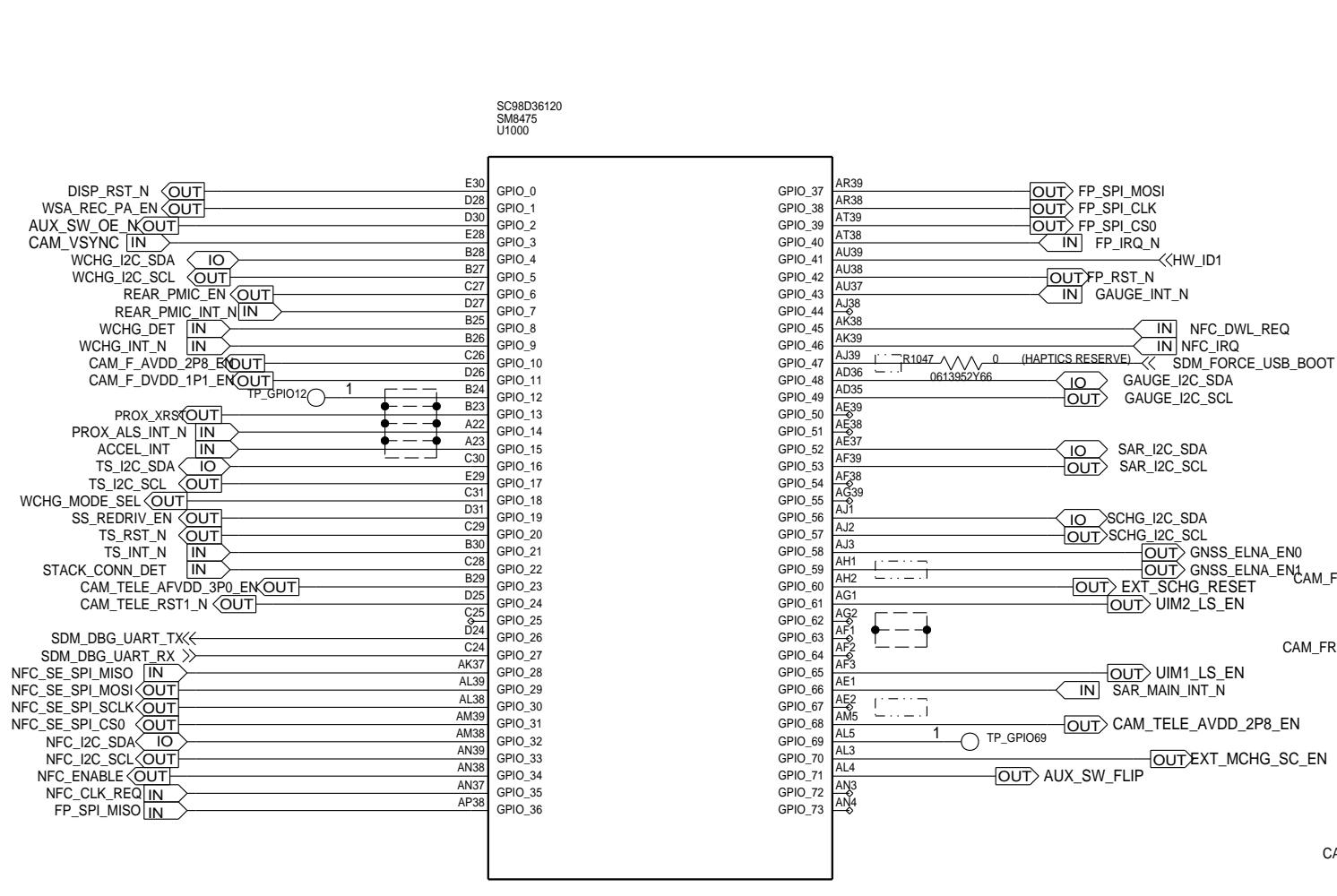
1.21 NXP AS SINGLE SOURCE



APPROVALS	MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA	BLOCK TITLE: ap_pmic	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:52 2022		
MOTOROLA CONFIDENTIAL RESTRICTED	PROJECT NAME	REV
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CAD FILE NAME:	PAGE 3 OF 77	

SM8475: GPIO

REFDES: 1000-1099



二二二 MPM GPIO 12 13 14 15 62 63 83 FOR SENSOR

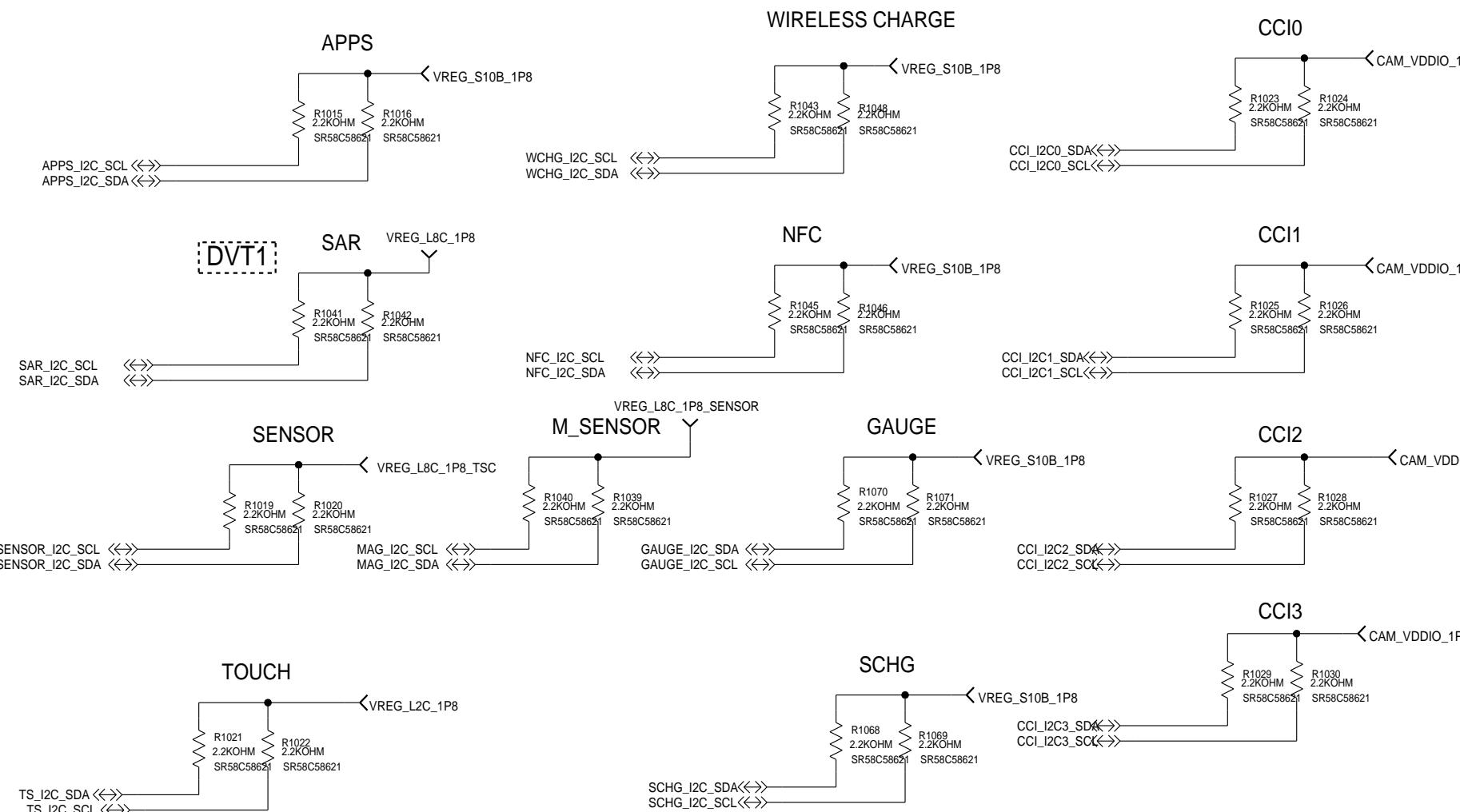
BOOT CONFIG GPIO 47 59 67 149 158 161 164

APPROVALS	 MOTOROLA MOBILE DEVICES		
ENGINEER: MOTOROLA	BLOCK TITLE: ap_pmic		
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:50 2022			
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	CAD FILE NAME:		PAGE 4 OF 77

SM8475: SUPPORT

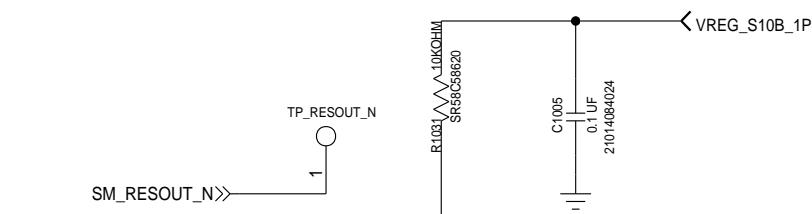
REFDES: 1000-1099

I2C PULL-UP RESISTORS



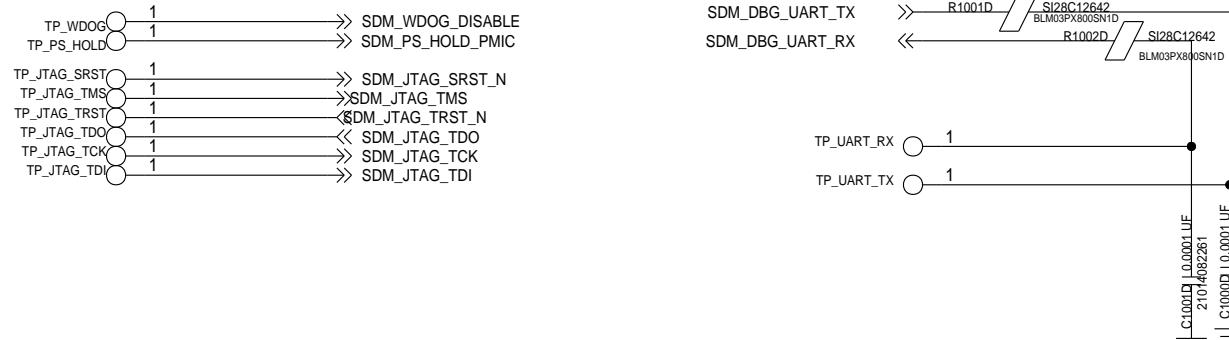
(DNI FOR PRODUCTION AND LAB ENTRY)

FORCE FLASH SUPPORT WITH VOL UP KEY



[PVT]

JTAG CONNECTOR

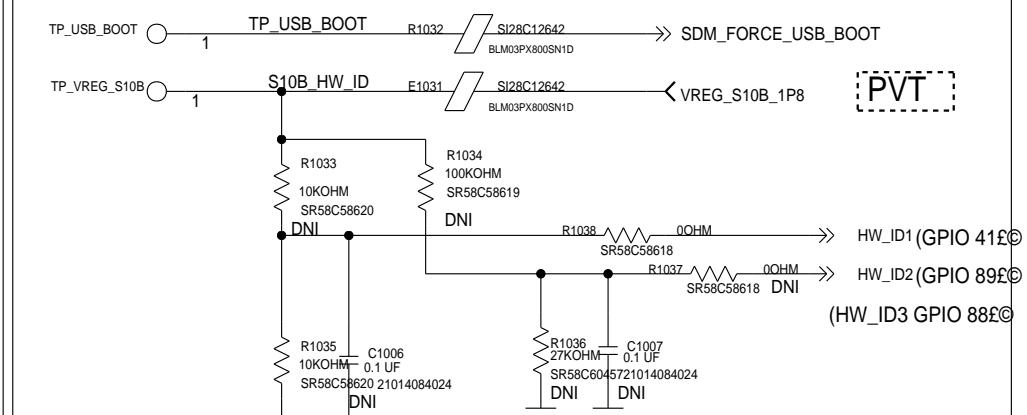


[PVT]

(DNI FOR PRODUCTION)

PRODUCT HW ID

THESE 2 SIGNALS ARE USED TO IDENTIFY THE REVISION OF THE BOARD FOR RUNTIME REVISION INFORMATION, REFER TO:



[PVT]

APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

BLOCK TITLE:

ap_pmic

LAST MODIFIED BY: MOTOROLA

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PROJECT NAME
frontier_sm8475.cpm

REV
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CAD FILE NAME:

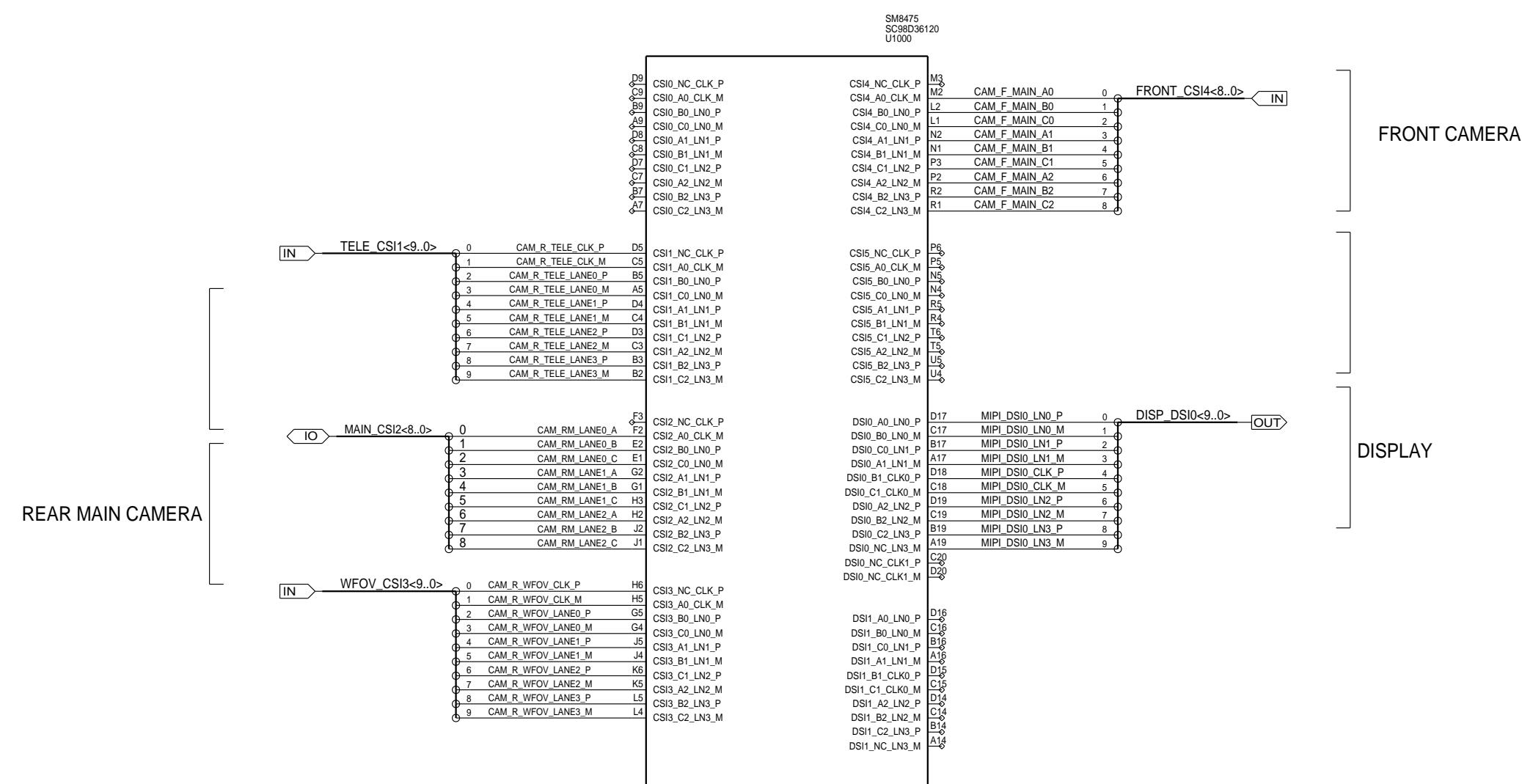
PAGE 5 OF 77

SM8475: MIPI/PCIE

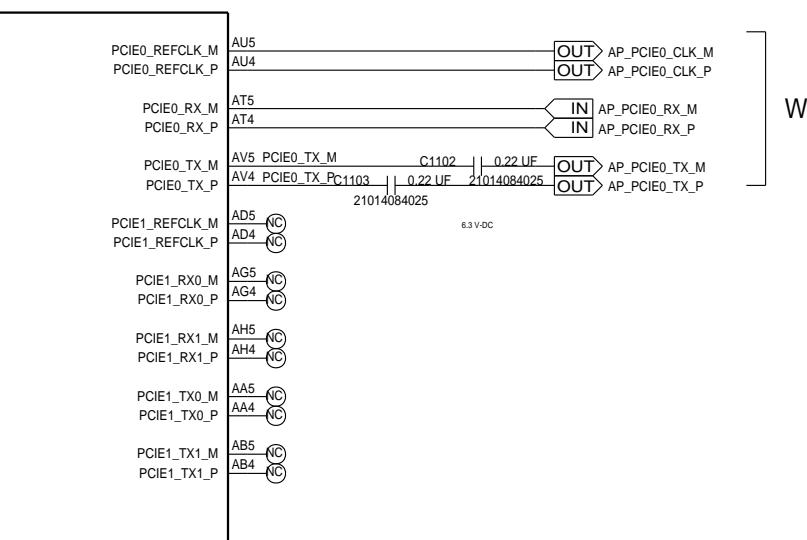
REFDES: 1100-1199

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SC98D36120
SM8475
U1000



ENGINEER: MOTOROLA
LAST MODIFIED BY: MOTOROLA
Mon Jun 27 12:22:54 2022

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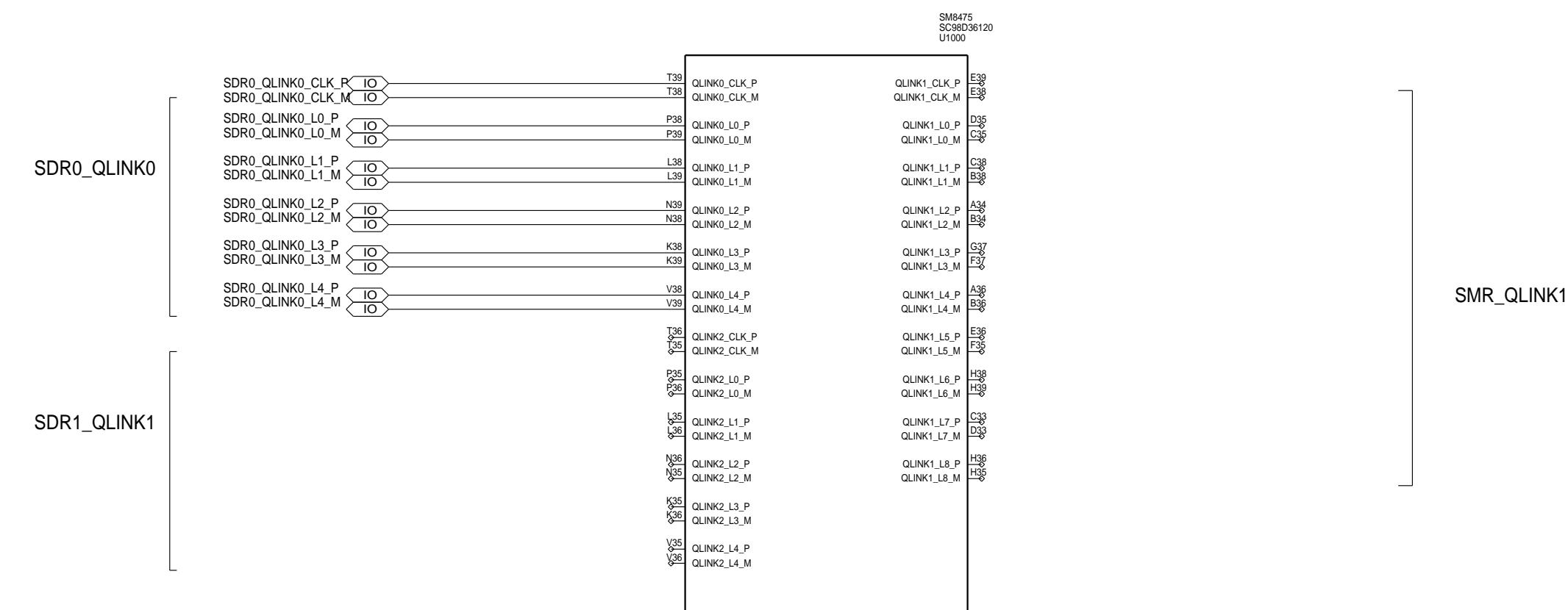
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B frontier_sm8475.cpm
REV
01

CAD FILE NAME:
PAGE 6 OF 77

SM8475: QLINK

REFDES: 1100-1199



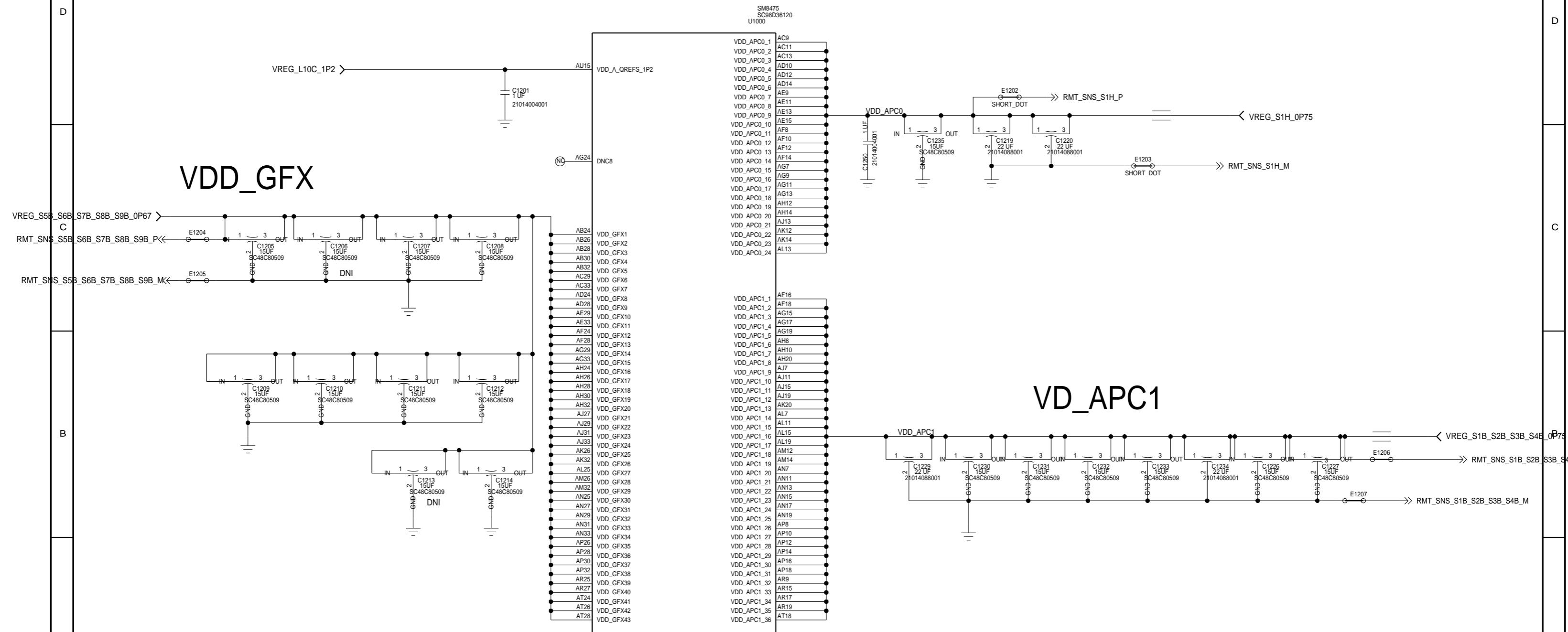
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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:54 2022		ap_pmic	
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CAD FILE NAME:			PAGE 7 OF 77

SM8475: PWR 1

REFDES: 1200-1249

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APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:53 2022		ap_pmic	
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CAD FILE NAME:		REV 01	
PAGE 8 OF 77			

SM8475: PWR 2

REFDES: 1250-1299

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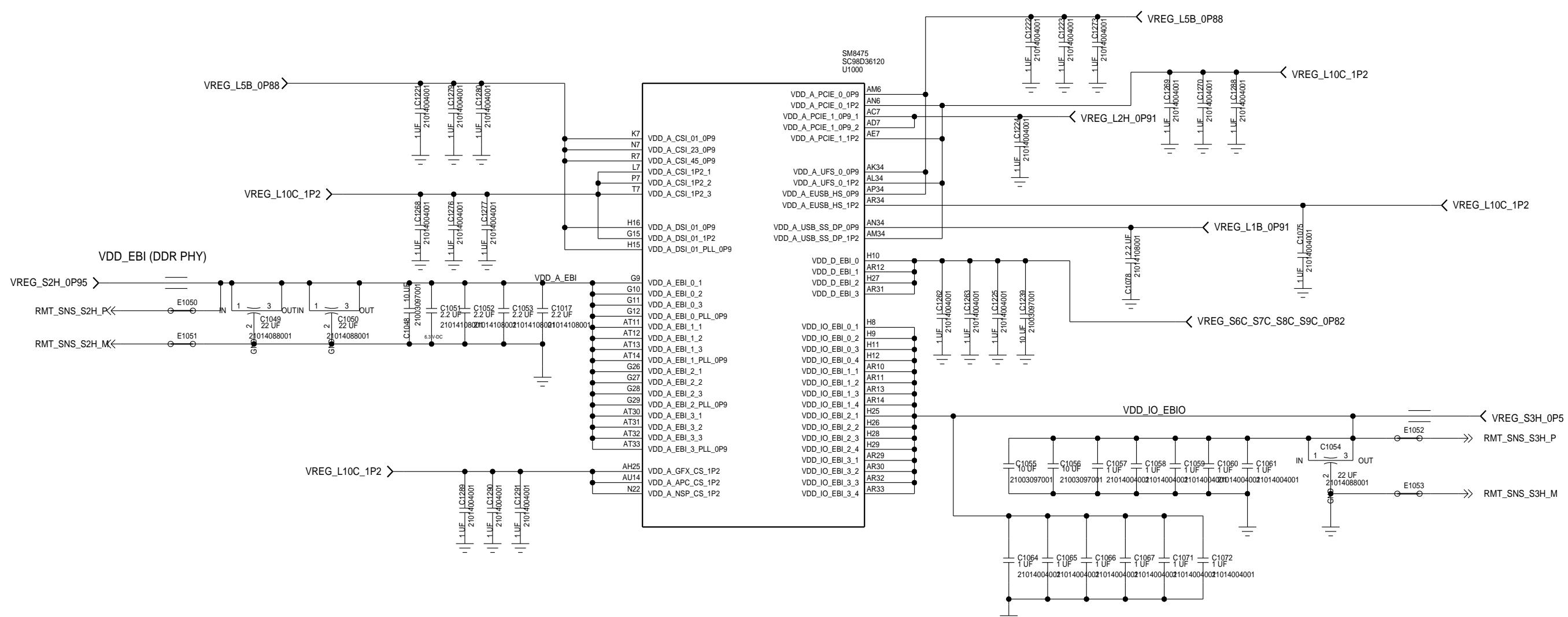
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ENGINEER:	MOTOROLA
LAST MODIFIED BY:	MOTOROLA Mon Jun 27 12:22:54 2022
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CAD FILE NAME:	
PAGE 9 OF 77	

MOTOROLA
MOBILE DEVICES

BLOCK TITLE:

ap_pmic

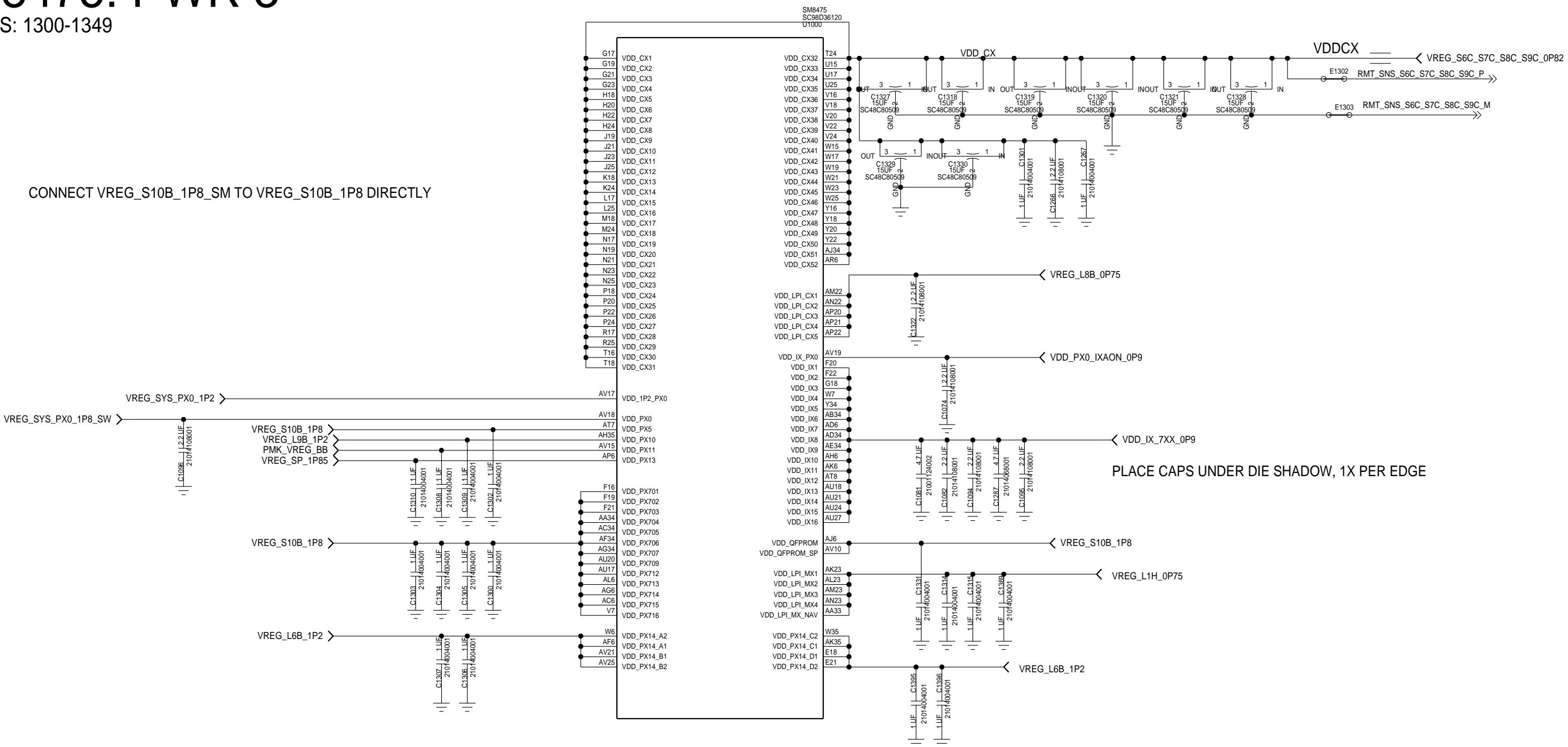
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REV
01

SM8475: PWR 3

REFDES: 1300-1349

CONNECT VREG_S10B_1P8_SM TO VREG_S10B_1P8 DIRECTLY



APPROVALS	 MOTOROLA MOBILE DEVICES		
ENGINEER: MOTOROLA	BLOCK TITLE: ap_pmic		
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:54 2022			
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	CAD FILE NAME:		PAGE 10 OF 77

SM8475: PWR 4

REFDES: 1350-1399

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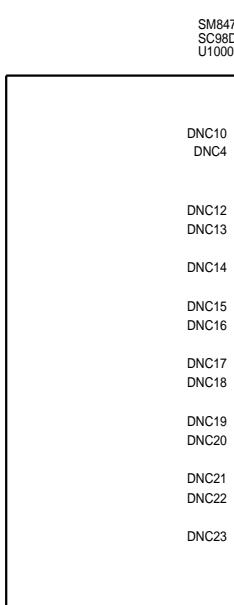
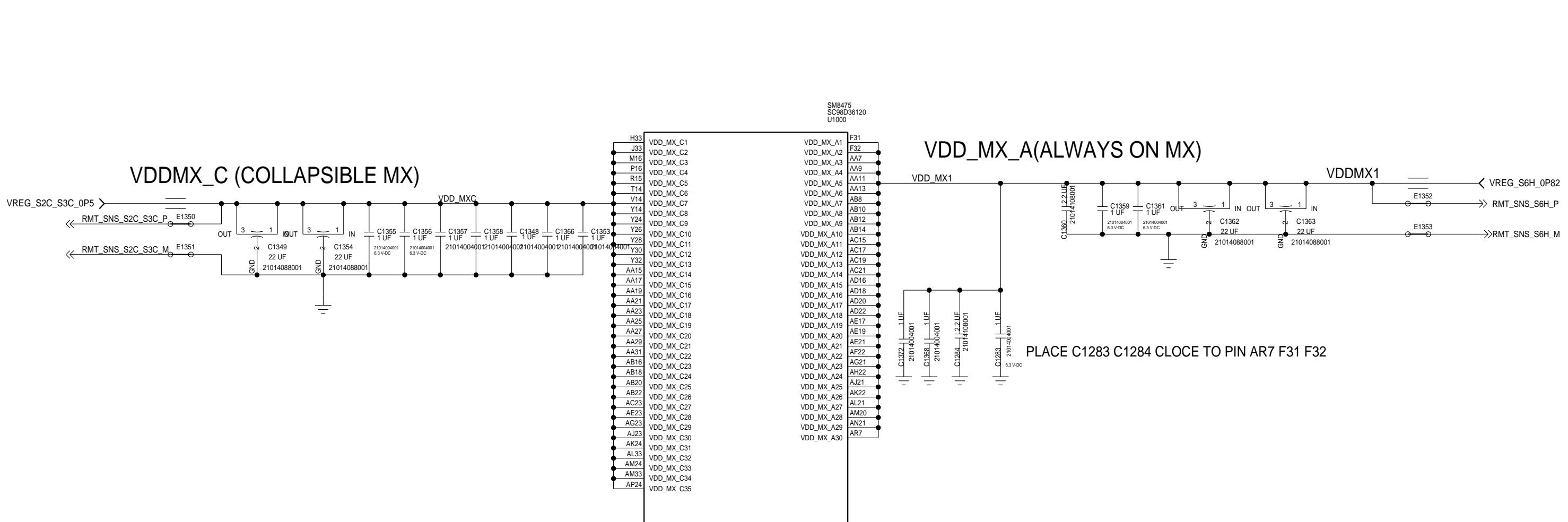
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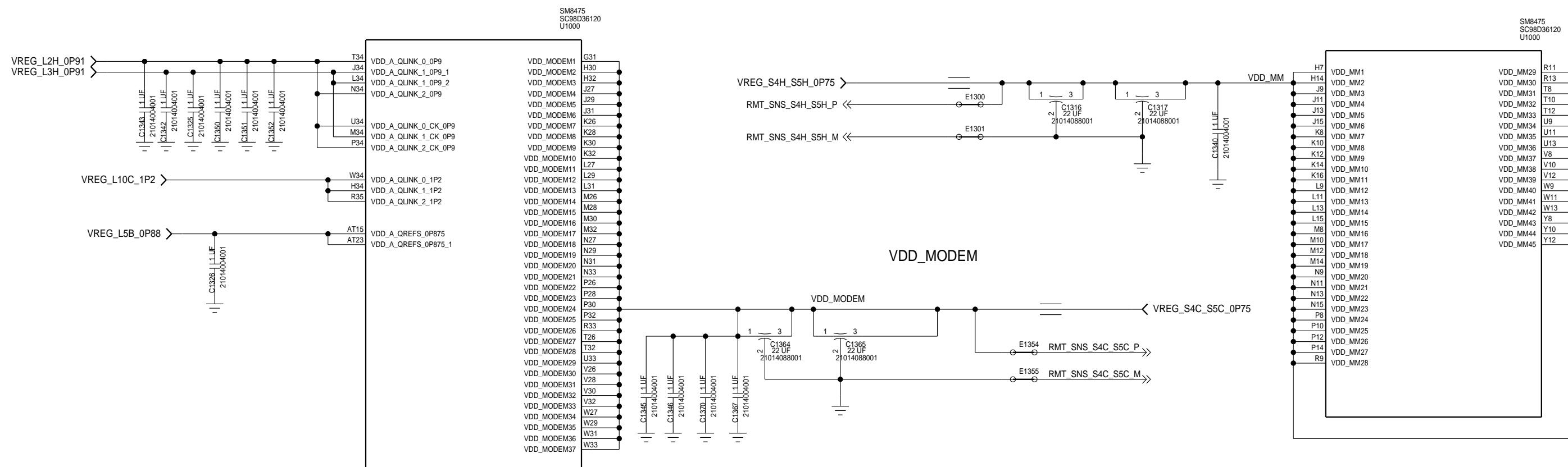
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APPROVALS		MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:53 2022			
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CAD FILE NAME:			PAGE 11 OF 77

SM8475: PWR 5

REFDES: 1350-1399

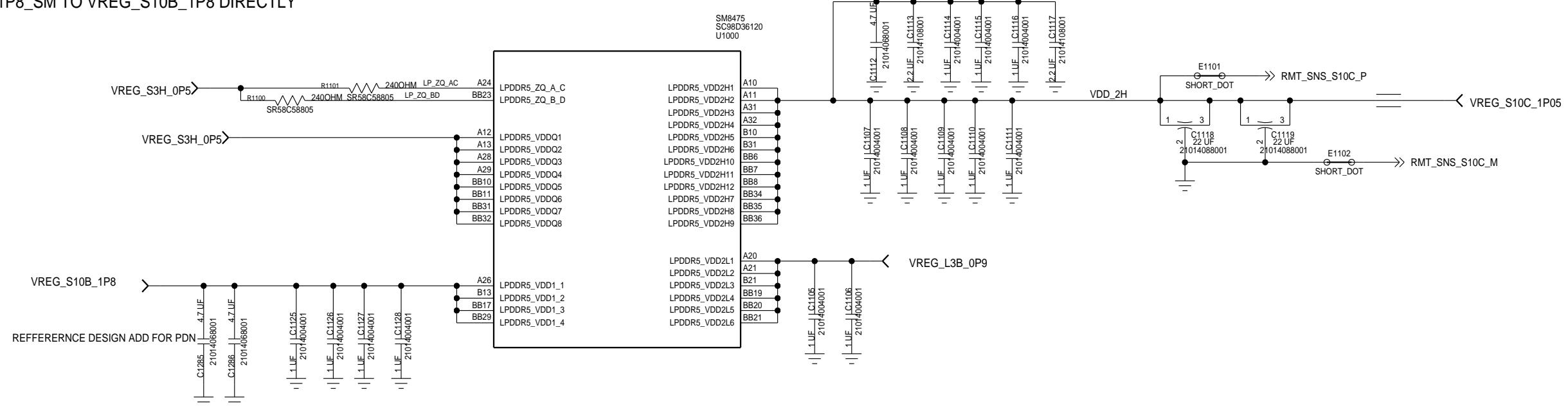


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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:54 2022	ap_pmic	
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	CAD FILE NAME: 	PAGE 12 OF 77

SM8475: PWR_LPDDR5

REFDES: 1100-1199

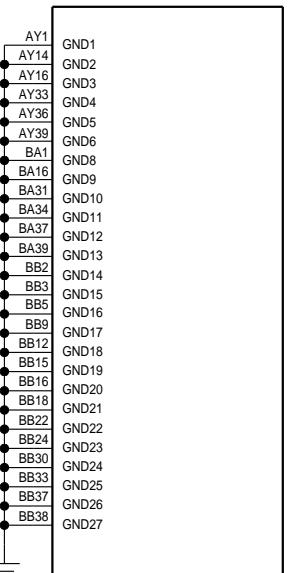
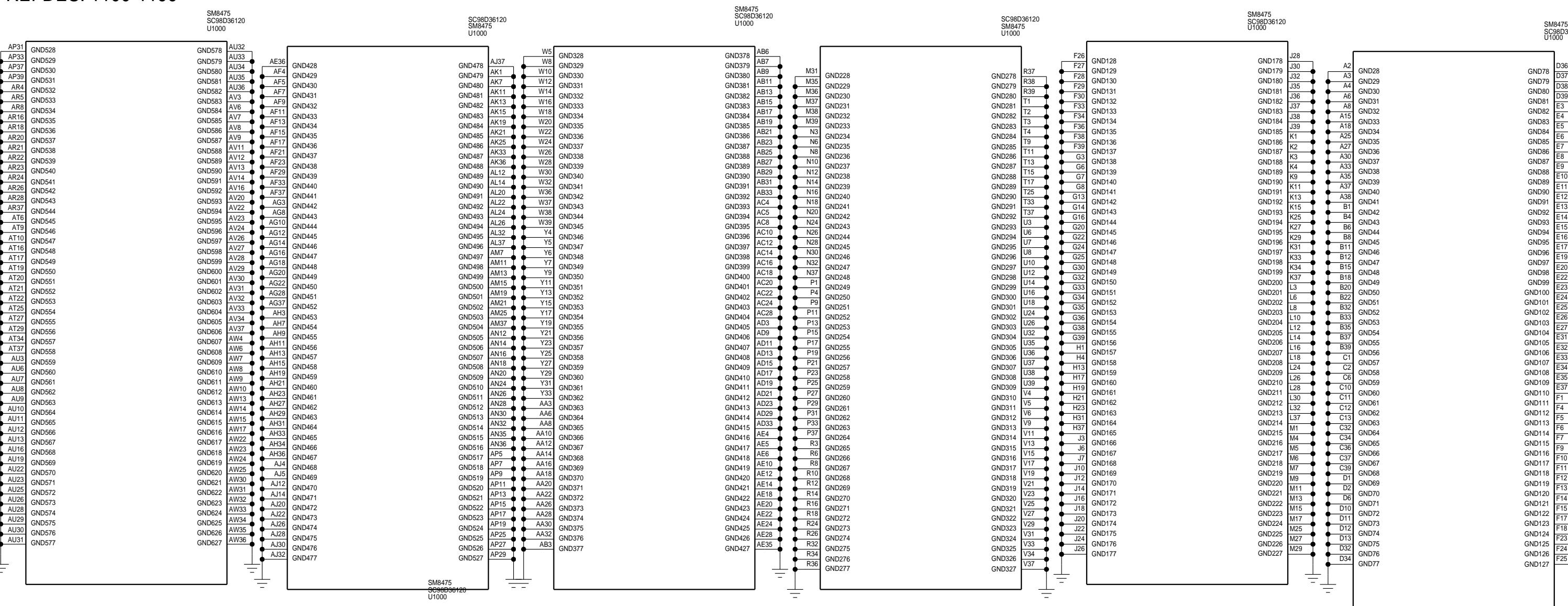
CONNECT VREG_S10B_1P8_SM TO VREG_S10B_1P8 DIRECTLY



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: ap_pmic	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:54 2022			
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CAD FILE NAME:			PAGE 13 OF 77

SM8475: GND

REFDES: 1100-1199



APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

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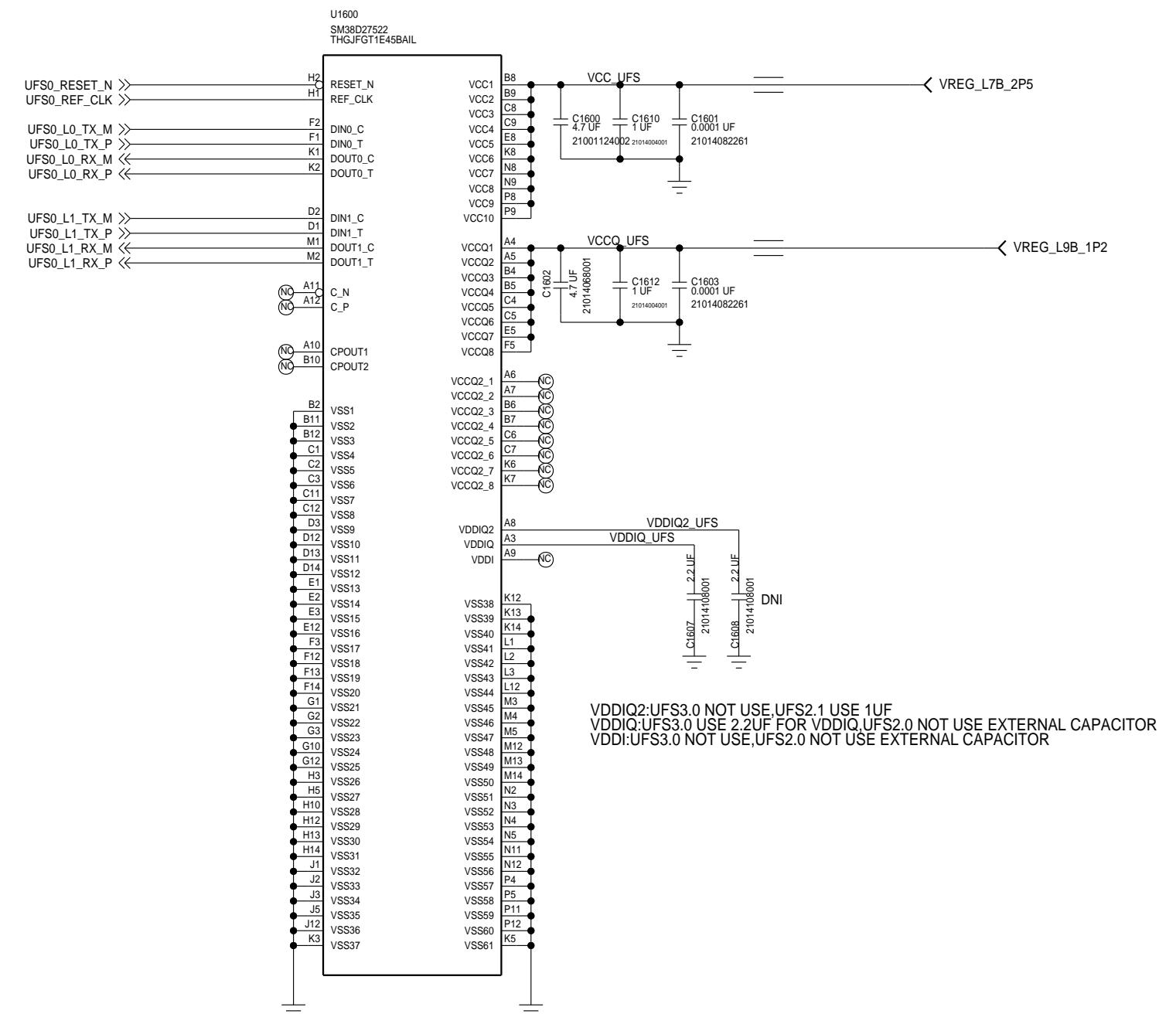
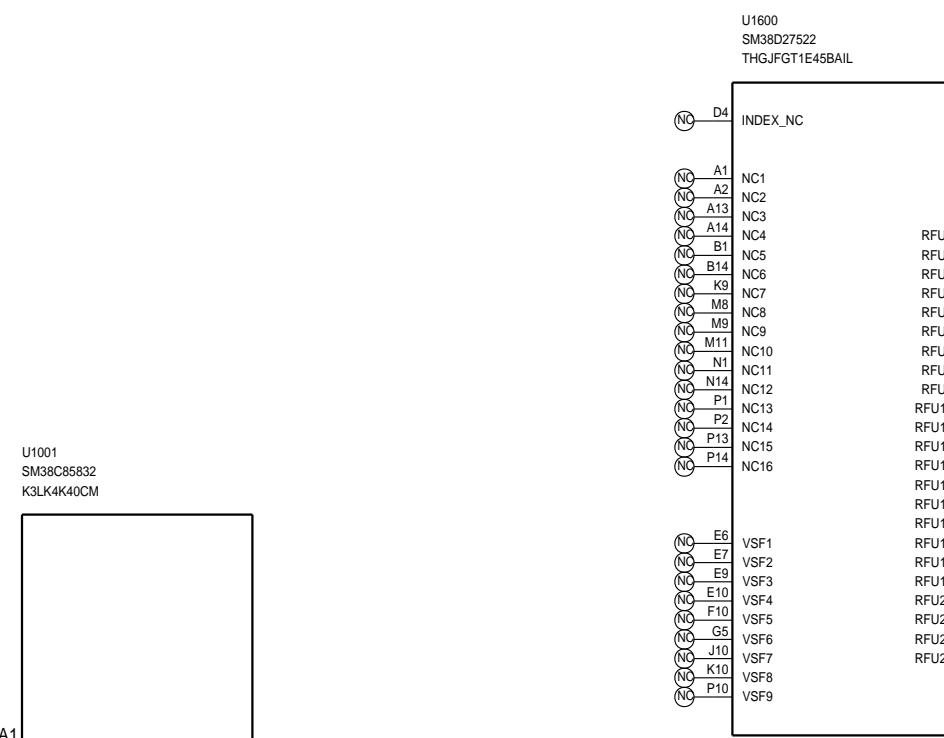
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PROJECT NAME
B frontier_sm8475.cpm

PAGE 14 OF 77

MEMORY: UFS3.1/POP LPDDR5X

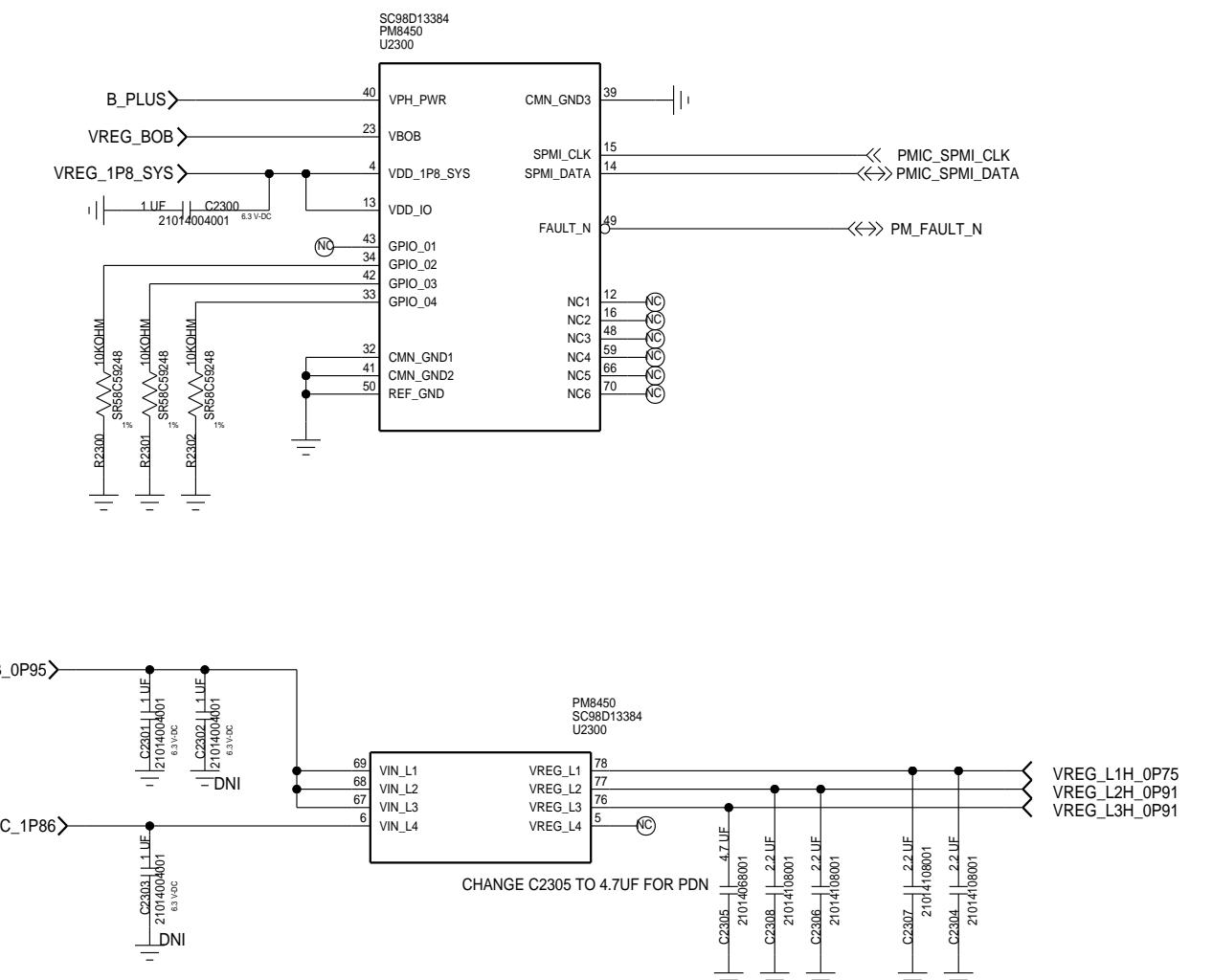
REFDES: 1600-1699



APPROVALS		MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:52 2022		ap_pmic	
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B	frontier_sm8475.cpm	CAD FILE NAME:	PAGE 15 OF 77

PM8450: CTRL/LDOS

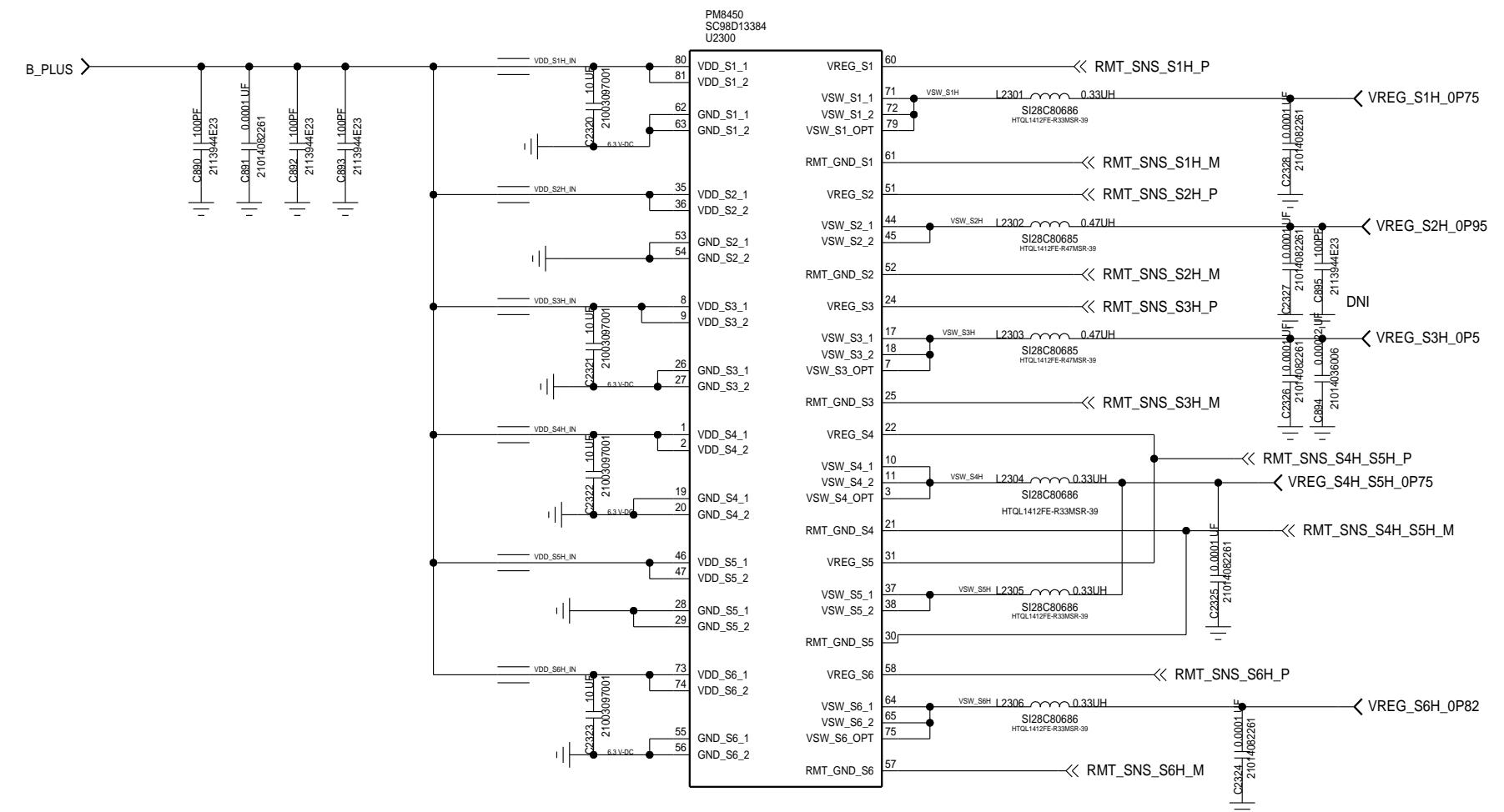
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APPROVALS	 MOTOROLA MOBILE DEVICES		
ENGINEER: MOTOROLA	BLOCK TITLE: ap_pmic		
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:51 2022			
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	CAD FILE NAME:		PAGE 16 OF 77

PM8450: BUCKS

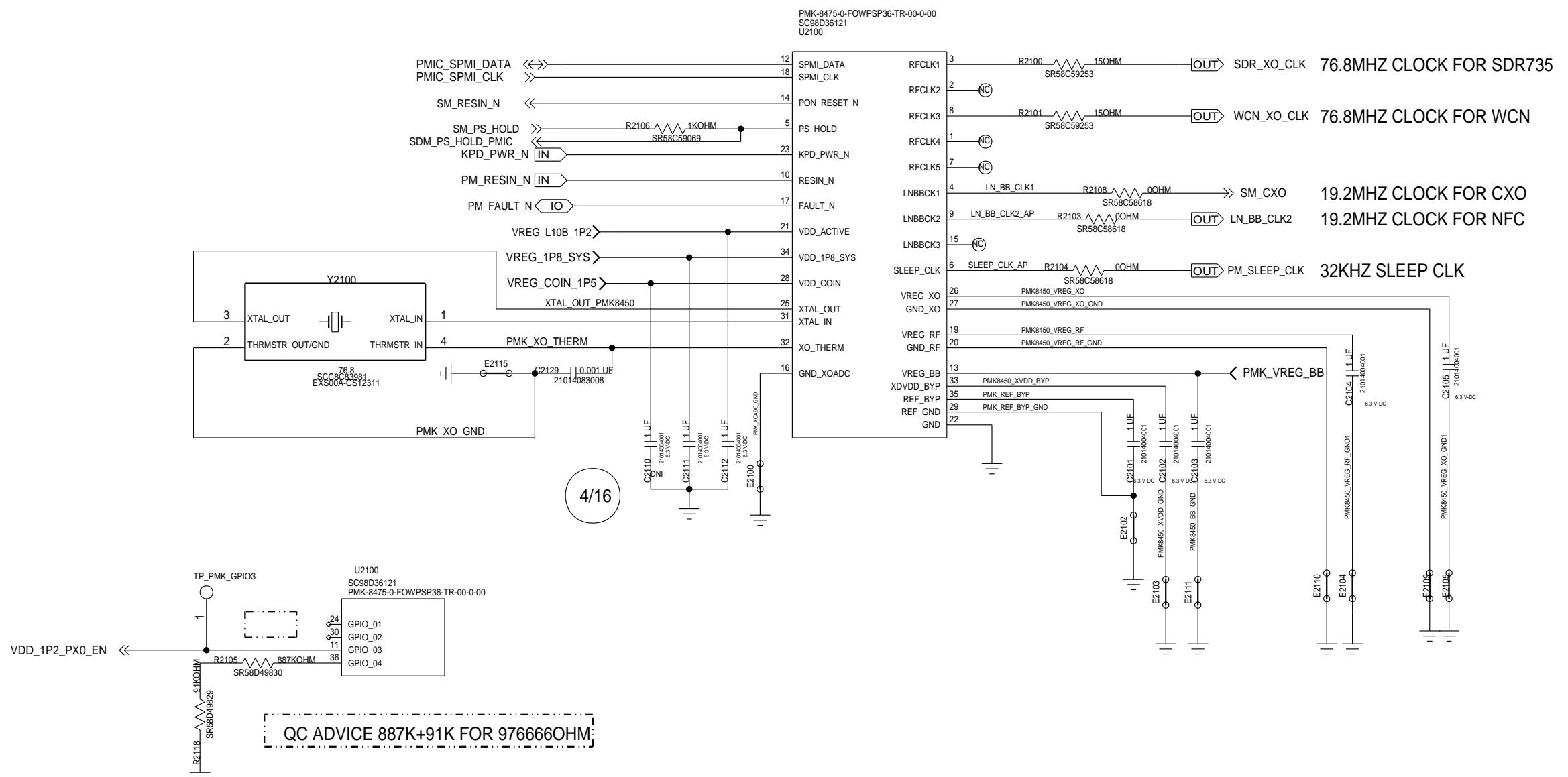
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APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:53 2022		ap_pmic	
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CAD FILE NAME:		PAGE 17 OF 77	

PMK8475

REFDES: 1200-1249

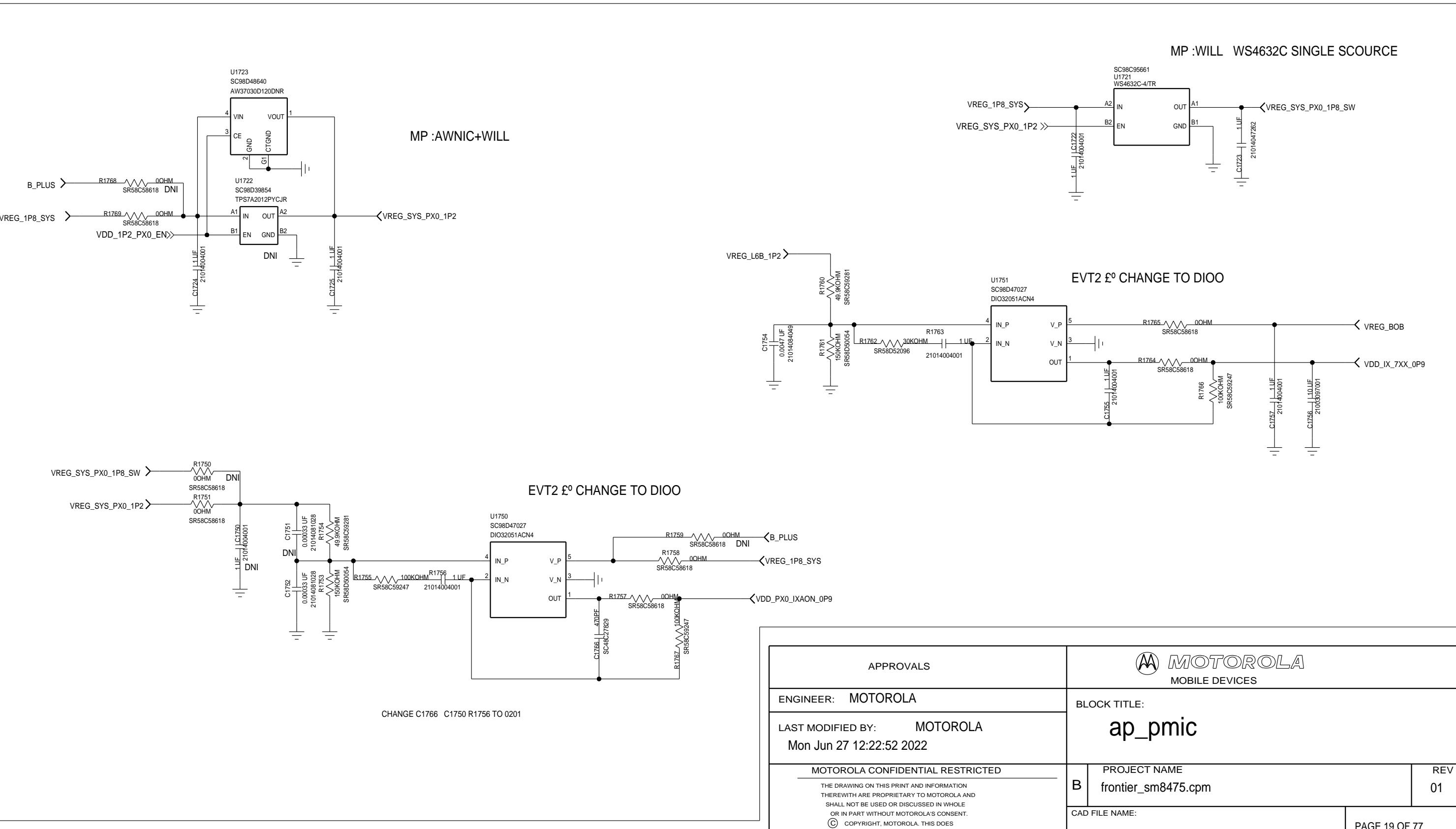


976666OHM (SKIP SD CARD LDO ENABLE, GPIO_05 (PM8350) ACTIVE HIGH),GPIO_03(PMK8475)FOR EXT 1.2V LDO

APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:52 2022		ap_pmic	
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	REV
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CAD FILE NAME:			PAGE 18 OF 77

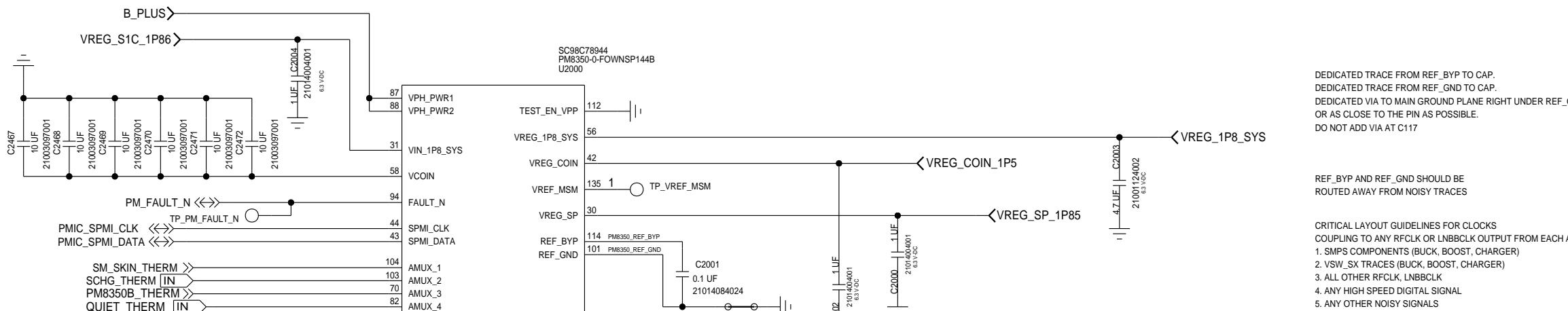
EXT_PART_OPA

REFDES: 1700-1799



PM8350: CTRL/GPIO/AMUX

REFDES: 2000-2099



DEDICATED TRACE FROM REF_BYP TO CAP.
DEDICATED TRACE FROM REF_GND TO CAP.
DEDICATED VIA TO MAIN GROUND PLANE RIGHT UNDER REF_GND PIN
OR AS CLOSE TO THE PIN AS POSSIBLE.
DO NOT ADD VIA AT C117

REF_BYP AND REF_GND SHOULD BE
ROUTED AWAY FROM NOISY TRACES

Critical Layout Guidelines for Clocks:
COUPLING TO ANY RFCLK OR LNBCLK OUTPUT FROM EACH AGGRESSOR SHOULD BE < 50 FF.
1. SMPS COMPONENTS (BUCK, BOOST, CHARGER)
2. VSW_XTAL TRACES (BUCK, BOOST, CHARGER)
3. ALL OTHER RFCLK, LNBCLK
4. ANY HIGH SPEED DIGITAL SIGNAL
5. ANY OTHER NOISY SIGNALS

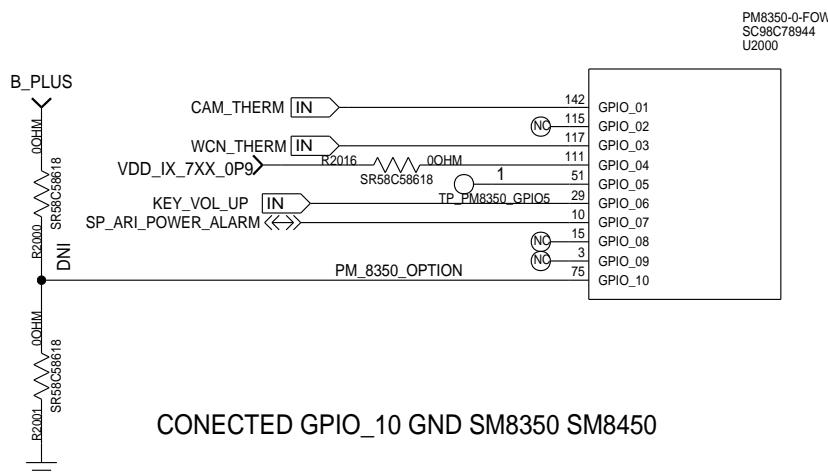
Critical Layout Guidelines for VREF_RF (GO/PMICLAYOUTCHECKLIST):
PREFERRED: SHORT, DEDICATED ROUTE TO VREG_RF CAP,
AWAY FROM NOISY SIGNALS AND DEDICATED VIA TO MAIN GROUND PLANE AT CAP OR PMIC PIN.
ACCEPTABLE: DEDICATED VIA DIRECTLY TO MAIN GROUND PLANE AT THE GND_CLKS0 PIN
AND AT VREG_RF CAP (WITHOUT ROUTING TO CAP).

DO NOT CONNECT TO ANY OTHER GROUND.
LOOP DCR MUST BE <1000 M.
LOOP INDUCTANCE MUST BE <3 NH

GND_XOADC
SKINNY TRACE TO XTAL COMPONENTS GROUND.
ONE DEDICATED VIA TO MAIN GROUND PLACE AS CLOSE TO PMIC PIN AS POSSIBLE

Critical Layout Guidelines for VREG_XO:
SHORT, DEDICATED ROUTE TO VREG_XO CAP,
AWAY FROM NOISY SIGNALS AND DEDICATED VIA TO MAIN GROUND PLANE AT CAP.

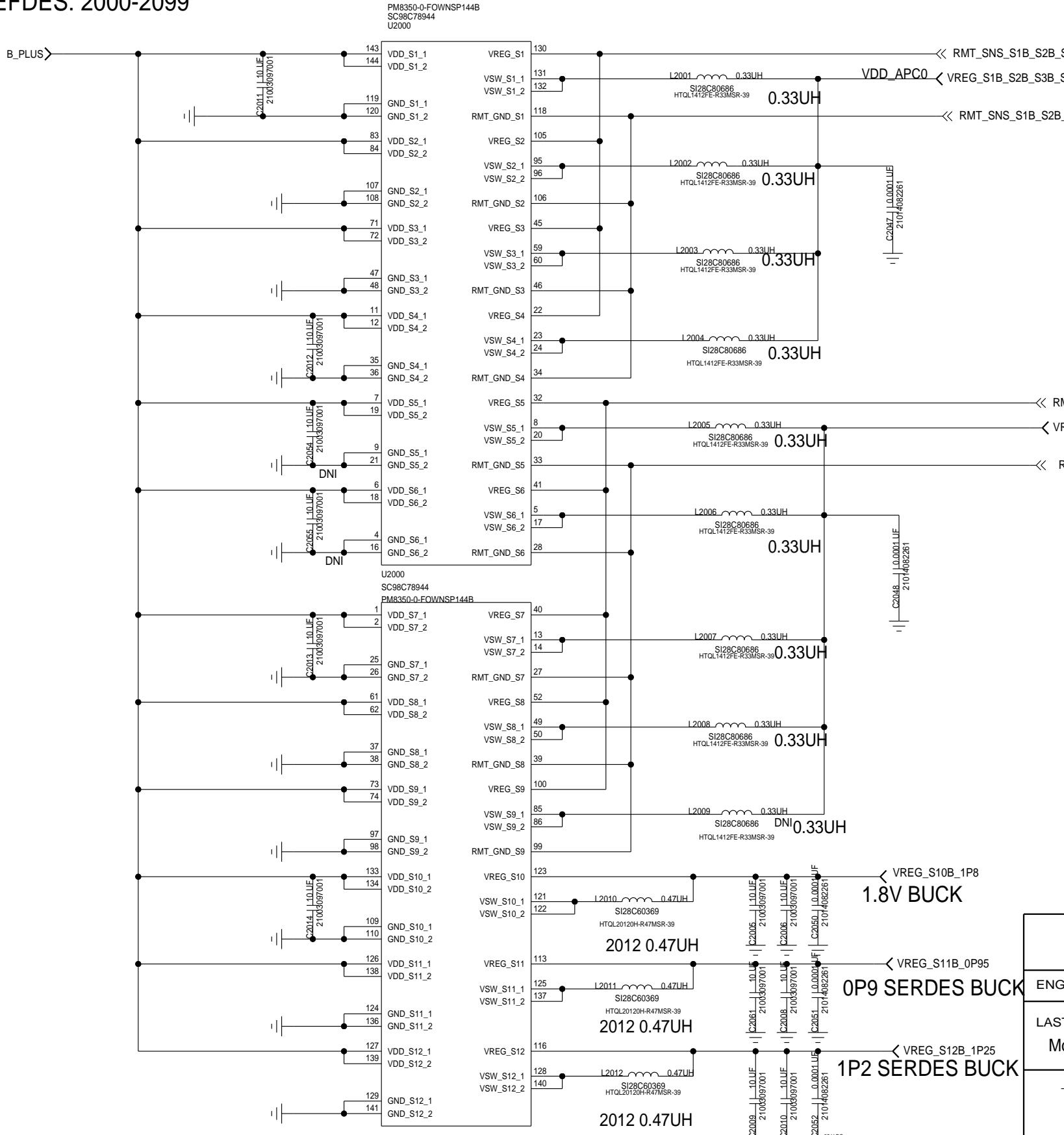
DO NOT CONNECT TO ANY OTHER GROUND EXCEPT THE MAIN GND PLANE.



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: ap_pmic	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:51 2022			
MOTOROLA CONFIDENTIAL RESTRICTED	B	PROJECT NAME frontier_sm8475.cpm	REV 01
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CAD FILE NAME:			

PM8350: BUCKS

REFDES: 2000-2099



VDD_AP1(QUAD PHASE)

VDD_GFX

VREG_S10B_1
1.8V BUCK

OP9 SERDES BU

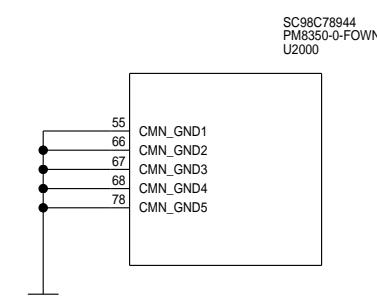
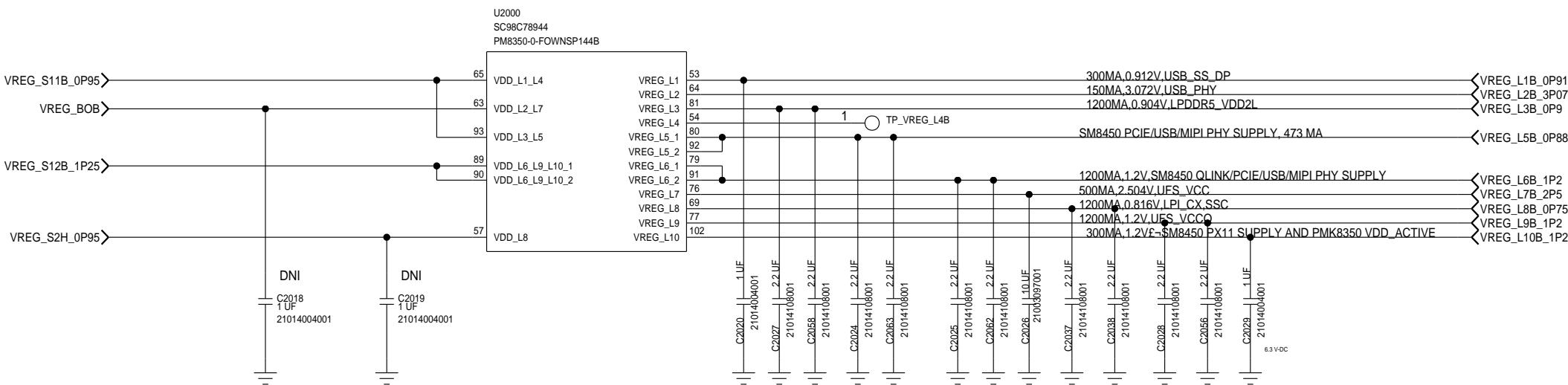
VREG_S12B_1P25

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: ap_pmic	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:51 2022			
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		CAD FILE NAME:	PAGE 21 OF 77

PM8350: LDOS/GND

REFDES: 2000-2099

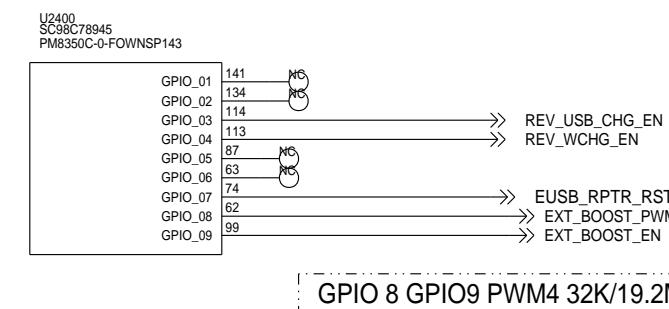
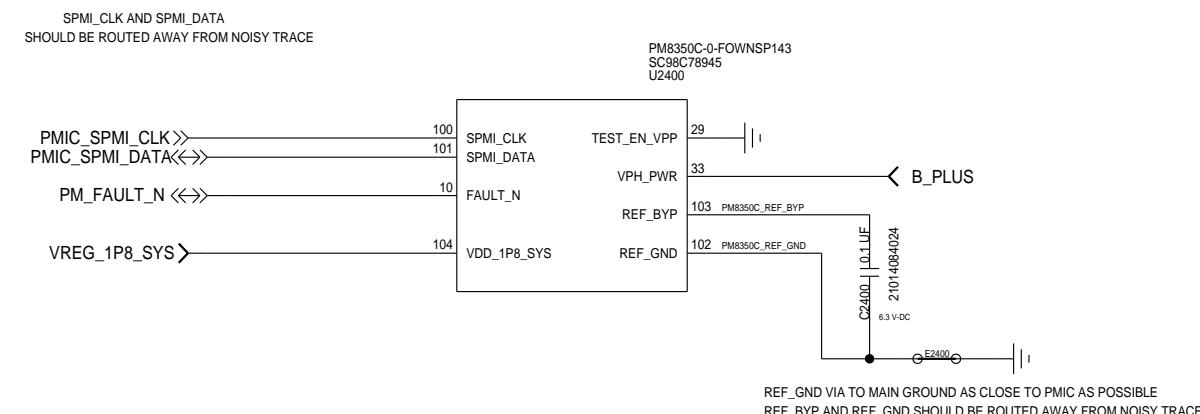
P-TYPE LDOS ARE PSEUDO-CAPLESS, CAPACITORS CAN BE PLACED NEAR LOAD.
REGULATORS POWERING PMIC INTERNAL CIRCUITS NEED CAPACITORS NEAR PMIC
N-TYPE LDOS WITH >300mA RATING NEED LOCAL CAPACITORS NEAR PMIC.



APPROVALS	 MOTOROLA MOBILE DEVICES		
ENGINEER: MOTOROLA	BLOCK TITLE: ap_pmic		
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:51 2022			
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	CAD FILE NAME:		PAGE 22 OF 77

PM8350C: CTRL/GPIO

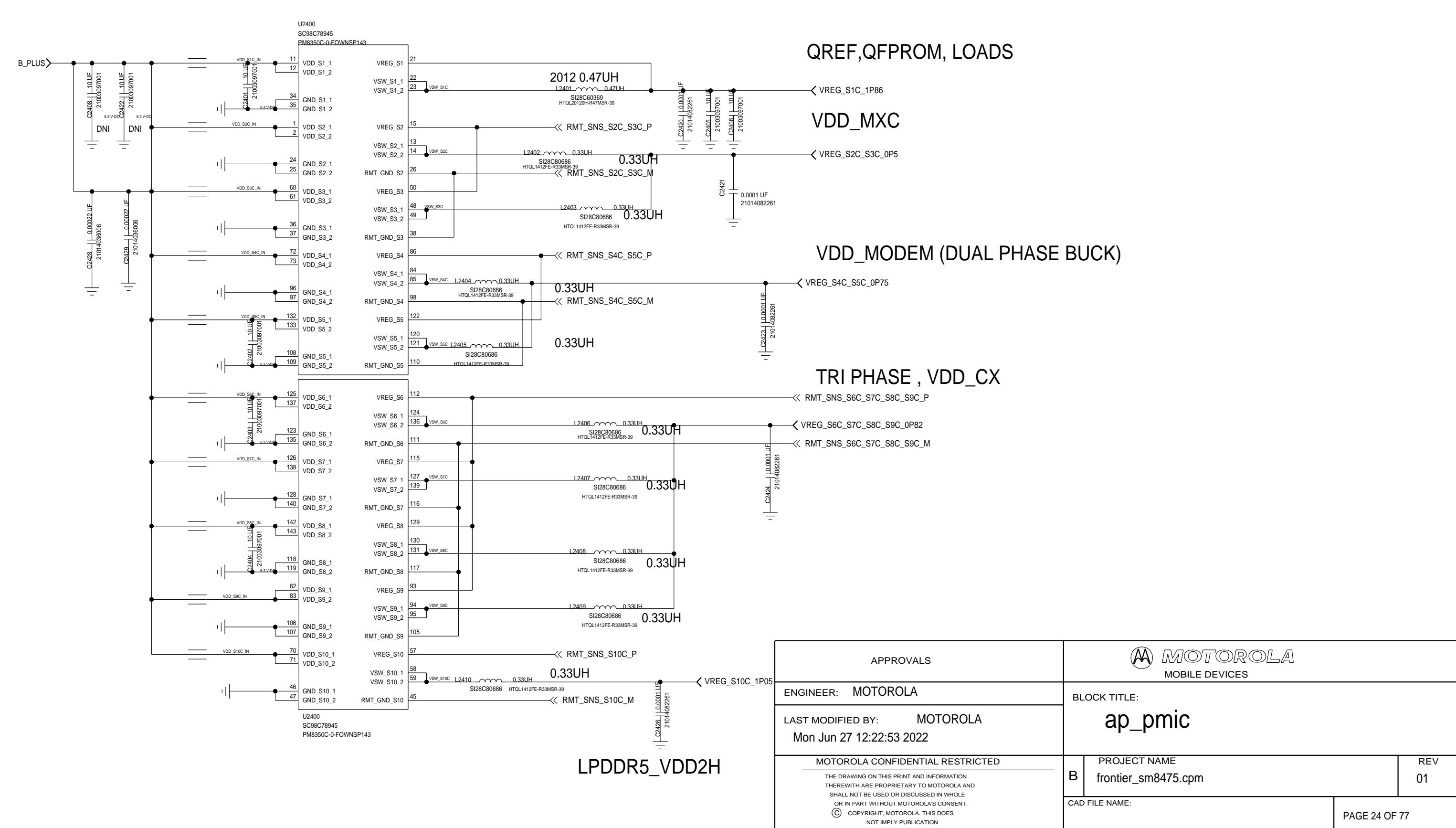
REFDES: 2400-2499



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: ap_pmic	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:53 2022			
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	REV
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CAD FILE NAME:			PAGE 23 OF 77

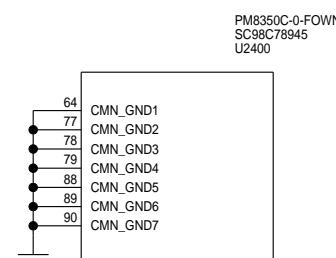
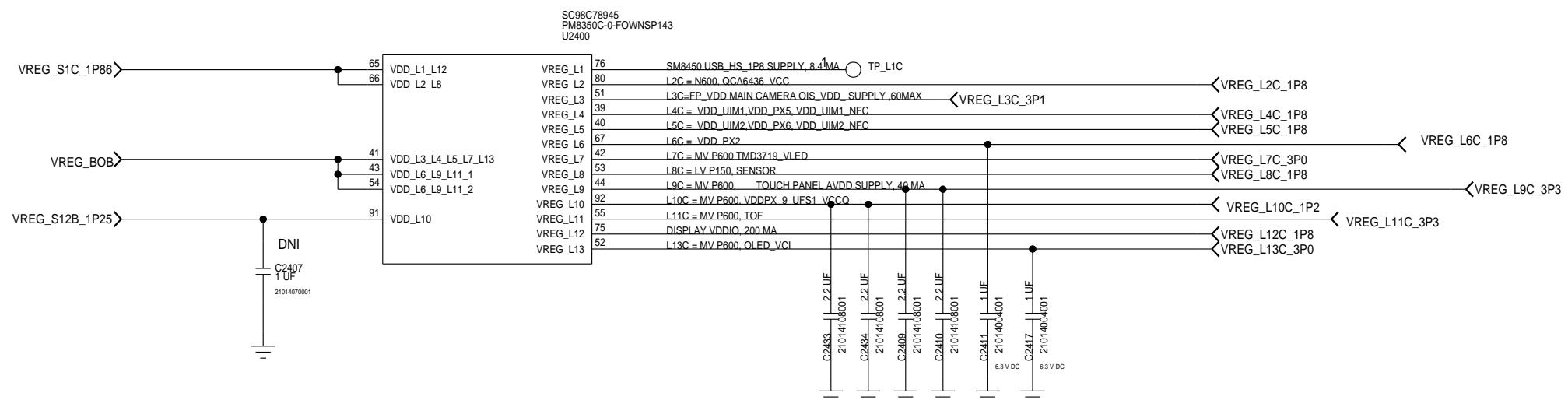
PM8350C: BUCKS

REFDES: 2400-2499



PM8350C: LDOS/GND

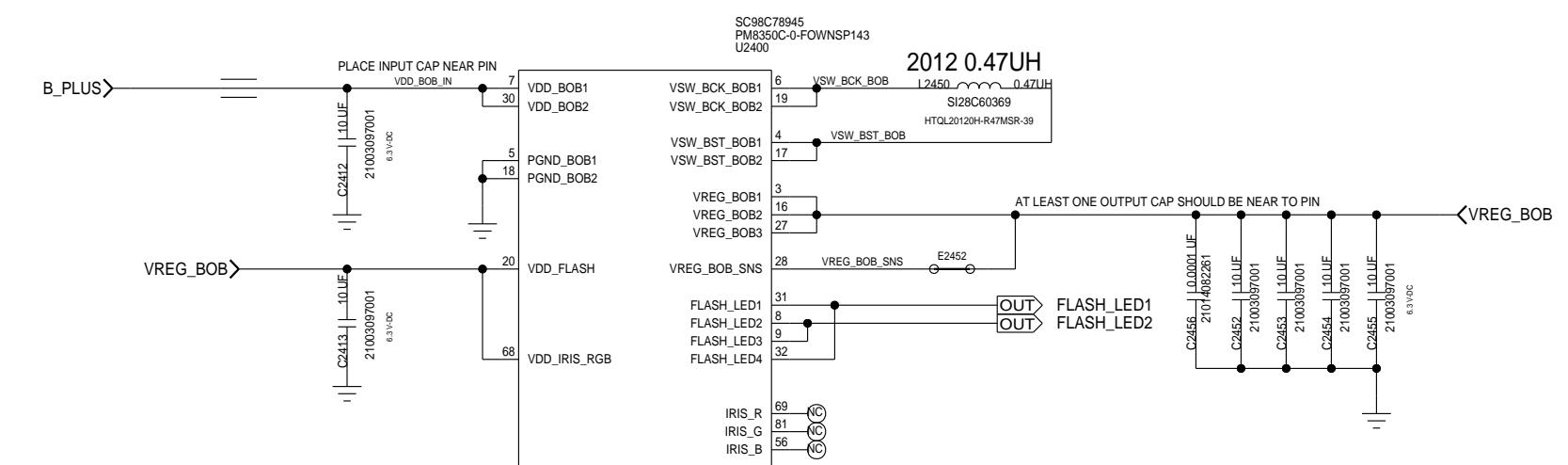
REFDES: 2400-2499



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: ap_pmic	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:52 2022			
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CAD FILE NAME:		REV 01	
		PAGE 25 OF 77	

PM8350C: BOB/FLASH

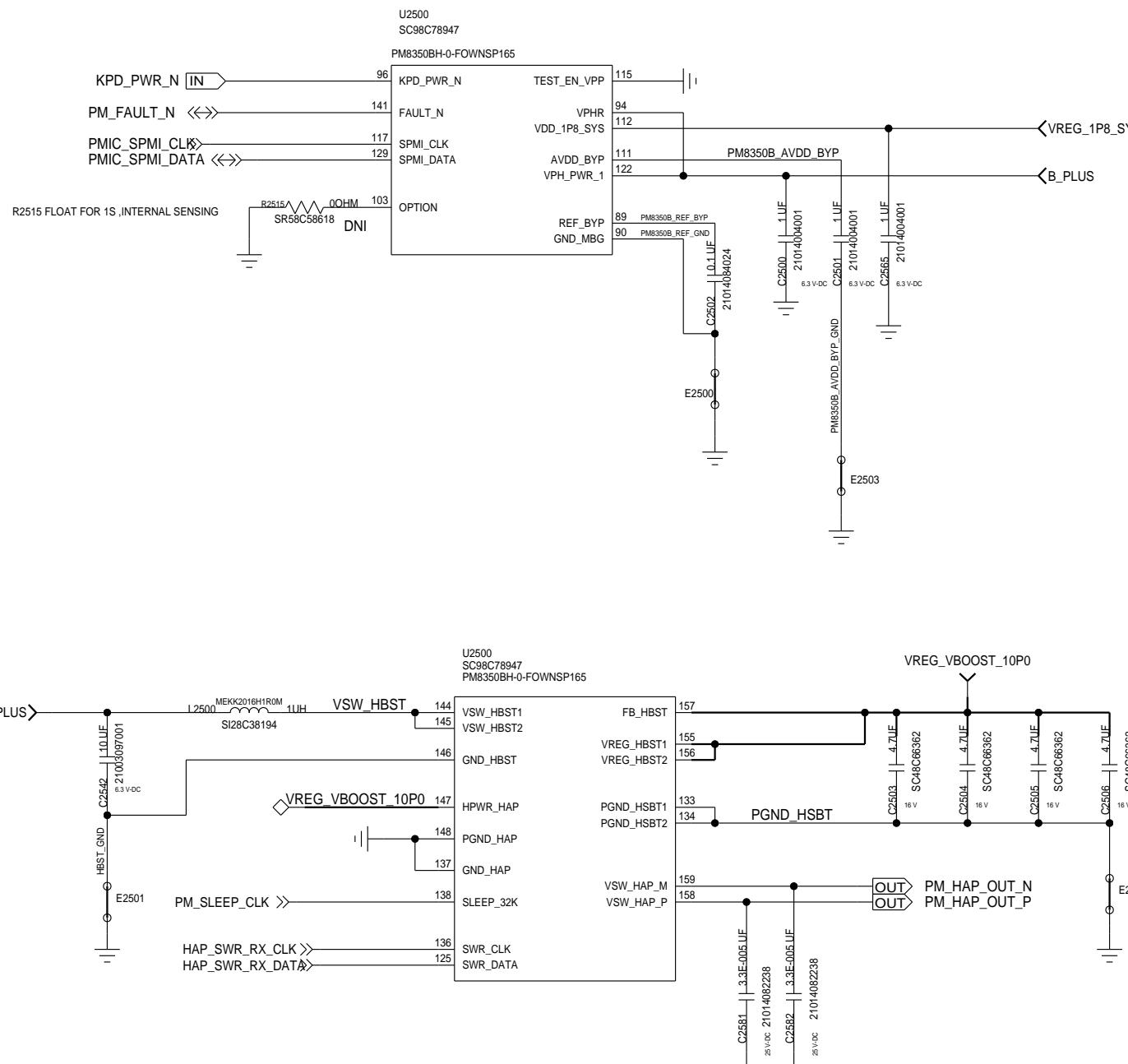
REFDES:2400-2499



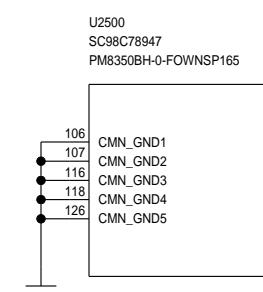
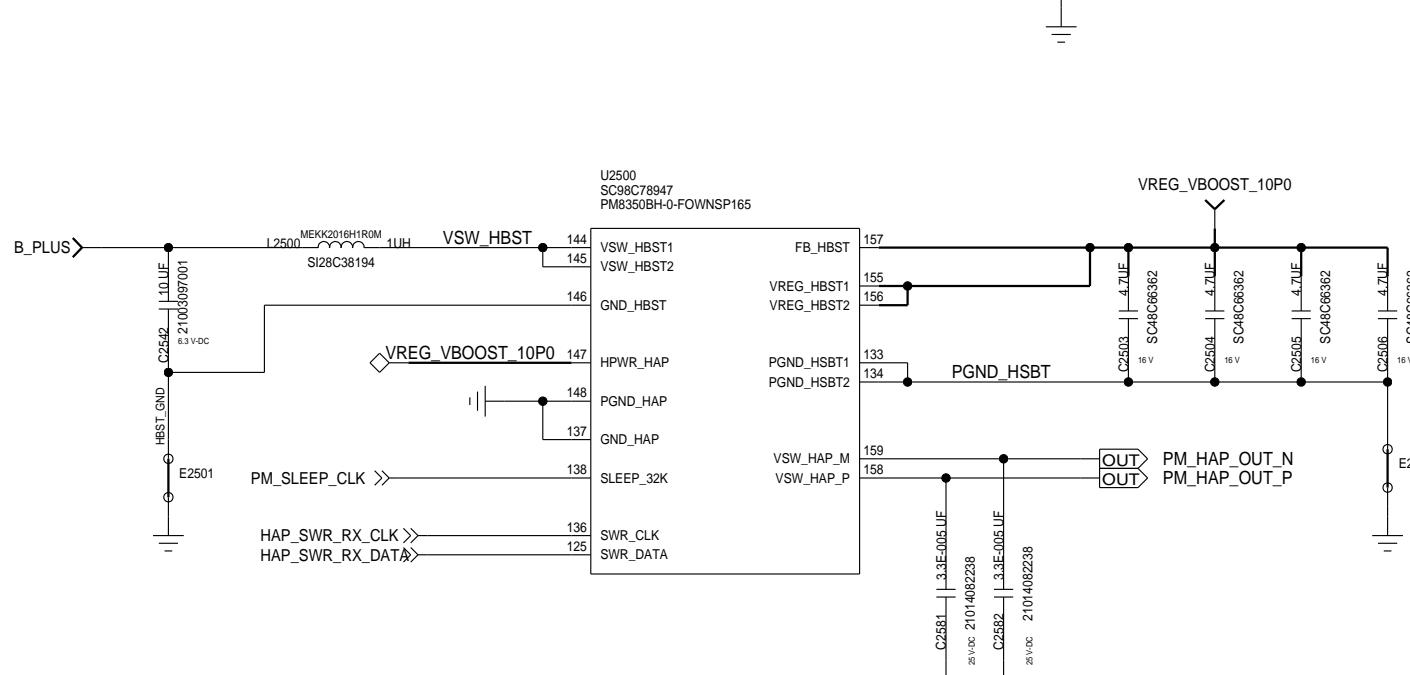
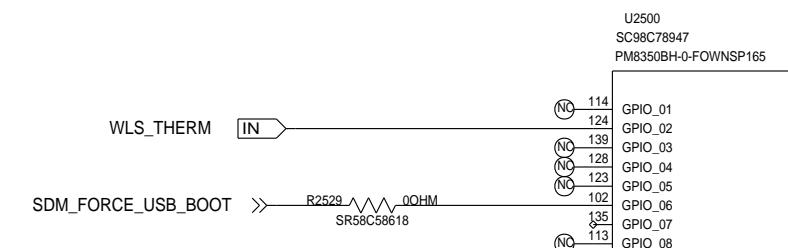
APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:53 2022		ap_pmic	
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CAD FILE NAME:			PAGE 26 OF 77

PM8350BH: CTRL/HAPTICS

REFDES: 2500-2599



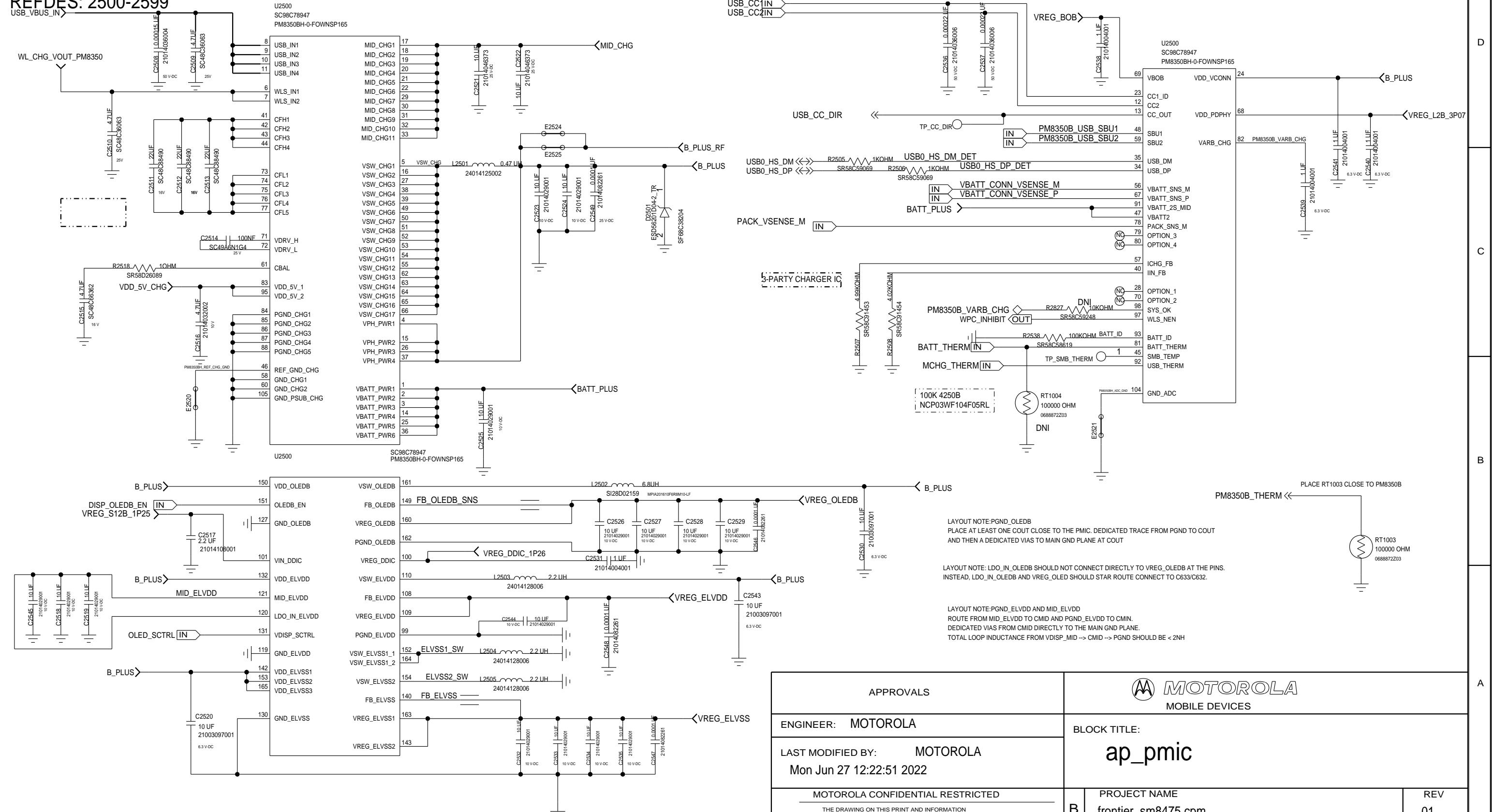
REV A: ADD 0OHM R3234 RESISTOR FROM PM8350B GPIO_06 TO
SM8350 GPIO_43 TO PROVIDE THE ABILITY TO DRIVE GPIO_06 HIGH
WHEN FACTORY MODE CABLE IS ATTACHED.
THIS WILL ALLOW THE HANDSET TO ENTER EDL MODE



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:51 2022		ap_pmic	
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	REV
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CAD FILE NAME:			PAGE 27 OF 77

PM8350BH: CHARGE/FG/OLEDB

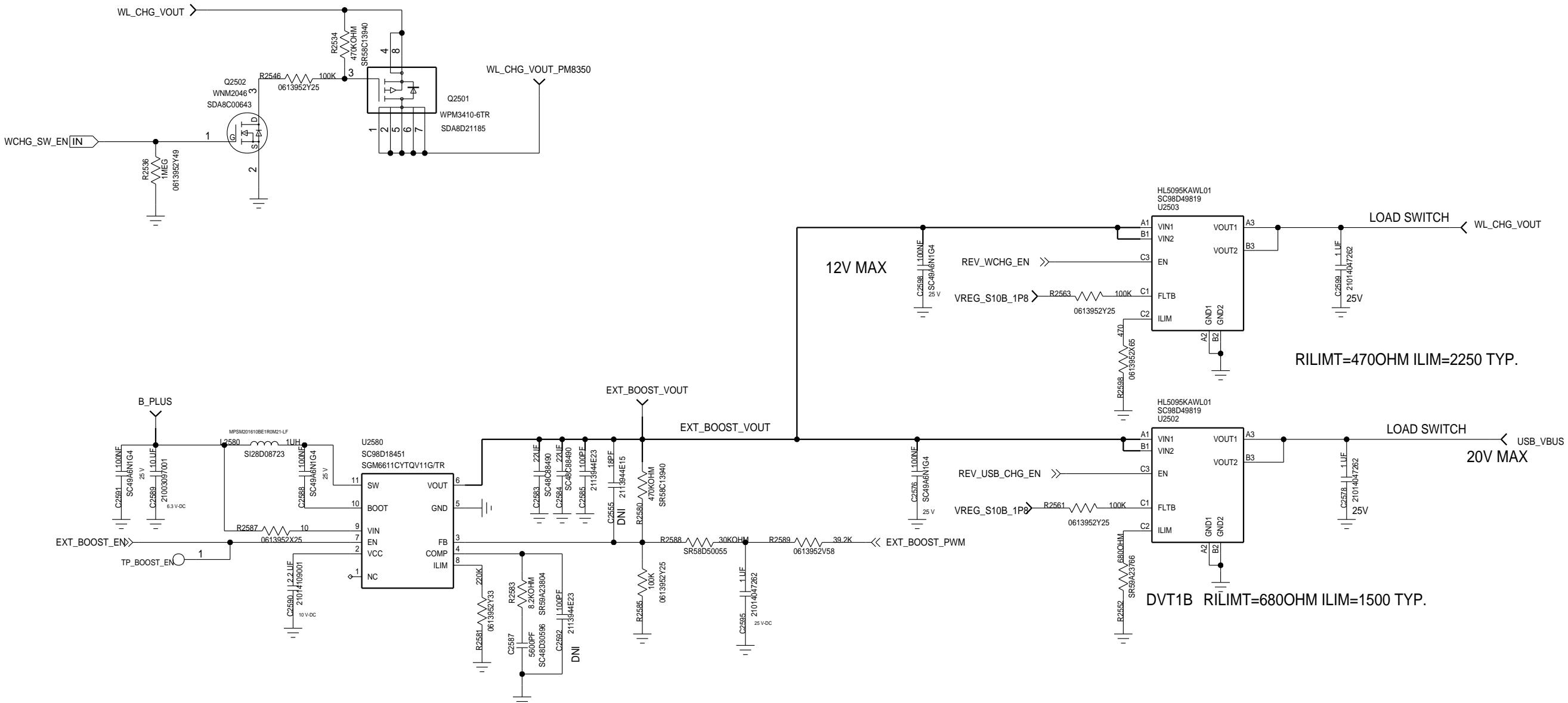
REFDES: 2500-2599



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PM8350BH: EXT BOOST

REFDES: 4900-4999



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: ap_pmic	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:52 2022			
MOTOROLA CONFIDENTIAL RESTRICTED	PROJECT NAME	REV	01
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B frontier_sm8475.cpm			
CAD FILE NAME:			

SMB1393: 68W CHARGING

REFDES: 2700-2799

APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Thu Jan 20 13:56:07 2022		ap_pmic	
MOTOROLA CONFIDENTIAL RESTRICTED		B	PROJECT NAME
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CAD FILE NAME:			PAGE 30 OF 77

AUDIO: QUALCOMM CODEC

REFDES: 4650-4699

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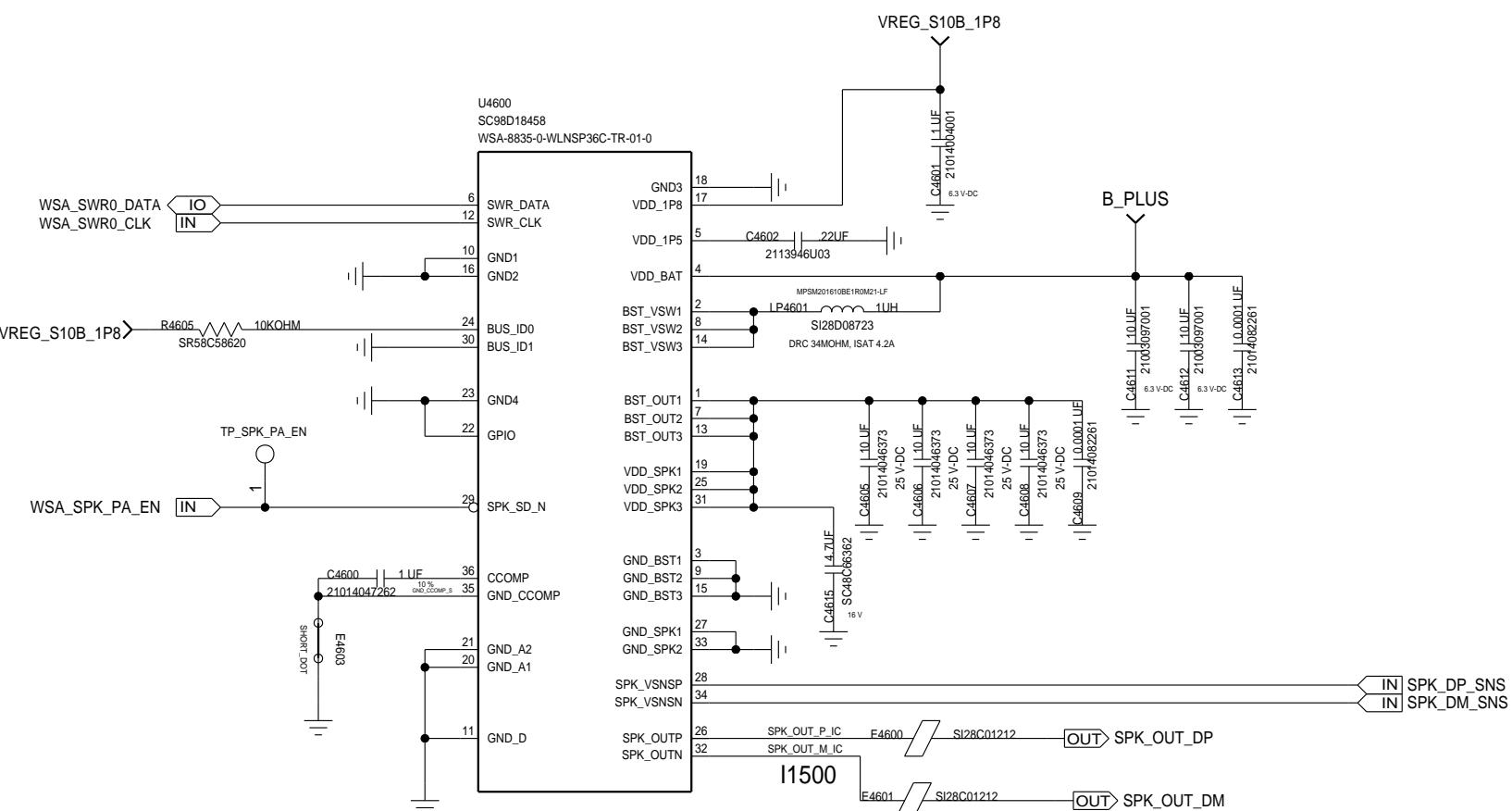
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LAST MODIFIED BY: MOTOROLA Thu Feb 10 10:52:58 2022		XMCS	
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	
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CAD FILE NAME:			PAGE 31 OF 77

AUDIO: QUALCOMM SMART BOOST

REFDES: 4600-4649

SPEAKER SMART PA REFDES: 4600-4699



AUDIO: AUDIO SWITCH

REFDES: 4650-4699

ENGINEER: MOTOROLA	 MOTOROLA	
DRAWN BY: MOTOROLA		
R&D CHK:	TITLE: <DESIGN_NAME> SHEET TITLE	
DOC CTRL CHK:		
MFG CTRL CHK:		
QA CTRL CHK:	REV: 01	DWG Number: PAGE 33 OF 77
LAST MODIFIED BY: MOTOROLA		
Thu Feb 10 10:53:10 2022		

D

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AUDIO: HAPTICS

REFDES: 4900-4999

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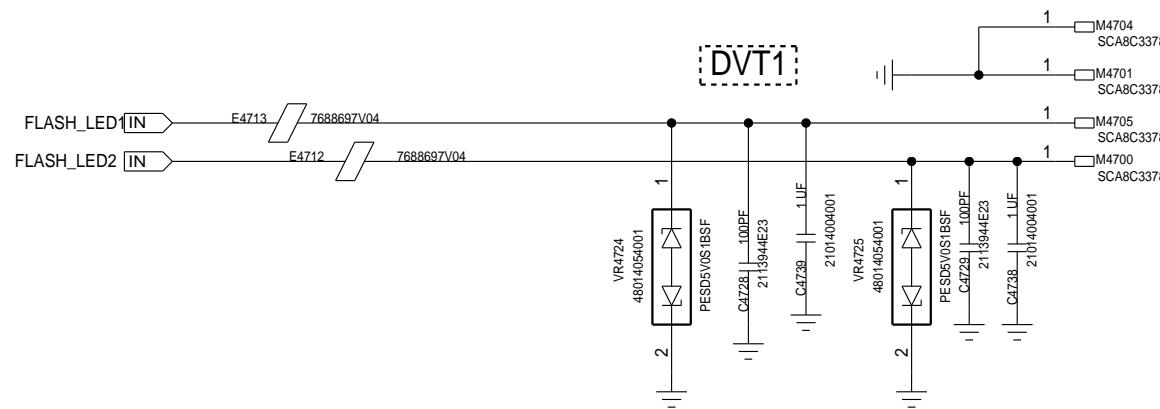
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CAD FILE NAME:			PAGE 34 OF 77

AUDIO: MICS/SPEAKERS

REFDES: 4700-4799

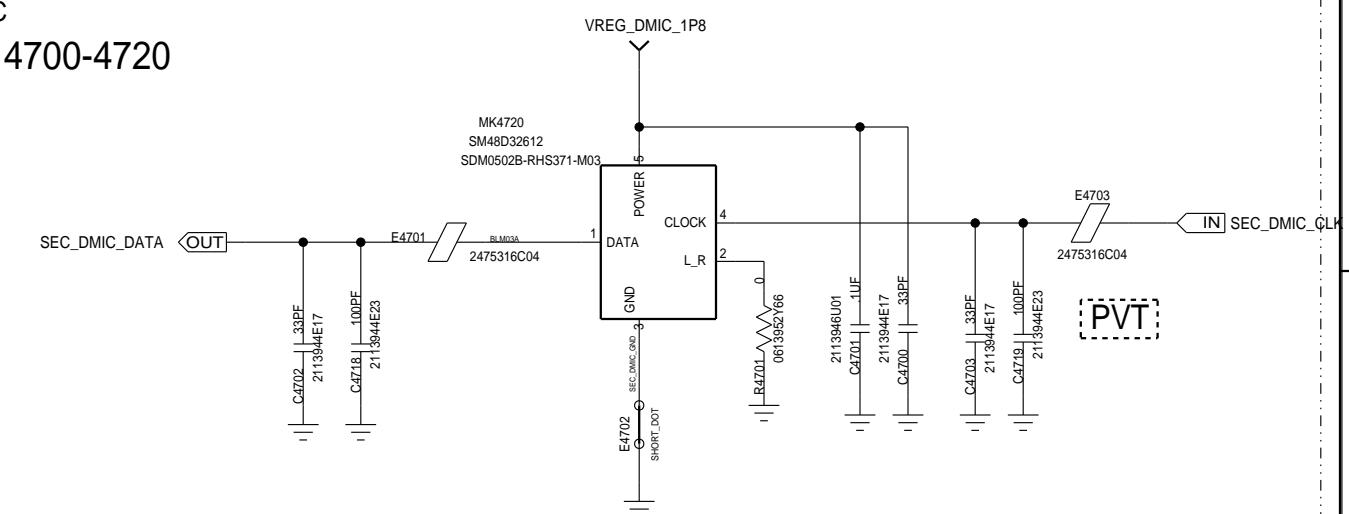
THIRD MIC

REFDES: 4721-4739



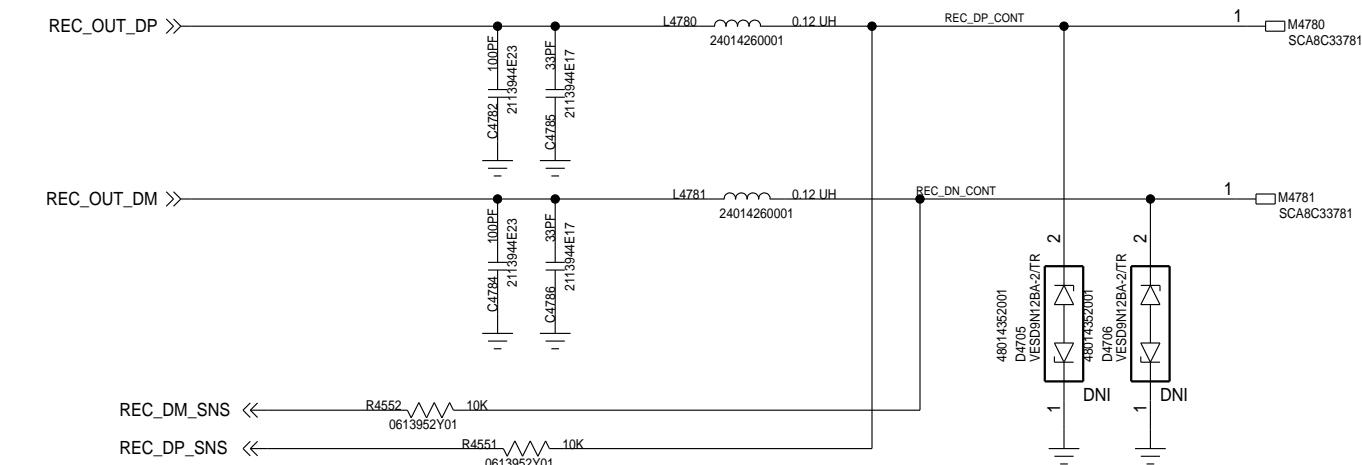
SECOND MIC

REFDES: 4700-4720



LOUDSPEAKER/EAR PIECE

REFDES: 4780-4799



APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

BLOCK TITLE:

LAST MODIFIED BY: MOTOROLA

XMCS

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PROJECT NAME
frontier_sm8475.cpm

REV
01

CAD FILE NAME:

PAGE 35 OF 77

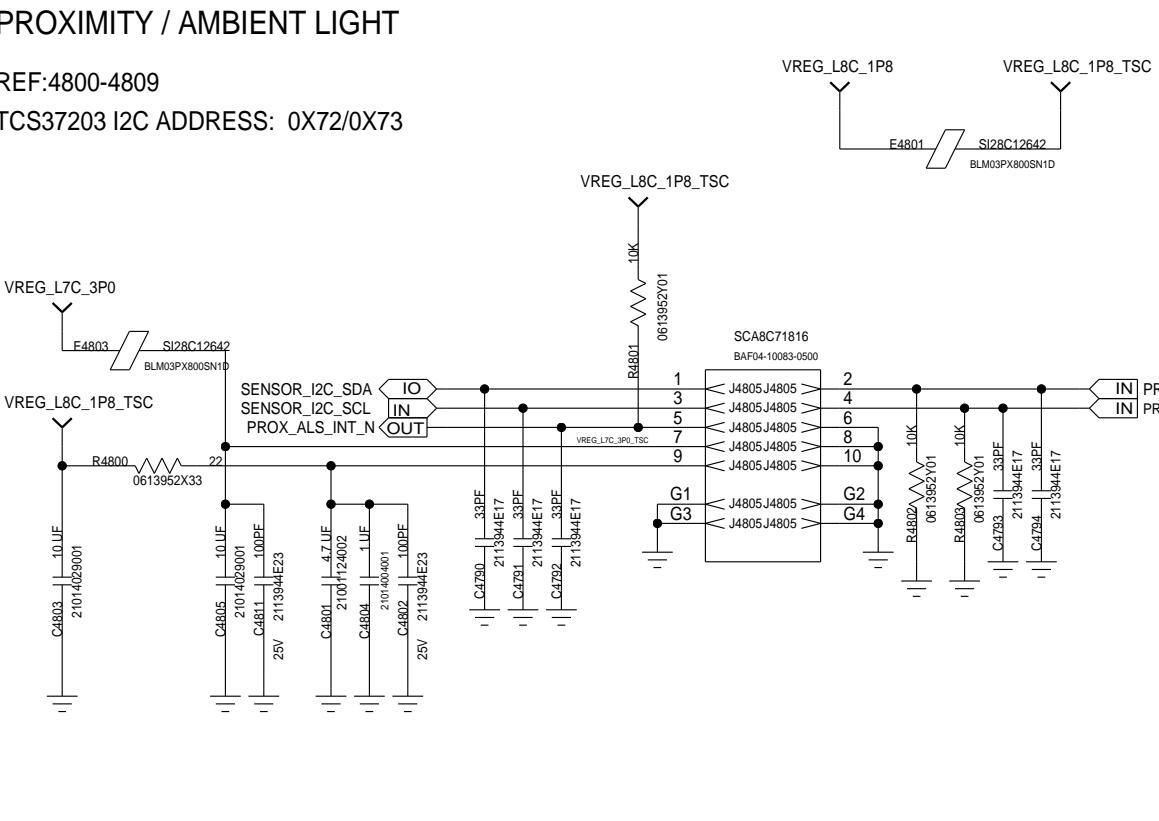
SENSOR: MOTIONS

REF: 4800-4999

PROXIMITY / AMBIENT LIGHT

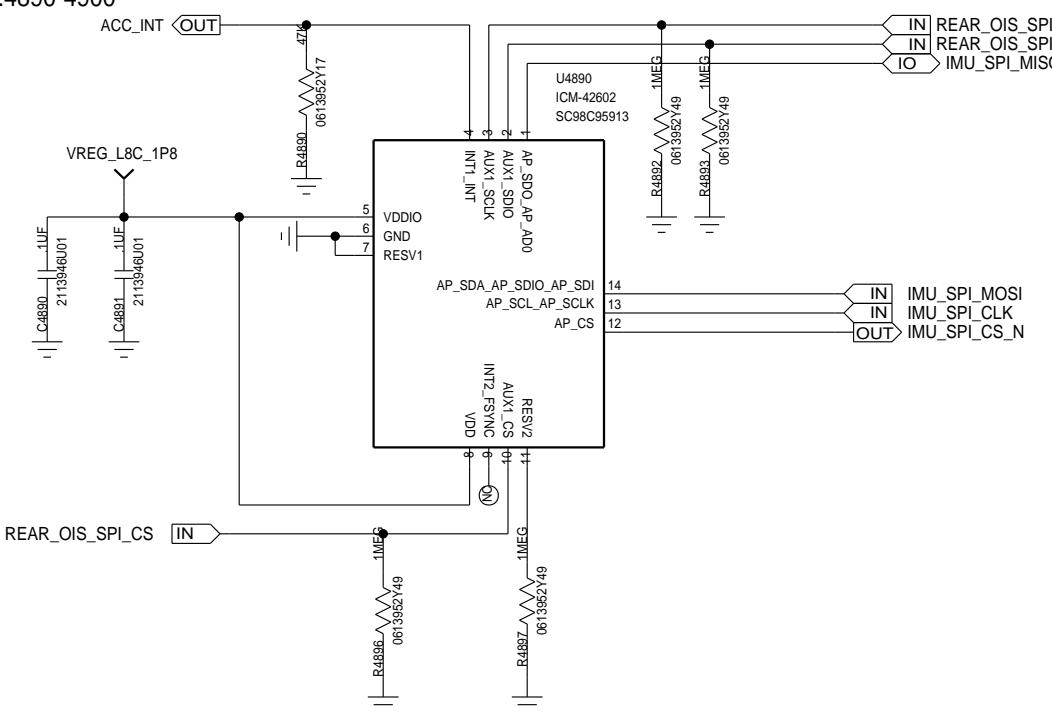
REF: 4800-4809

TCS37203 I2C ADDRESS: 0X72/0X73



ACCELEROMETER/GYROSCOPE

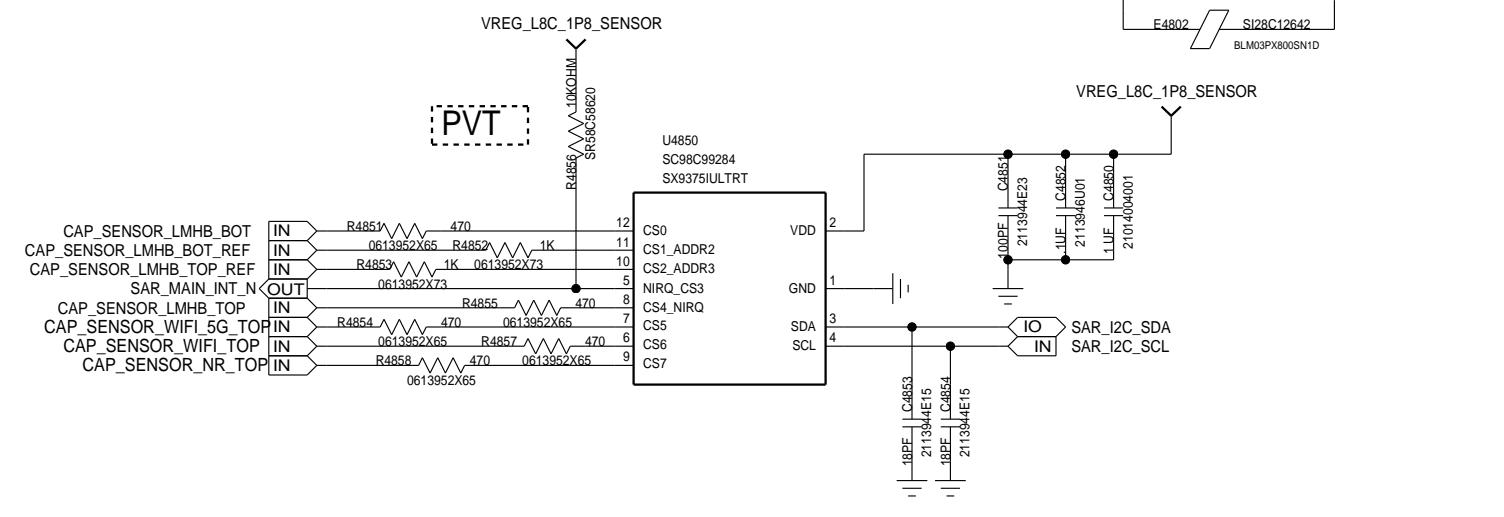
REF: 4890-4900



SAR SENSOR

REF: 4850-4869

SX9375 I2C ADDRESS: 0X58/0X59

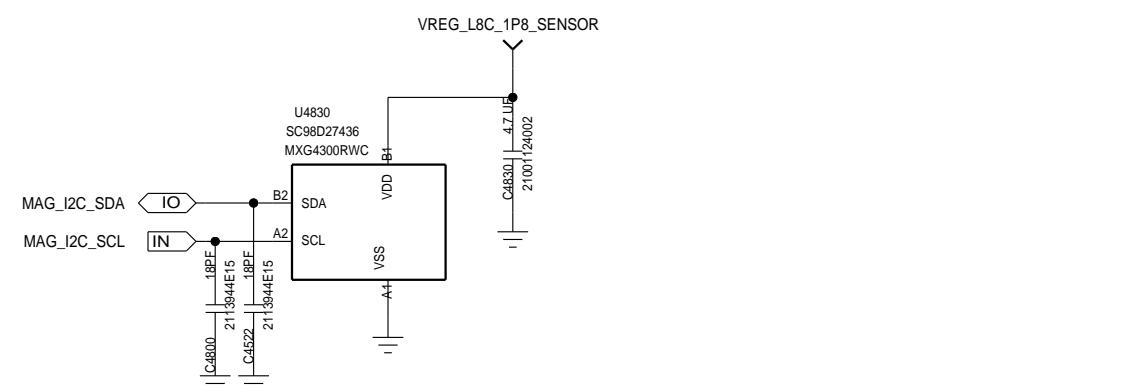


MAGNETOMETER

REF: 4830-4839

MXG4300RWC I2C ADDRESS: 0X18/0X19

QMC6308 I2C ADDRESS: 0X58/0X59



APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

LAST MODIFIED BY: MOTOROLA
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BLOCK TITLE:

XMCS

PROJECT NAME
B frontier_sm8475.cpm

REV
01

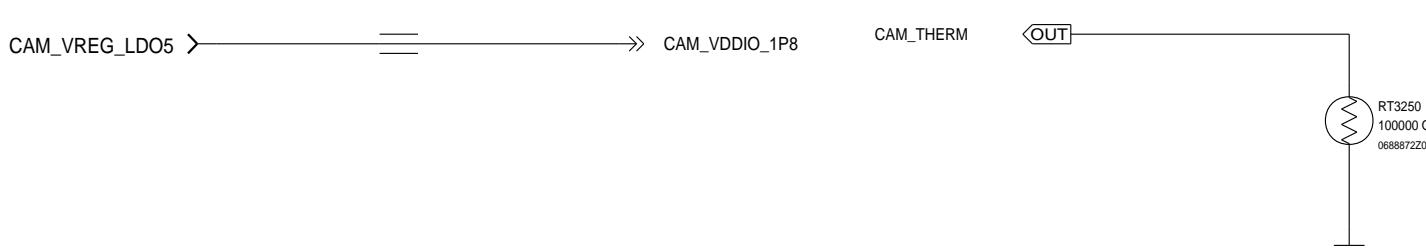
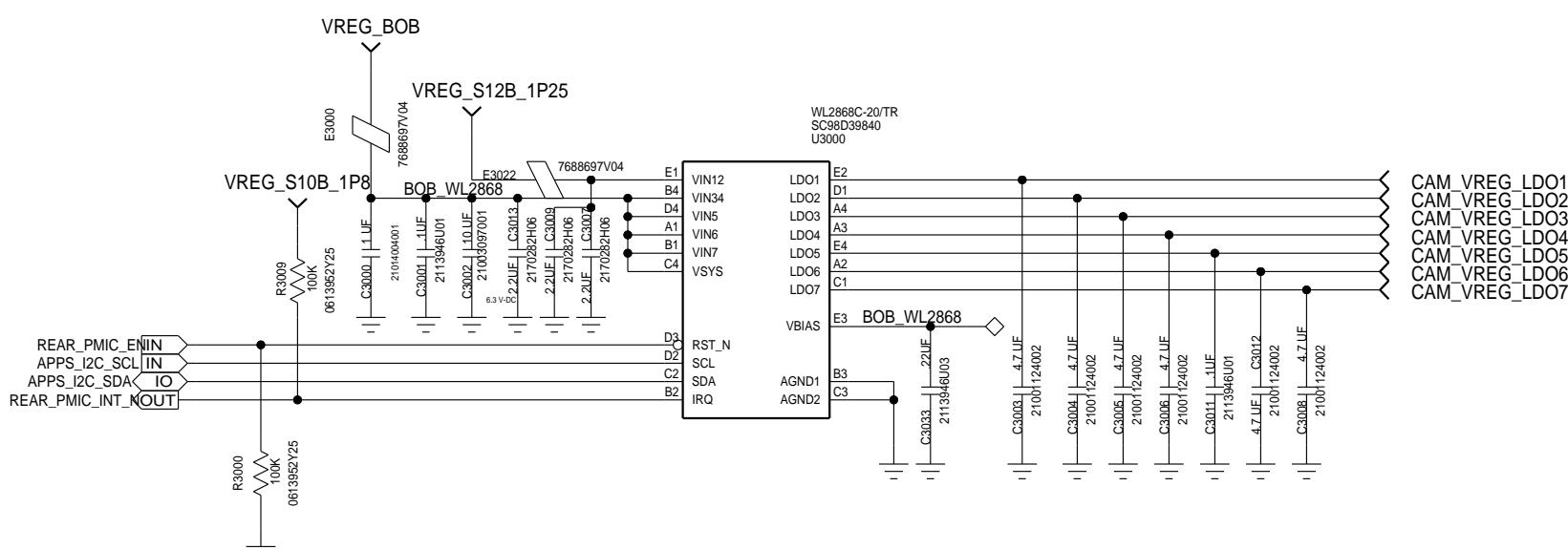
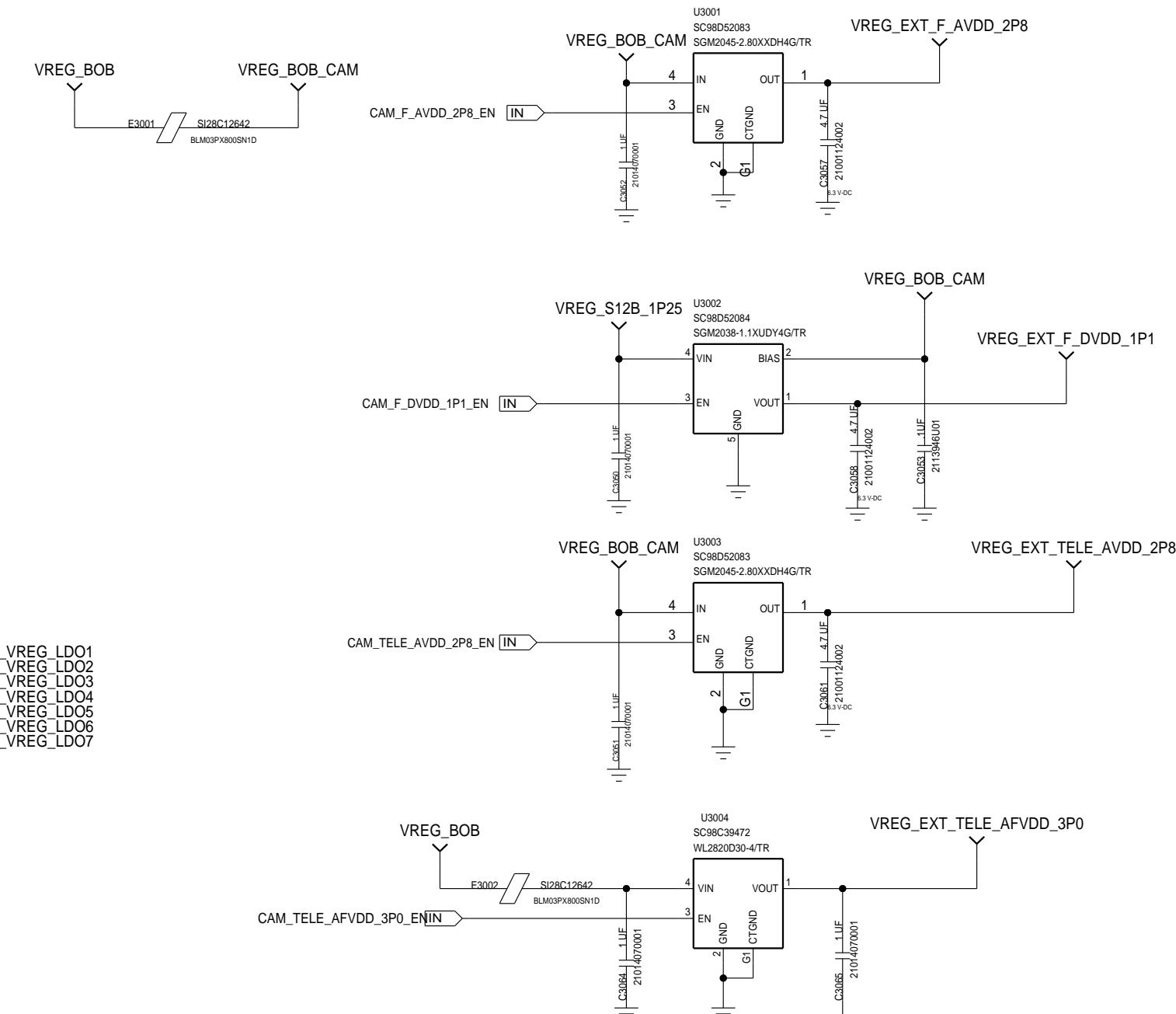
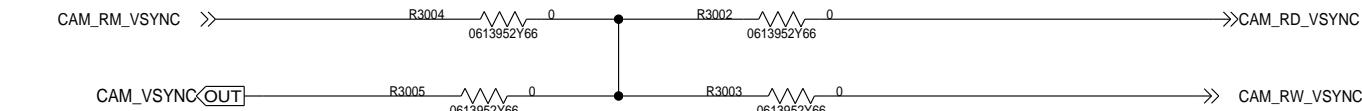
CAD FILE NAME:

PAGE 36 OF 77

IMG: POWER MANAGEMENT

REFDES: 3000-3099

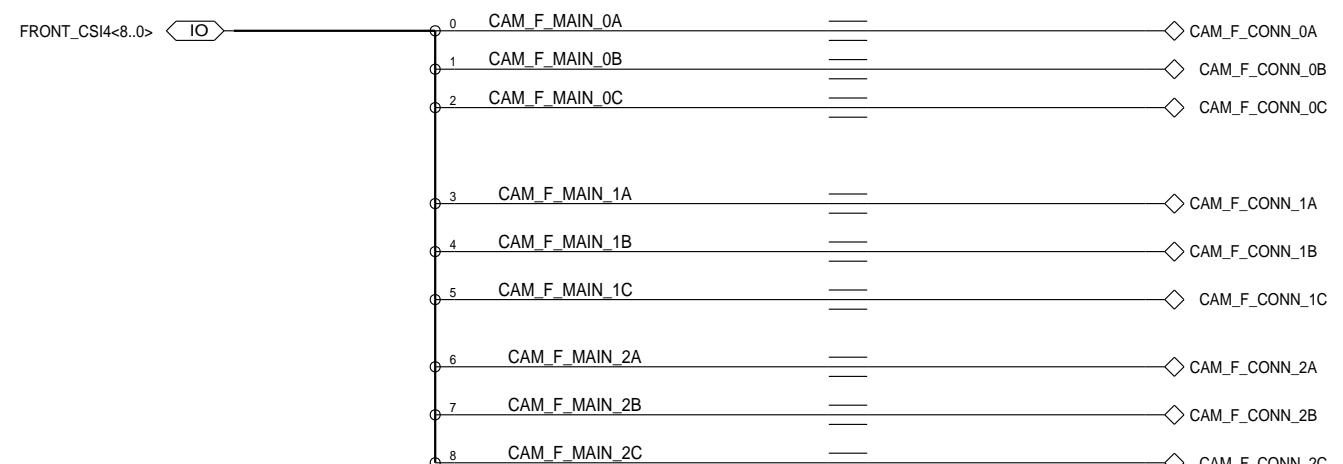
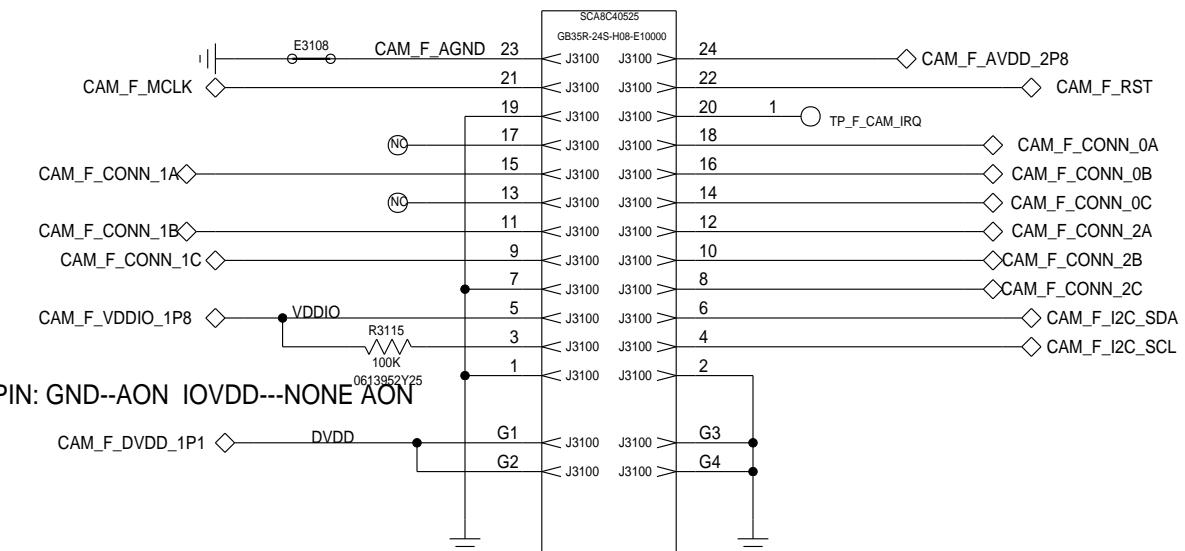
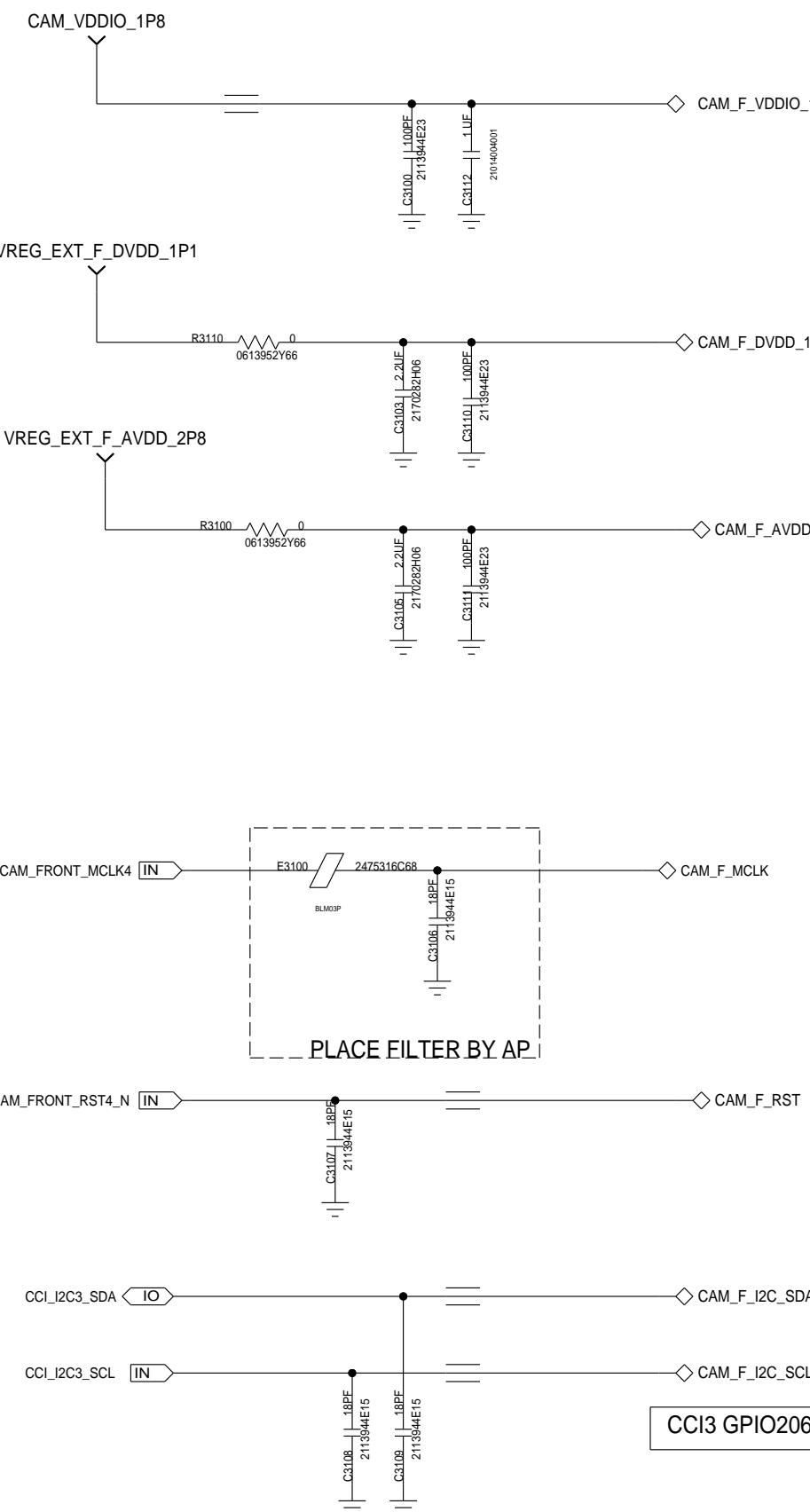
SLG5BM43670 I2C ADDRESS: 0xEA/0xEE



APPROVALS	 MOTOROLA MOBILE DEVICES		
ENGINEER: MOTOROLA	BLOCK TITLE: multimedia		
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:59 2022			
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	CAD FILE NAME:		PAGE 37 OF 77

IMG: FRONT 60M CONNECTOR

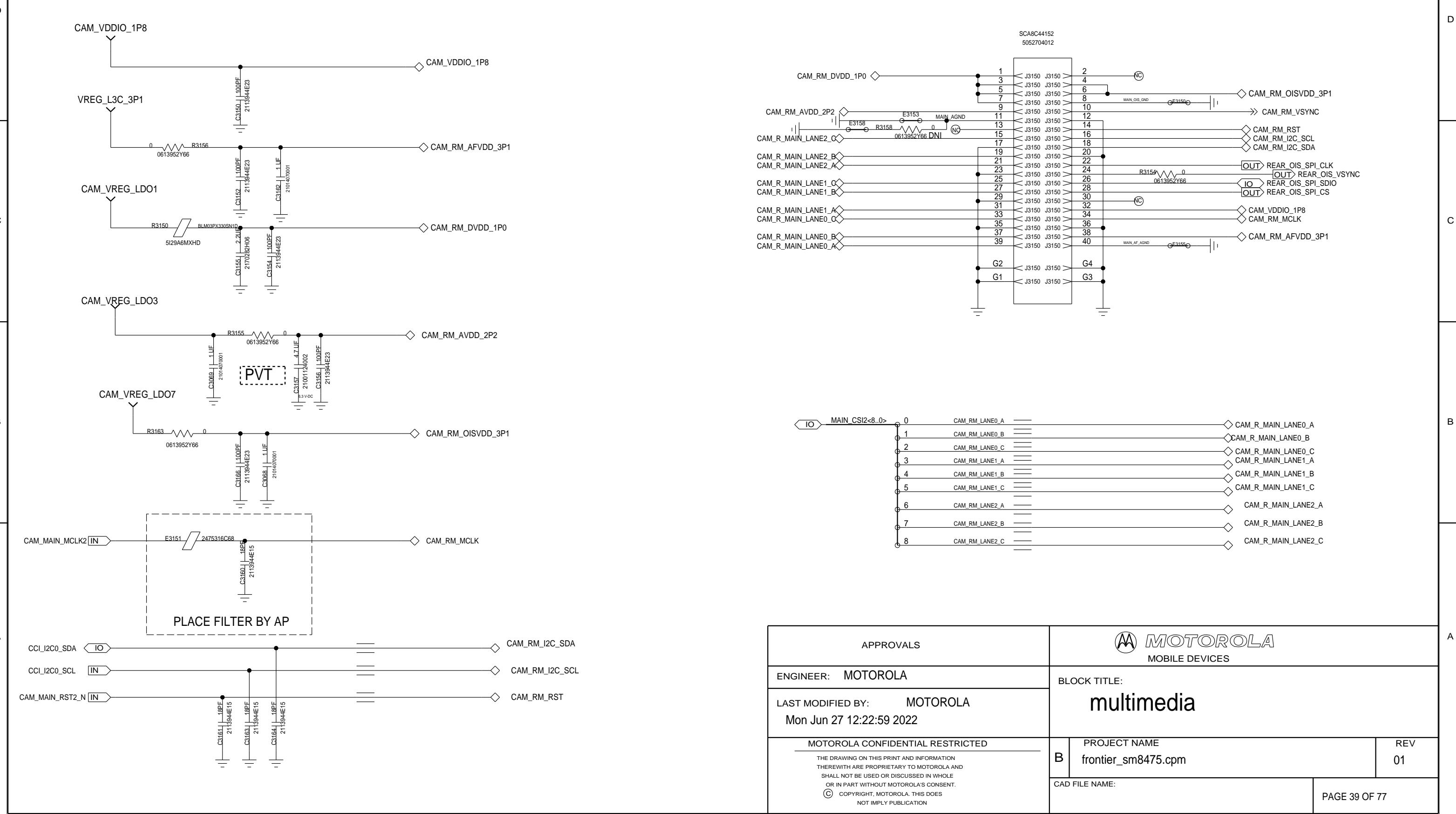
REFDES: 3150-3199



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE:	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:58 2022		multimedia	
B	PROJECT NAME frontier_sm8475.cpm	REV 01	
CAD FILE NAME:			PAGE 38 OF 77
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IMG: REAR MAIN CONNECTOR

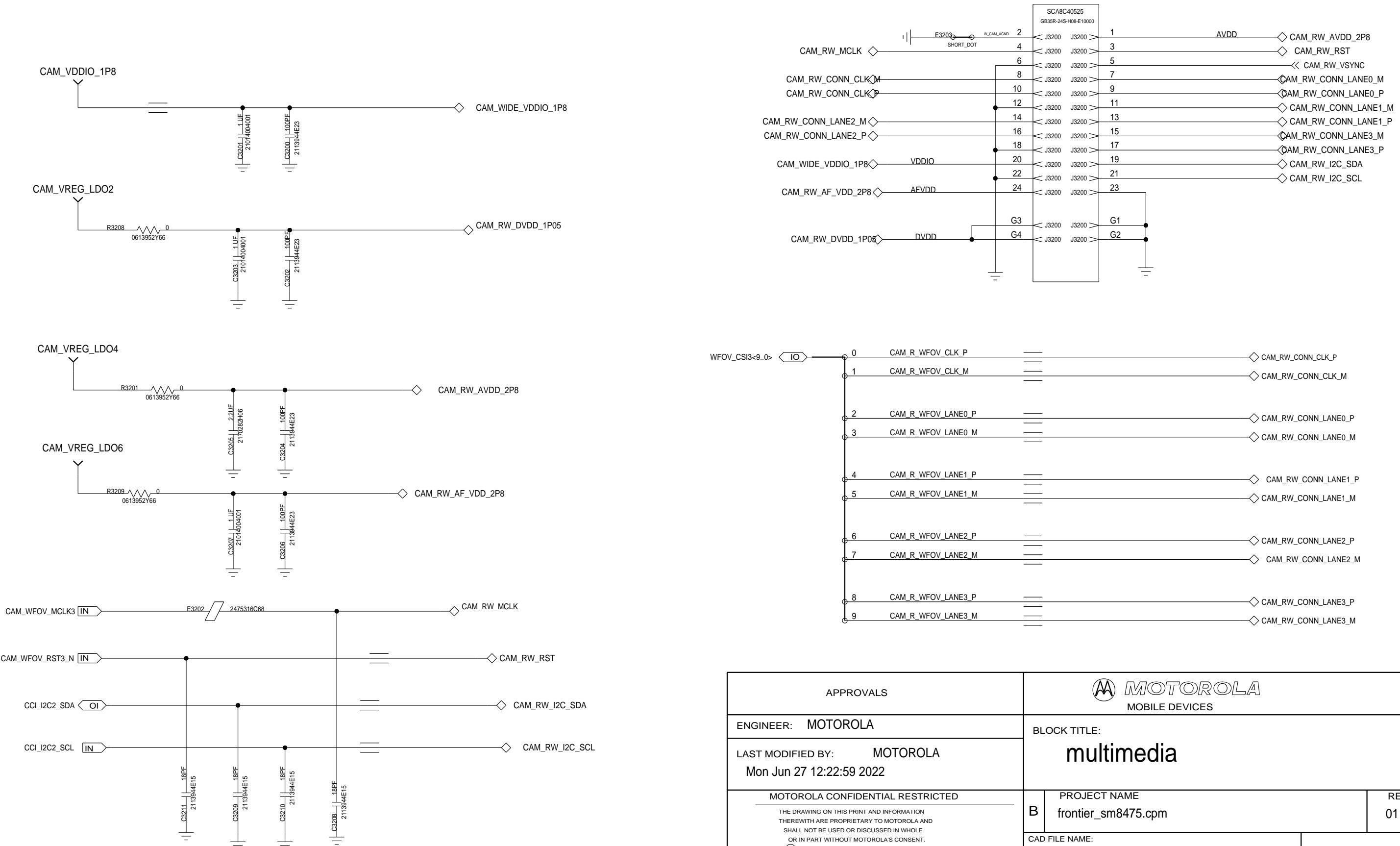
REFDES: 3150-3199



IMG: REAR WIDE 50M CONNECTOR

REFDES: 3200-3249

BERLIN FRONT PIN MAP L32BF00



APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

LAST MODIFIED BY: MOTOROLA

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BLOCK TITLE:

multimedia

PROJECT NAME
frontier_sm8475.cpm

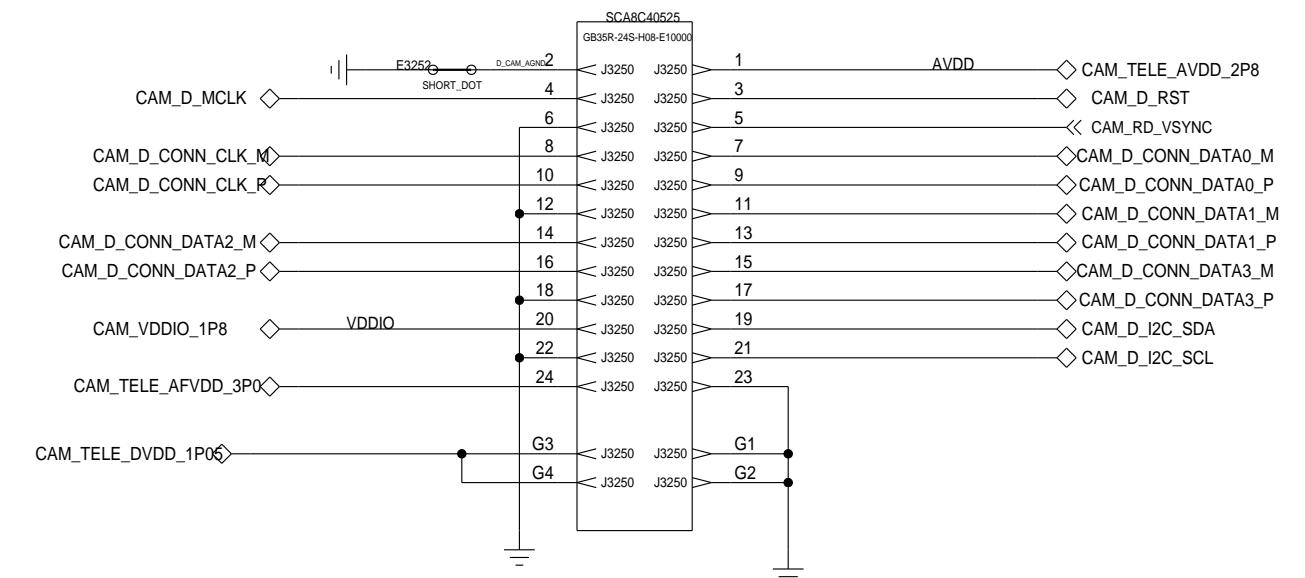
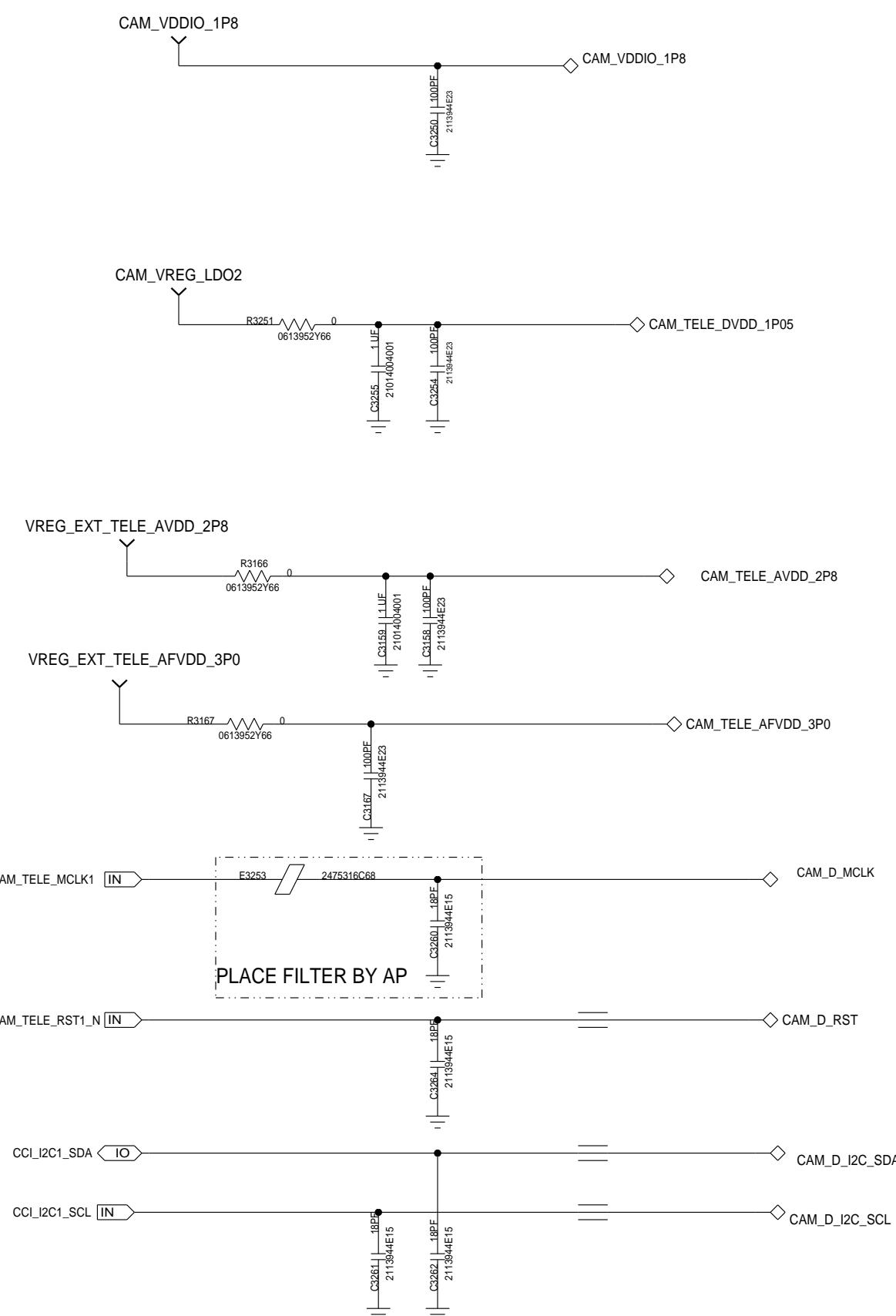
REV
01

CAD FILE NAME:

PAGE 40 OF 77

IMG: REAR TELE 12M CONNECTOR

REFDES: 3250-3299



APPROVALS		MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: multimedia	
LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:59 2022			
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CAD FILE NAME:		REV 01	
PAGE 41 OF 77			

IMG: CAMERA FLASH

REFDES: 3300-3349

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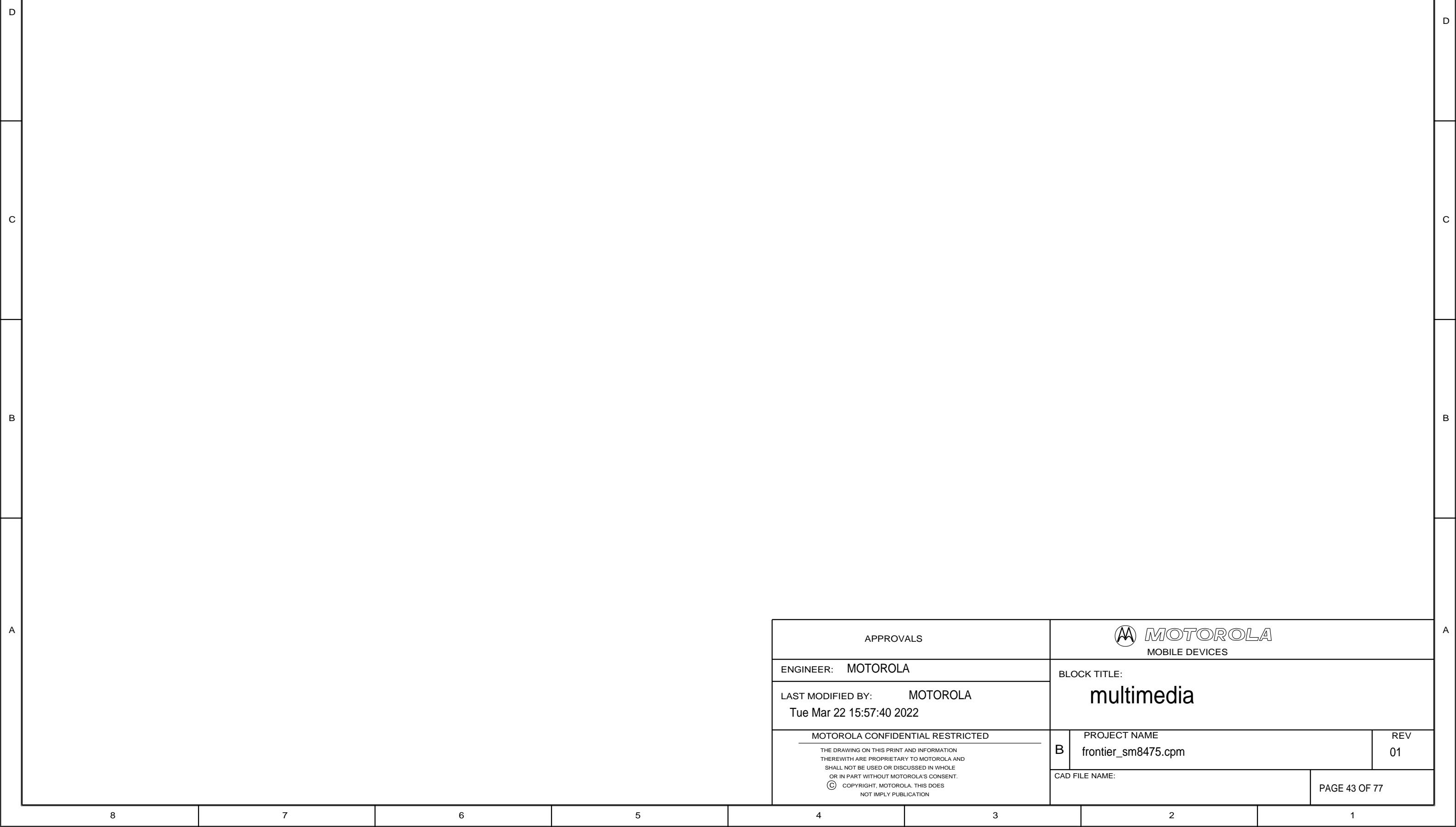
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APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: multimedia	
LAST MODIFIED BY: MOTOROLA Fri Feb 05 17:15:34 2021			
MOTOROLA CONFIDENTIAL RESTRICTED		B	PROJECT NAME
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CAD FILE NAME:			PAGE 42 OF 77

IMG: REAR TELE2 CONNECTOR

REFDES: 3350-3499

PIN MAP BASED ON 6/30 4X



APPROVALS		 MOTOROLA MOBILE DEVICES	
ENGINEER: MOTOROLA		BLOCK TITLE: multimedia	
LAST MODIFIED BY: MOTOROLA Tue Mar 22 15:57:40 2022			
MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	
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CAD FILE NAME:			PAGE 43 OF 77

DISP: OLED CONNECTOR

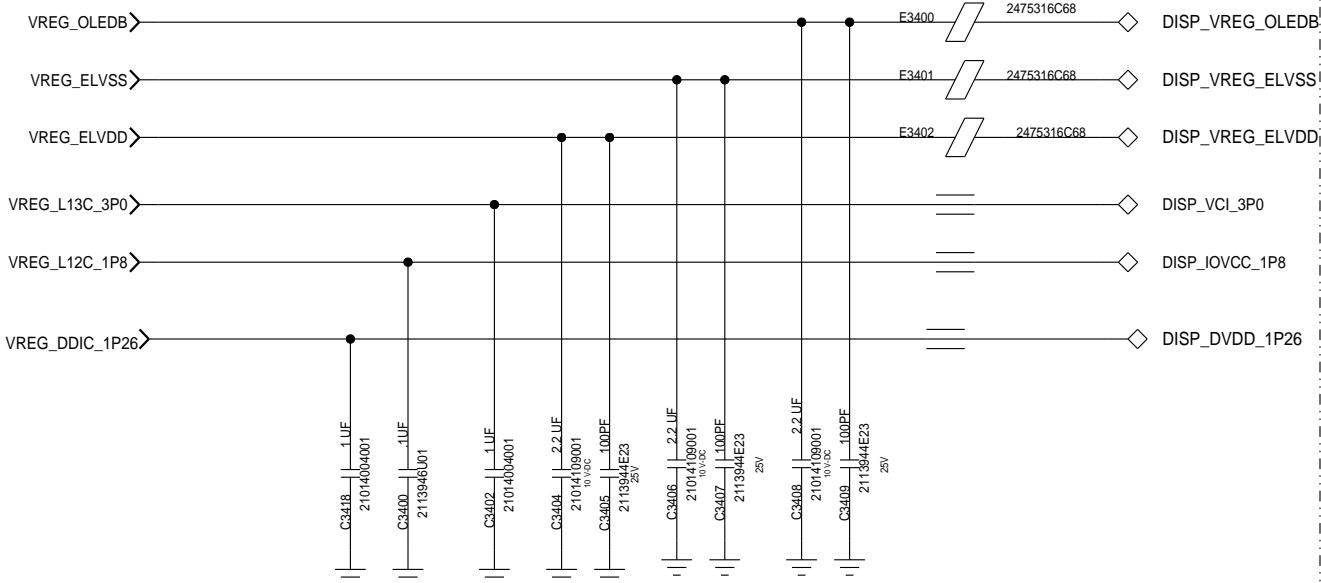
REFDES: 3400-3499

BERLIN SLIM PIN MAP

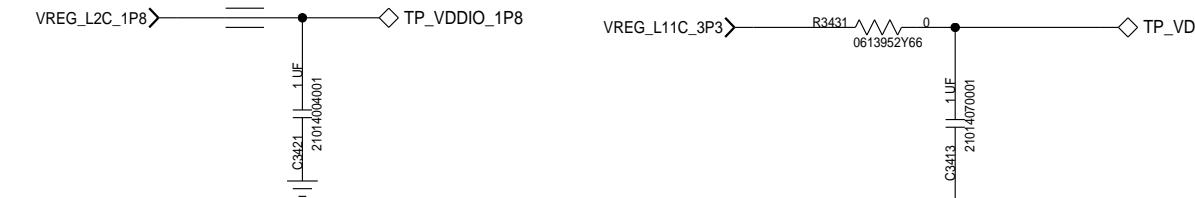
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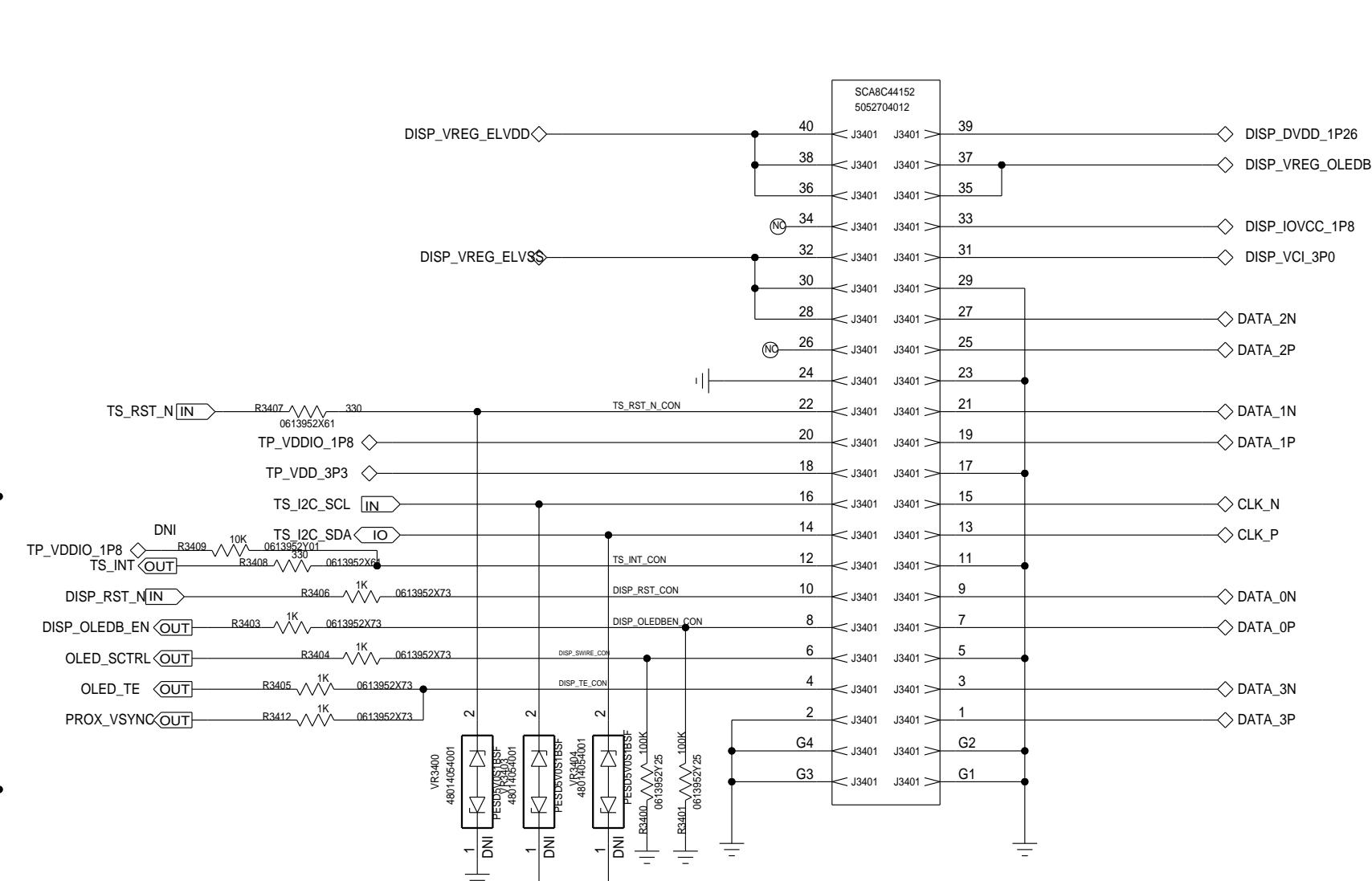
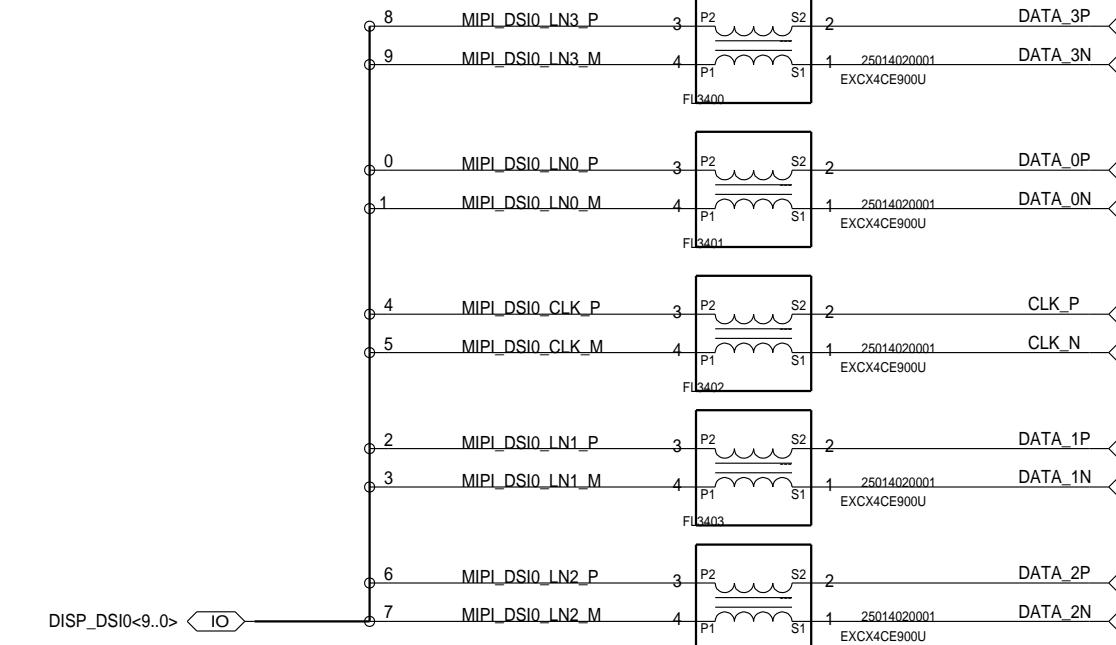
POWER_SUPPLY FOR OLED PLACE CLOSE TO CONNECTOR



POWER_SUPPLY FOR TOUCH SCREEN



COMMON FILTER



APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

LAST MODIFIED BY: MOTOROLA
Mon Jun 27 12:22:59 2022

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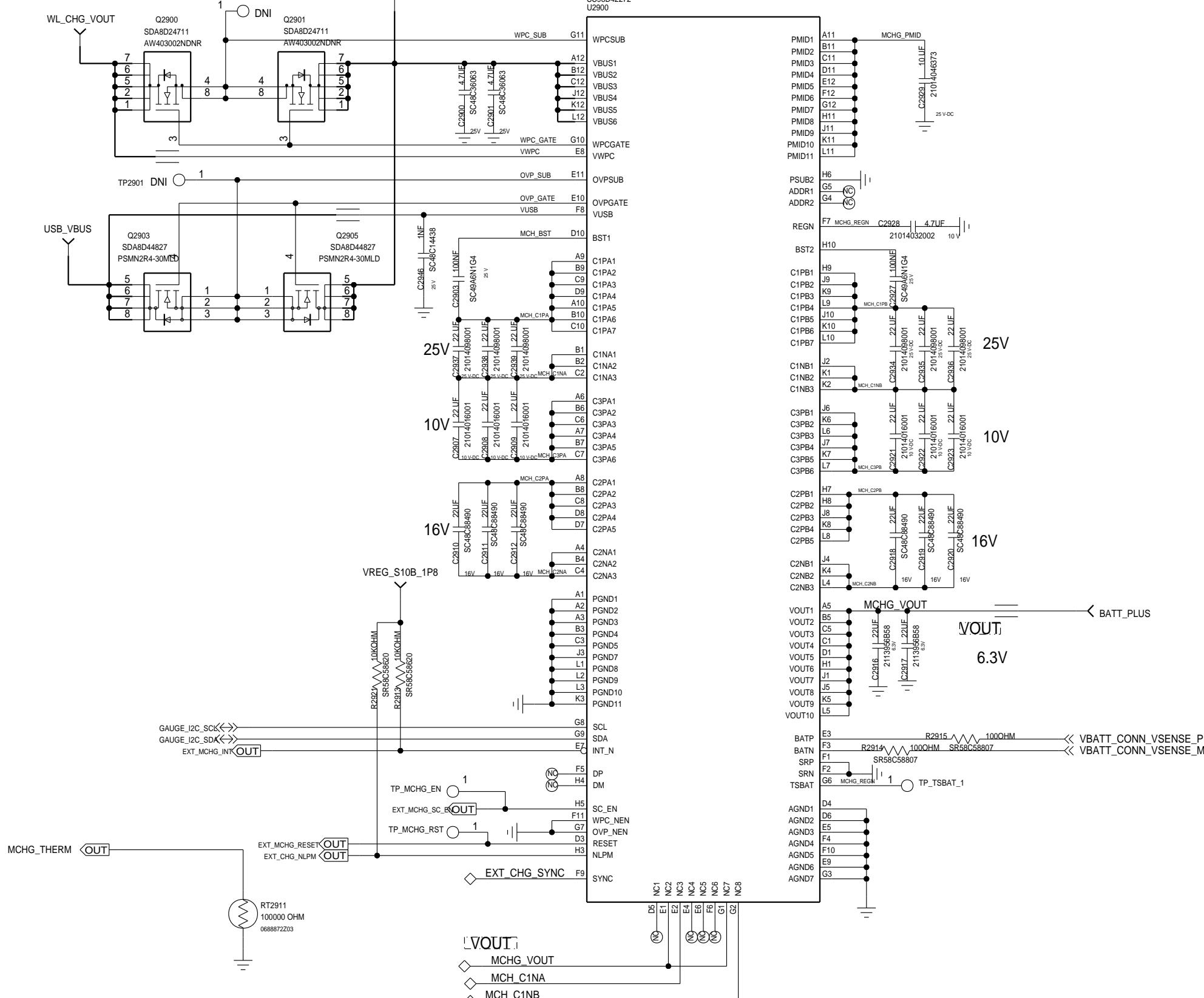
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PROJECT NAME
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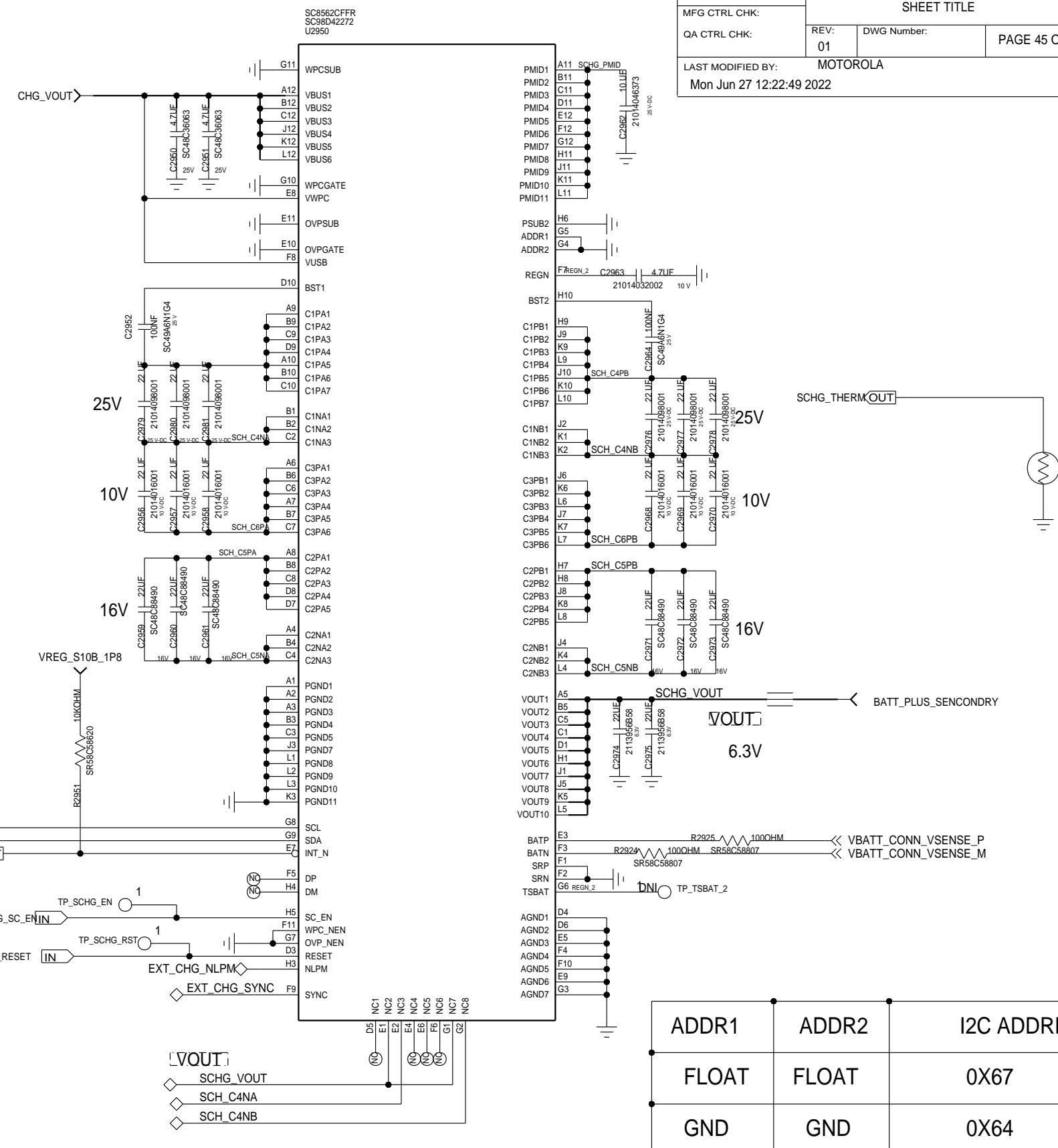
PAGE 44 OF 77

PWR: SOUTHCHIP 120W CHARGE

REFDES:2900-2949



REFDES:2950-2999



ENGINEER: MOTOROLA	MOTOROLA	
DRAWN BY: MOTOROLA	TITLE: <DESIGN_NAME>	
R&D CHK:	SHEET TITLE	
DOC CTRL CHK:	SC98042272	
MFG CTRL CHK:	SC8562CFRR U2950	
QA CTRL CHK:	REV: 01	DWG Number: PAGE 45 OF 77

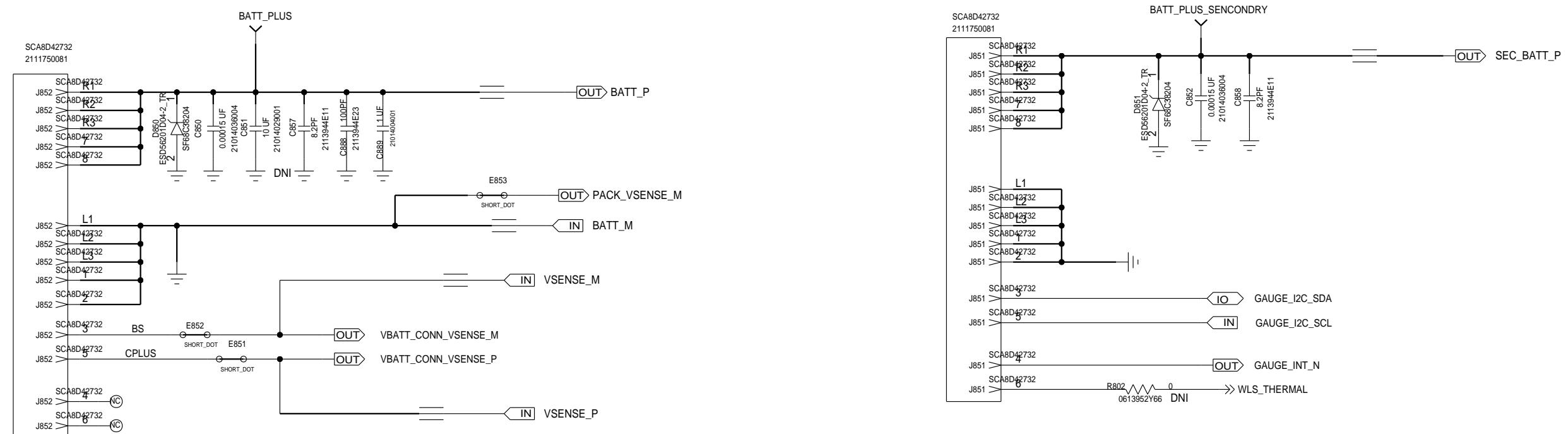
LAST MODIFIED BY: MOTOROLA

Mon Jun 27 12:22:49 2022

Size:
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PWR: BATT

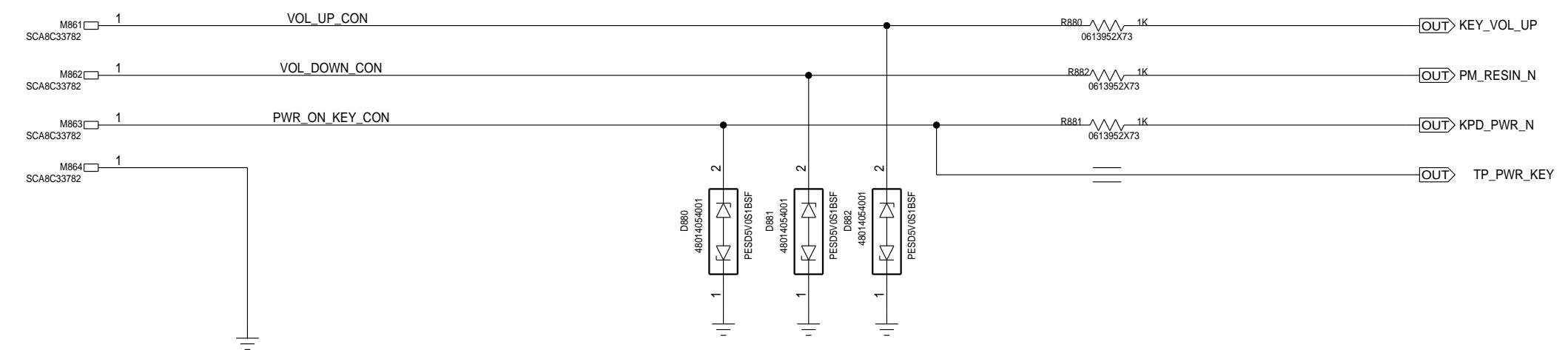
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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:48 2022		power	
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PWR: SIDEKEYS

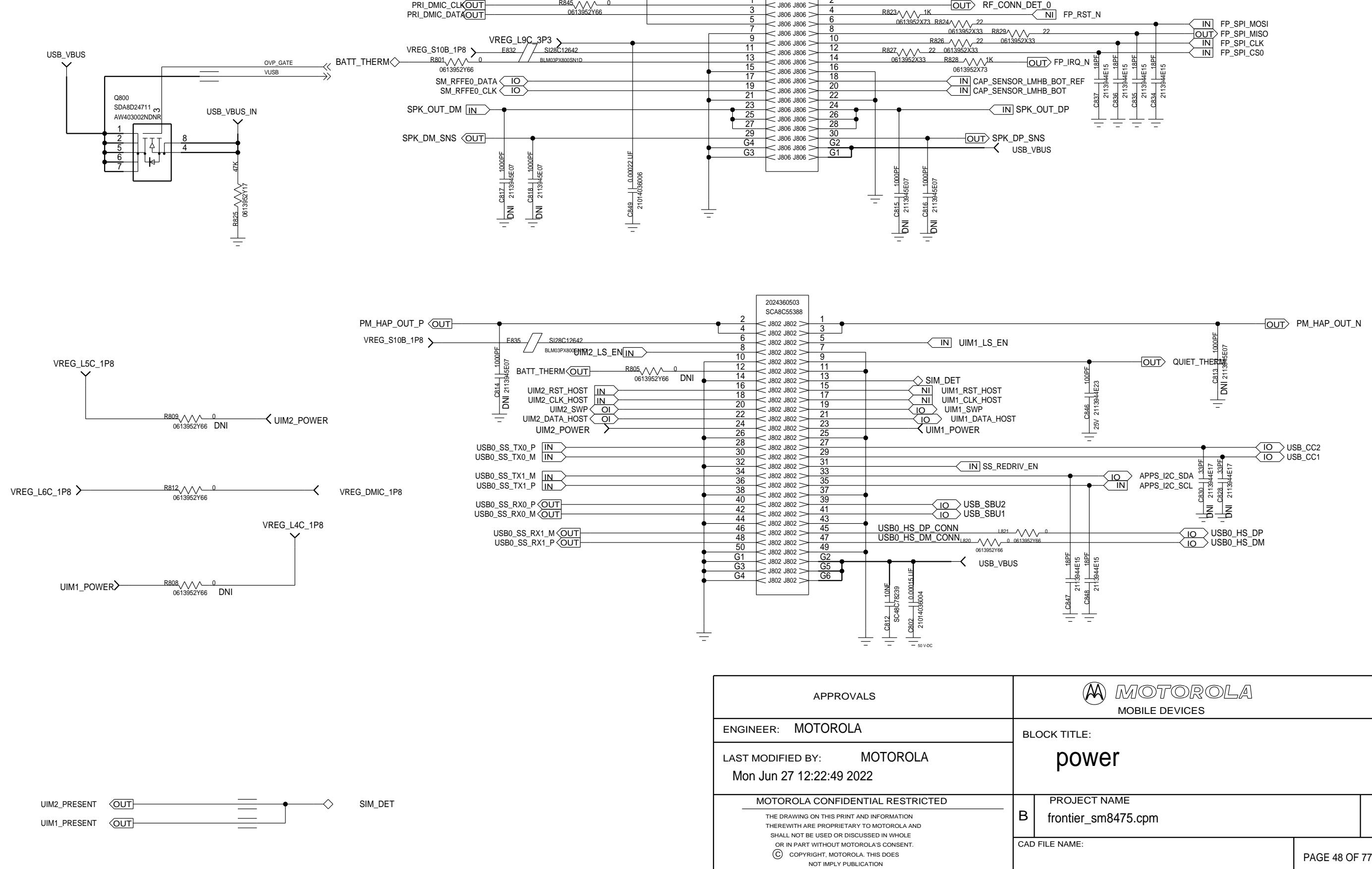
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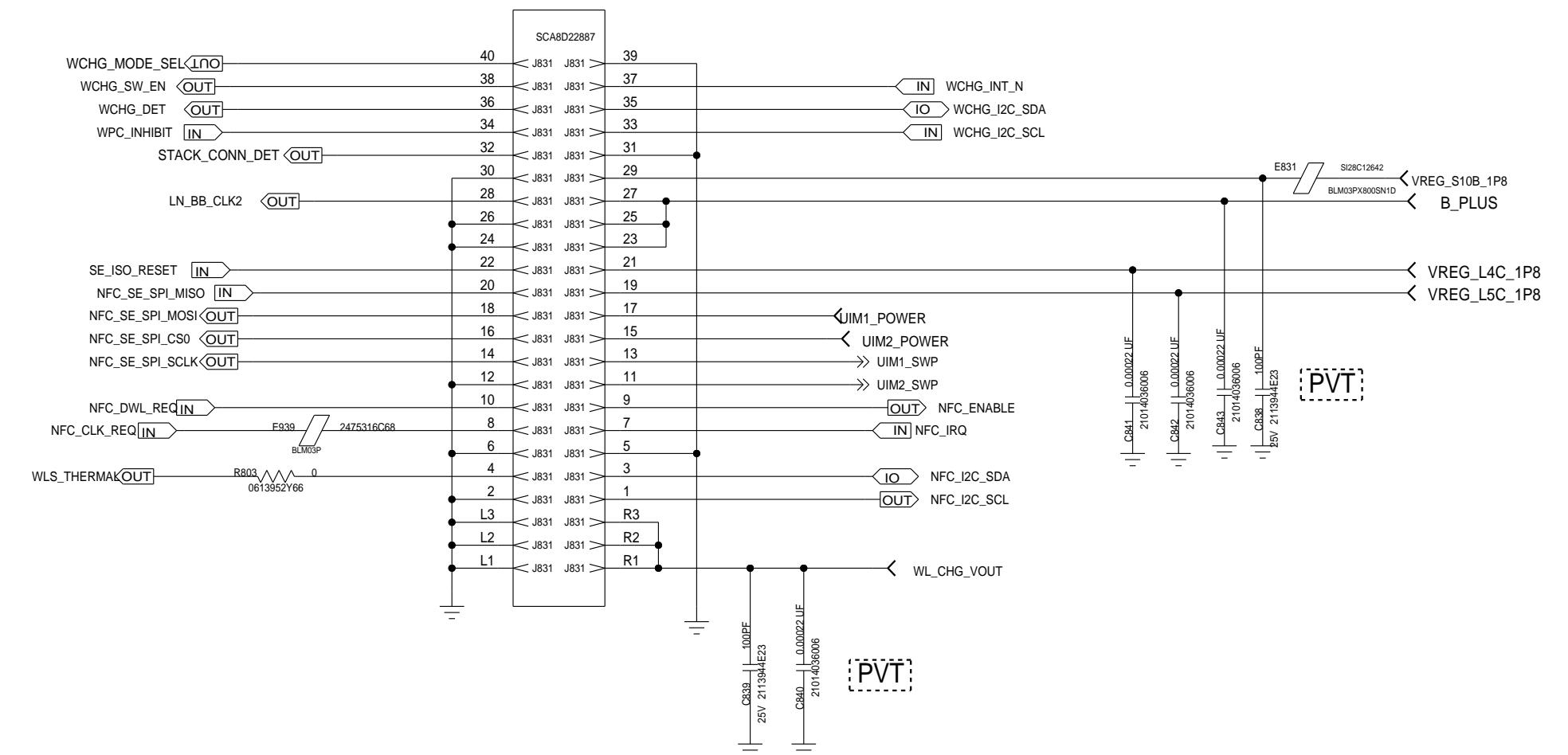
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REF: 800-839



BRIDGE: STACK BOARD CONN

REFDES: 831-839



APPROVALS		 MOTOROLA MOBILE DEVICES	
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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:49 2022		power	
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PWR:WIRELESS CHARGE 50W

REFDES: 900-999

WIRELESS CHARGE BQ25968 I2C ADDRESS: 0X76/0X77

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APPROVALS		 MOTOROLA MOBILE DEVICES	
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MOTOROLA CONFIDENTIAL RESTRICTED		PROJECT NAME	
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CAD FILE NAME:			PAGE 50 OF 77

NFC: NXP SN110U

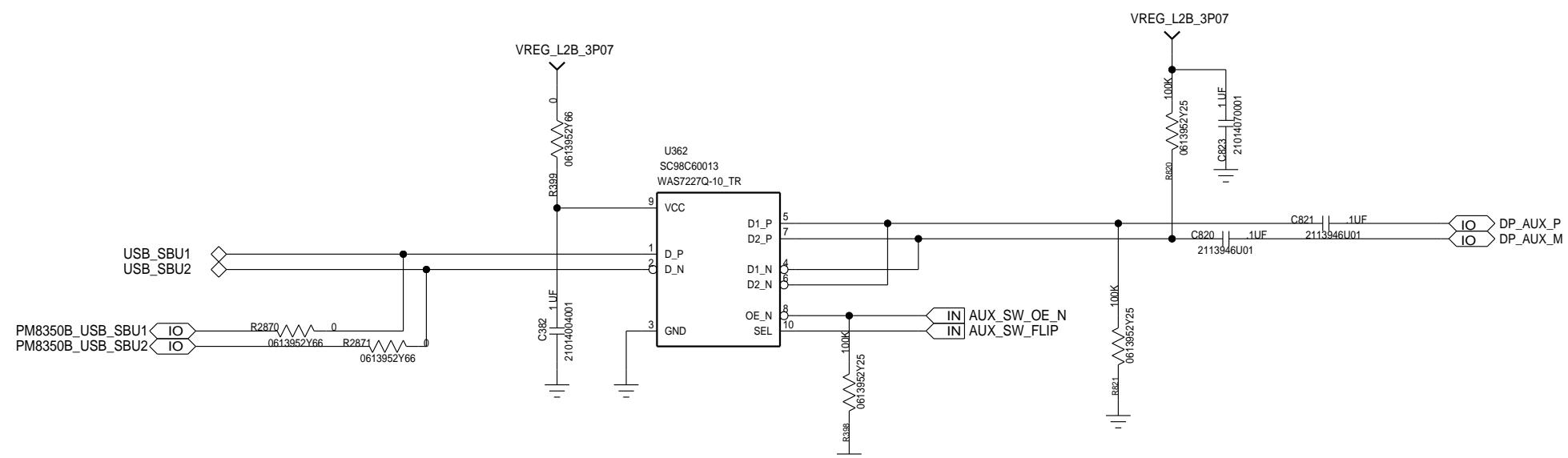
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CAD FILE NAME:		PAGE 51 OF 77

PWR: AUX SWITCH

REFDES: 2870-2899

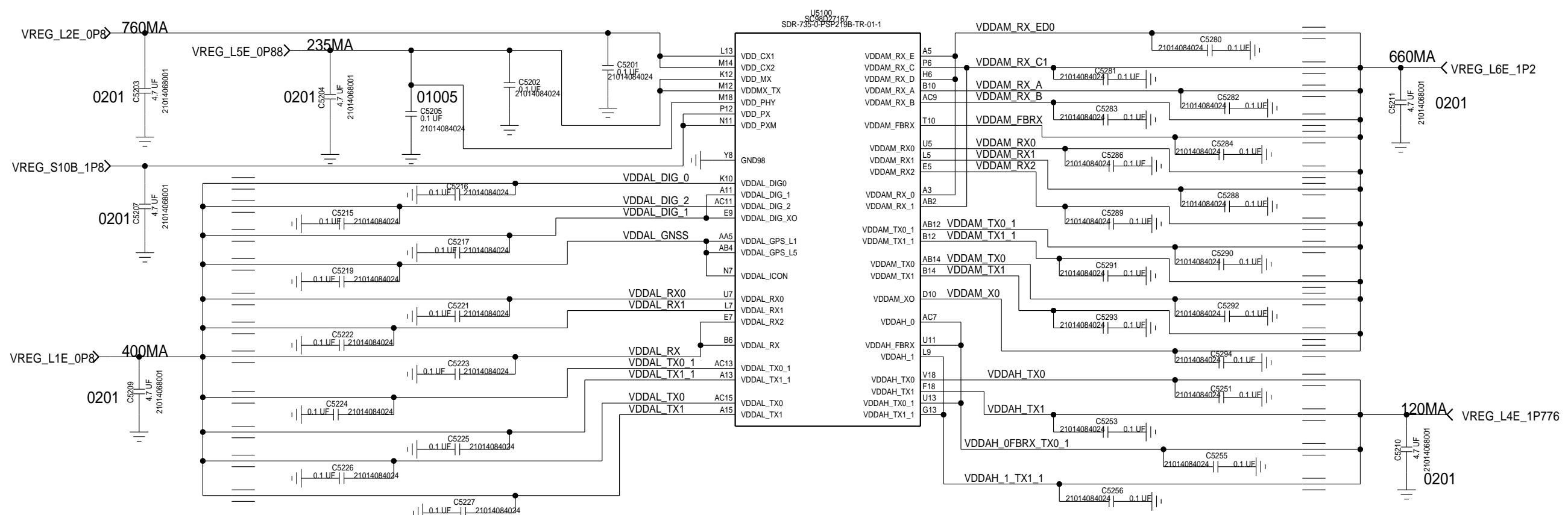
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APPROVALS		MOTOROLA MOBILE DEVICES	
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MOTOROLA CONFIDENTIAL RESTRICTED	B	PROJECT NAME	REV
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CAD FILE NAME:			PAGE 52 OF 77

SDR735#1: PWR

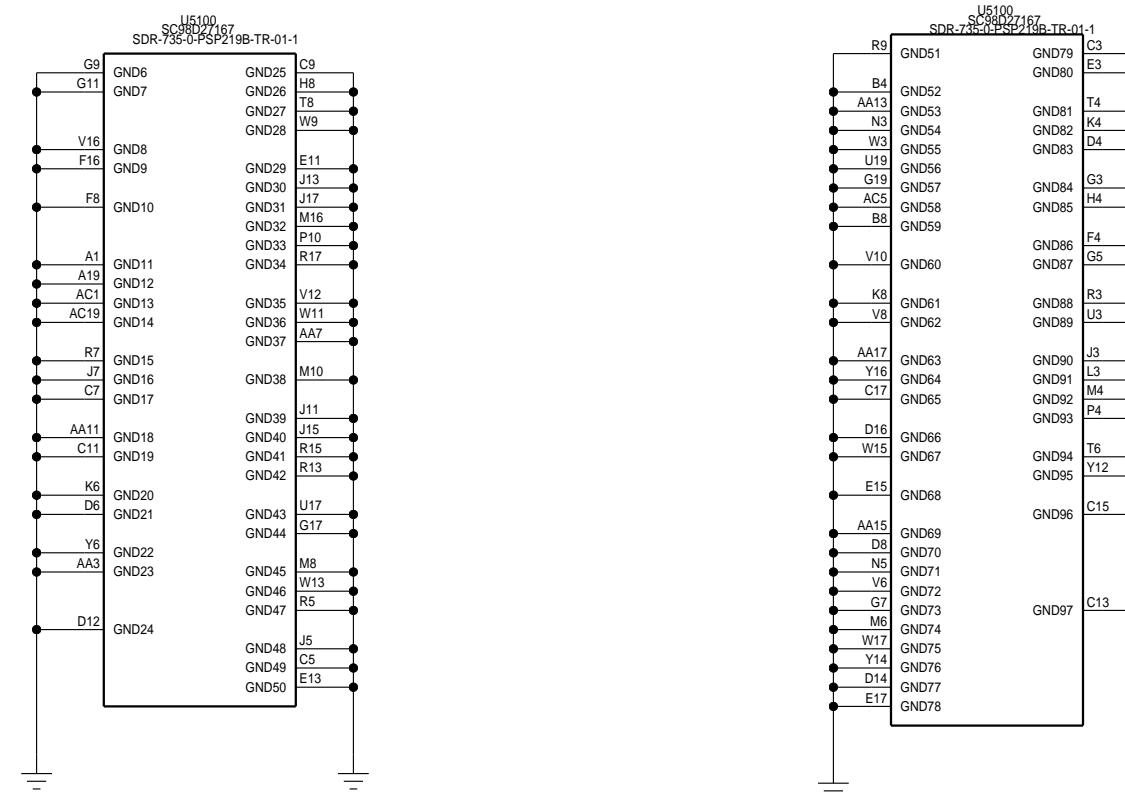
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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:23:00 2022			
MOTOROLA CONFIDENTIAL RESTRICTED	PROJECT NAME	REV	
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SDR735#1: GND

REF 5200-5299

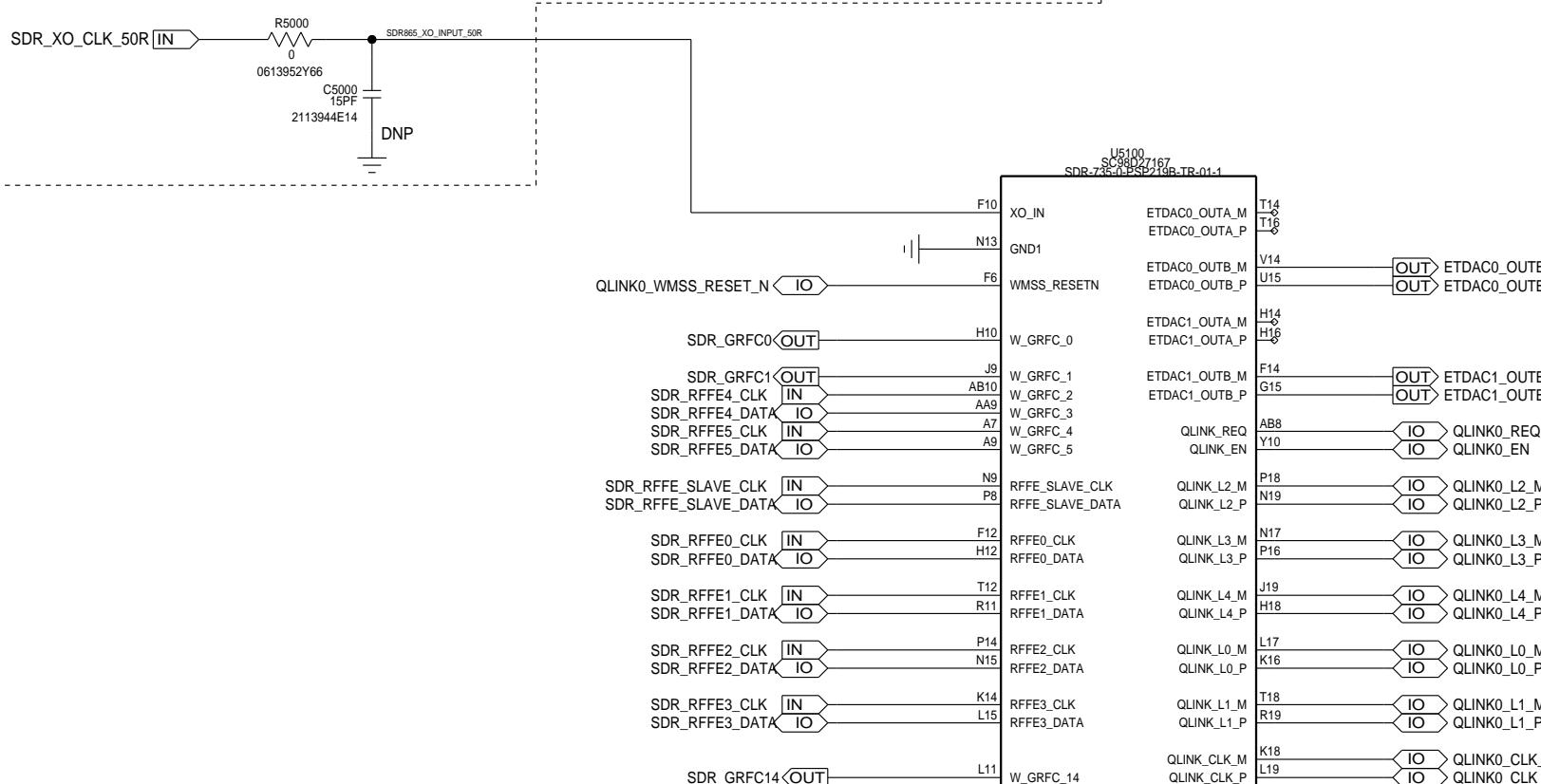


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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:23:00 2022		rf_sdr	
MOTOROLA CONFIDENTIAL RESTRICTED	B	PROJECT NAME	REV
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CAD FILE NAME:			PAGE 54 OF 77

SDR735#1: CTRL/QLINK

REF 5000-5099

NOTE: CONTROLLED IMPEDANCE FOR XO LINE MUST BE BETWEEN 30 OHM AND 50OHM
 LOW IMPEDANCE IS PREFERRED.
 THE SERIES PLACEHOLDER MUST BE A 0 OHM RESISTOR SINCE THE XO SIGNAL CARRIES A DC BIAS TO THE XO_IN INPUT.
 DO NOT SHUNT THE CAPACITOR ON THE XO_IN TRACE.



APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

LAST MODIFIED BY: MOTOROLA
Mon Jun 27 12:23:00 2022

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BLOCK TITLE:

rf_sdr

PROJECT NAME
B frontier_sm8475.cpm

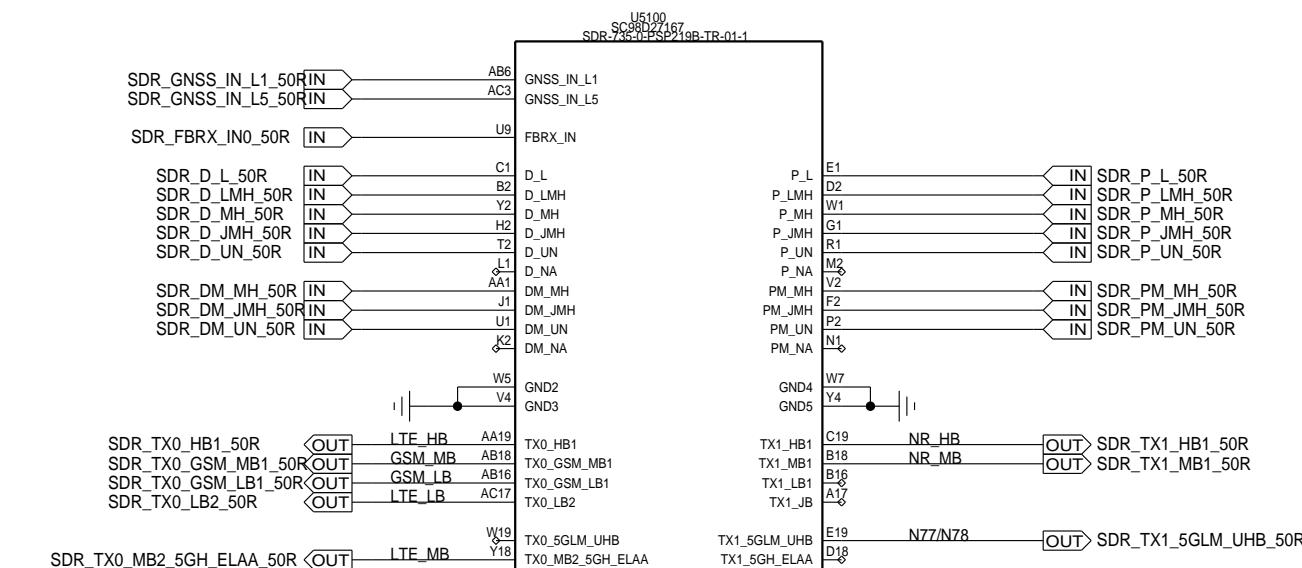
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CAD FILE NAME:

PAGE 55 OF 77

SDR735#1: TX/RX

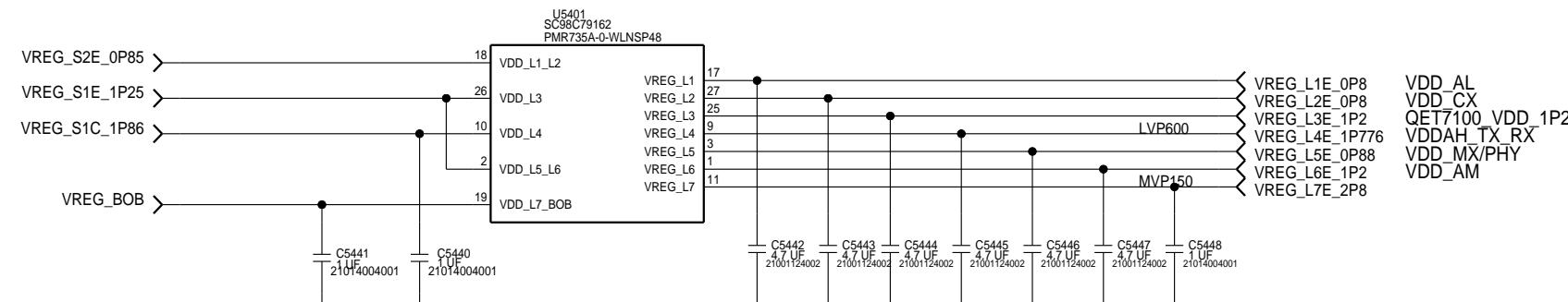
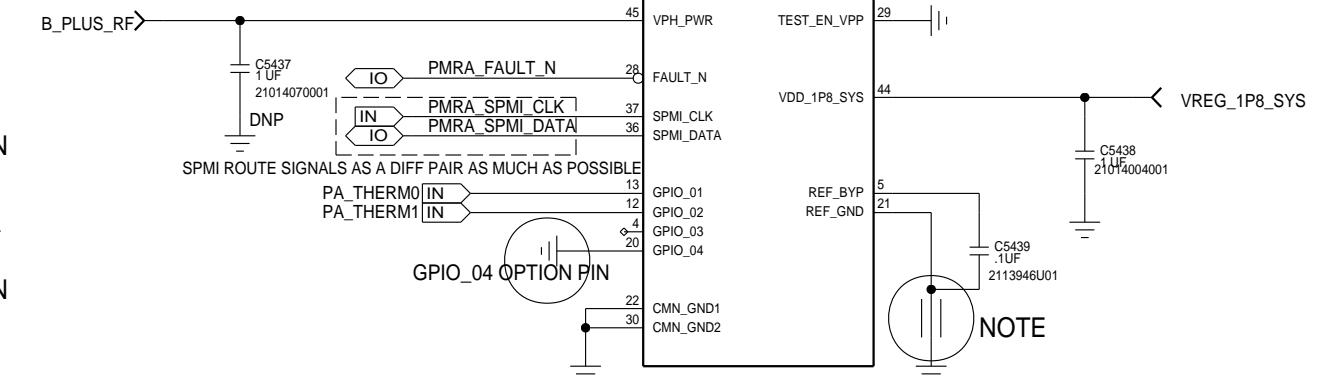
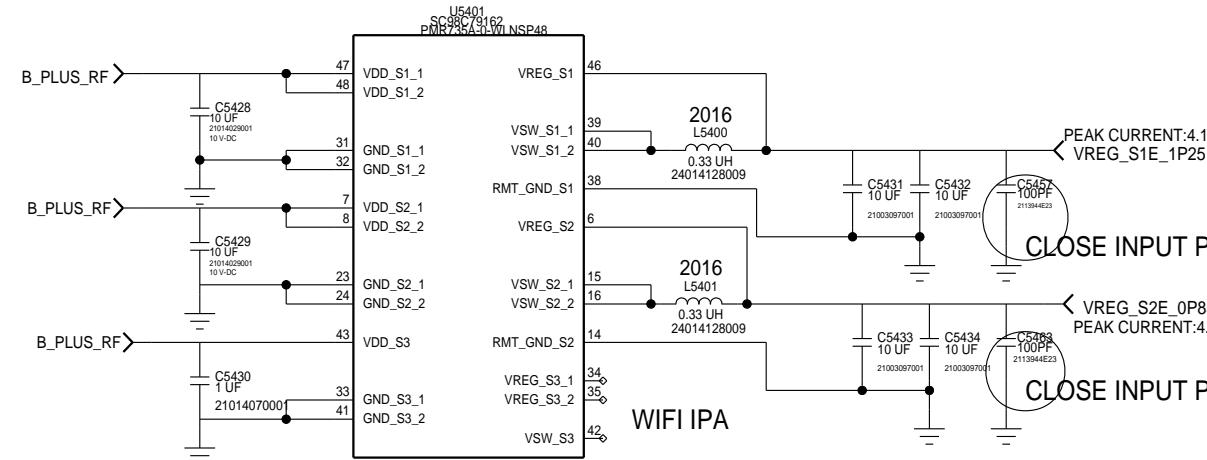
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CAD FILE NAME:			PAGE 56 OF 77

PMR735A: RF_PMIC

REF 5300-5399



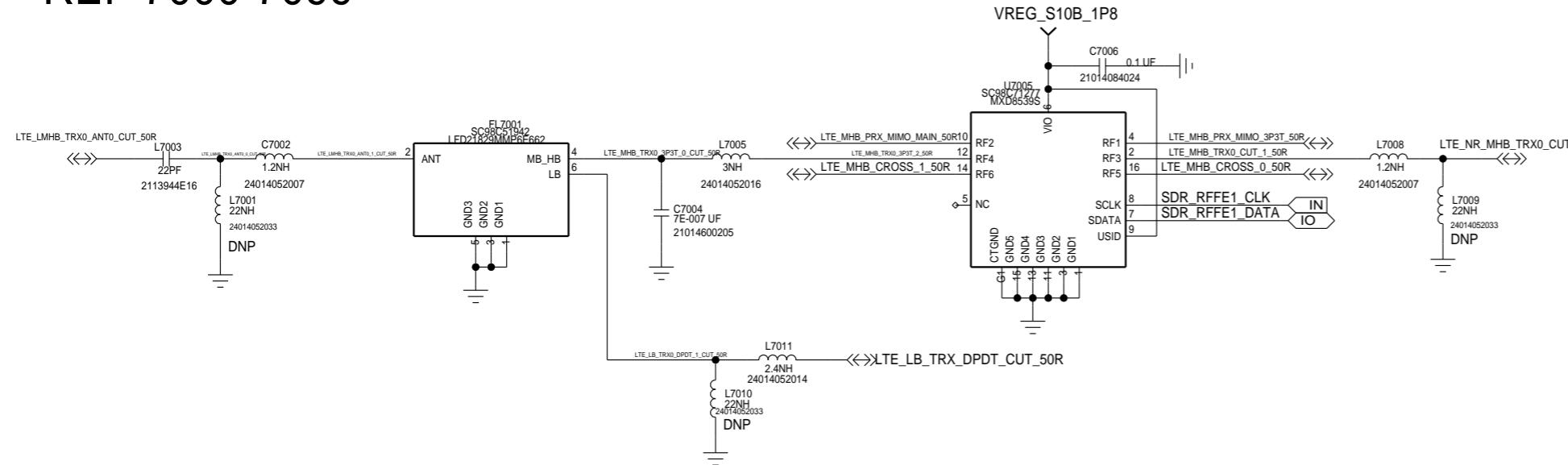
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APPROVALS		MOTOROLA MOBILE DEVICES	
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MOTOROLA CONFIDENTIAL RESTRICTED	PROJECT NAME	REV	
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B frontier_sm8475.cpm			CAD FILE NAME:
			PAGE 57 OF 77

RFFE: XSW+DIP

REF 7000-7099

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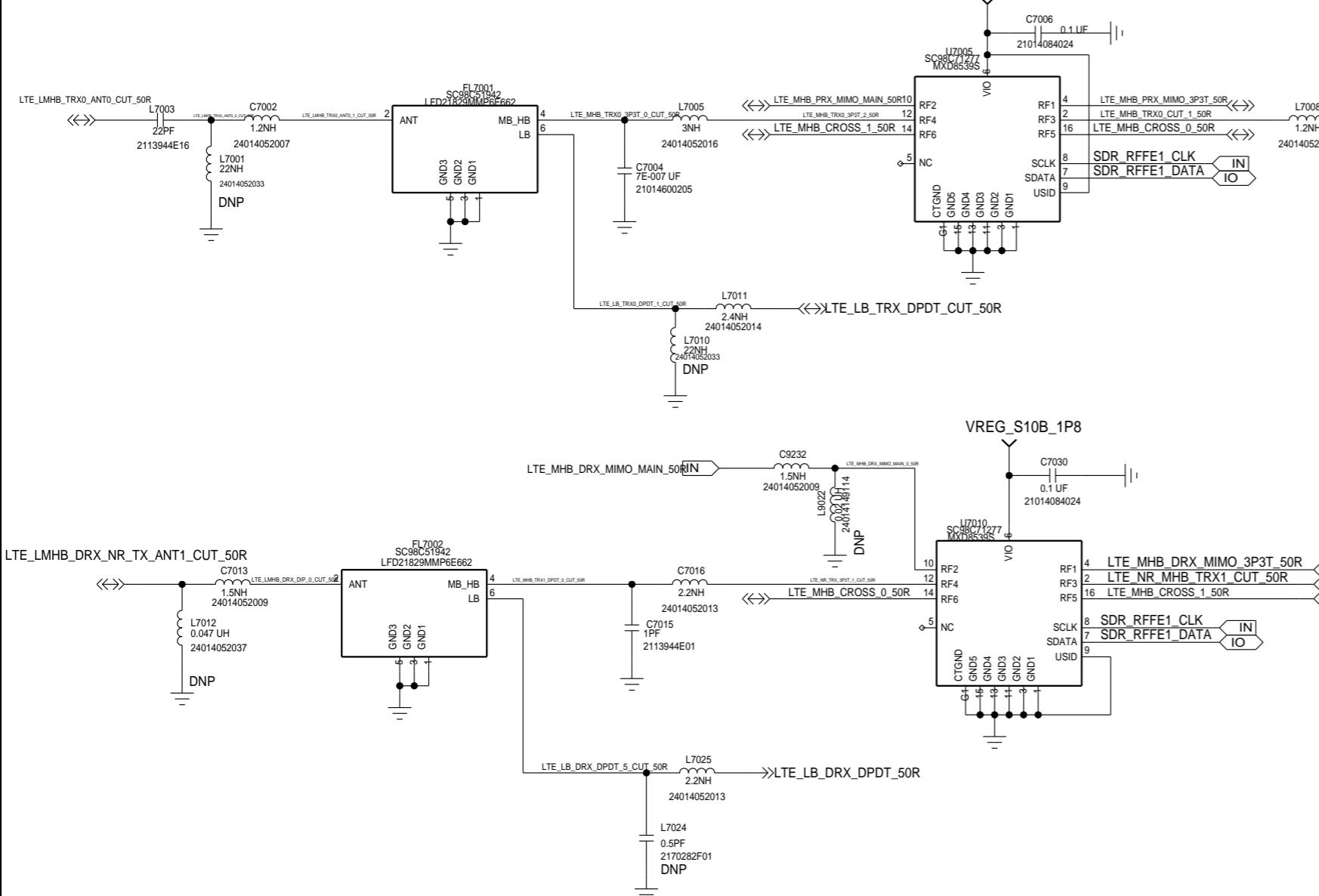


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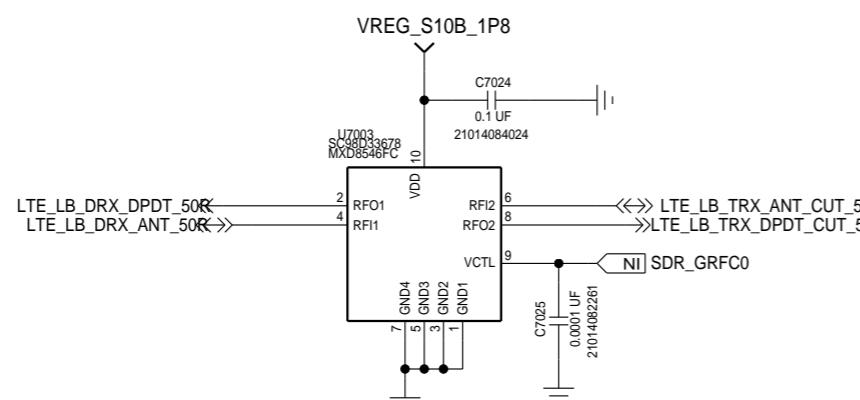
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APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER:

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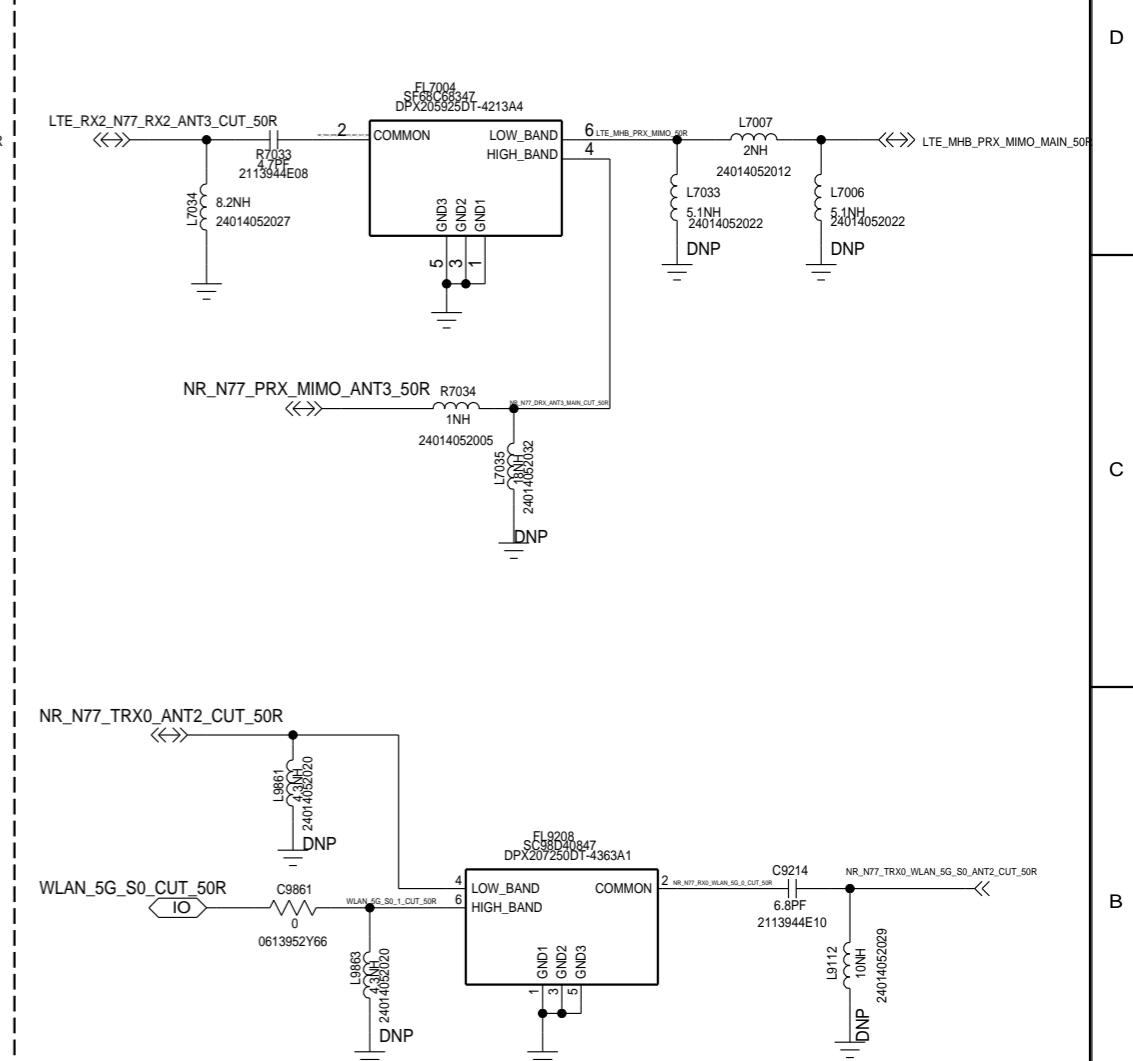
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PROJECT NAME
frontier_sm8475.cpm

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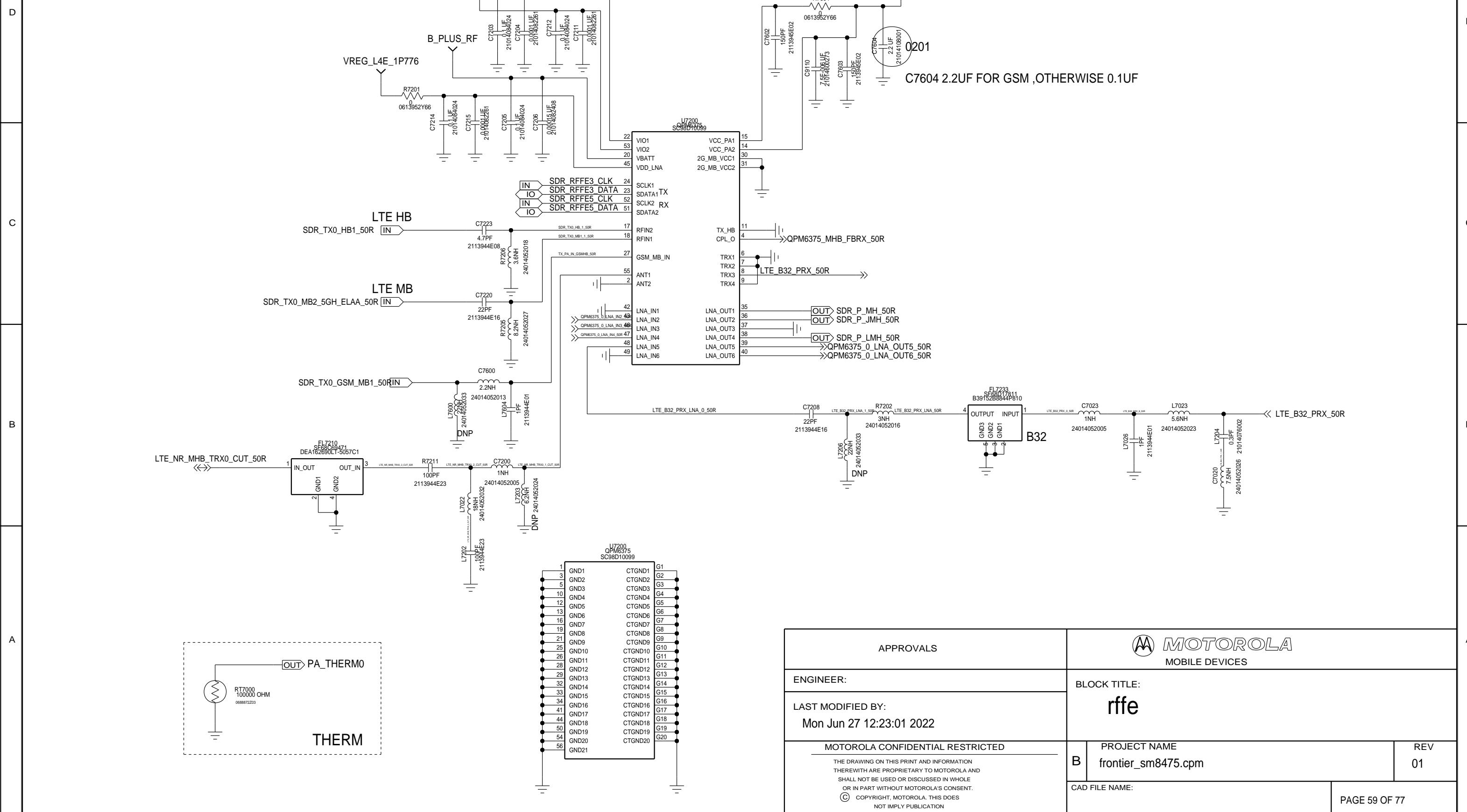
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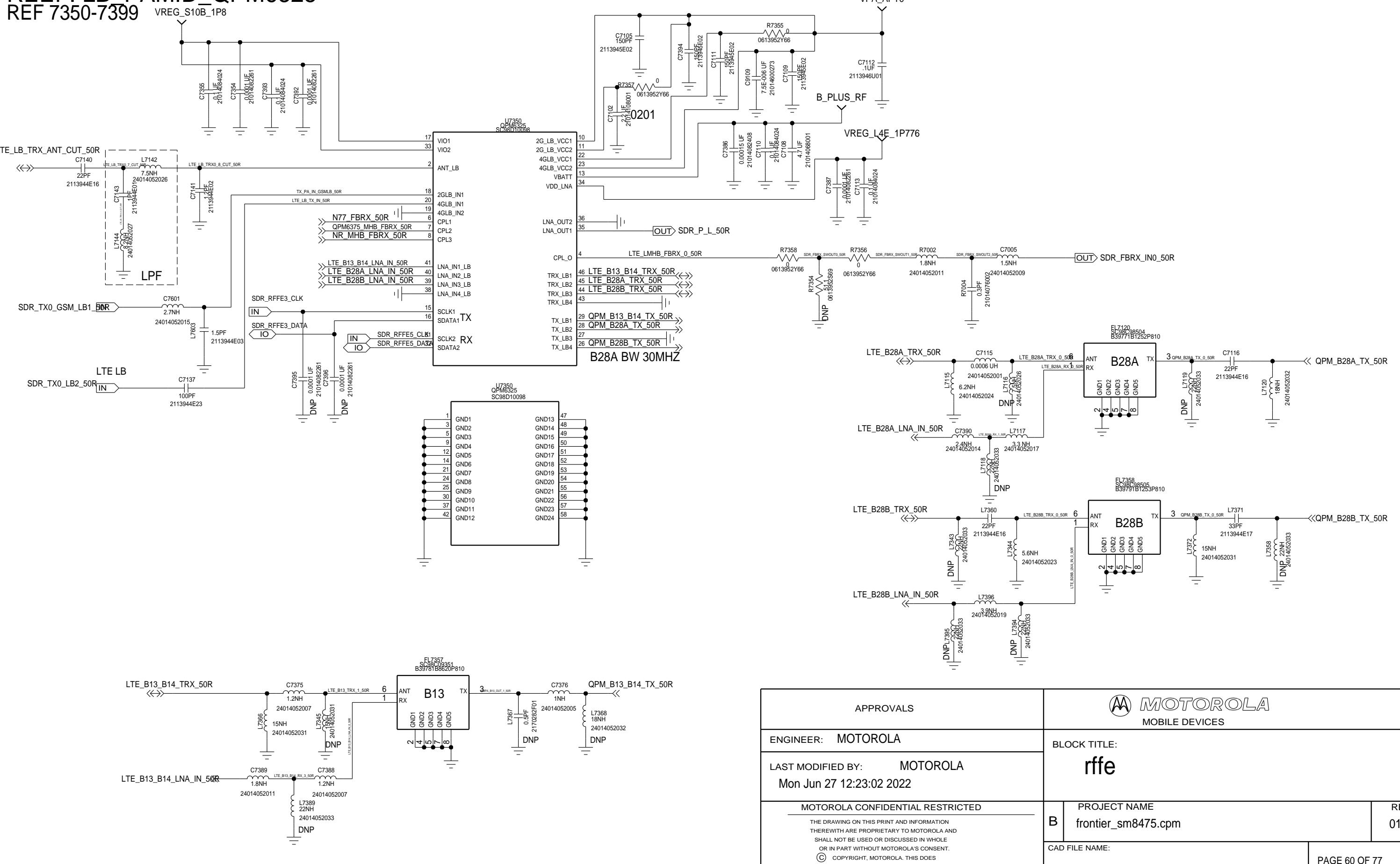
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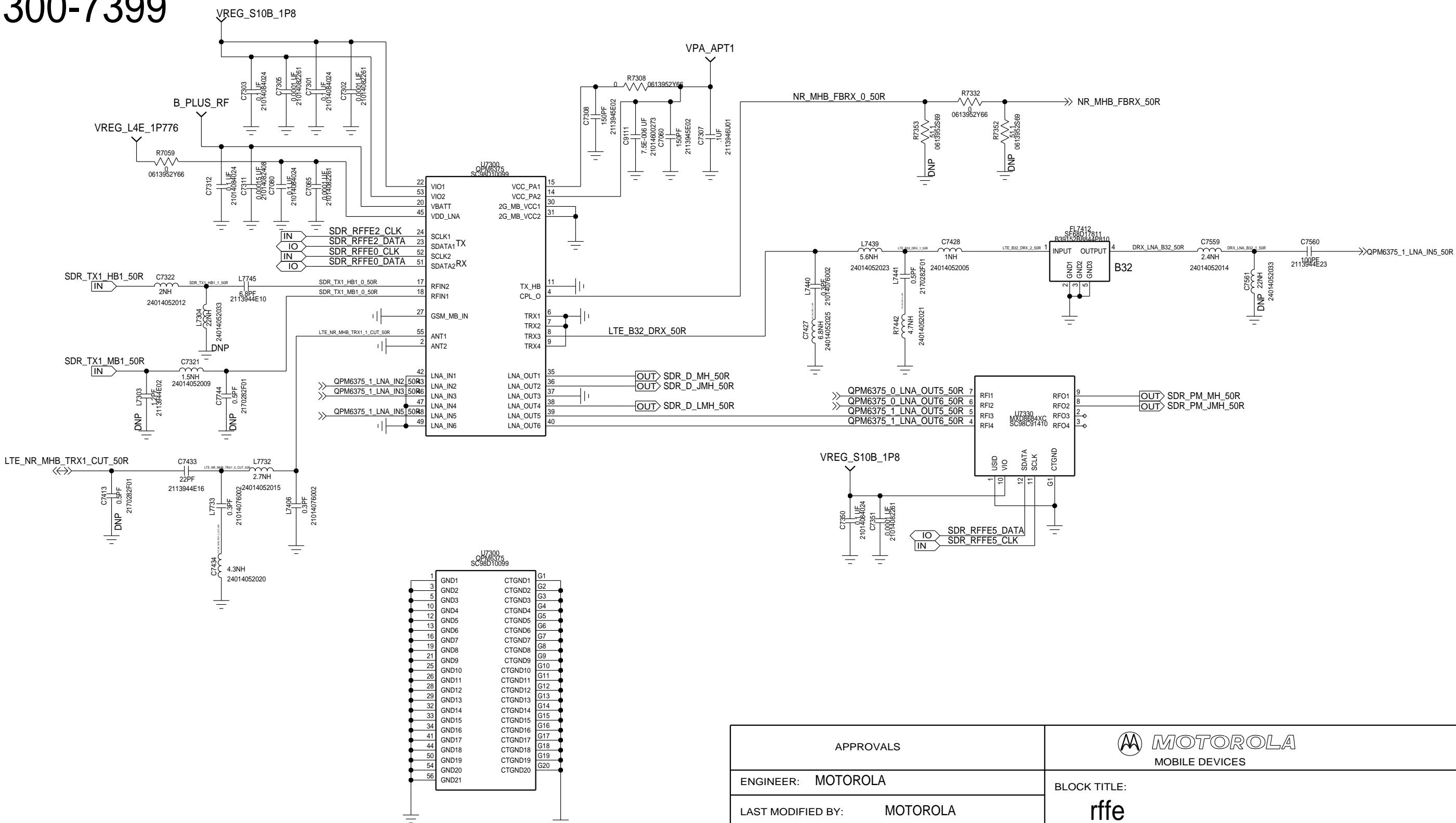
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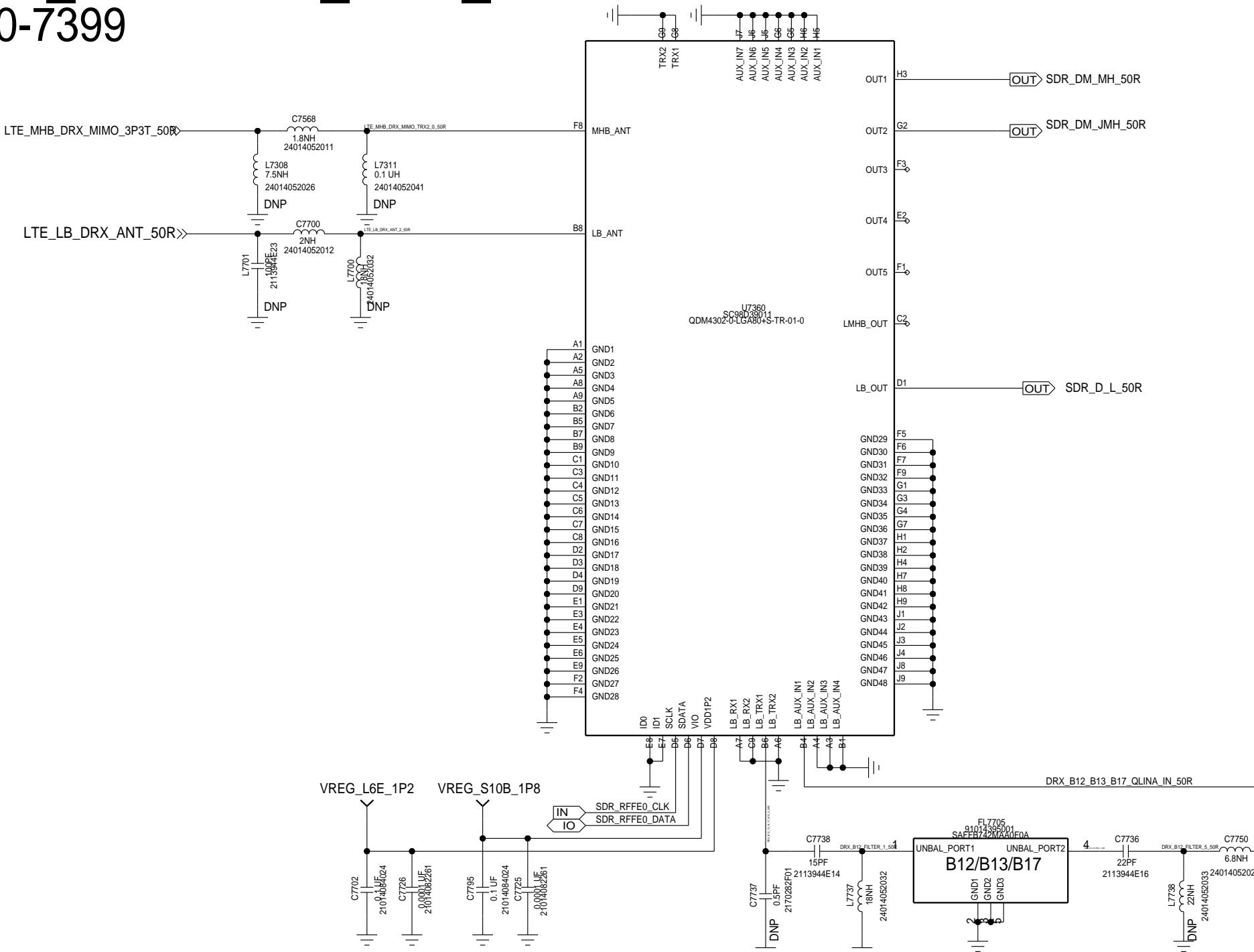
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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:23:01 2022			
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CAD FILE NAME:		PAGE 61 OF 77	

RFFE: LB_DRX/MHB_DRX_MIMO-QDM4302

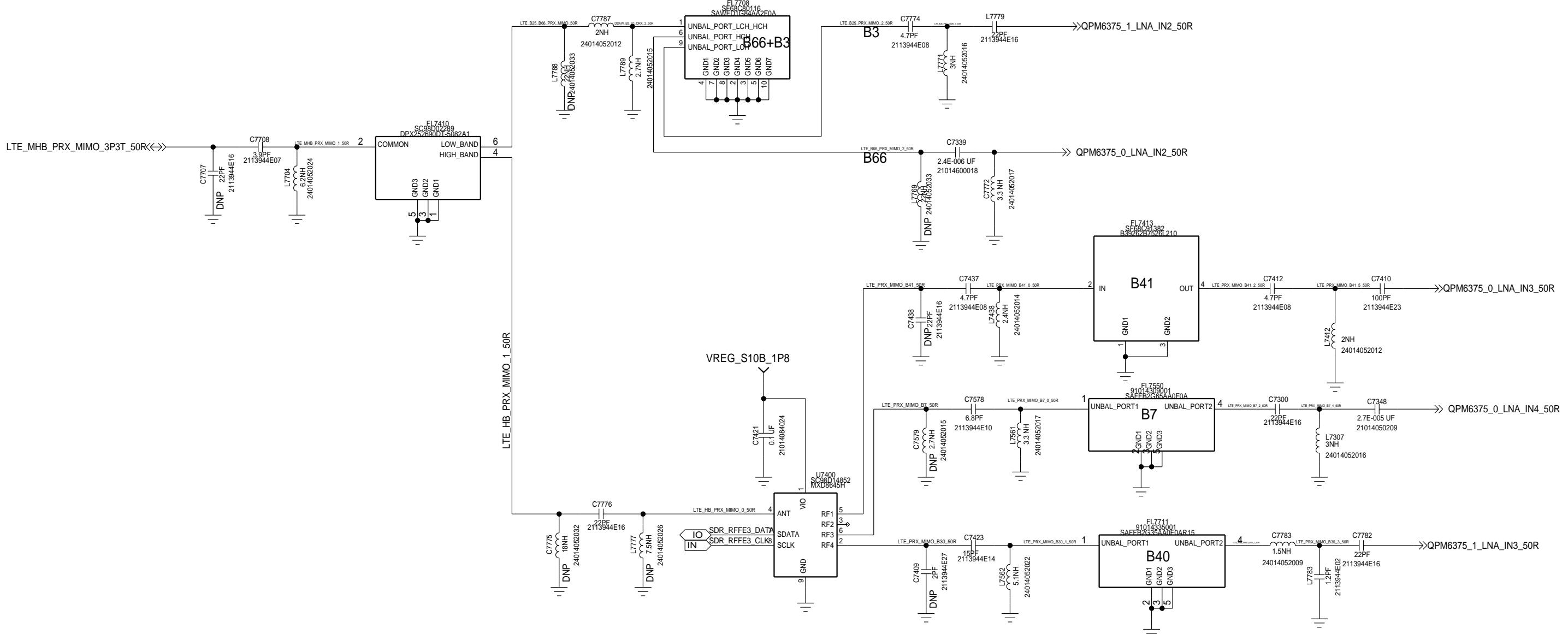
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CAD FILE NAME:			PAGE 62 OF 77

RFFE: MHB-PRX_MIMO_DISCRETE

REF 7300-7399



APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

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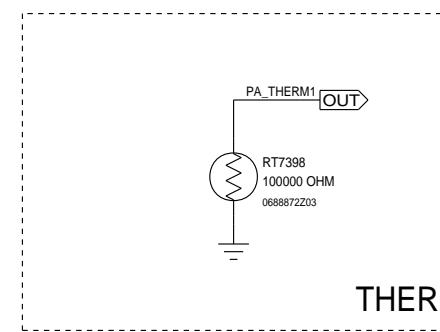
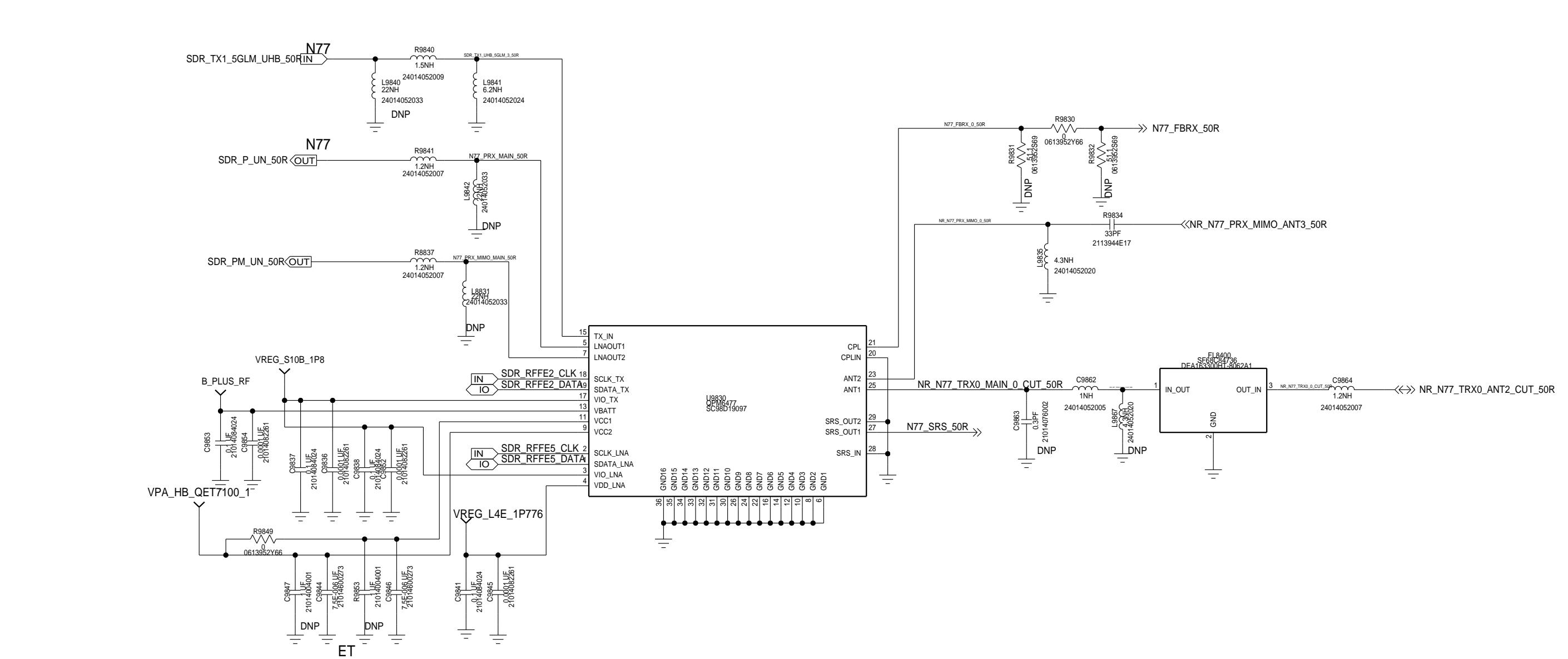
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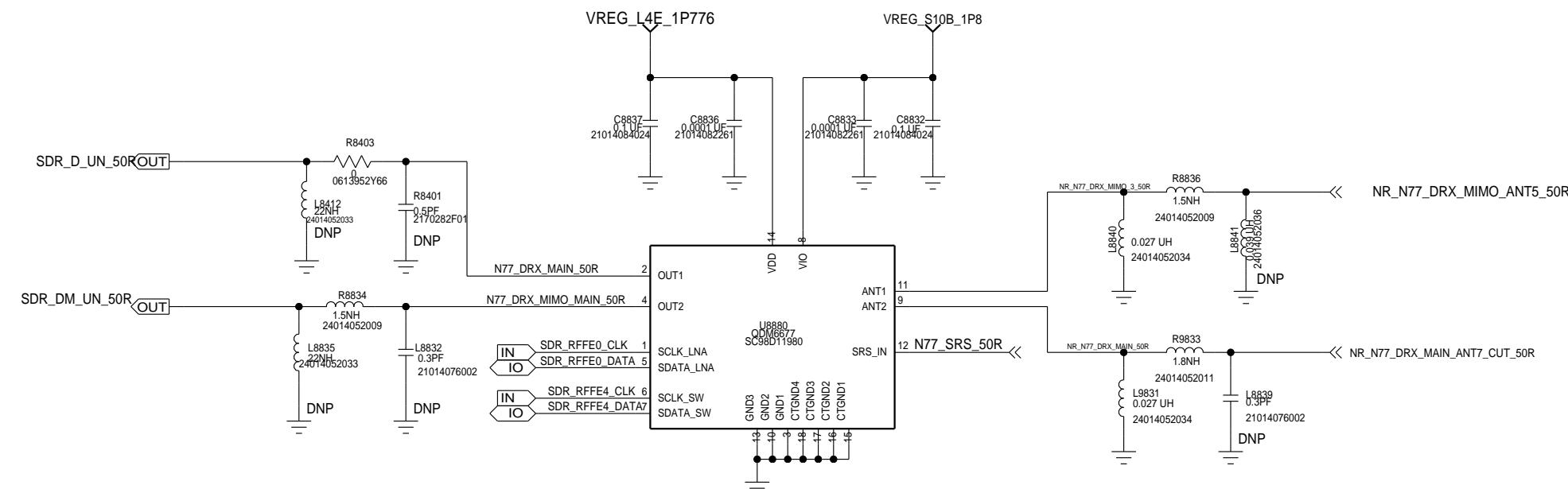
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CAD FILE NAME:		PAGE 64 OF 77	

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REF 8830-8899



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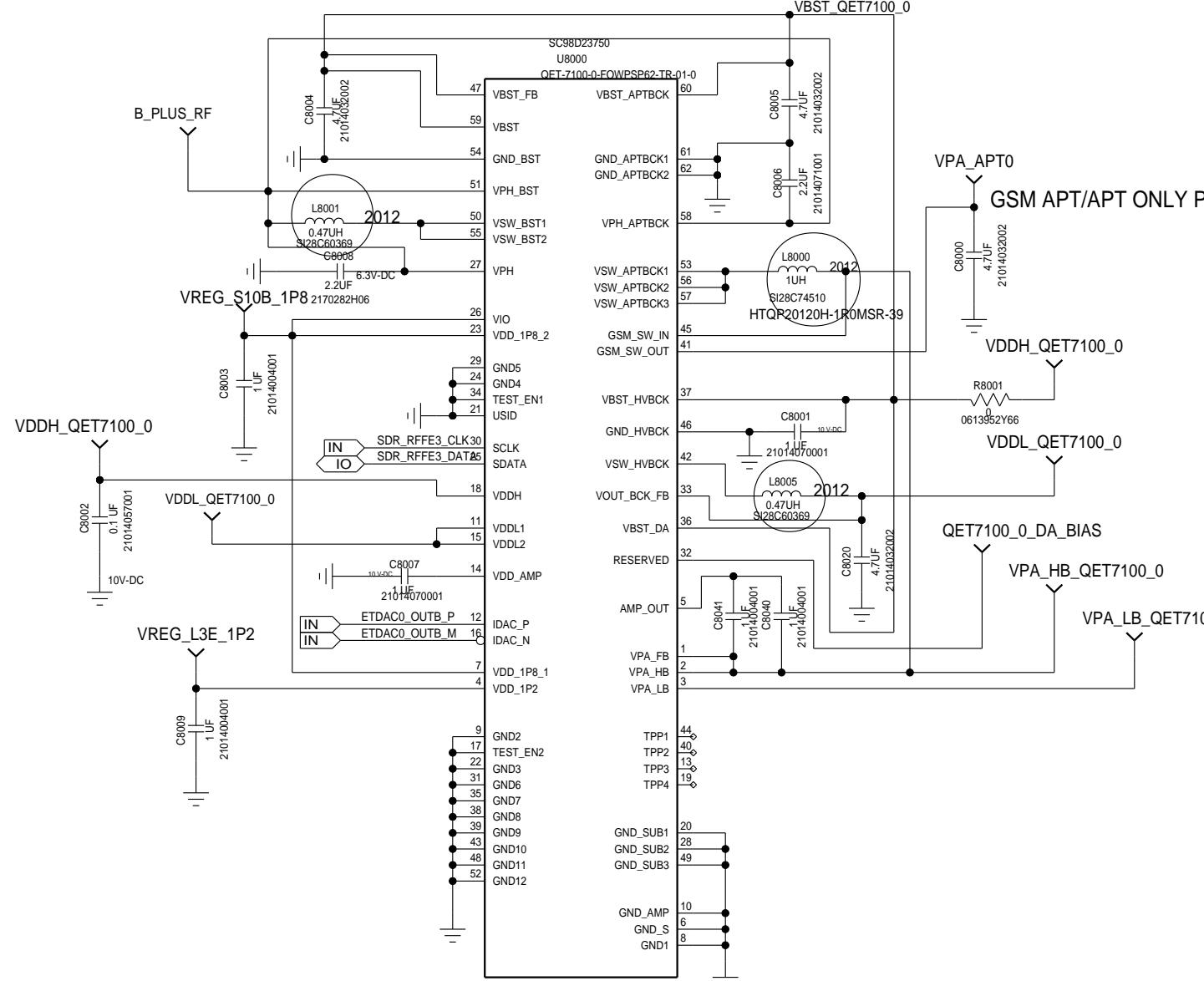
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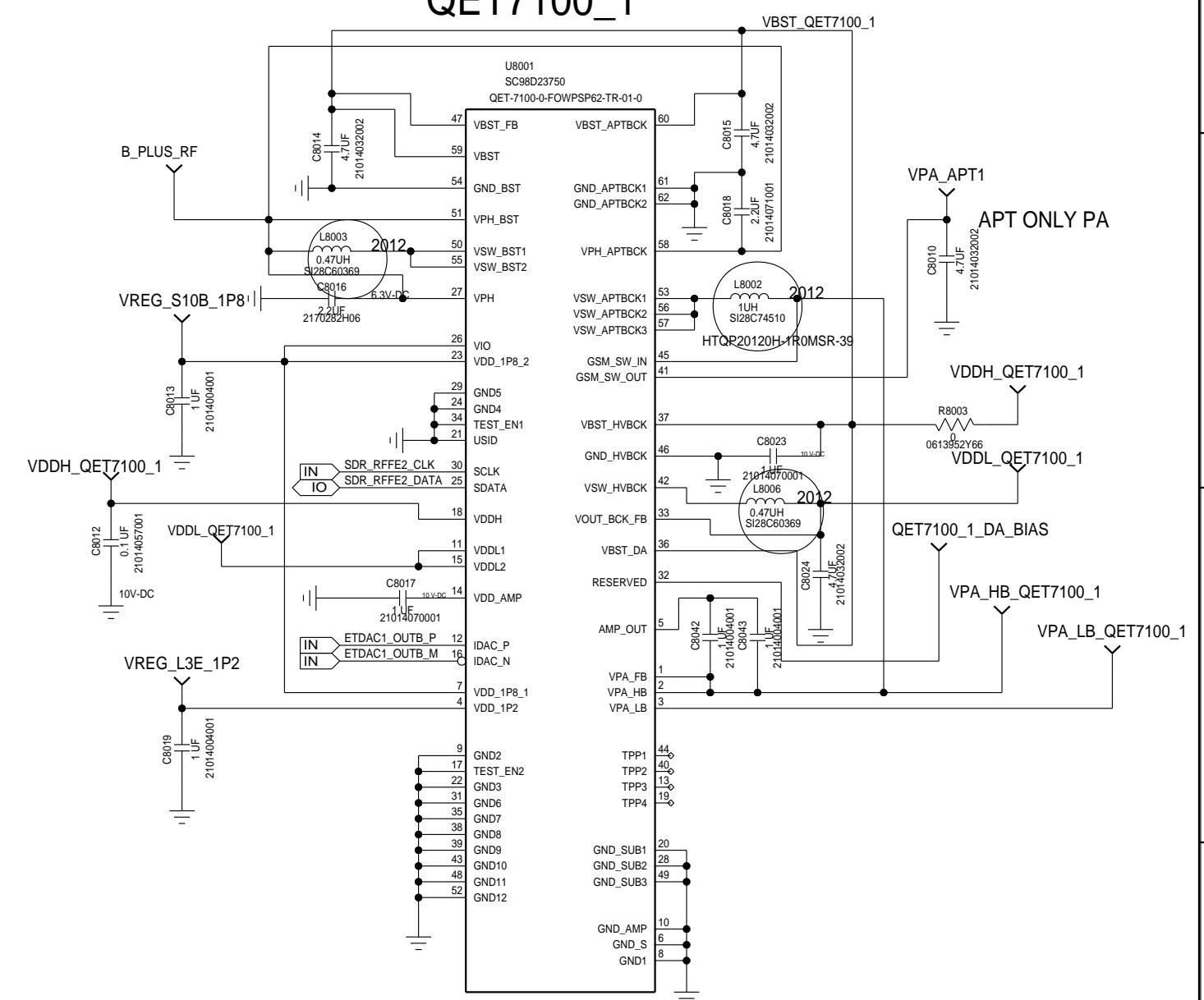
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APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

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PROJECT NAME
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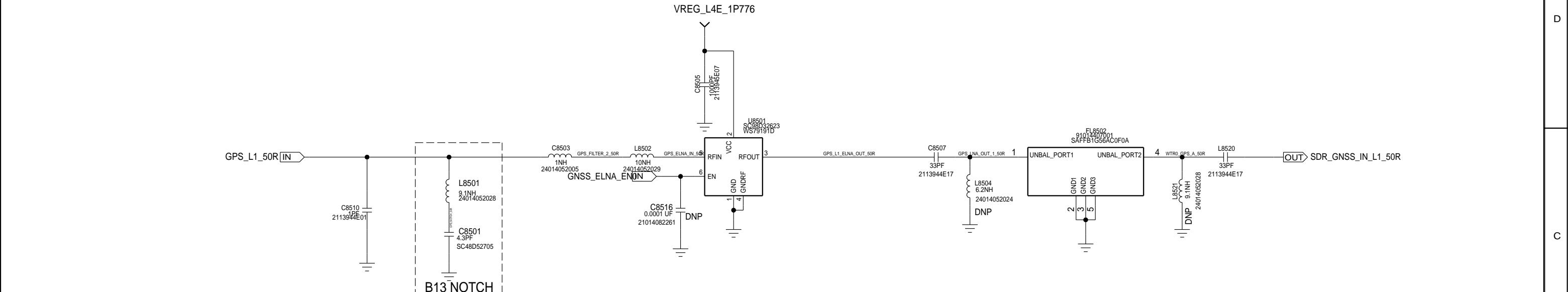
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RFFE: GPS

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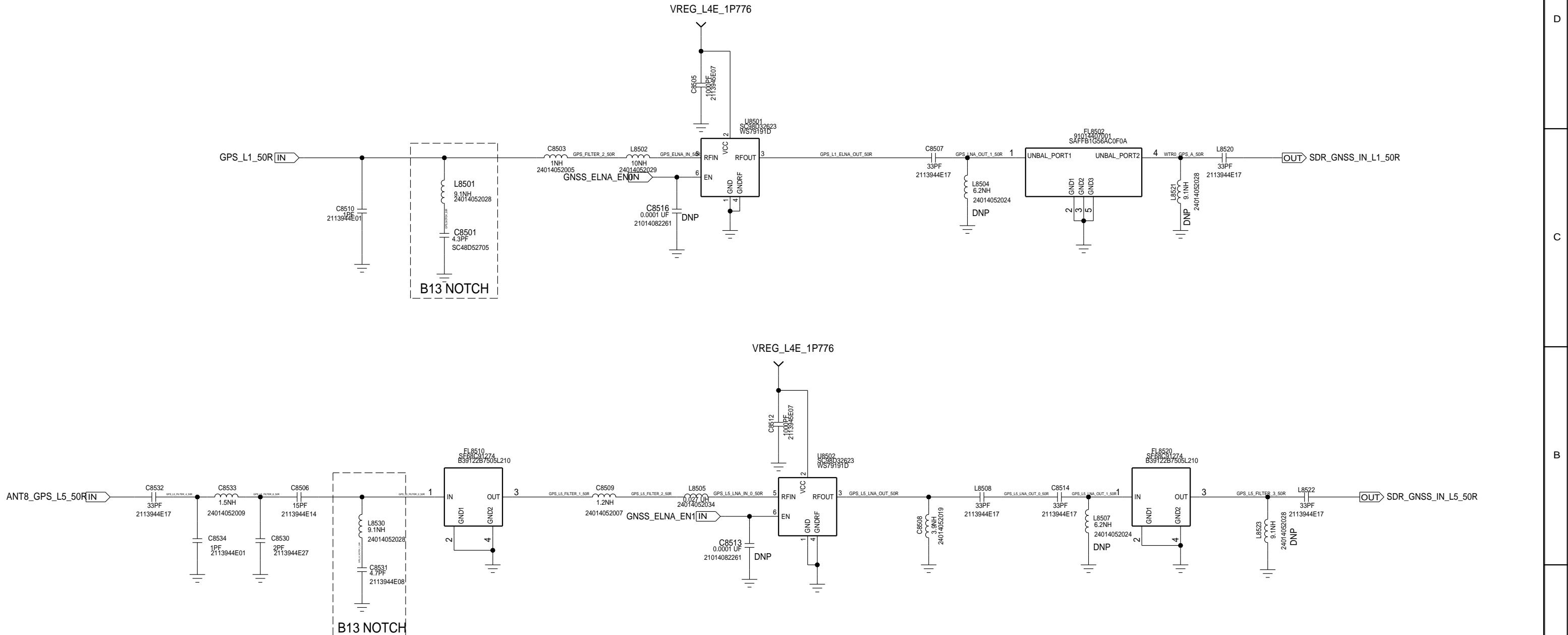
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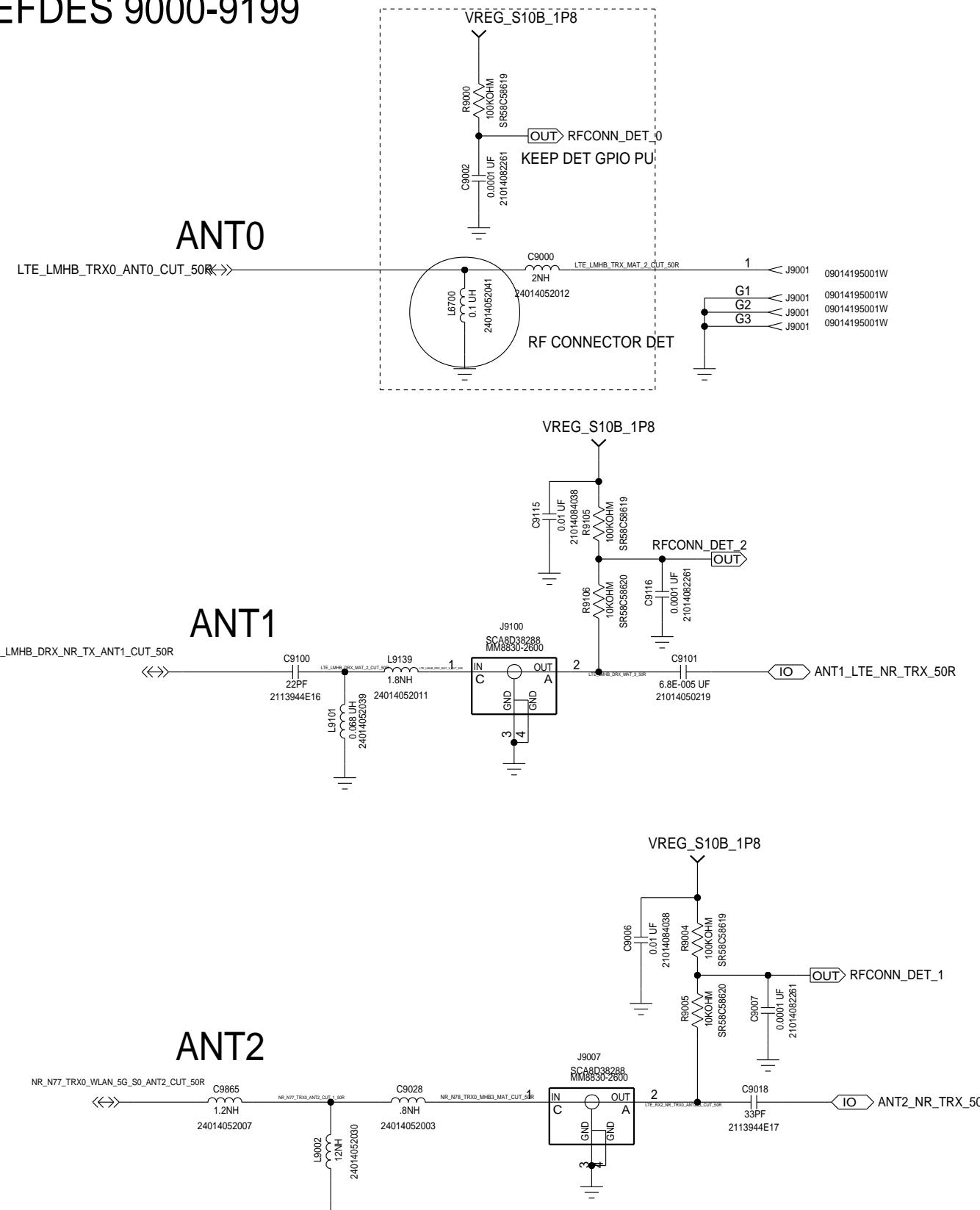
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LAST MODIFIED BY:		rffe	
Mon Jun 27 12:23:02 2022			
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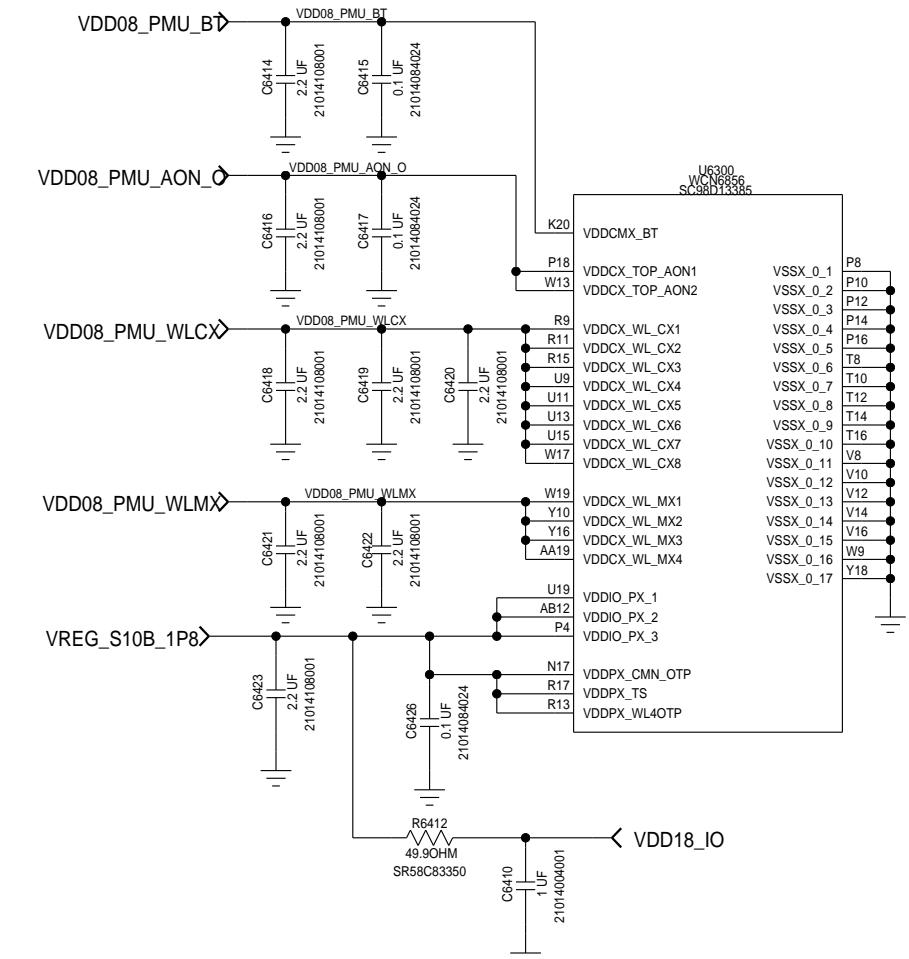
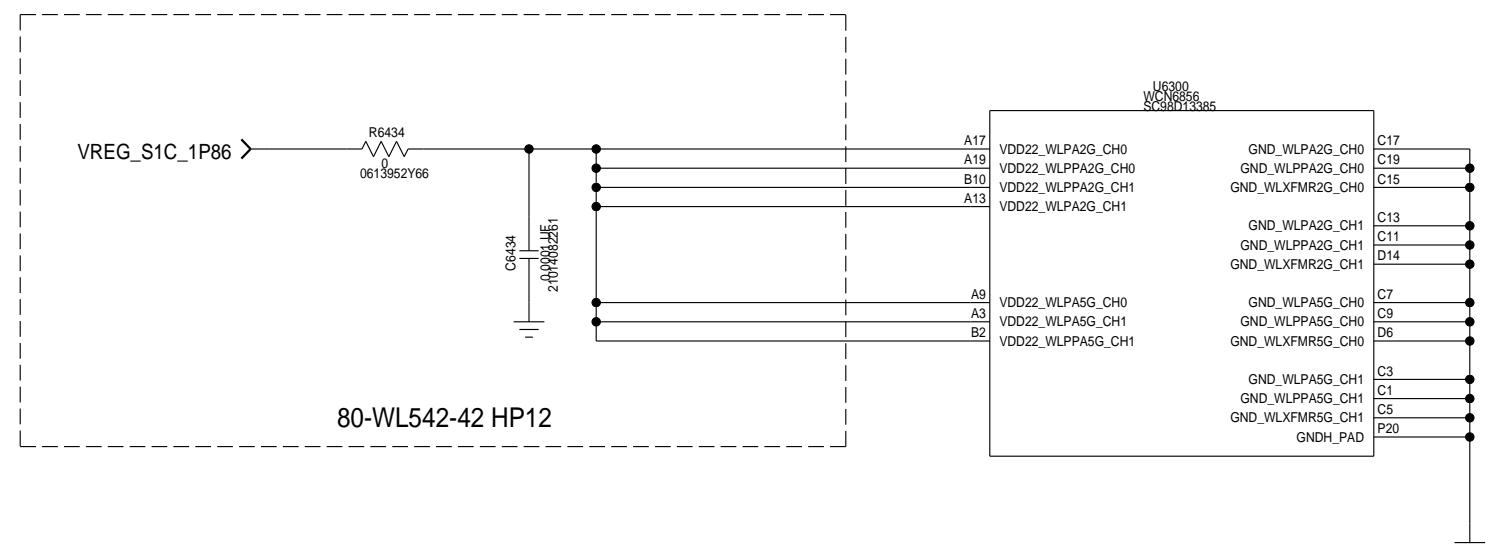
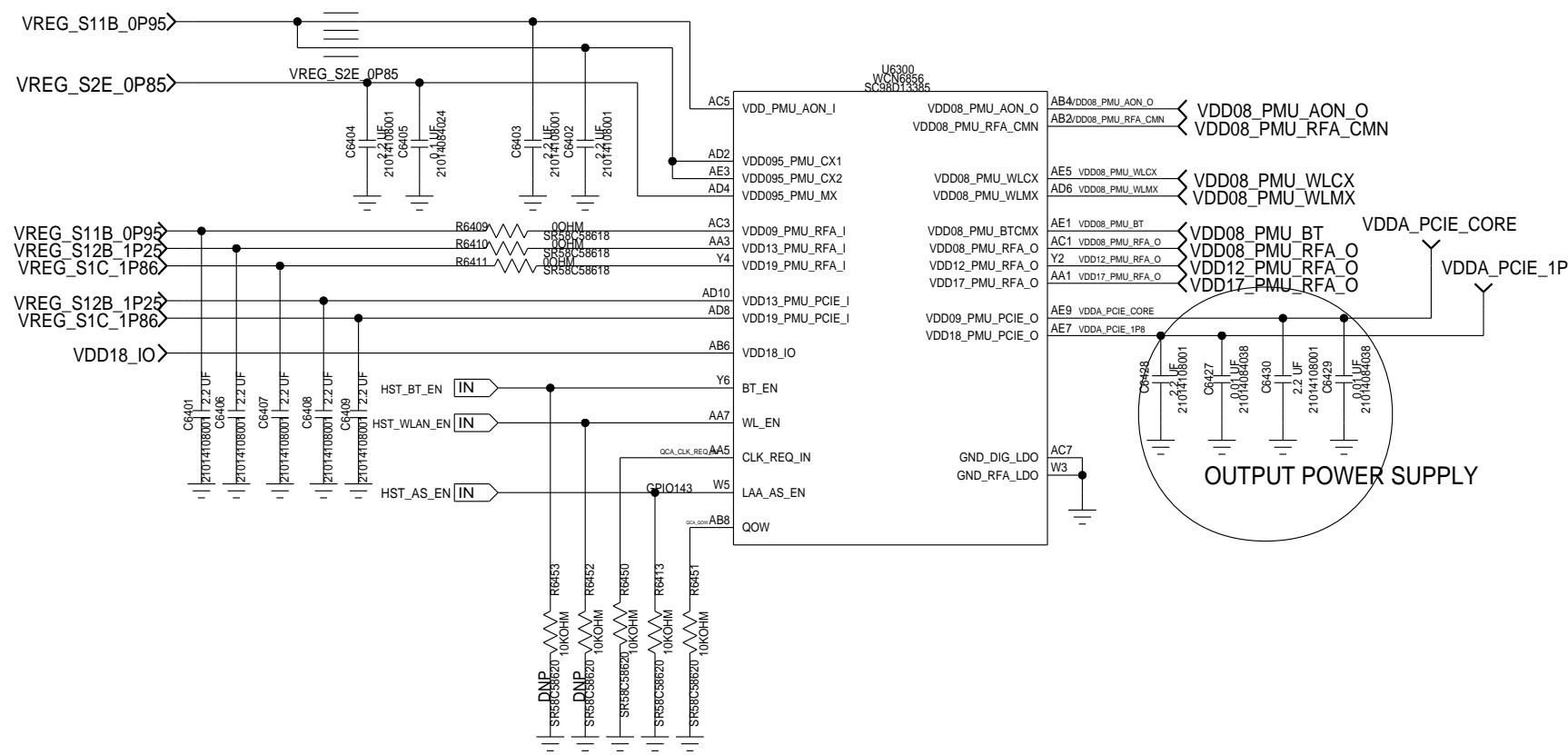
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REFDES 9000-9199



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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:23:02 2022			
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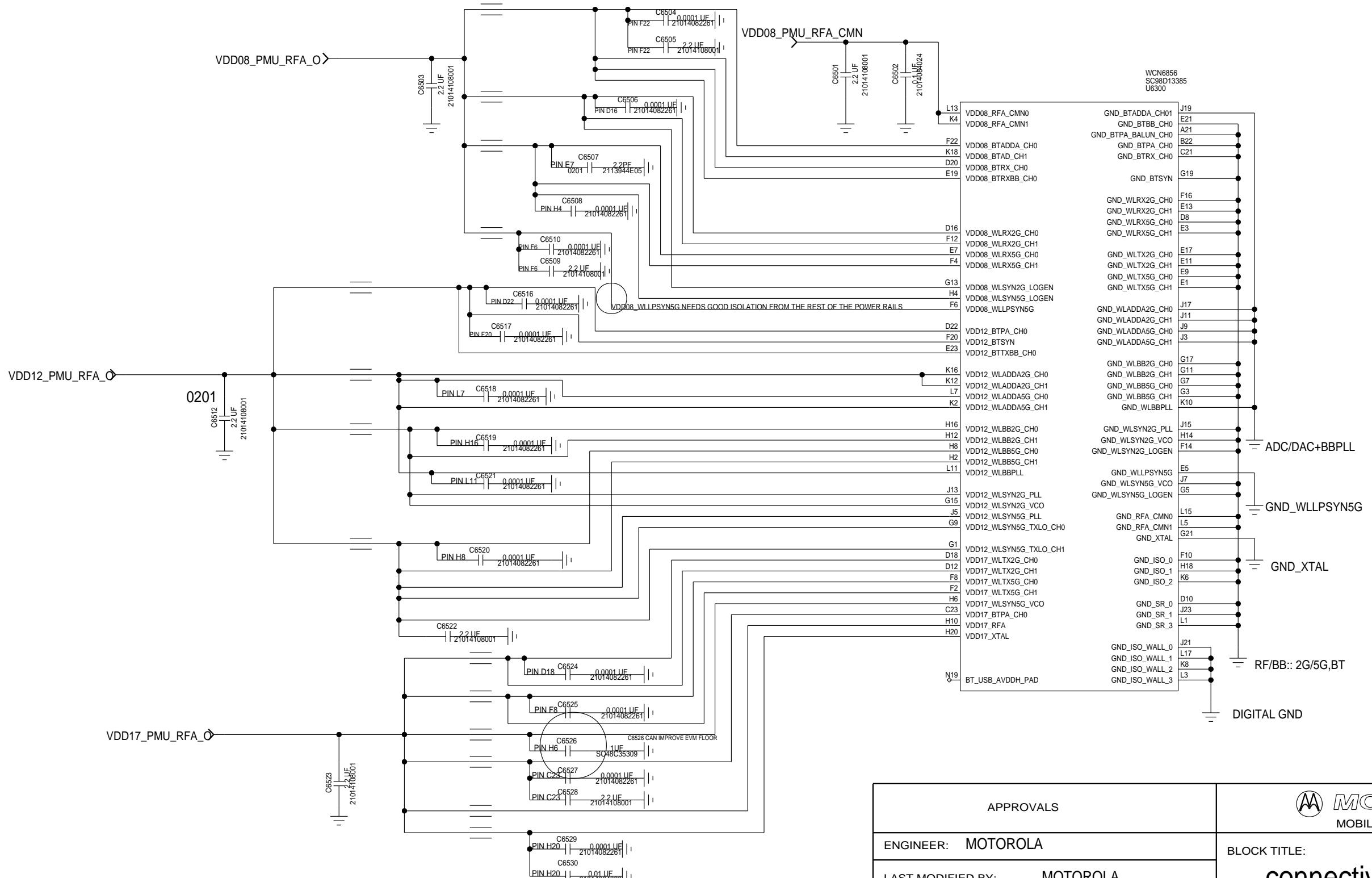
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REF 6300-6999**



APPROVALS	 MOTOROLA MOBILE DEVICES		
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LAST MODIFIED BY: MOTOROLA Mon Jun 27 12:22:57 2022			
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REF 6300-6999

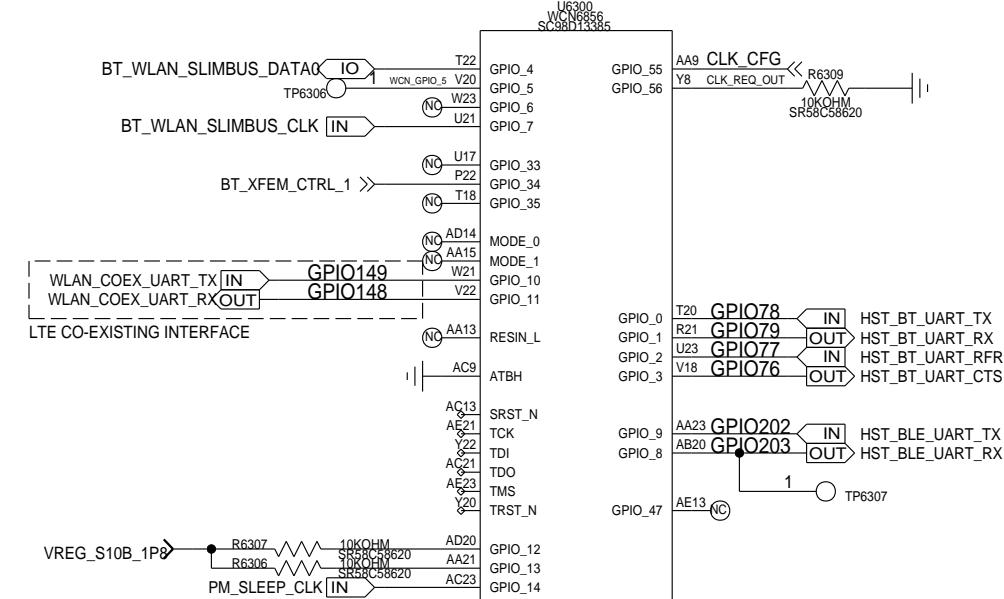


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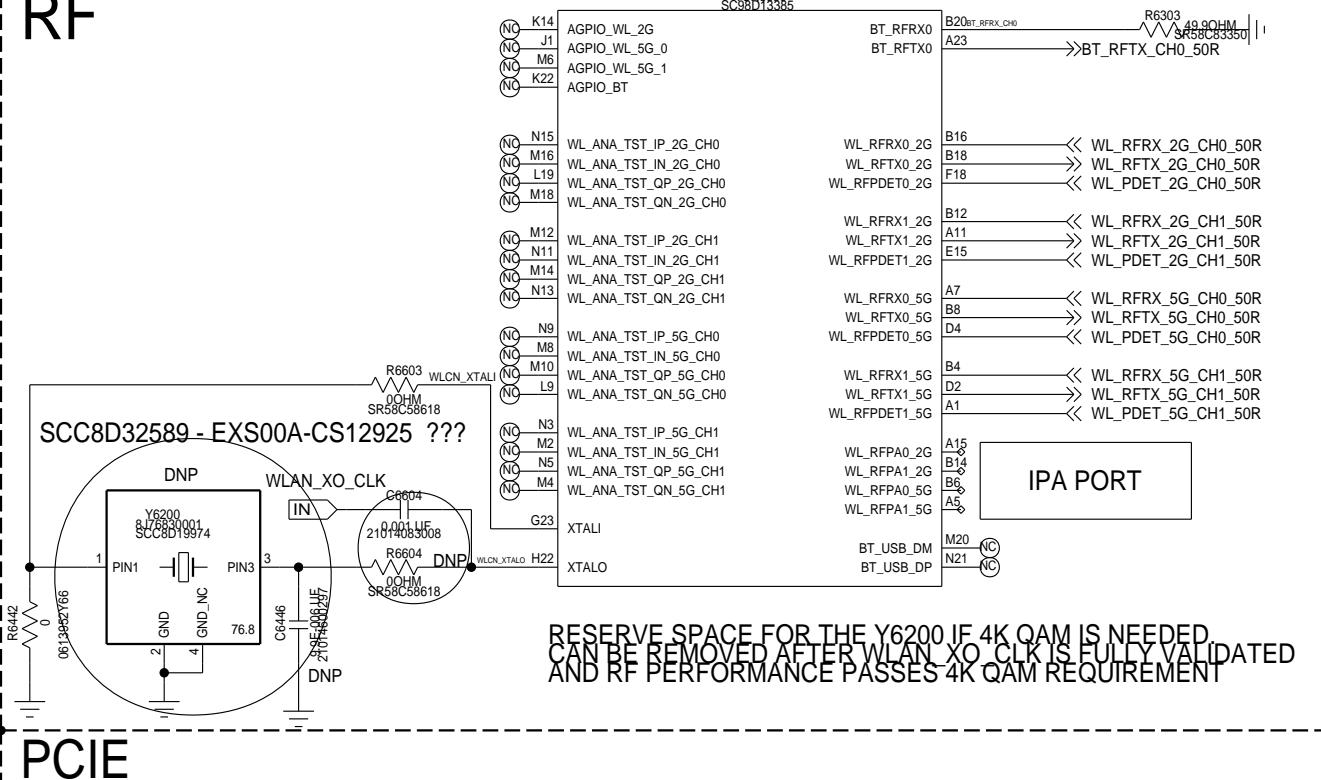
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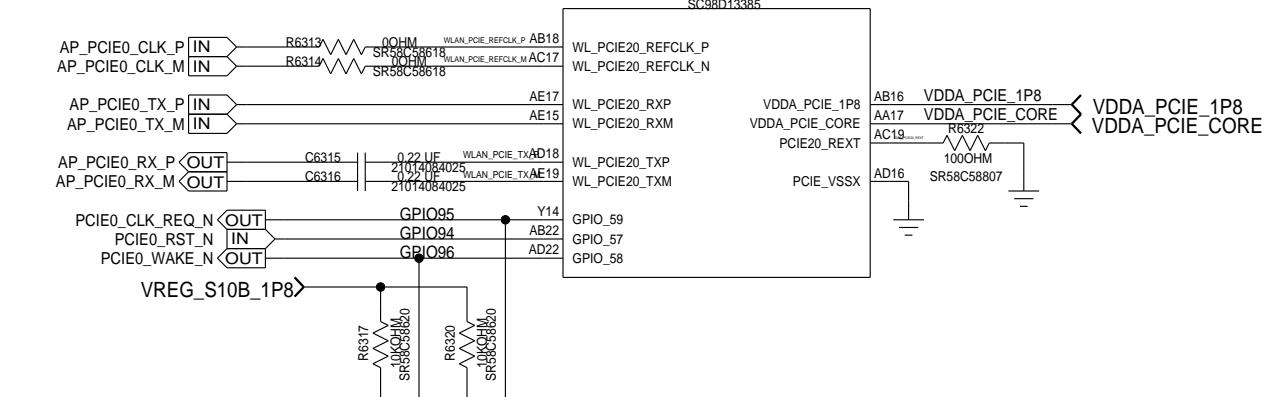
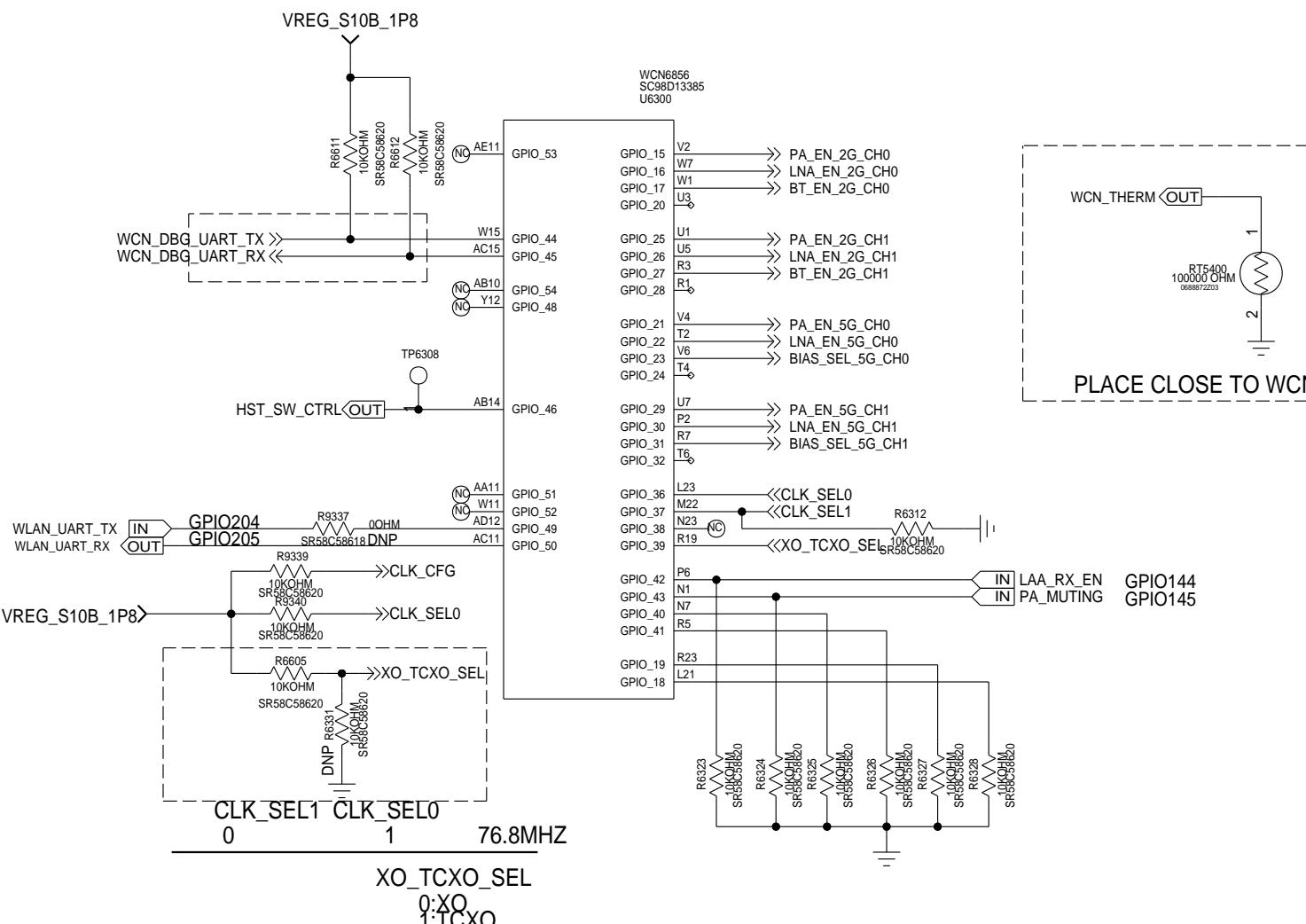
CTRL



RF



PCIE



APPROVALS

MOTOROLA
MOBILE DEVICES

ENGINEER: MOTOROLA

LAST MODIFIED BY: MOTOROLA
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BLOCK TITLE:
connectivity

PROJECT NAME
B frontier_sm8475.cpm

REV
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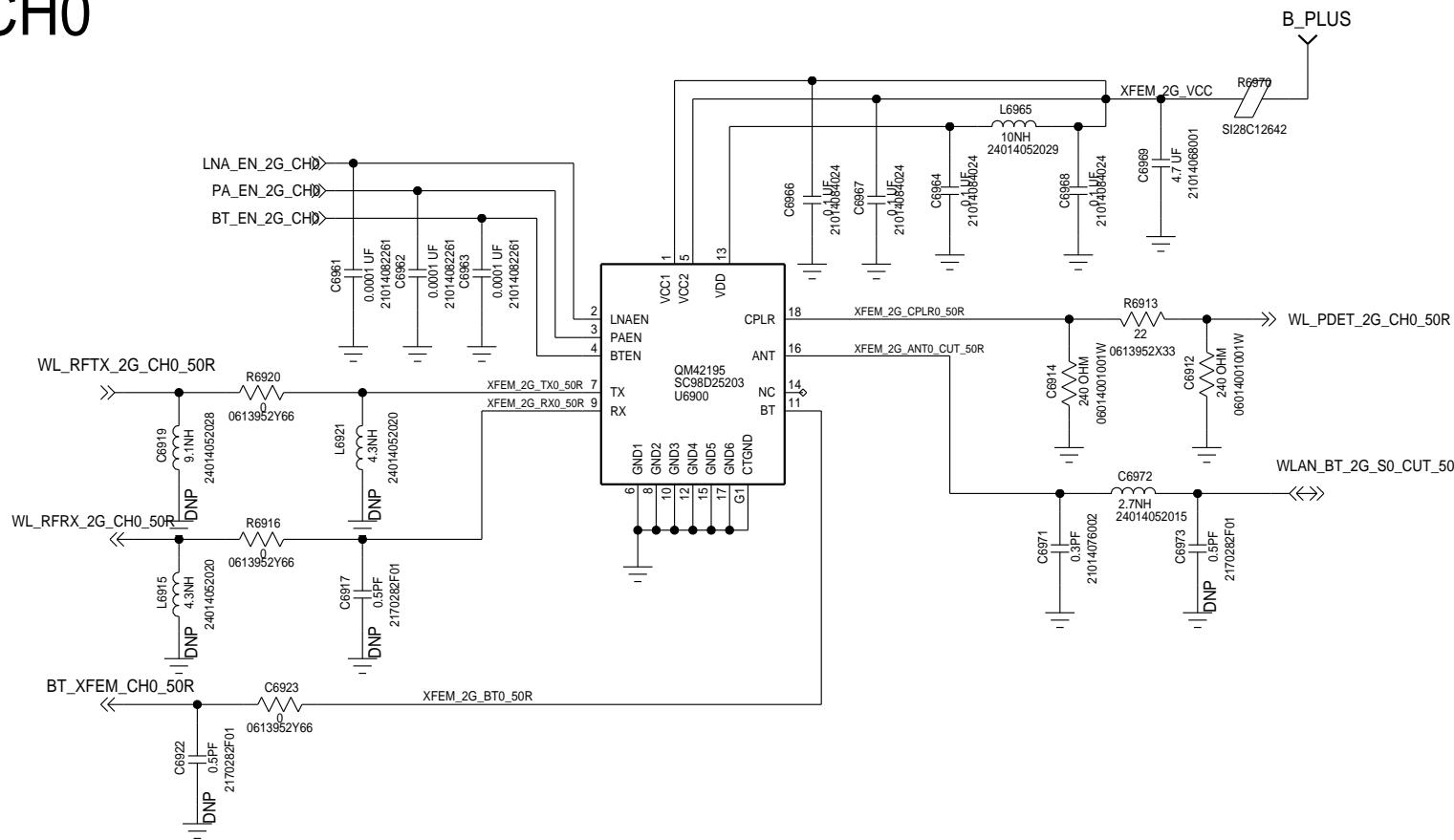
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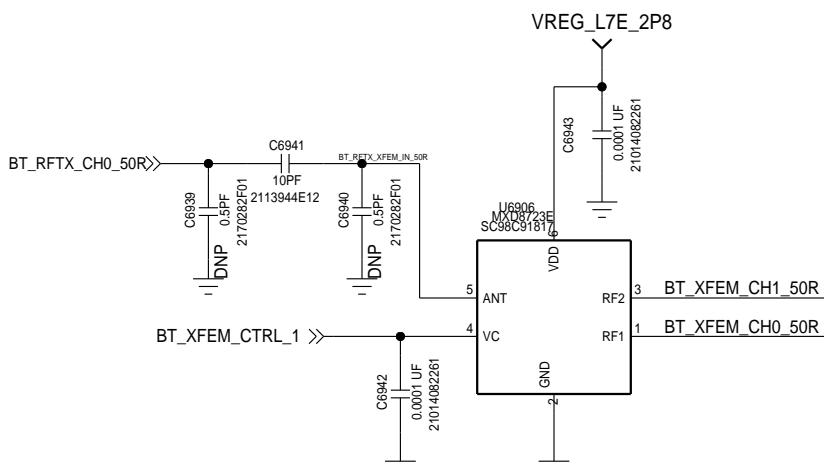
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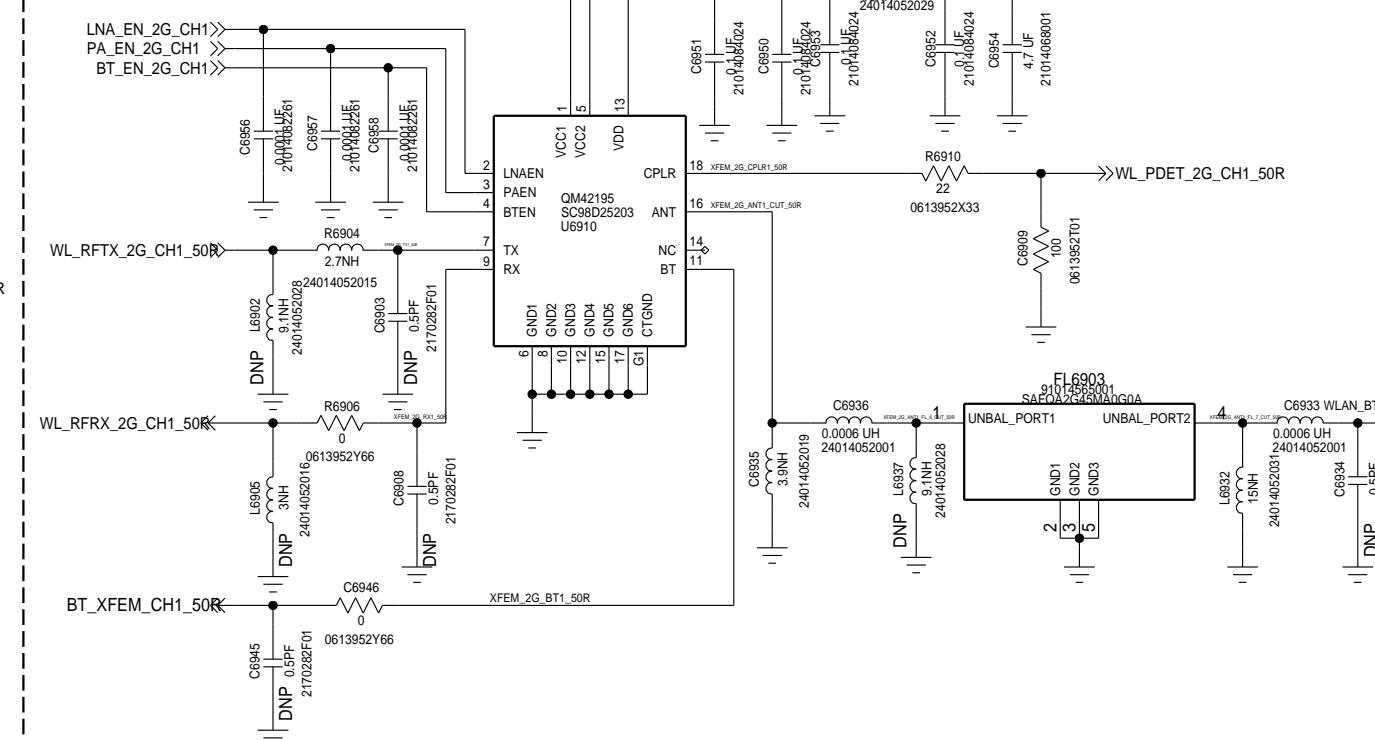
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MOBILE DEVICES

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connectivity

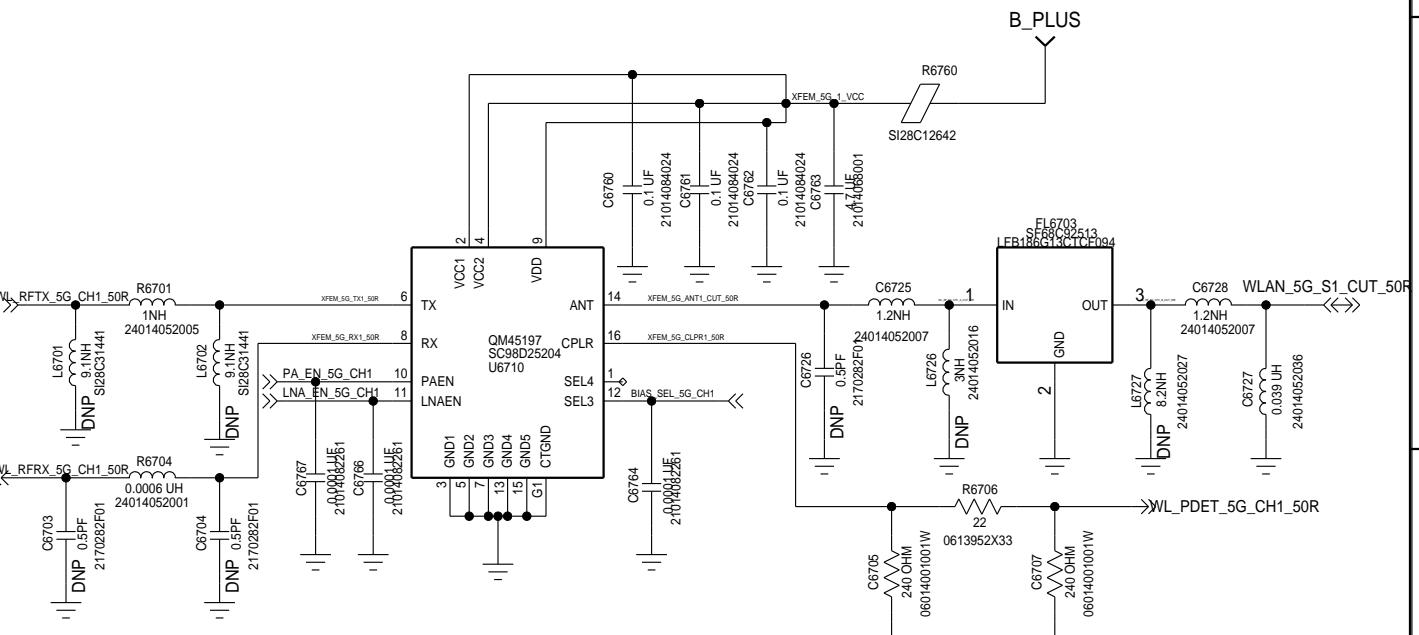
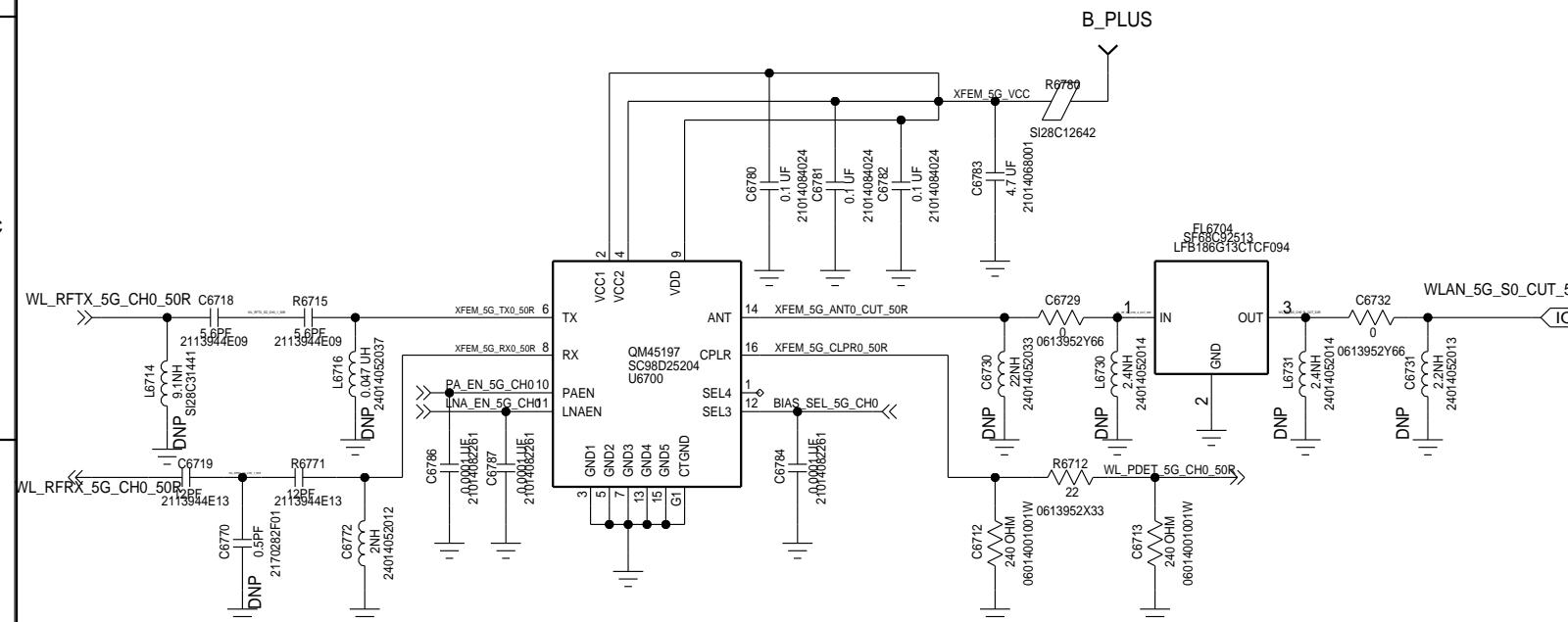
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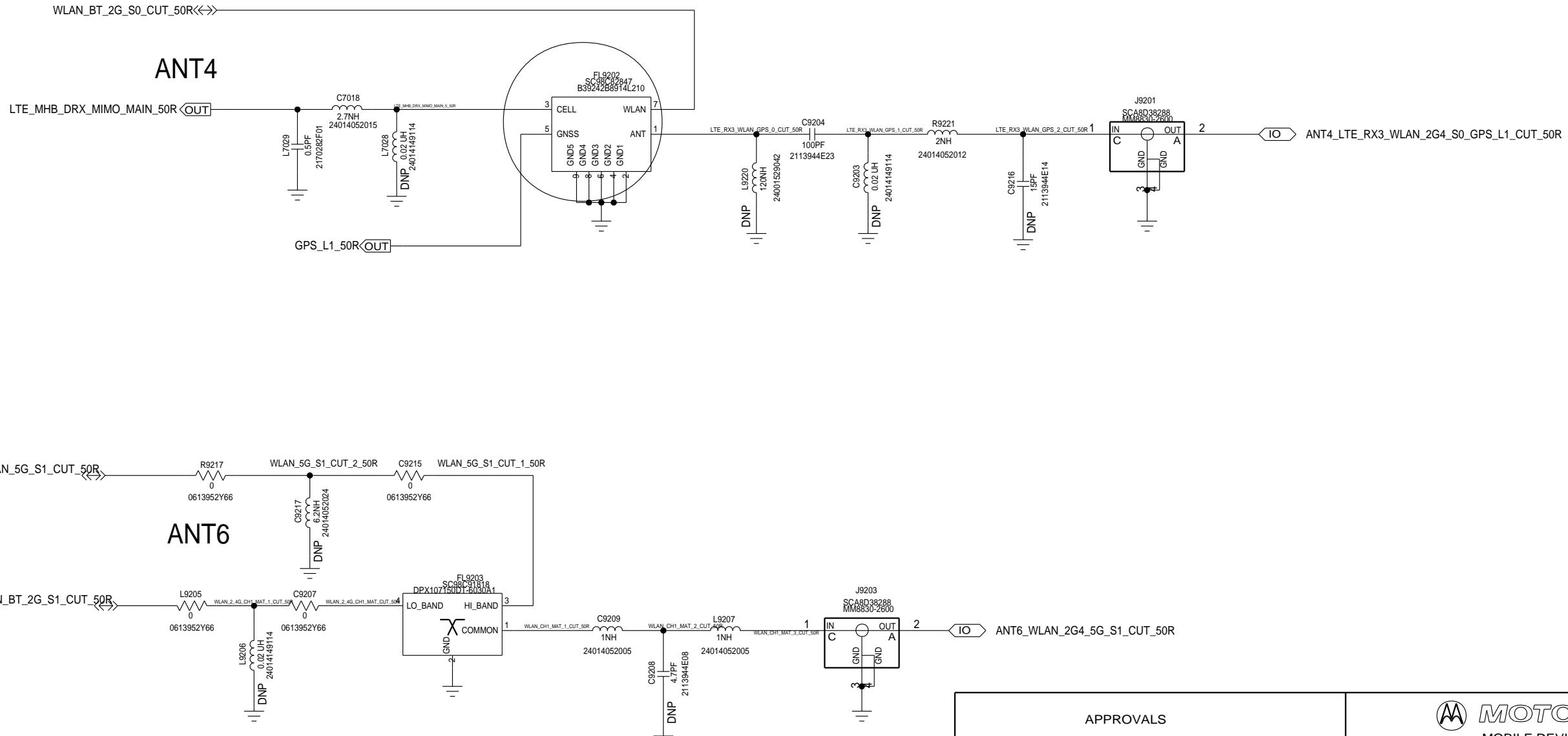
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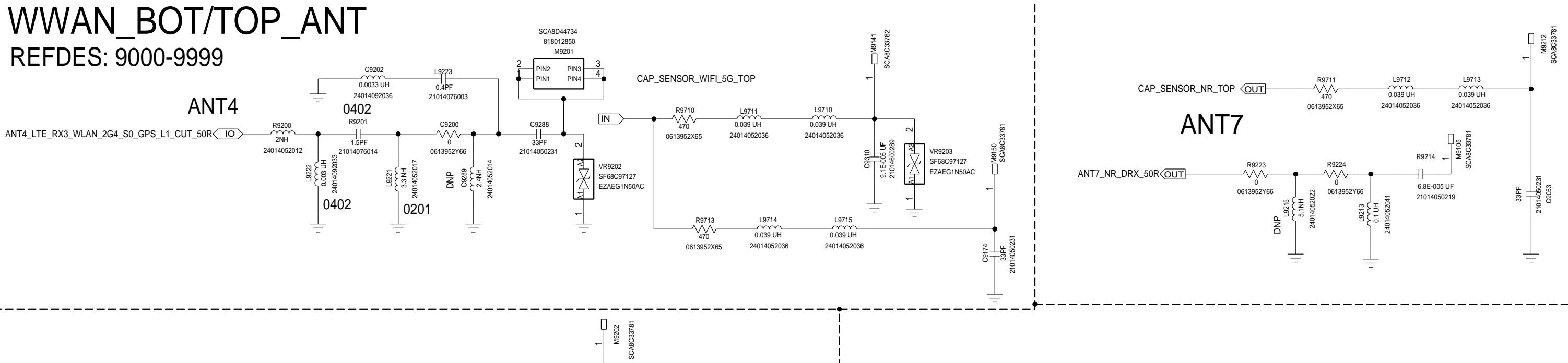


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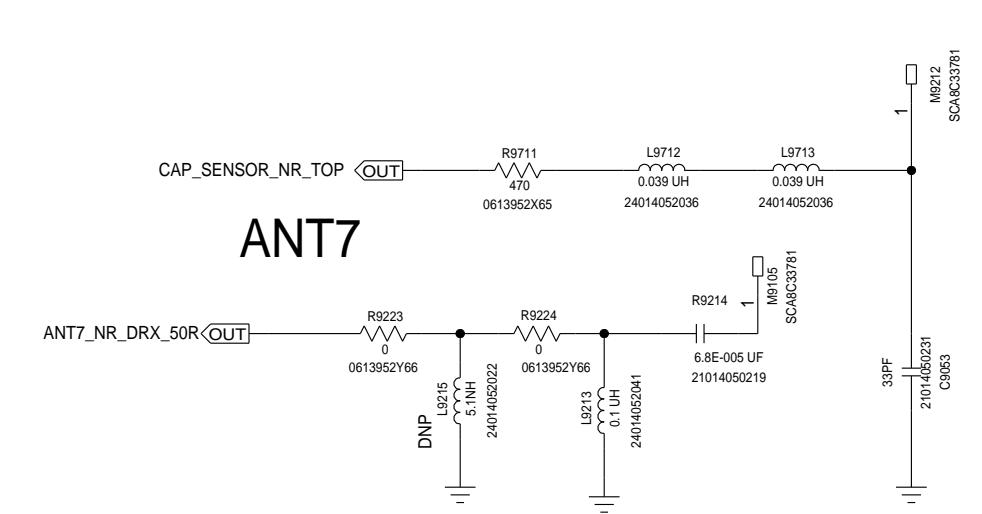
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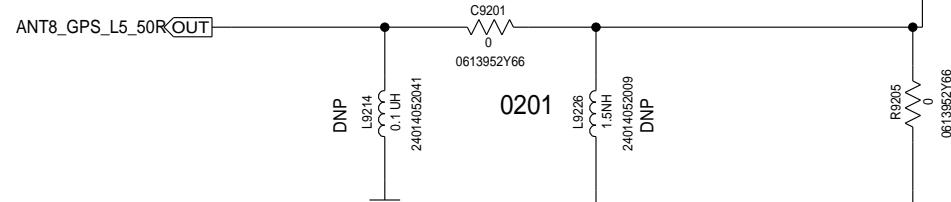
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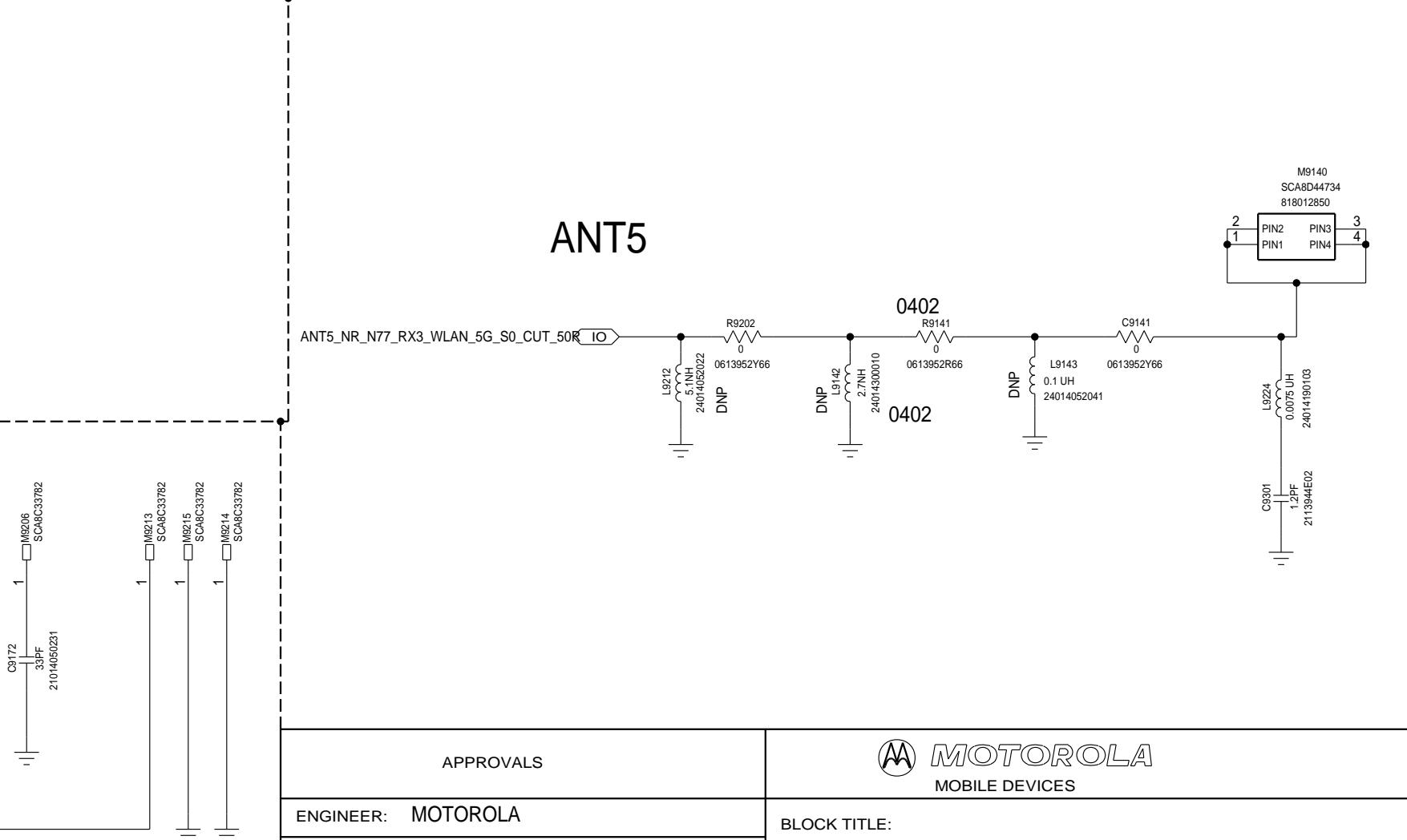
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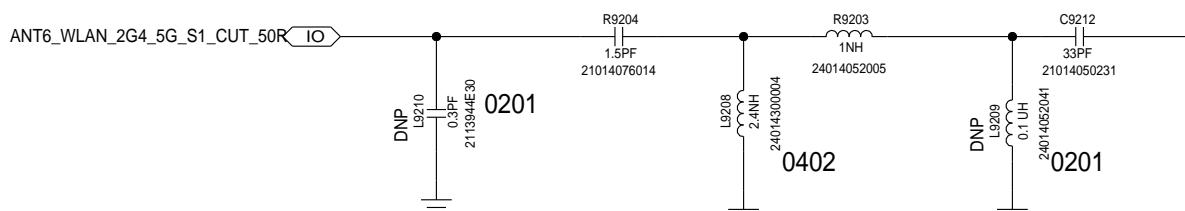
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ANT5



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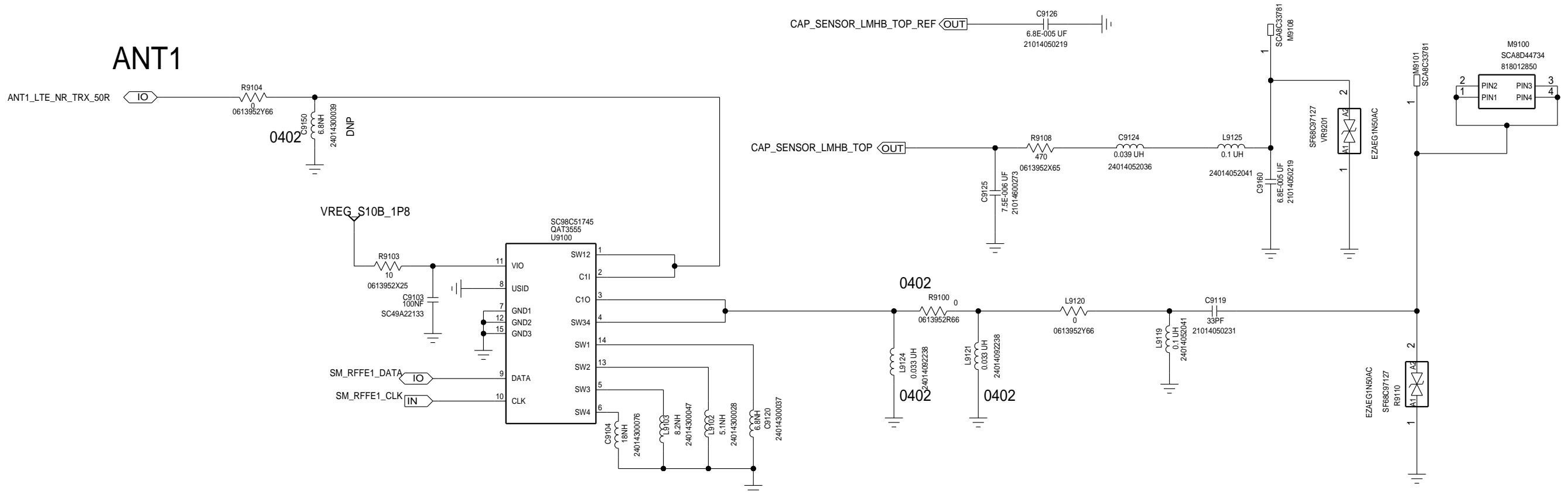
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WWAN_TOP_ANT_TUNER

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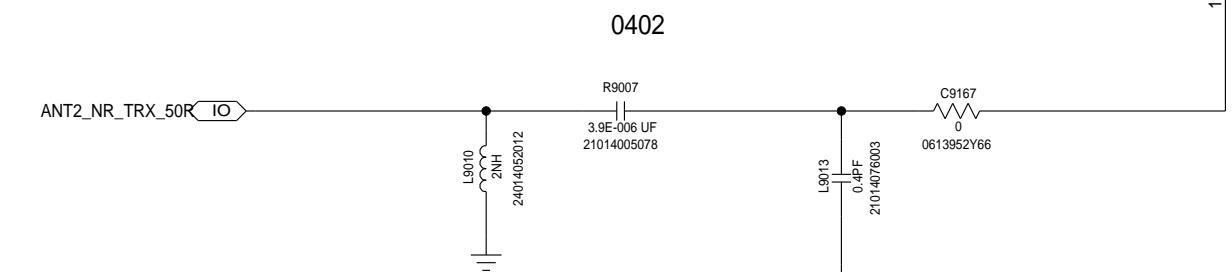


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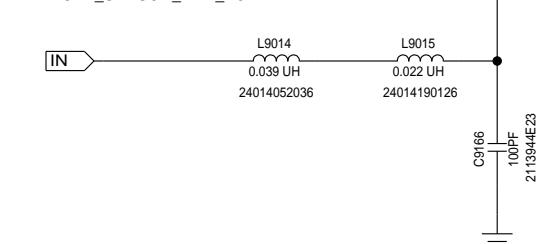
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SCA8C33782

CAP_SENSOR_WIFI_TOP



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2113944E23

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