Left block

Pad	Name	Function
1	GND	Analog GND, Analog VREF-
2	3.3V	Analog AGD, Analog VREF+
3	PF10	LCD DE
4	PA8	LCD R6
5	PD0	UART4 RX, I2C6 SDA, CAN1 RX
6	PE1	UART8_TX
7	PD11	SAI2 SD A
8	PC13	WKUP2
9	PE0	SAI MCK A
10	PD1	UART4 TX, I2C6 SCL, CAN1 TX
11	PI1	SPI1 MOSI
12	PI0	UART1 RX
13	PZ0	SPI5 SCK
14	PH2	APP
15*	PH7	SPI1 MISO
16	PD3	UART1 CTS
17	PH3	MOD
18	PF7	UART1 RTS
19	PI7	UART1 TX
20	PE12	SPI4 SCK
21	PE13	SPI4 MISO
22	PE14	SPI4 MOSI
23	PC11	SDMMC1 D3, I2S3_SDI
24	PC10	SDMMC1 D2, I2S3_CK
25	PC9	SDMMC1 CK, I2S_CKIN
26	PC12	SDMMC1 CK, I2S3_SDO
27	PC8	SMDDC1 CK
28	GND	GND
29		BOOT2
30	PD2	SDMMC1 CMD
31	PB13	UART5 TX, CAN2 TX
	PB12	UART5 RX, CAN2 RX

Middle block

Pad	Name	Function
32	PE8	UART7 RX
33	PE7	UART7 TX
34	PH9	DC D0, PWM12.2
35	PH10	DC D1
36	PH11	DC D2, PWM5.2
37	PH12	DC D3, PWM5.3
38	PI4	DC D4
39	PI5	DC D5
40	PI6	SPI5 MISO, PWM15.1
41	PE5	DC D6, SPI5 MOSI
42	PE6	DC D7, SPI5 SCK
43	PB9	PWM17.1
44		BOOT1
45		GND
46		BOOT0
47		3.3V
48	PD6	UART2 RX
49	PD7	UART2 TX
50	PA5	ADC1.19, DAC7
51	PF13	ADC2.2
52	PE9	UART7 RTS, TIM1 CH1
53	PC3	ADC1.13
54		RESET
55	PA4	DC PIXCLK, ADC1.18, I2S3_WS
56	PA6	PWM13.1, ADC1.3
57	PA13	DC XCLK
58		DC VS
59		DC HS
60		DC DE
61	PF11	ADC1.2, SAI SD B
62		VBAT
63	PD12	I2C1 SCL, SAI FS A, PWM4 CH1

Right block

Pad	Name	Function
64	PE10	UART7 CTS
65	PC1	I2C1 SDA, SPI MCK A, I2S3_MCK
66	PC0	ADC1.10
67	PA3	PWM2.4, ADC1.15
68		USB DP2
69		USB DM2
70		USB DM1
71		USB DP1
72	PF14	ADC2.6
73	PI3	LCD G6
74	PI2	LCD G3, PWM1.2
75	PI1	LCD G7, WKUP0, PWM5.4
76	PI0	LCD G5, ADC1.16
77	PH13	LCD G2
78	PI7	LCD R7, PWM8.4
79	PG12	LCD B4
80	PI6	LCD B6, PWM8.1
81	PI7	LCD B7, PWM8.4
82		GND
83	PI5	LCD B5
84	PI4	LCD B8.1
85	PB0	ADC1.9, LCD R3, PWM3.3
86	PI10	LCD HS
87	PG7	LCD CLK
88		LCD R5, PWM5.2
89		LCD R4, PWM1.4
90	PI9	LCD VS
93	PF12	ADC1.6

*Pad 15 connected to two different pins using resistor